

XRT Timeline to be uploaded on 2010/08/20

Period: 2010/08/20 10:02:00 - 2010/08/24 09:36:00

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Normal mode

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XOB #1803: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 512x512 at 1064 1048, 100s cad													
Term	Pointing (x, y)		Comment										
08/20 10:29:02 - 08/20 17:59:54	Track (-280.6, -496.3) ^{Ⓞ 08/20 10:12:00}		# OP start + 10min, track AR 11100.										
08/20 19:02:32 - 08/20 23:16:30	Track (-219.0, -498.8) ^{Ⓞ 08/20 18:10:00}		# AR 11100. HOP-128 officially from 23.40-01.40UT.										
08/21 01:45:00 - 08/21 05:36:00	Track (-219.0, -498.8) ^{Ⓞ 08/20 18:10:00}		# AR 11100. HOP-128 officially from 23.40-01.40UT.										
08/21 06:25:32 - 08/21 07:00:00	Track (-122.2, -501.5) ^{Ⓞ 08/21 06:22:30}		# AR 11100.										
PROG= 12 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 6 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 51 20-time(s) 100.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term	Pointing (x, y)		Comment										
08/20 18:03:02 - 08/20 18:21:00	Fixed (0.0, 0.0)		synoptic										
08/21 06:15:32 - 08/21 06:22:24	Fixed (0.0, 0.0)		synoptic, shifted 12.5 min										
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1800: AR Standard-B(Morphology) with PFB, FW1=Open, Al/Mesh, 512x512 at 1064 1048, 30sec-cad													
Term	Pointing (x, y)		Comment										
08/20 23:45:02 - 08/21 01:41:52	Track (-219.0, -498.8) ^{Ⓞ 08/20 18:10:00}		# AR 11100. HOP-128 officially from 23.40-01.40UT.										
PROG= 04 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 16 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 50 90-time(s) 30.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #17EF: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
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Term	Pointing (x, y)	Comment
08/20 10:29:02 - 08/20 17:59:54	Track (-280.6, -496.3) ^{Ⓢ 08/20 10:12:00}	# OP start + 10min, track AR 11100.
08/20 19:02:32 - 08/20 23:16:30	Track (-219.0, -498.8) ^{Ⓢ 08/20 18:10:00}	# AR 11100. HOP-128 officially from 23.40-01.40UT.
08/20 23:45:02 - 08/21 01:41:52	Track (-219.0, -498.8) ^{Ⓢ 08/20 18:10:00}	# AR 11100. HOP-128 officially from 23.40-01.40UT.
08/21 01:45:00 - 08/21 05:36:00	Track (-219.0, -498.8) ^{Ⓢ 08/20 18:10:00}	# AR 11100. HOP-128 officially from 23.40-01.40UT.
08/21 06:25:32 - 08/21 07:00:00	Track (-122.2, -501.5) ^{Ⓢ 08/21 06:22:30}	# AR 11100.

PROG= 07 1-time(s)

Subr=	Seqn=	1-time(s)	2.0sec	30-time(s)	60.0sec	24-time(s)	600.0sec	Open/Thick	Close	Safe	Norm	Exp.	Obs	ROI: size (center)	Q=	Comp.	AEC Buffer	Interval
Subr= 1	Seqn= 87	1-time(s)	2.0sec	30-time(s)	60.0sec			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
								Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 91	1-time(s)	2.0sec					Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	512x512 (1024, 1024)	Q=95	2	0	2.0sec
								Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	Seqn= 90	1-time(s)	2.0sec					Open/G-band	Open/G-band	open	Safe	Norm	63ms	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	Seqn= 87	1-time(s)	2.0sec	30-time(s)	60.0sec			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
								Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec					Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	512x512 (1024, 1024)	Q=95	2	0	2.0sec
								Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	Seqn= 90	1-time(s)	2.0sec					Open/G-band	Open/G-band	open	Safe	Norm	63ms	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	Seqn= 87	1-time(s)	2.0sec	30-time(s)	60.0sec			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
								Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec					Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	512x512 (1024, 1024)	Q=95	2	0	2.0sec
								Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	Seqn= 90	1-time(s)	2.0sec					Open/G-band	Open/G-band	open	Safe	Norm	63ms	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	Seqn= 87	1-time(s)	2.0sec	30-time(s)	60.0sec			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
								Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec					Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	512x512 (1024, 1024)	Q=95	2	0	2.0sec
								Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	Seqn= 89	1-time(s)	2.0sec	24-time(s)	600.0sec			Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	384x384 (1024, 1024)	Q=95	1	0	2.0sec
								Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	384x384 (1024, 1024)	Q=95	1	0	2.0sec
								Open/G-band	Open/G-band	open	Safe	Norm	63ms	384x384 (1024, 1024)	Q=98	0	0	2.0sec
								Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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Flare Detection

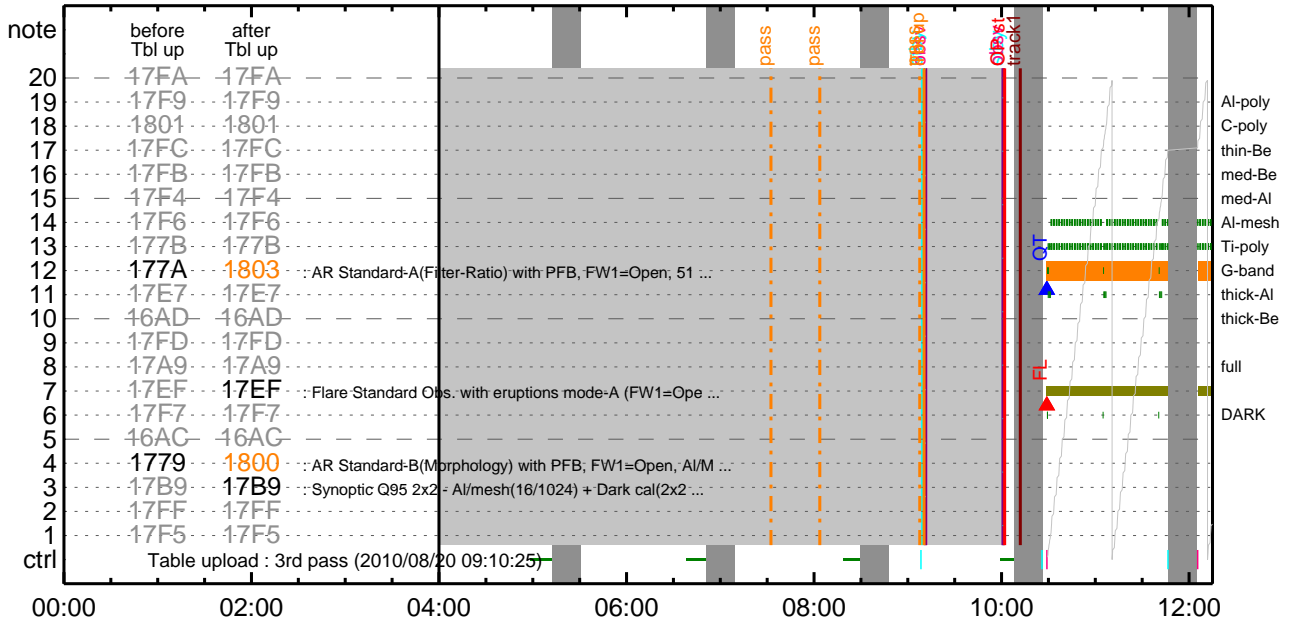
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FLD Patrol

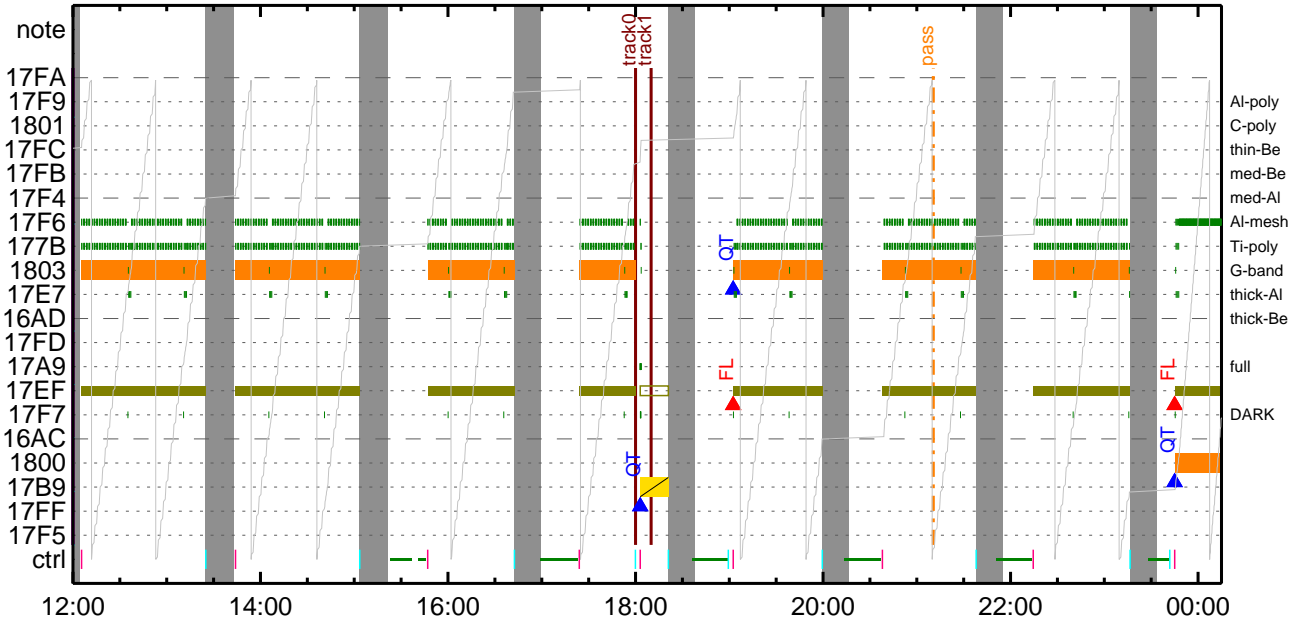
Term	Pointing (x, y)	Comment
08/20 10:26:16 - 08/20 18:00:16	Track (-280.6, -496.3) ^{Ⓢ 08/20 10:12:00}	# OP start + 10min, track AR 11100.
08/20 18:59:46 - 08/21 06:12:46	Track (-219.0, -498.8) ^{Ⓢ 08/20 18:10:00}	# AR 11100. HOP-128 officially from 23.40-01.40UT.
08/21 06:22:46 - 08/24 09:36:00	Track (-122.2, -501.5) ^{Ⓢ 08/21 06:22:30}	# AR 11100.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

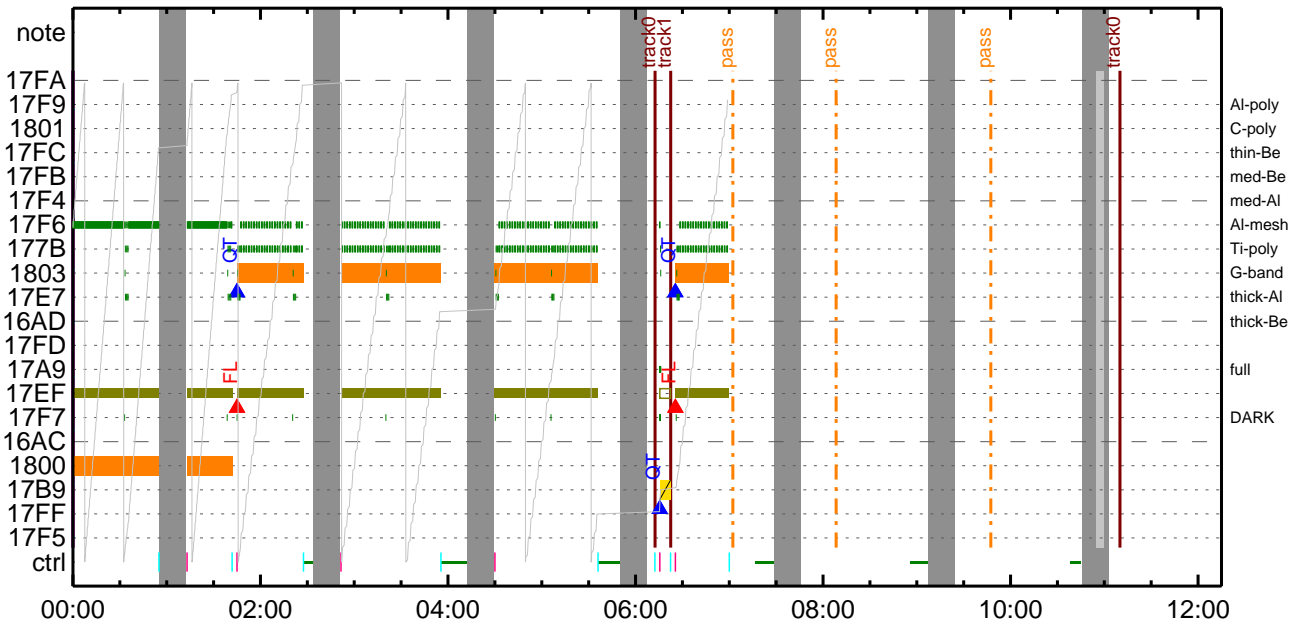
CMDI #0444 2010/08/20



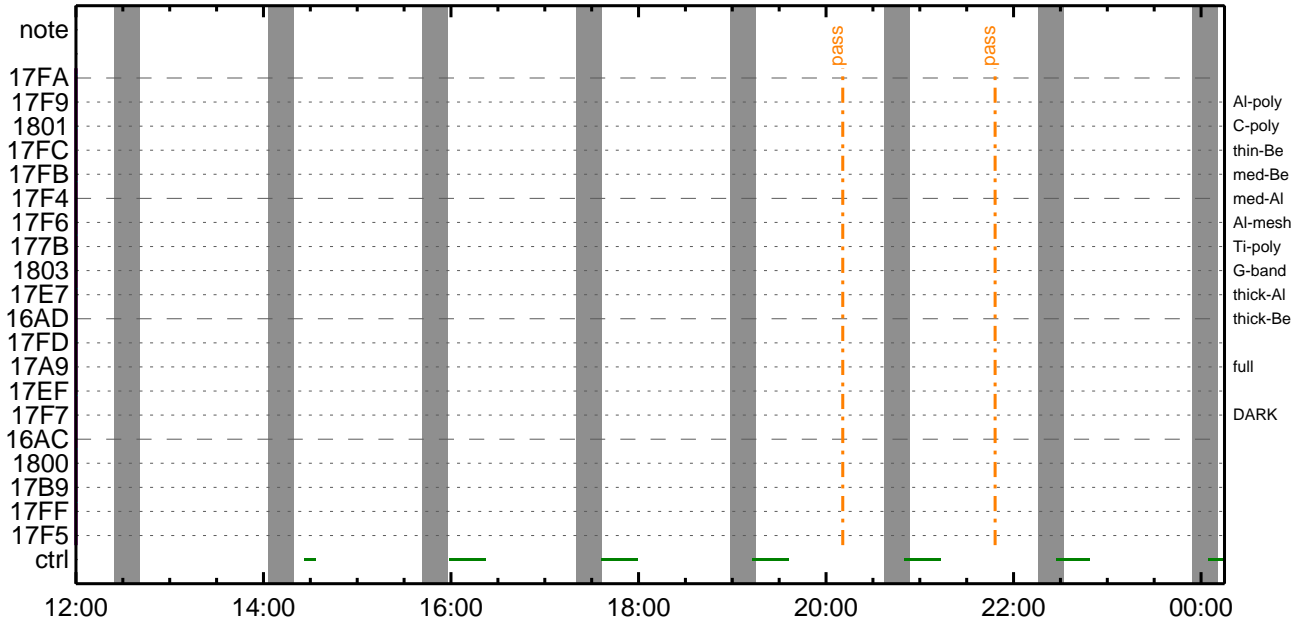
CMDI #0444 2010/08/20



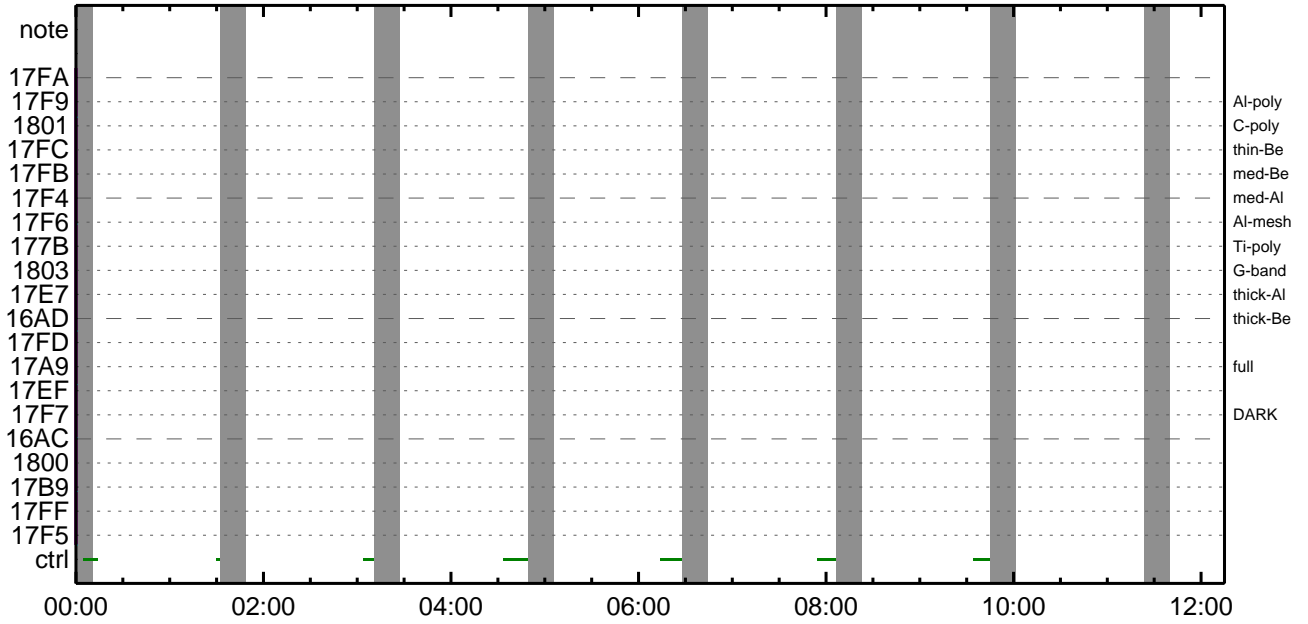
CMDI #0444 2010/08/21



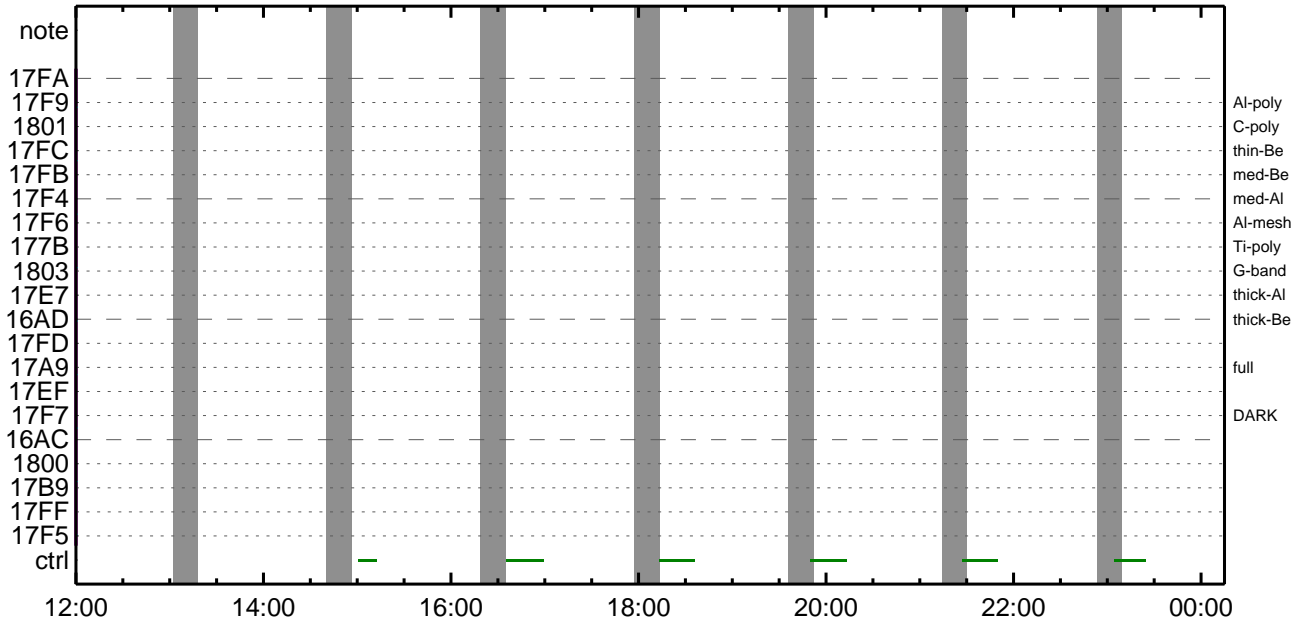
CMDI #0444 2010/08/21



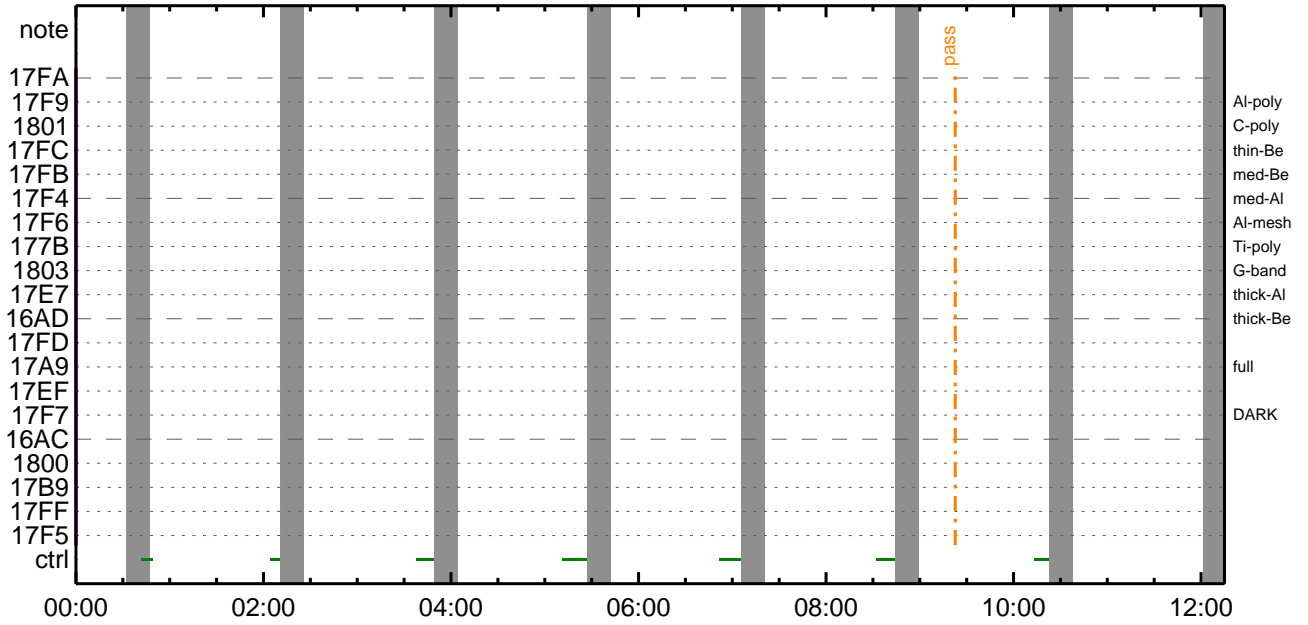
CMDI #0444 2010/08/22



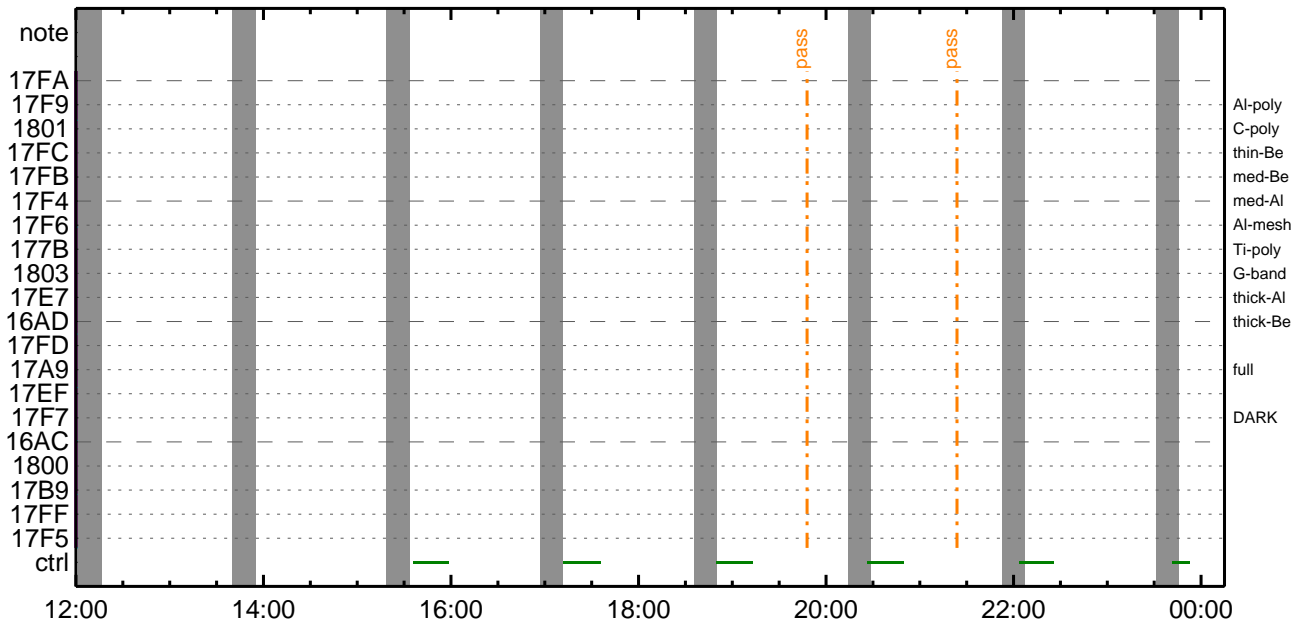
CMDI #0444 2010/08/22



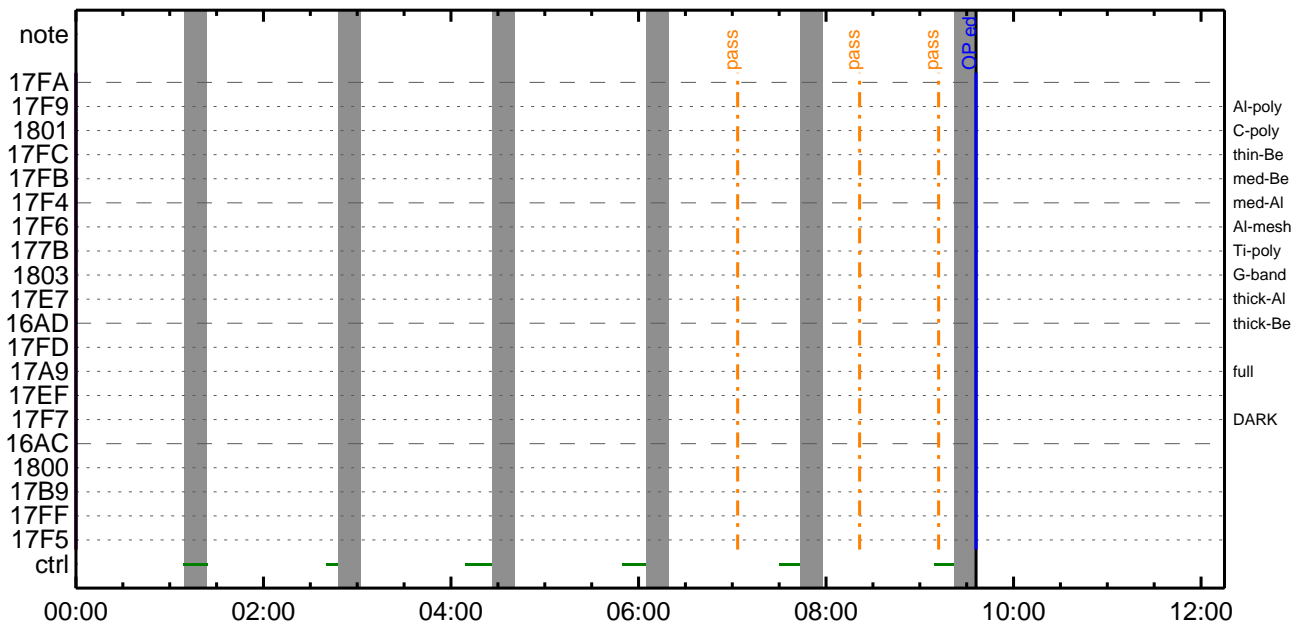
CMDI #0444 2010/08/23



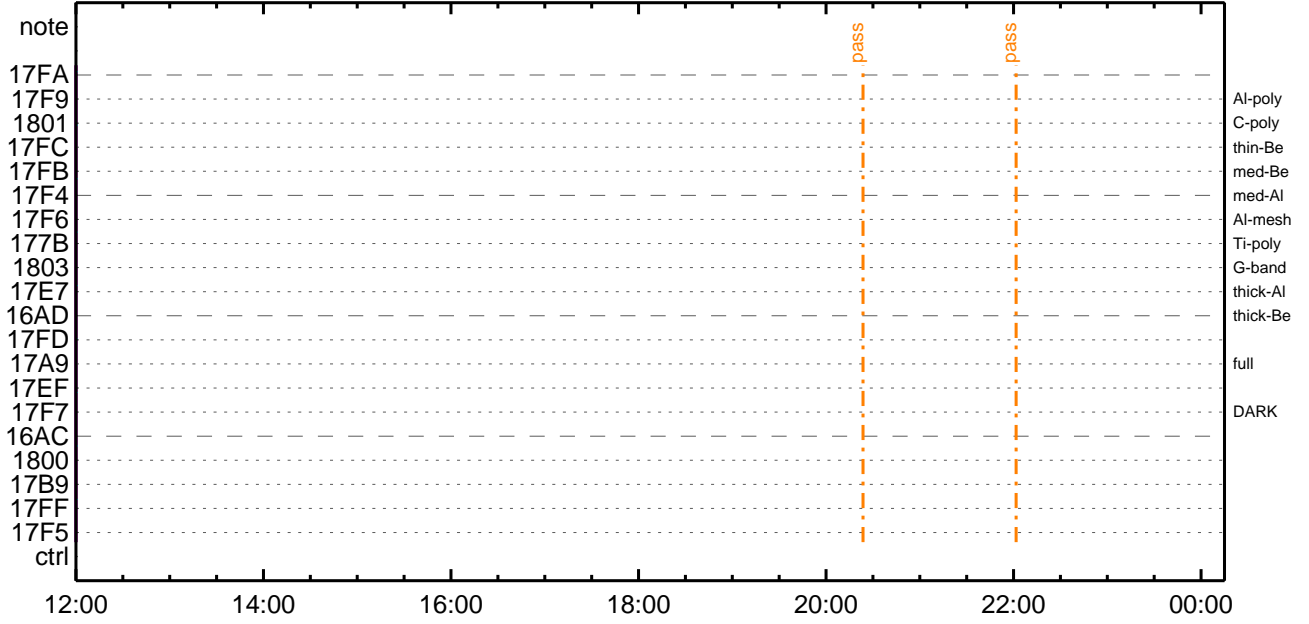
CMDI #0444 2010/08/23



CMDI #0444 2010/08/24



CMDI #0444 2010/08/24




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-537:OP
0104 ( )
0105 S. OG og-537:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGðî¼È¹ç•è²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGðî¼È¹ç•è²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPðî¼È¹ç•è²îOKð³îÇ§
0165 C.
0166 C. ***** òÈ²¼ðî¼Ä´¶Á°òÈÈ-ò°Á÷¿@ (¼âµ-YAYôYx½ªî»ð³îÇ§) *****
0167 C. DHUYâ;¼YE;È¼Y½;Yi;¼YE;Èòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADð-Á÷¿@NGðî¼Ä¹ç;ç°È²¼ðî¼TI-CMDÁ÷¿@ðî¼Ä¹Ôð•ðÈðð³ðÈ;f
0180 C. ððð¿;çSETðÈDUMPðî¼È±°îYÑY¹ç¹Ôð|ð³ðÈ;f
0181 C.
0182 C. TIY³YÞYôYÈðððÄî¿¿(UT)
0183 +. TI 2010-08-20 09:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-08-20 09:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-08-20 09:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```


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*** OP Sequence for XRT ***

2010/08/20	10:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/08/20	10:25:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	10:25:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/08/20	10:26:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/20	10:26:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/20	10:26:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/20	10:26:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/20	10:28:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	10:28:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2010/08/20	10:29:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2010/08/20	10:29:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	11:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	11:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	11:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/20	11:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/20	12:04:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/20	12:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	13:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	13:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	13:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/20	13:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/20	13:43:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/20	13:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	15:03:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	15:03:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	15:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/20	15:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/20	15:46:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/20	15:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	16:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	16:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	16:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/20	16:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/20	17:23:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/20	17:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/08/20	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/08/20	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/20	18:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/20	18:00:20.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/20	18:02:58.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	18:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/08/20	18:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/20	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/08/20	18:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/20	18:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/20	18:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2010/08/20	18:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/20	18:59:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/20	18:59:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/20	18:59:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/20	18:59:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/20	18:59:50.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/20	18:59:52.5	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/20	19:02:26.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/20	19:02:28.5	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2010/08/20	19:02:30.5	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/20	19:02:32.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/20	19:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/20	19:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/20	19:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/20	20:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/20	20:37:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/20	20:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/20	21:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/20	21:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/20	21:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/20	21:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/20	22:13:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/20	22:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/20	23:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/20	23:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/20	23:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/20	23:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/20	23:41:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/20	23:41:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/20	23:42:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/20	23:42:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/20	23:42:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/20	23:42:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/20	23:44:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/20	23:44:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/08/20	23:45:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/20	23:45:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	00:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	00:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	00:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	00:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/21	01:12:00.5	XRT_Custom_418_OG [0x1a2]				
2010/08/21	01:13:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	01:41:52.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	01:41:54.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/21	01:42:14.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/21	01:42:16.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/21	01:42:18.0	XRT_AEC_RESET_441_OG [0x1b9]				

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2010/08/21	01:42:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/21	01:44:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	01:44:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2010/08/21	01:44:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/08/21	01:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/21	02:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	02:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	02:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/21	02:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/21	02:50:30.0	XRT_Custom_418_OG [0x1a2]					
2010/08/21	02:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/21	03:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	03:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	03:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/21	03:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/21	04:29:00.0	XRT_Custom_418_OG [0x1a2]					
2010/08/21	04:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/21	05:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	05:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	05:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/21	05:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/21	06:12:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	06:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2010/08/21	06:12:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2010/08/21	06:12:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/08/21	06:12:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/08/21	06:12:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/21	06:15:28.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	06:15:30.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2010/08/21	06:15:32.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/21	06:22:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	06:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/08/21	06:22:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00
2010/08/21	06:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/08/21	06:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/08/21	06:22:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/08/21	06:22:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/21	06:25:26.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/21	06:25:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2010/08/21	06:25:30.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/08/21	06:25:32.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/21	07:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/21	11:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00