

XRT Timeline to be uploaded on 2010/08/21

Period: 2010/08/21 11:00:00 - 2010/08/26 10:49:00

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Normal mode

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XOB #1804: AR Standard-B(Morphology) for FW1=Open, 384x384 at 1064 1048, 30sec-cad													
Term	Pointing (x, y)		Comment										
08/21 11:13:02 - 08/21 14:25:30	Track (-93.7, -522.2) @ 08/21 11:10:00		# OP start + 10min, Observe AR 11100. HOP-159 test: 11.10-14.00UT.										
PROG= 13 Inf.-time(s)													
┌ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 10 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
┌ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 25 240-time(s) 30.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1807: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 512x512 at 1064 1048, 225s cad													
Term	Pointing (x, y)		Comment										
08/21 14:37:08 - 08/21 17:20:00	Track (-93.7, -522.2) @ 08/21 11:10:00		# OP start + 10min, Observe AR 11100. HOP-159 test: 11.10-14.00UT.										
08/21 18:13:02 - 08/21 22:15:30	Track (-27.2, -502.9) @ 08/21 18:10:00		# AR 11100. HOP-128 officially from 22.50-00.50.										
08/22 00:53:08 - 08/22 05:46:24	Track (-27.2, -502.9) @ 08/21 18:10:00		# AR 11100. HOP-128 officially from 22.50-00.50.										
08/22 05:59:32 - 08/22 17:47:24	Track (68.1, -502.9) @ 08/22 05:56:30		# AR 11100.										
08/22 18:40:08 - 08/22 22:53:00	Track (164.5, -501.6) @ 08/22 17:57:30		# AR 11100. HOP-128 officially form 23.25-01.25UT.										
08/23 01:28:08 - 08/23 05:59:54	Track (164.5, -501.6) @ 08/22 17:57:30		# AR 11100. HOP-128 officially form 23.25-01.25UT.										
08/23 06:13:02 - 08/23 15:36:00	Track (260.2, -498.8) @ 08/23 06:10:00		# AR 11100.										
08/23 20:03:08 - 08/23 21:52:30	Track (326.9, -494.1) @ 08/23 16:10:00		# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.										
08/24 00:33:08 - 08/24 06:25:54	Track (326.9, -494.1) @ 08/23 16:10:00		# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.										
08/24 06:39:02 - 08/24 09:09:00	Track (440.3, -489.4) @ 08/24 06:36:00		# AR 11100.										
PROG= 20 Inf.-time(s)													
┌ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 6 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
┌ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 51 20-time(s) 225.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term	Pointing (x, y)		Comment										
08/21 18:03:02 - 08/21 18:09:54	Fixed (0.0, 0.0)		synoptic										
08/22 05:49:32 - 08/22 05:56:24	Fixed (0.0, 0.0)		synoptic, shifted -13.5 min										
08/22 17:50:32 - 08/22 17:57:30	Fixed (0.0, 0.0)		synoptic, shifted -12.5 min										
08/23 06:03:02 - 08/23 06:09:54	Fixed (0.0, 0.0)		synoptic										
08/23 16:03:02 - 08/23 16:09:54	Fixed (0.0, 0.0)		synoptic, shifted -7.0 min, shifted back another ~2 hours.										
08/24 06:29:02 - 08/24 06:35:54	Fixed (0.0, 0.0)		synoptic, shifted 26.0 min										
PROG= 03 1-time(s)													
┌ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1800: AR Standard-B(Morphology) with PFB, FW1=Open, AI/Mesh, 512x512 at 1064 1048, 30sec-cad

Term	Pointing (x, y)	Comment
08/21 22:53:09 - 08/22 00:50:00	Track (-27.2, -502.9) @ 08/21 18:10:00	# AR 11100. HOP-128 officially from 22.50-00.50.
08/22 23:28:08 - 08/23 01:25:00	Track (164.5, -501.6) @ 08/22 17:57:30	# AR 11100. HOP-128 officially form 23.25-01.25UT.
08/23 22:33:08 - 08/24 00:30:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
PROG= 04 Inf.-time(s)		
└ Subr= 2 1-time(s) 2.0sec		
└ Seqn= 17 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-AI close	Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
└ Seqn= 16 4-time(s) 2.0sec		
Open/AI-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-AI	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 50 90-time(s) 30.0sec		
Open/AI-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/AI-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 1 2.0sec
Open/AI-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 2 2.0sec
Open/AI-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1806: AR Dymanics (RAISE) for FW1=Open, 768x768 2x2 at 1064 1048, 30sec-

Term	Pointing (x, y)	Comment
08/23 16:13:02 - 08/23 17:36:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/23 19:17:09 - 08/23 20:00:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
PROG= 01 Inf.-time(s)		
└ Subr= 2 1-time(s) 2.0sec		
└ Seqn= 54 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-AI close	Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 768x768 (1064, 1048) Q=98 0 0 2.0sec
└ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 52 240-time(s) 30.0sec		
Open/Ti-poly	Open/thick-AI close	Safe Norm 125ms Obs 2x2 768x768 (1064, 1048) Q=95 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1805: AR Dymanics (RAISE) for FW1=Open, 768x768 2x2 at 1064 1048, 10sec-cad

Term	Pointing (x, y)	Comment
08/23 17:39:08 - 08/23 18:35:30	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
PROG= 15 Inf.-time(s)		
└ Subr= 2 1-time(s) 2.0sec		
└ Seqn= 54 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-AI close	Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 768x768 (1064, 1048) Q=98 0 0 2.0sec
└ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 52 240-time(s) 10.0sec		
Open/Ti-poly	Open/thick-AI close	Safe Norm 125ms Obs 2x2 768x768 (1064, 1048) Q=95 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #17EF: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
08/21 11:13:02 - 08/21 14:25:30	Track (-93.7, -522.2) @ 08/21 11:10:00	# OP start + 10min, Observe AR 11100. HOP-159 test: 11.10-14.00UT.
08/21 14:37:08 - 08/21 17:20:00	Track (-93.7, -522.2) @ 08/21 11:10:00	# OP start + 10min, Observe AR 11100. HOP-159 test: 11.10-14.00UT.
08/21 18:13:02 - 08/21 22:15:30	Track (-27.2, -502.9) @ 08/21 18:10:00	# AR 11100. HOP-128 officially from 22.50-00.50.
08/21 22:53:09 - 08/22 00:50:00	Track (-27.2, -502.9) @ 08/21 18:10:00	# AR 11100. HOP-128 officially from 22.50-00.50.
08/22 00:53:08 - 08/22 05:46:24	Track (-27.2, -502.9) @ 08/21 18:10:00	# AR 11100. HOP-128 officially from 22.50-00.50.
08/22 05:59:32 - 08/22 17:47:24	Track (68.1, -502.9) @ 08/22 05:56:30	# AR 11100.
08/22 18:40:08 - 08/22 22:53:00	Track (164.5, -501.6) @ 08/22 17:57:30	# AR 11100. HOP-128 officially form 23.25-01.25UT.
08/22 23:28:08 - 08/23 01:25:00	Track (164.5, -501.6) @ 08/22 17:57:30	# AR 11100. HOP-128 officially form 23.25-01.25UT.
08/23 01:28:08 - 08/23 05:59:54	Track (164.5, -501.6) @ 08/22 17:57:30	# AR 11100. HOP-128 officially form 23.25-01.25UT.
08/23 06:13:02 - 08/23 15:36:00	Track (260.2, -498.8) @ 08/23 06:10:00	# AR 11100.
08/23 16:13:02 - 08/23 17:36:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/23 17:39:08 - 08/23 18:35:30	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/23 19:17:09 - 08/23 20:00:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/23 20:03:08 - 08/23 21:52:30	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/23 22:33:08 - 08/24 00:30:00	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/24 00:33:08 - 08/24 06:25:54	Track (326.9, -494.1) @ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.
08/24 06:39:02 - 08/24 09:09:00	Track (440.3, -489.4) @ 08/24 06:36:00	# AR 11100.
PROG= 07 1-time(s)		
└ Subr= 1 30-time(s) 20.0sec		
└ Seqn= 87 1-time(s) 2.0sec		

	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 91		1-time(s)	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Subr= 2		1-time(s)	2.0sec									
	Seqn= 90		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Subr= 3		30-time(s)	60.0sec									
	Seqn= 87		1-time(s)	2.0sec									
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s)	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Subr= 2		1-time(s)	2.0sec									
	Seqn= 90		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Subr= 3		30-time(s)	60.0sec									
	Seqn= 87		1-time(s)	2.0sec									
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s)	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Subr= 2		1-time(s)	2.0sec									
	Seqn= 90		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Subr= 3		30-time(s)	60.0sec									
	Seqn= 87		1-time(s)	2.0sec									
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s)	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Subr= 4		24-time(s)	600.0sec									
	Seqn= 89		1-time(s)	2.0sec									
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

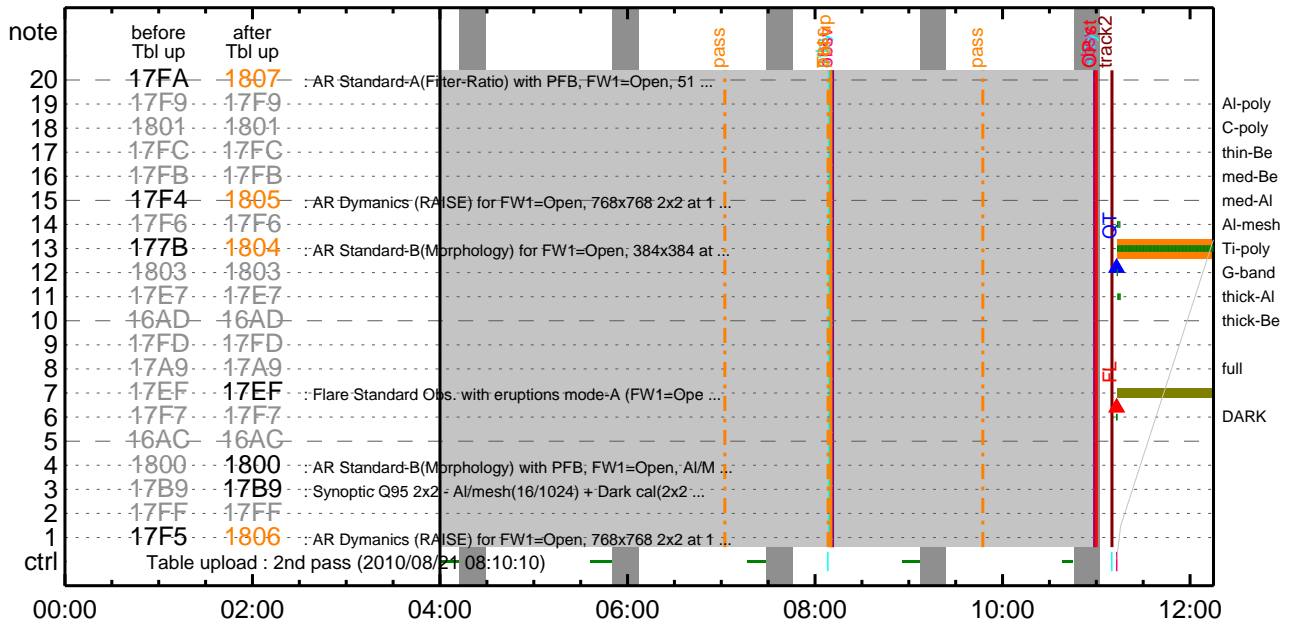
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Flare Detection

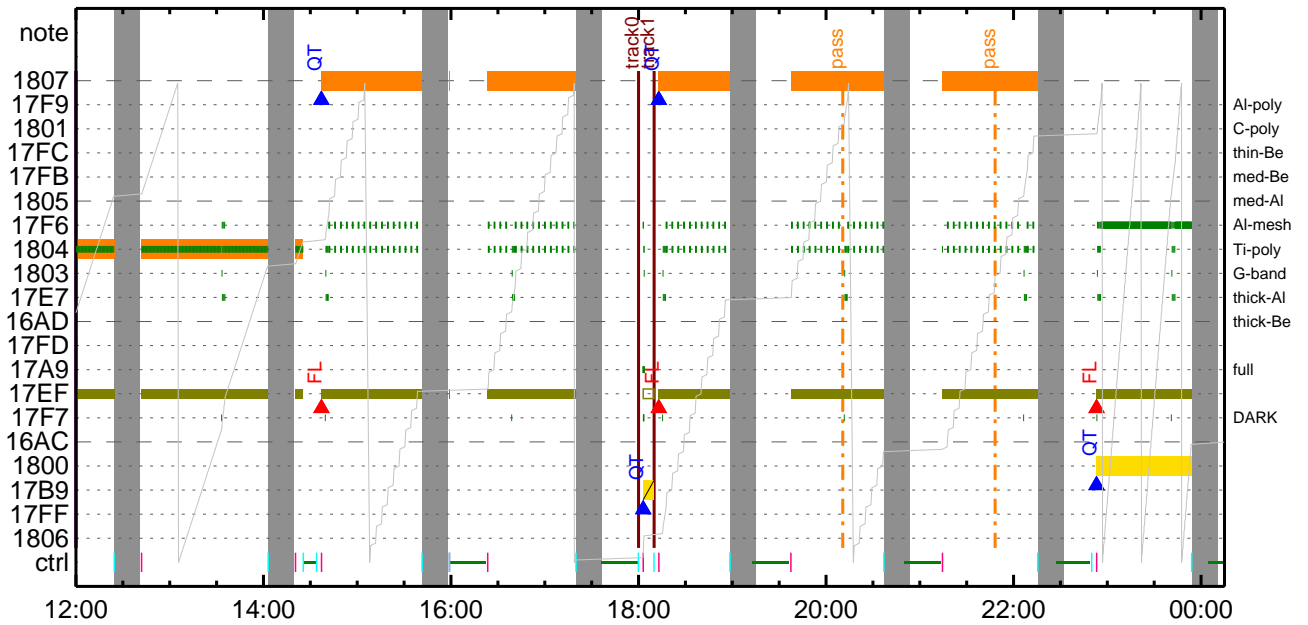
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FLD Patrol												
Term	Pointing (x, y)					Comment						
08/21 11:10:16 - 08/21 18:00:16	Track (-93.7,	-522.2)	@ 08/21 11:10:00	# OP start + 10min, Observe AR 11100. HOP-159 test: 11.10-14.00UT.							
08/21 18:10:16 - 08/22 05:46:46	Track (-27.2,	-502.9)	@ 08/21 18:10:00	# AR 11100. HOP-128 officially from 22.50-00.50.							
08/22 05:56:46 - 08/22 17:47:46	Track (68.1,	-502.9)	@ 08/22 05:56:30	# AR 11100.							
08/22 18:37:22 - 08/23 06:00:16	Track (164.5,	-501.6)	@ 08/22 17:57:30	# AR 11100. HOP-128 officially form 23.25-01.25UT.							
08/23 06:10:16 - 08/23 16:00:16	Track (260.2,	-498.8)	@ 08/23 06:10:00	# AR 11100.							
08/23 16:10:16 - 08/24 06:26:16	Track (326.9,	-494.1)	@ 08/23 16:10:00	# AR 11100. HOP170 - RAISE, launch at 17.57. HOP-128 officially from 22.30-00.30.							
08/24 06:36:16 - 08/26 10:49:00	Track (440.3,	-489.4)	@ 08/24 06:36:00	# AR 11100.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

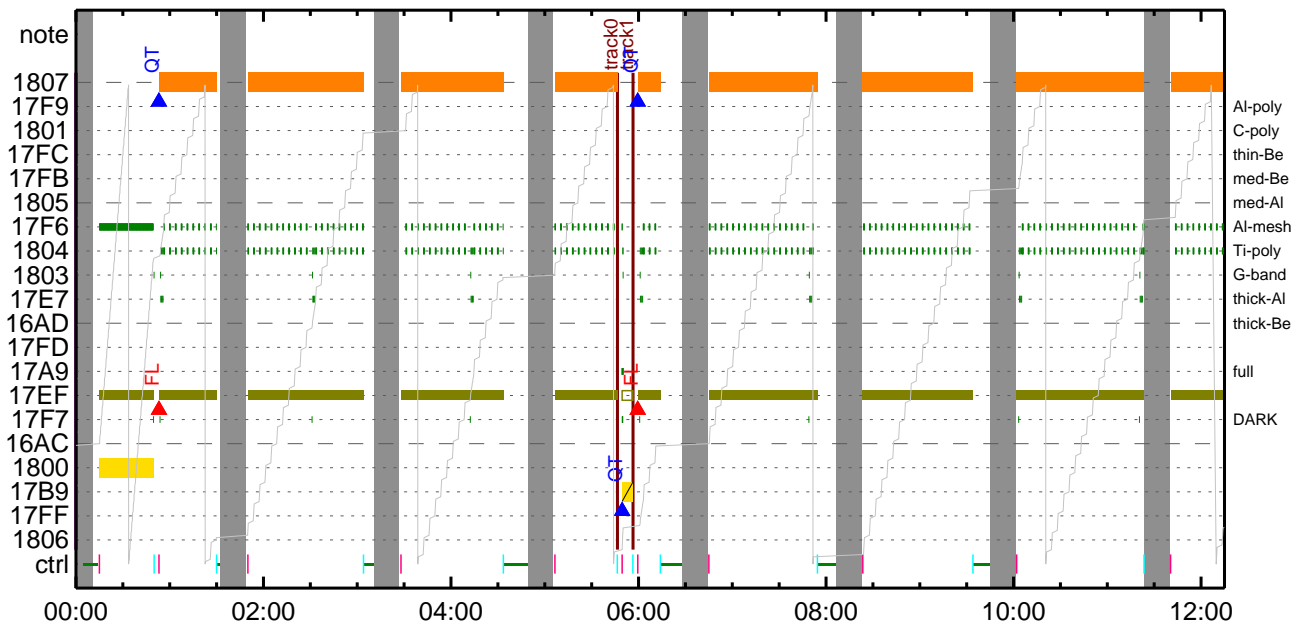
CMDI #0447 2010/08/21



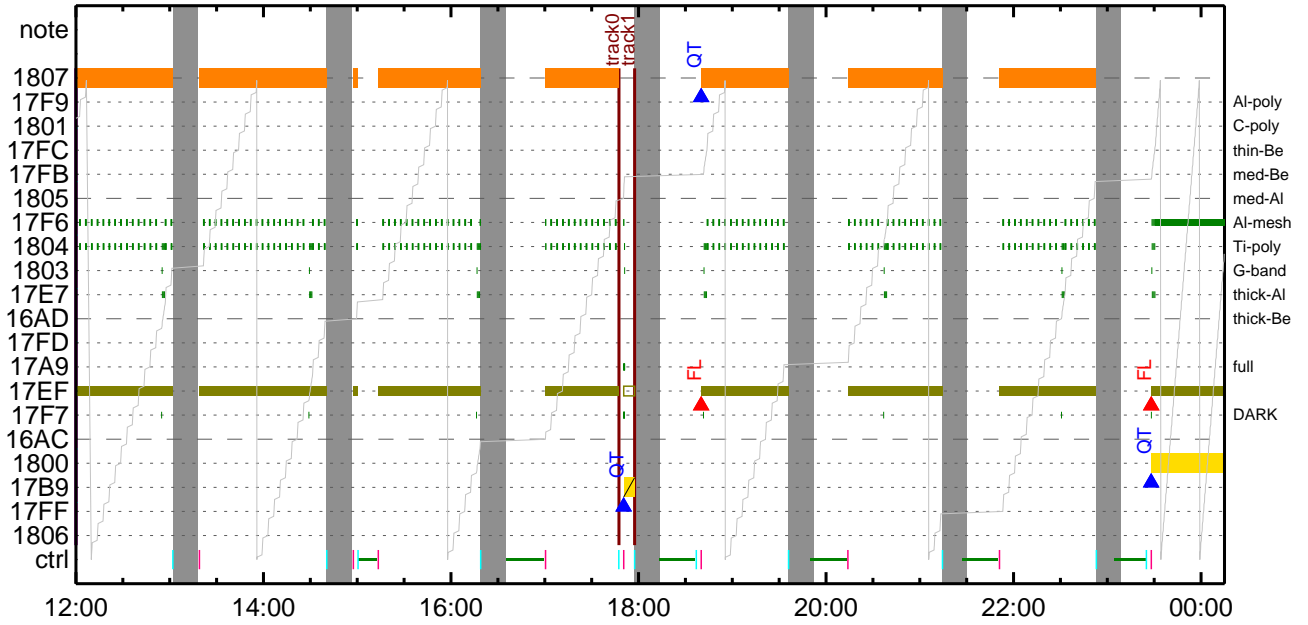
CMDI #0447 2010/08/21



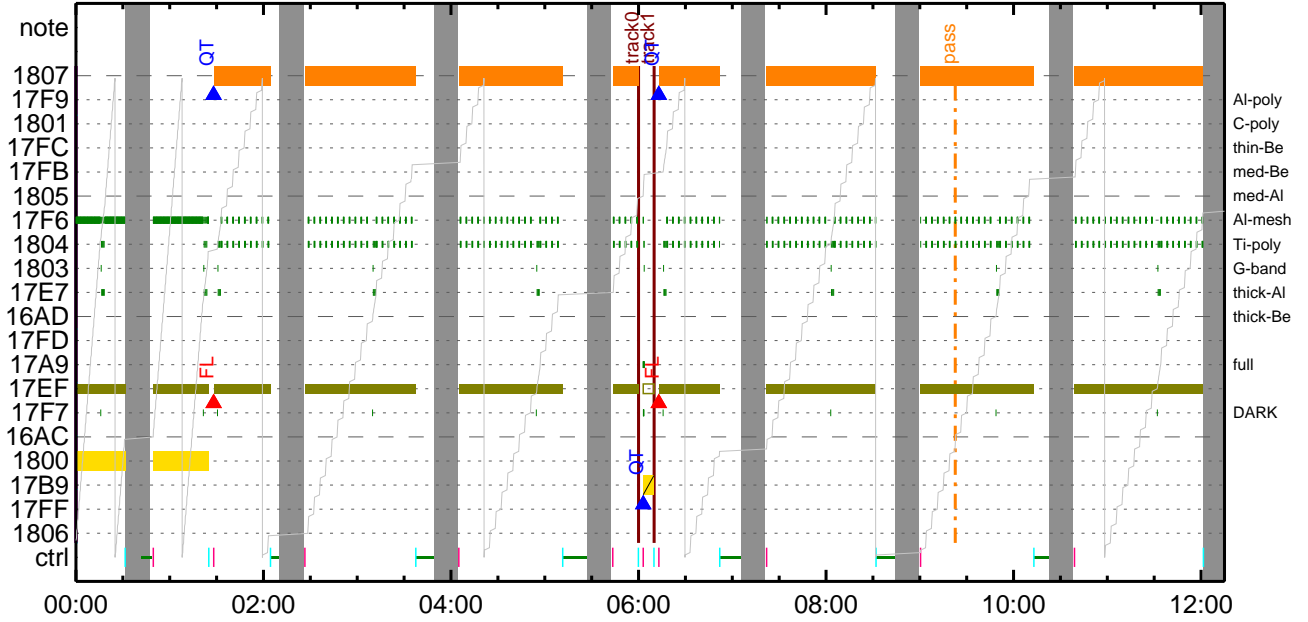
CMDI #0447 2010/08/22



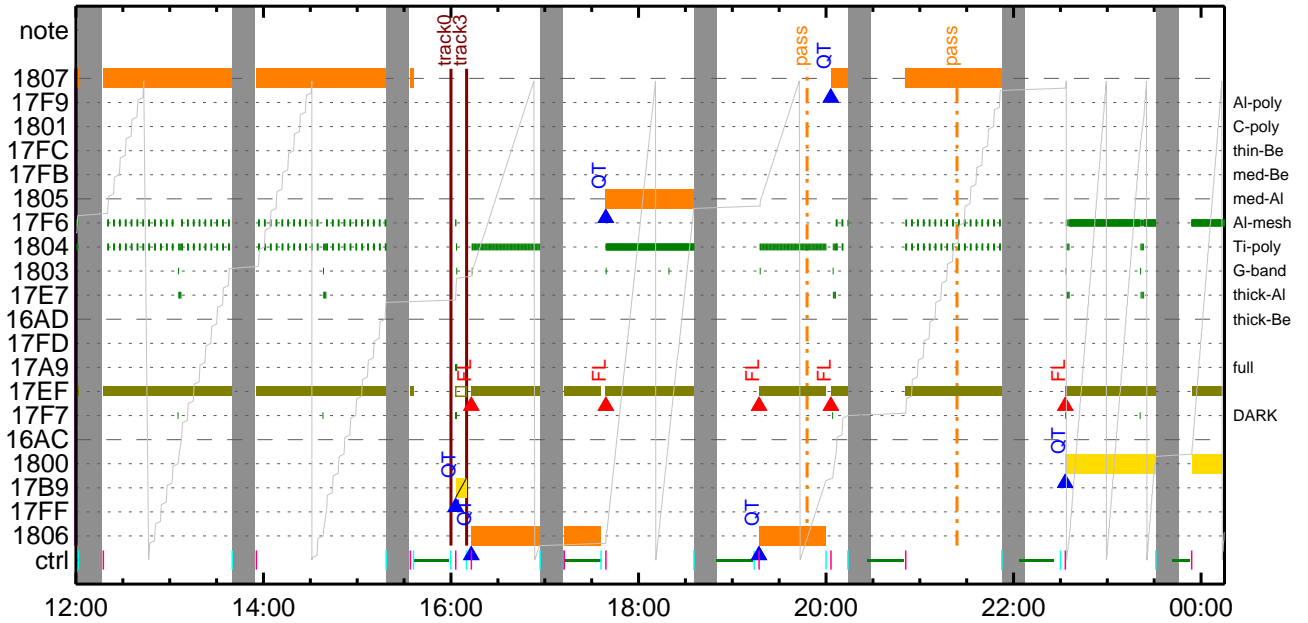
CMDI #0447 2010/08/22



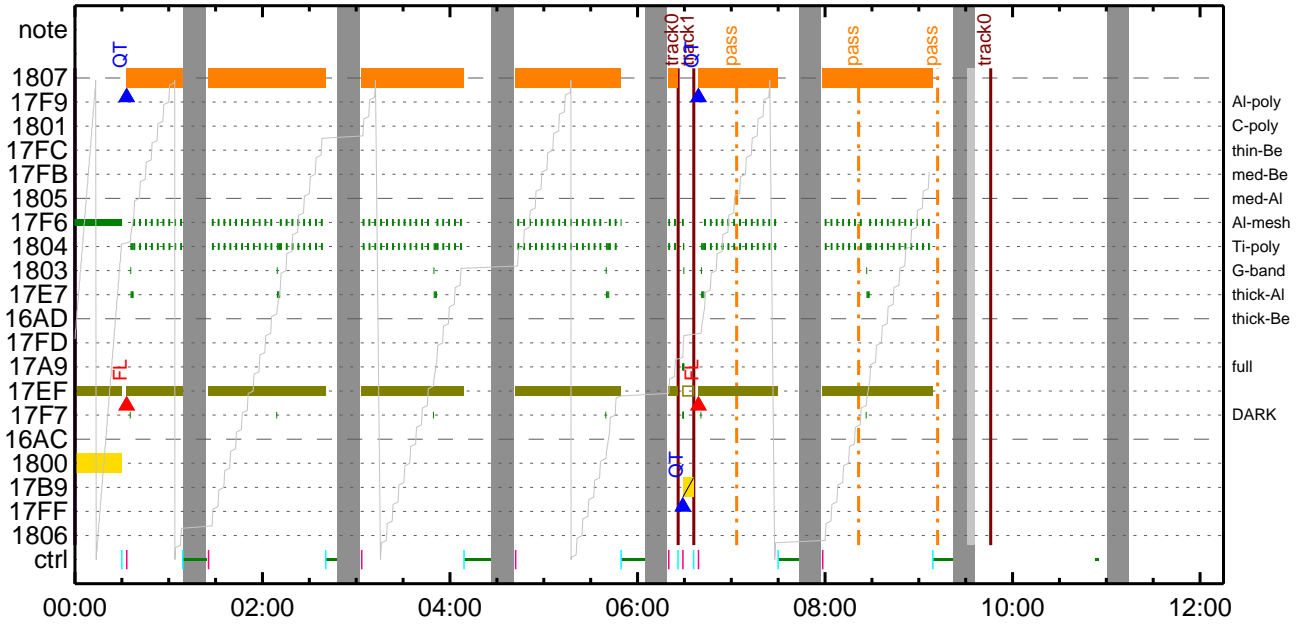
CMDI #0447 2010/08/23



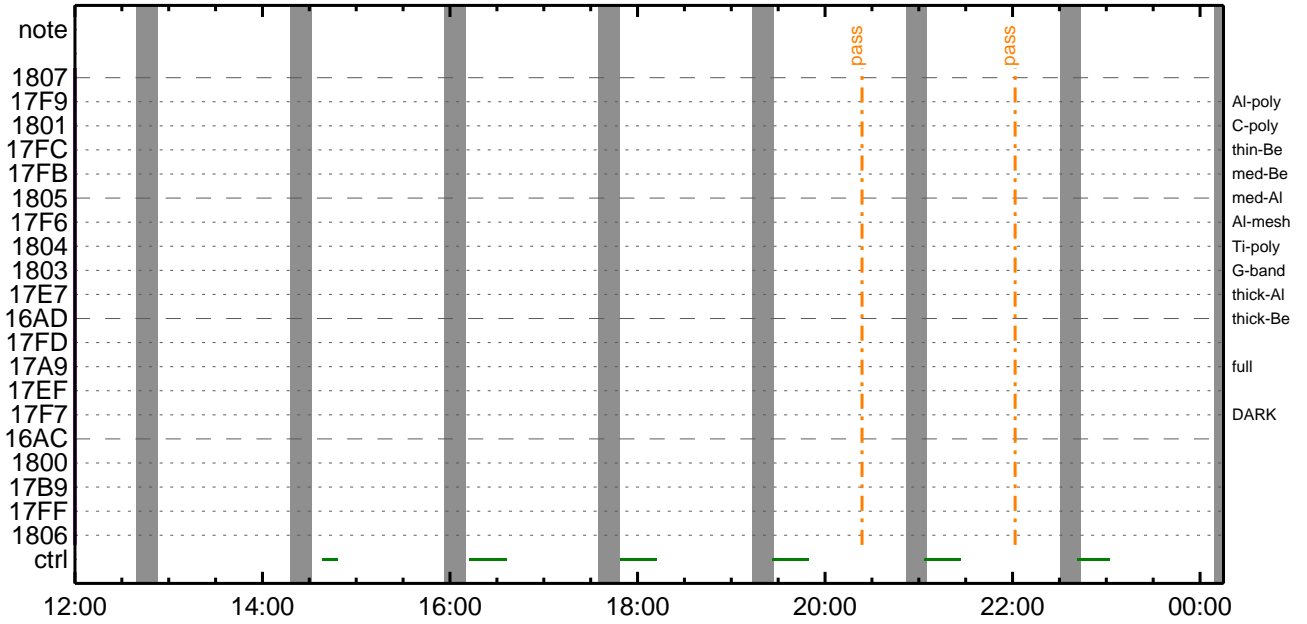
CMDI #0447 2010/08/23



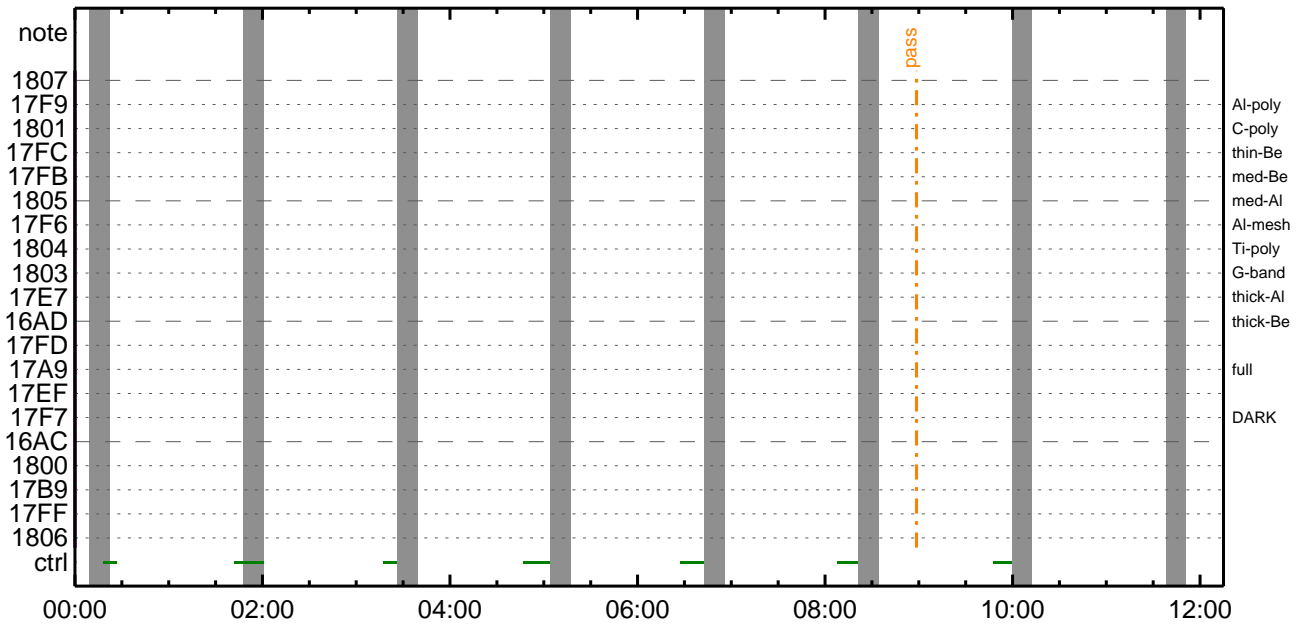
CMDI #0447 2010/08/24



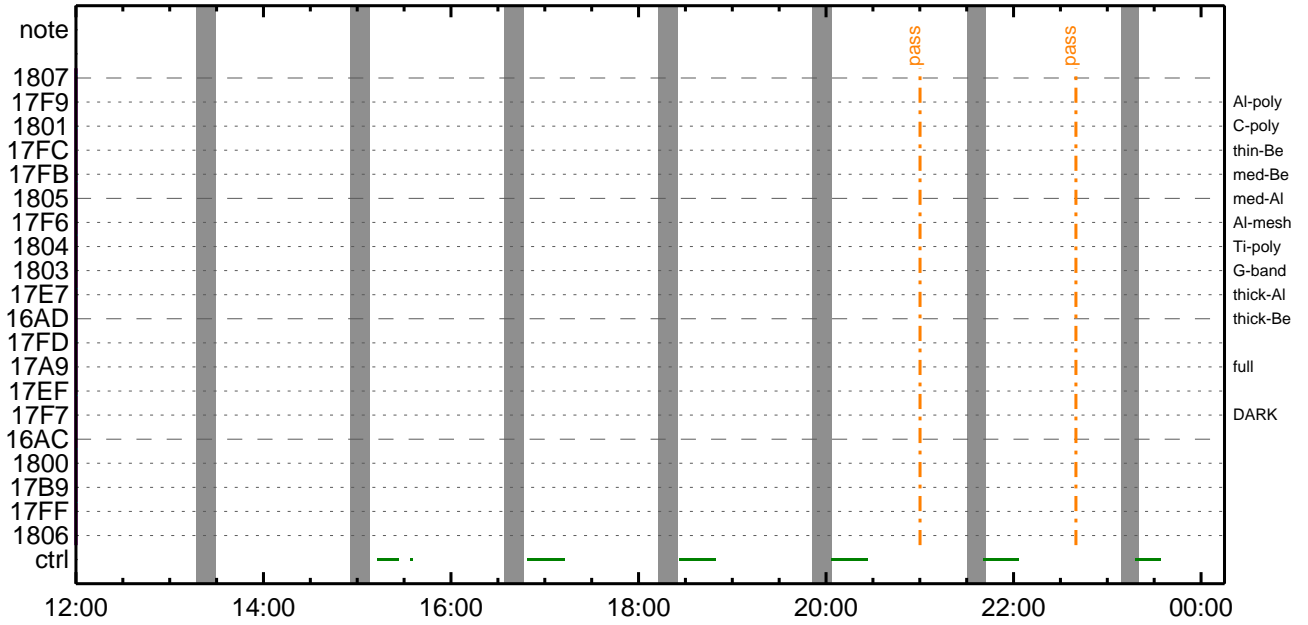
CMDI #0447 2010/08/24



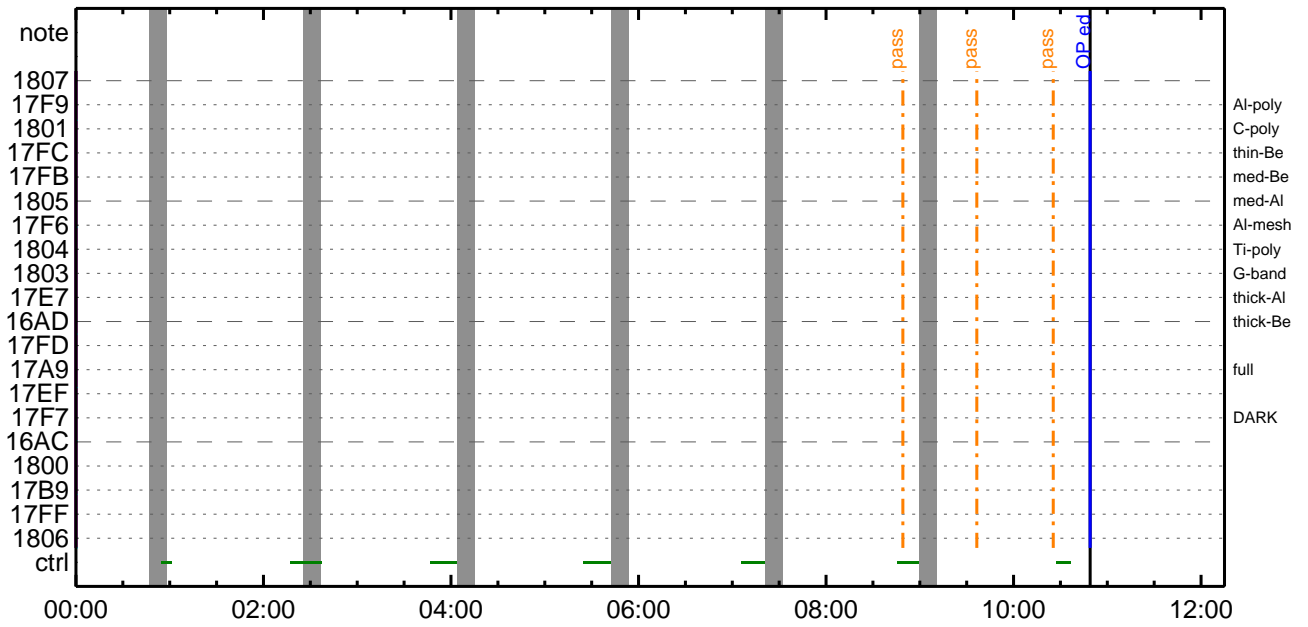
CMDI #0447 2010/08/25



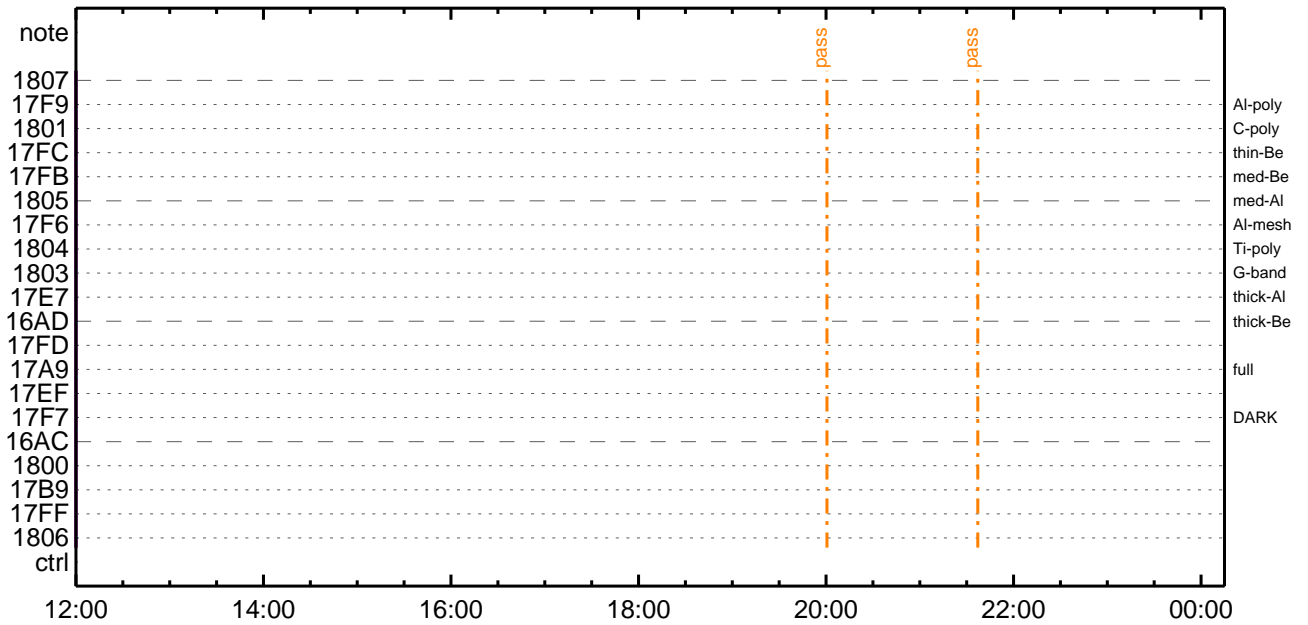
CMDI #0447 2010/08/25



CMDI #0447 2010/08/26



CMDI #0447 2010/08/26



(a) Spacecraft Operation Procedure (real-commands)

```

main-540 2010-08-21 11:58:28 205 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ä
0005  C.
0006  C. YÀYB;¼Y³YFYóYÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Áí;Èð¿ðÀð•µ°Æ»Í×ÁÇðÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•íÉ;ÈðÈ¼°ÇÒð•¼¿¼l¹çðÍ;çÀ®, ùñ¹ðÈððÇÁ÷¿®ð•ðÈððð³ðÈ;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. OP/OGYí;¼YÉ;|YÁYóY×
0016  C. *****
0017  C.
0018  . C. ;ãOP/OGYí;¼YÉ;ä
0019  . S. OP      op-540:OP
0020  ()
0021  . S. OG      og-540:OG
0022  ()
0023  C.
0024  . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025  C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027  BC      (20 00 7f 01 02)
0028  C.      çç[HK1_DMP_TOP_ADRES_1]      EQ      40
0029  C.      çç[HK1_DMP_TOP_ADRES_0]      EQ      0
0030  C.      çç[HK1_DMP_BLOCK_NUM]        EQ      127
0031  C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0032  C.      çç[HK1_DMA_DMP_PIM]          EQ      DHU
0033  +. DC 01-22 DHU_MODE_CHNG
0034  BC      (07 0b f8)
0035  C.      çç[HK1_PKT_FORM_NO]          EQ      7
0036  C.      çç[HK1_PKT_GEN_TIME]          EQ      0.25 s
0037  C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32k
0038  C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0039  C.      çç[HK1_DMP_CHK_FLG]          EQ      EXEC
0040  . C.  YÀYóY×¼ªª Î»ðð³ÍÇ§
0041  C.      çç[HK1_DMP_CHK_FLG]          EQ      NON
0042  . C.  RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ÍÇ§
0043  C.
0044  C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046  BC      (20 80 7f 01 02)
0047  C.      çç[HK1_DMP_TOP_ADRES_1]      EQ      41
0048  C.      çç[HK1_DMP_TOP_ADRES_0]      EQ      0
0049  C.      çç[HK1_DMP_BLOCK_NUM]        EQ      127
0050  C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0051  C.      çç[HK1_DMA_DMP_PIM]          EQ      DHU
0052  +. DC 01-22 DHU_MODE_CHNG
0053  BC      (07 0b f8)
0054  C.      çç[HK1_PKT_FORM_NO]          EQ      7
0055  C.      çç[HK1_PKT_GEN_TIME]          EQ      0.25 s
0056  C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32k
0057  C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0058  C.      çç[HK1_DMP_CHK_FLG]          EQ      EXEC
0059  . C.  YÀYóY×¼ªª Î»ðð³ÍÇ§
0060  C.      çç[HK1_DMP_CHK_FLG]          EQ      NON
0061  . C.  RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ÍÇ§
0062  C.
0063  C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065  BC      (21 00 41 01 02)
0066  C.      çç[HK1_DMP_TOP_ADRES_1]      EQ      42
0067  C.      çç[HK1_DMP_TOP_ADRES_0]      EQ      0
0068  C.      çç[HK1_DMP_BLOCK_NUM]        EQ      65
0069  C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0070  C.      çç[HK1_DMA_DMP_PIM]          EQ      DHU
0071  +. DC 01-22 DHU_MODE_CHNG
0072  BC      (07 0b f8)
0073  C.      çç[HK1_PKT_FORM_NO]          EQ      7
0074  C.      çç[HK1_PKT_GEN_TIME]          EQ      0.25 s
0075  C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32k
0076  C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0077  C.      çç[HK1_DMP_CHK_FLG]          EQ      EXEC
0078  . C.  YÀYóY×¼ªª Î»ðð³ÍÇ§
0079  C.      çç[HK1_DMP_CHK_FLG]          EQ      NON
0080  . C.  RAM ID=NMOG,RAM ID=OPðÍ¼È¹ç•è²ÍOKðð³ÍÇ§
0081  C.
0082  . C.  *****  øÈ²¼ðÍ¼Ä´ ¶¼°ðÈÈ-ð°Á÷¿® (¼áµ-YÀYóY×¼¼è¼çððÁÓÃæç¼ªª ð°¼¼¼l¹çðçðá) *****
0083  C. DHUYá;¼YÉ;È¼Y¼; Yí;¼YÉ;ÈððÍáð¹
0084  +. DC 01-22 DHU_MODE_CHNG
0085  BC      (02 0a f8)
0086  C.      çç[HK1_PKT_FORM_NO]          EQ      2
0087  C.      çç[HK1_PKT_GEN_TIME]          EQ      0.5S
0088  C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32K
0089  C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0090  C.
0091  . C. *****
0092  C. TI-CMD SET (OPOG STOP/COPY/START)
0093  C. *****
0094  C.
0095  . C. NOTICE |§ OPOG UPLOADð-Á÷¿®NGðÍ¼l¹ç;ç°È²¼¼ðÍTI-CMDÁ÷¿®ðÍ¼Á¹Ôð•ðÈððð³ðÈ;f

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```

0096 C.          d; SET EDUMPAI±°iYÑY¹aÇ¹Ôa|³aE;f
0097 C.
0098 C.  TI³YF¥ÖYÉpaðÄDİ¸(UT)
0099 +. TI 2010-08-21 10:55:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0102 C.
0103 +. TI 2010-08-21 10:55:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0106 C.
0107 +. TI 2010-08-21 10:55:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0110 C.
0111 +. TI 2010-08-21 10:59:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0114 C.
0115 C.  °Ê²¼aİÄê%íîÑaİYÁY$YÁY-¹àlÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0117 C.          çç[HK1_TI_CMD_NUM]              EQ      4
0118 C.          çç[HK1_NEXT_EXEC_PIM]           EQ      DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]           EQ      0xB3
0120 C.
0121 C.  *****
0122 C.  TIÎî°èYÁYÖY×
0123 C.  *****
0124 C.
0125 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC       (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]           EQ      3
0131 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0132 C.          çç[HK1_DMA_DMP_PIM]            EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC       (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0136 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0139 C.          çç[HK1_DMP_CHK_FLG]            EQ      EXEC
0140 C.
0141 C.  YÁYÖY×½¹aİ»ðð³İÇ$
0142 C.          çç[HK1_DMP_CHK_FLG]            EQ      NON
0143 C.
0144 C.  RAM ID=TI_TBLaİ%Ê¹ç•ê²İOKaðð³İÇ$
0145 C.
0146 C.  DHUYâ;¼YÉ;Ê¼Y½.Yì;¼YÊ;Ëaððİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC       (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0150 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0153 C.
0154 C.  *****
0155 C.  SOT TI command set
0156 C.  *****
0157 C.  Execute, after the success of OP upload.
0158 +. TI 2010-08-21 10:59:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC       (41)
0161 C.  -----
0162 C.  HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0163 C.  -----
0164 C.  ***** SOT END *****
0165 C.  Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C.  ***** Start EIS operation (TI set) *****
0169 C.  Execute, after the success of OP upload.
0170 C.  Set EIS TI-commands
0171 +. TI 2010-08-21 10:59:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC       (21 02)
0174 +. TI 2010-08-21 10:59:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC       (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0178 C.  ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C.  ***** XRT START *****
0183 C.  Execute, after the success of OP upload.
0184 +. TI 2010-08-21 10:59:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC       (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0188 C.
0189 C.  ***** XRT END *****
0190 C.
0191 C.  ***** MDP `ûÄîaİ»ð¼YöÊÄa¹aèDCBC•×²è *****
0192 C.  (%â°îYÖYÁYÉYÉYáYçYéaÉ¼a¼Ä»Ùa¹aé)
0193 C.  DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 + DC 07-F0 MDP_XRT_MODE_STBY
0135 BC (c3)
0136 . C. ----- Success Verify ? OK / NG____
0137 C.
0138 C. XRT Obs. Table Upload
0139 . S. RAM ram-291:MDP_OBS_X
0140 ( )
0141 C.
0142 +. DC 07-F0 MDP_DUMP_XRTTBL
0143 BC (84 07 00 00 00 3a d4)
0144 . C. ----- Comparison Check ? OK / ERR ____
0145 C.
0146 C.
0147 +. DC 07-F0 MDP_XRT_ROI_SET
0148 BC (cd 01 b1 b1 04 04)
0149 + DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 02 b1 b1 08 08)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 03 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 04 b1 b1 06 06)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 05 85 83 08 08)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 06 85 83 06 06)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 07 85 83 08 08)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 08 80 80 20 20)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 09 80 80 20 08)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0a 80 80 08 20)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0b 85 83 0c 0c)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 0f 80 80 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 10 80 80 08 08)
0173 + DC 07-F0 MDP_XRT_AEC_RESET
0174 BC (d0)
0175 . C. ----- Success Verify ? OK / NG ____
0176 C.
0177 C.
0178 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0179 C.
0180 +. DC 07-F0 MDP_XRT_MODE_OBSV
0181 BC (c2)
0182 +. TI 2010-08-21 10:59:02.0
0183 DC 07-F0 MDP_XRT_MODE_OBSV
0184 BC (c2)
0185 . C. ----- Success Verify ? OK / NG ____
0186 C.
0187 C. ***** XRT END *****
0188 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0189 +. DC 07-FC EIS_MODE_MANU
0190 BC (21 02)
0191 . C. Verify EIS in MANUAL mode
0192 . C. Estimated OBSTBL upload time is 37s
0193 C. *****

```

```
0194 C. EIS START OBSTBL LOAD
0195 C. *****
0196 . S. RAM ram-820:EIS_OBSTBL
0197 ( )
0198 +. DC 07-FC EIS_DUMP_OBSTBL
0199 BC (07 07 07 00 00 70 00)
0200 C.
0201 C. Execute, after the success of OBSTBL upload.
0202 C. Set EIS TI-commands
0203 +. TI 2010-08-21 10:59:50.0
0204 DC 07-FC EIS_MODE_CHG_ENA
0205 BC (20)
0206 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0207 C. *****
0208 C. EIS END OBSTBL LOAD
0209 C. *****
0210 C.
0211 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂðð¹æDCBC•x²è *****
0212 C. (¼ã°îÝÓÝÃÝÊÝÞÝËÝáÝçÝèæ¼ð¼Ã»Û¹æ)
0213 . S. DC-BC dcbc-402:DCBC
0214 (MDP_known_event)
0215 C.
0216 C.
0217 . C. ***** ÝÐÝ¹•İ Daily±¿İÑæ'Ø¹æDCBC•x²è *****
0218 . S. DC-BC dcbc-153:DCBC
0219 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0220 C.
0221 C.
0222 . C. ;ãLOSÝÁÝ$ÝÃÝ-¼Ã»Û;ã
0223 C.
0224 . C. ***** LOS *****
0225 C.
```


Aug 21, 10 11:59

XRT_OGLIST_0447.chk

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*** OP Sequence for XRT ***

2010/08/21	11:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	11:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/21	11:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/08/21	11:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/21	11:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/21	11:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/21	11:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/21	11:12:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	11:12:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2010/08/21	11:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/21	11:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	12:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	12:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	12:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	12:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/21	12:41:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/21	12:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	14:03:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	14:03:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	14:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	14:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/21	14:19:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/21	14:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	14:25:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	14:25:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	14:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	14:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/21	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	14:34:02.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/21	14:34:22.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/21	14:34:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/21	14:34:26.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/21	14:34:28.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/21	14:37:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	14:37:04.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/21	14:37:06.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/21	14:37:08.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	15:41:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	15:41:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	15:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	15:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/21	15:58:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/21	15:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/21	15:59:00.5	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/21	15:59:02.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/21	15:59:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/21	16:02:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2010/08/21	16:22:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/21	16:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	17:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	17:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	17:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/21	17:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/21	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/21	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/21	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/21	18:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/21	18:00:20.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/21	18:02:58.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	18:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/21	18:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/21	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2010/08/21	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/21	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/21	18:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/21	18:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/21	18:12:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	18:12:58.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/08/21	18:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/21	18:13:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	18:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	18:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	18:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/21	19:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/21	19:36:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/21	19:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	20:37:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	20:37:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	20:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/21	20:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/21	21:13:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/21	21:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	22:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	22:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	22:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/21	22:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/21	22:50:01.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	22:50:03.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/21	22:50:23.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/21	22:50:25.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/21	22:50:27.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/21	22:50:29.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2010/08/21	22:53:03.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	22:53:05.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2010/08/21	22:53:07.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2010/08/21	22:53:09.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/21	23:54:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/21	23:54:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/21	23:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/21	23:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	00:14:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	00:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	00:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	00:50:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/08/22	00:50:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/22	00:50:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/22	00:50:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/22	00:50:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/22	00:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	00:53:04.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2010/08/22	00:53:06.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2010/08/22	00:53:08.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	01:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	01:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	01:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	01:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	01:49:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	01:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	03:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	03:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	03:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	03:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	03:27:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	03:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	04:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	04:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	04:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	04:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	05:05:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	05:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	05:46:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	05:46:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/08/22	05:46:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/08/22	05:46:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/22	05:46:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/22	05:46:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/22	05:49:28.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	05:49:30.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/08/22	05:49:32.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	05:56:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	05:56:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							

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2010/08/22	05:56:30.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	00	00	00
2010/08/22	05:56:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/08/22	05:56:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/08/22	05:56:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2010/08/22	05:56:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/08/22	05:59:26.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	05:59:28.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2010/08/22	05:59:30.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2010/08/22	05:59:32.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	06:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	06:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	06:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	06:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	06:44:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	06:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	07:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	07:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	07:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	07:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	08:22:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	08:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	09:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	09:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	09:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	09:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	10:01:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	10:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	11:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	11:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	11:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	11:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	11:39:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	11:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	13:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	13:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	13:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	13:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	13:18:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	13:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	14:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	14:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	14:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	14:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/22	14:56:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	14:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/22	15:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/22	15:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/22	15:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/22	15:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2010/08/22	15:12:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	15:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	16:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	16:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	16:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	16:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	16:59:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	17:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	17:47:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	17:47:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/22	17:47:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/22	17:47:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/22	17:47:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/22	17:47:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/22	17:50:28.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	17:50:30.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/22	17:50:32.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	17:57:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2010/08/22	17:57:30.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	17:57:32.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	17:57:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	18:00:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	18:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	18:37:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/22	18:37:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/22	18:37:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/22	18:37:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/22	18:37:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/22	18:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	18:40:04.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/08/22	18:40:06.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/22	18:40:08.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	19:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	19:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	19:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	19:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	20:13:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	20:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	21:14:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	21:14:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	21:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	21:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	21:50:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/22	21:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/22	22:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/22	22:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/22	22:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/22	22:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/22	23:25:00.5	XRT_CTRL_MANU_400_OG [0x190]							

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2010/08/22	23:25:02.5	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/22	23:25:22.5	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/22	23:25:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/22	23:25:26.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/22	23:25:28.5	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/22	23:28:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/22	23:28:04.5	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/22	23:28:06.5	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/08/22	23:28:08.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	00:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	00:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	00:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	00:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	00:48:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	00:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/08/23	01:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	01:25:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	01:25:22.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/23	01:25:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/23	01:25:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/23	01:25:28.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/23	01:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	01:28:04.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	01:28:06.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/23	01:28:08.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	02:04:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	02:04:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	02:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	02:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	02:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	02:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/08/23	03:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	03:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	03:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	03:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	04:04:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	04:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/08/23	05:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	05:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	05:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	05:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	05:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	05:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/08/23	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/08/23	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00
2010/08/23	06:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

2010/08/23	06:00:20.0	XRT_ARS_DIS_404_OG [0x194]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	06:02:58.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	06:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/08/23	06:03:02.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/23	06:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2010/08/23	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/23	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/23	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/23	06:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	06:12:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	06:12:58.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/23	06:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	06:13:02.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	06:52:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	06:52:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	06:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	06:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	07:21:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/23	07:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	08:32:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	08:32:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	08:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	08:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	08:59:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/23	09:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	10:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	10:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	10:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	10:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	10:38:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/23	10:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	12:01:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	12:01:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	12:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	12:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	12:16:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/23	12:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	13:40:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	13:40:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	13:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	13:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	13:54:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/23	13:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	15:18:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	15:18:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	15:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	15:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2010/08/23	15:33:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/23	15:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/23	15:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	15:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	15:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/23	15:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/23	15:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	15:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/23	16:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/23	16:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/23	16:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/23	16:00:20.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/23	16:02:58.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	16:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/23	16:03:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/23	16:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	16:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/23	16:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/08/23	16:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/23	16:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/23	16:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/23	16:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/23	16:12:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	16:12:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2010/08/23	16:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/23	16:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/23	16:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	16:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	16:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/23	17:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/23	17:11:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/23	17:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	17:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	17:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/23	17:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/23	17:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/23	17:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	17:36:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/23	17:36:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/23	17:36:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/23	17:36:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/23	17:36:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/23	17:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/23	17:39:04.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2010/08/23	17:39:06.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/23	17:39:08.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/23	18:35:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/23	18:35:32.0	XRT_FLD_RESET_412_OG [0x19c]							

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2010/08/23	18:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	18:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	19:14:01.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	19:14:03.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/23	19:14:23.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/23	19:14:25.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/23	19:14:27.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/23	19:14:29.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	19:17:03.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	19:17:05.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2010/08/23	19:17:07.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	19:17:09.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	20:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	20:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/23	20:00:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/23	20:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/23	20:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/23	20:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	20:03:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	20:03:04.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/23	20:03:06.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	20:03:08.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	20:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	20:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	20:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	20:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	20:50:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/23	20:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	21:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	21:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	21:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	21:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	22:30:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	22:30:02.5	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/23	22:30:22.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/23	22:30:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/23	22:30:26.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/23	22:30:28.5	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/23	22:33:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	22:33:04.5	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/08/23	22:33:06.5	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/23	22:33:08.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/23	23:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/23	23:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/23	23:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/23	23:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/23	23:53:00.0	XRT_Custom_418_OG [0x1a2]				

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2010/08/23	23:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	00:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	00:30:02.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/24	00:30:22.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/24	00:30:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/24	00:30:26.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/24	00:30:28.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/24	00:33:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	00:33:04.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/24	00:33:06.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/24	00:33:08.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	01:09:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	01:09:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	01:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/24	01:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/24	01:24:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/24	01:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	02:40:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	02:40:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	02:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/24	02:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/24	03:02:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/24	03:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	04:09:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	04:09:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	04:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/24	04:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/24	04:41:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/24	04:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	05:49:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	05:49:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	05:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/24	05:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/24	06:19:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/24	06:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	06:25:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	06:25:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/08/24	06:26:00.0	AOCs_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/08/24	06:26:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/24	06:26:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/08/24	06:26:20.0	XRT_ARS_DIS_404_OG [0x194]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/24	06:28:58.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	06:29:00.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/08/24	06:29:02.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	06:35:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	06:35:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/24	06:36:00.0	AOCs_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2010/08/24	06:36:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/24	06:36:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			

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2010/08/24	06:36:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/24	06:36:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/24	06:38:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	06:38:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/08/24	06:39:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/24	06:39:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	07:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	07:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/24	07:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/24	07:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/24	07:57:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/24	07:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/24	09:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/24	09:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00