

XRT Timeline to be uploaded on 2010/08/26

Period: 2010/08/26 10:49:00 - 2010/08/31 10:39:00

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Normal mode

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XOB #1809: HOP101 - Ti/poly 8s, G-band - 384x384-AEC1-ROI centered												
Term	Pointing (x, y)	Comment										
08/26 11:02:02 - 08/26 18:51:00	Fixed (-11.0, 890.0)	# OP start + 10min, HOP 101, polar helioseismology, N pole.										
08/27 05:49:02 - 08/27 13:45:54	Fixed (-11.0, 890.0)	# HOP 101, polar helioseismology, N pole.										
PROG= 08 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 23 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─ Subr= 2 30-time(s) 2.0sec												
└─┬─┬─ Seqn= 34 2-time(s) 30.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) Q=90 1 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)	Comment										
08/26 19:33:02 - 08/26 22:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually, extended for SOT flat fields and EIS long synoptic.										
08/27 05:39:02 - 08/27 05:45:54	Fixed (0.0, 0.0)	synoptic, shifted -24.0 min										
08/27 17:41:32 - 08/27 18:27:30	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min										
08/28 06:00:32 - 08/28 09:30:00	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min										
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 7 1-time(s) 4.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 5 1-time(s) 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 8 1-time(s) 4.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 4 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1800: AR Standard-B(Morphology) with PFB, FW1=Open, Al/Mesh, 512x512 at 1064 1048, 30sec-cad												
Term	Pointing (x, y)	Comment										
08/26 23:03:02 - 08/27 01:20:30	Track (548.1, -734.0) <small>© 08/26 23:00:00</small>	* HOP 128, with Hida, filament.										
08/27 23:18:43 - 08/28 00:24:30	Fixed (604.0, -719.0)	* HOP 128 with Hida, filament, fixed pointing at W limb.										
PROG= 04 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 17 1-time(s) 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Seqn= 16 4-time(s) 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 50 90-time(s) 30.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 1 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 2 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term	Pointing (x, y)	Comment										
08/27 01:45:00 - 08/27 03:29:54	Fixed (0.0, 945.0)	* Alignment offset pointing, N.										
PROG= 05 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─┬─ Seqn= 21 24-time(s) 300.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT												
Term	Pointing (x, y)	Comment										
08/27 03:45:00 - 08/27 05:35:54	Fixed (-945.0, 0.0)	* Alignment offset pointing, E.										

	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

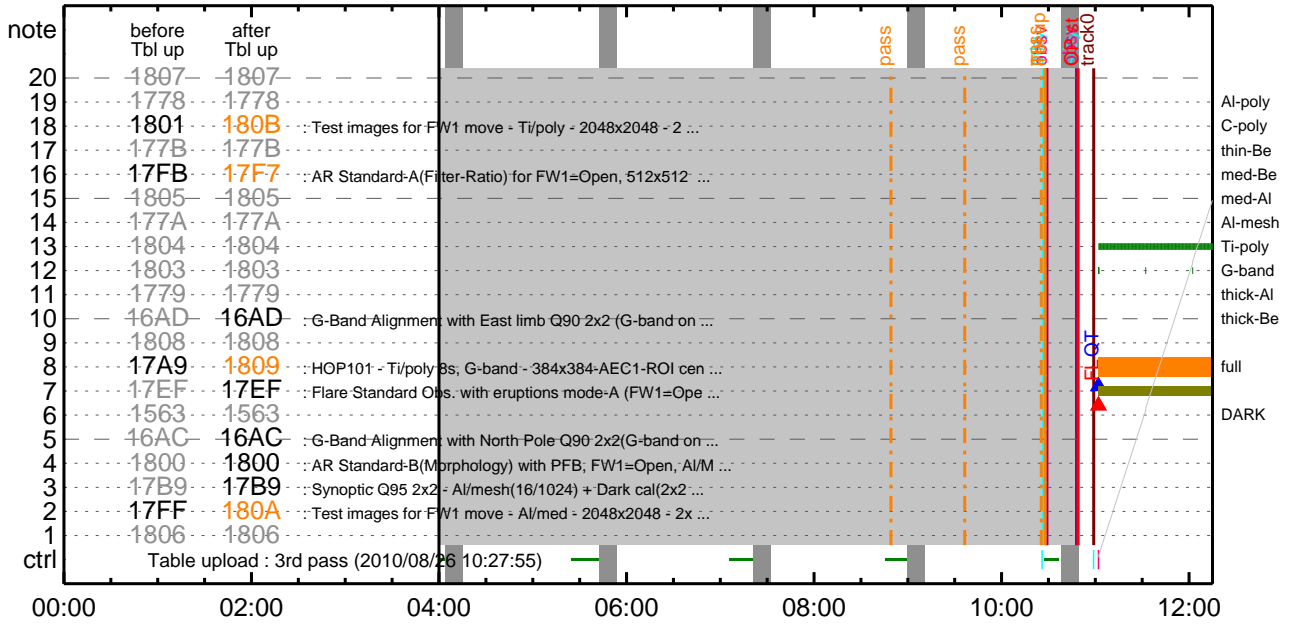
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Flare Detection

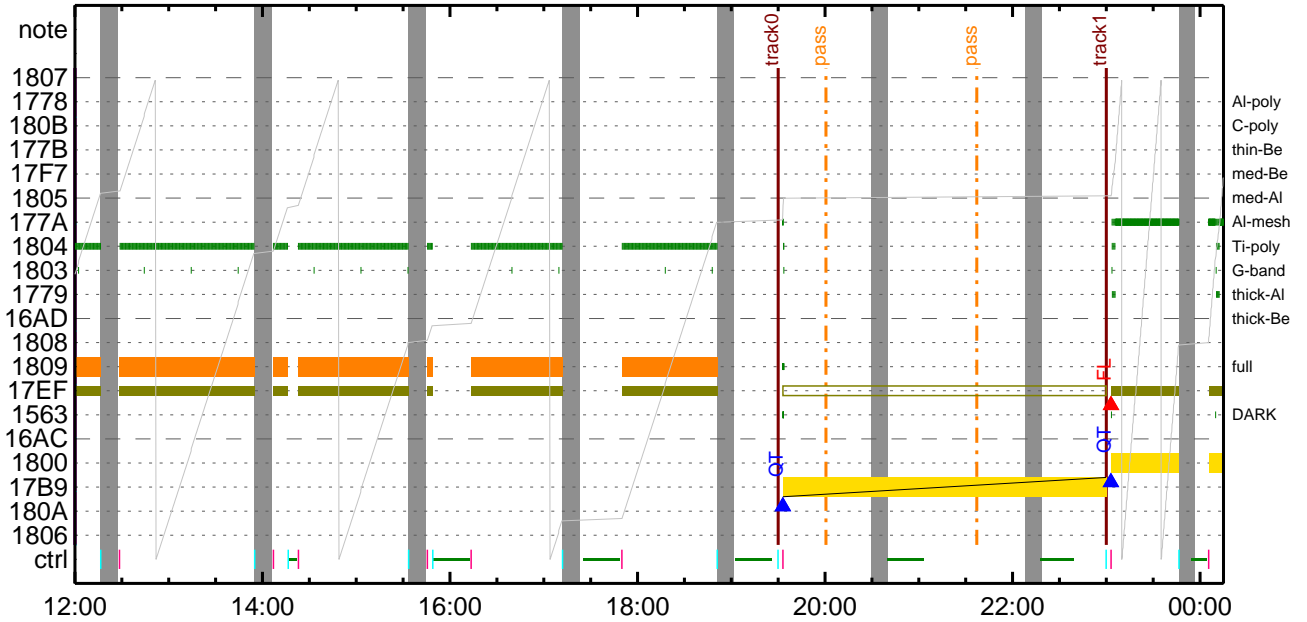
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FLD Patrol												
Term	Pointing (x, y)				Comment							
08/26 10:59:16 - 08/26 19:30:16	Fixed (-11.0, 890.0)				# OP start + 10min, HOP 101, polar helioseismology, N pole.							
08/26 23:00:16 - 08/27 01:30:18	Track (548.1, -734.0) ^{© 08/26 23:00:00}				* HOP 128, with Hida, filament.							
08/27 05:46:16 - 08/27 17:38:46	Fixed (-11.0, 890.0)				# HOP 101, polar helioseismology, N pole.							
08/27 18:27:52 - 08/28 05:57:46	Fixed (815.0, -441.0)				# Cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

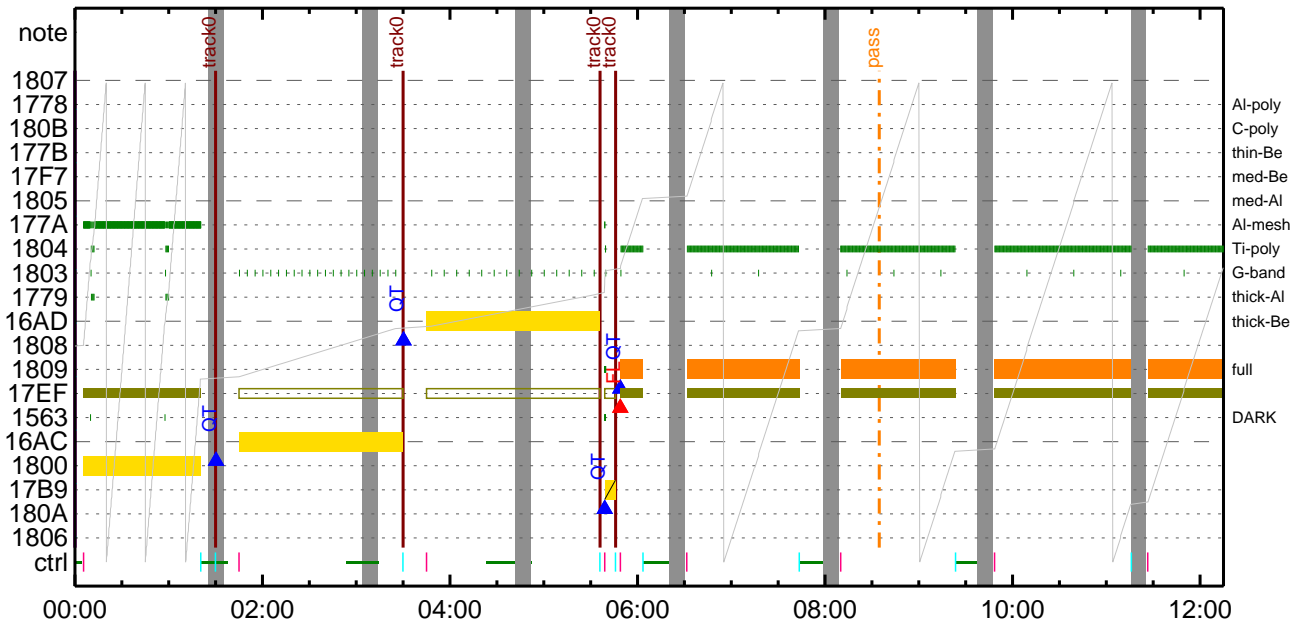
CMDI #0454 2010/08/26



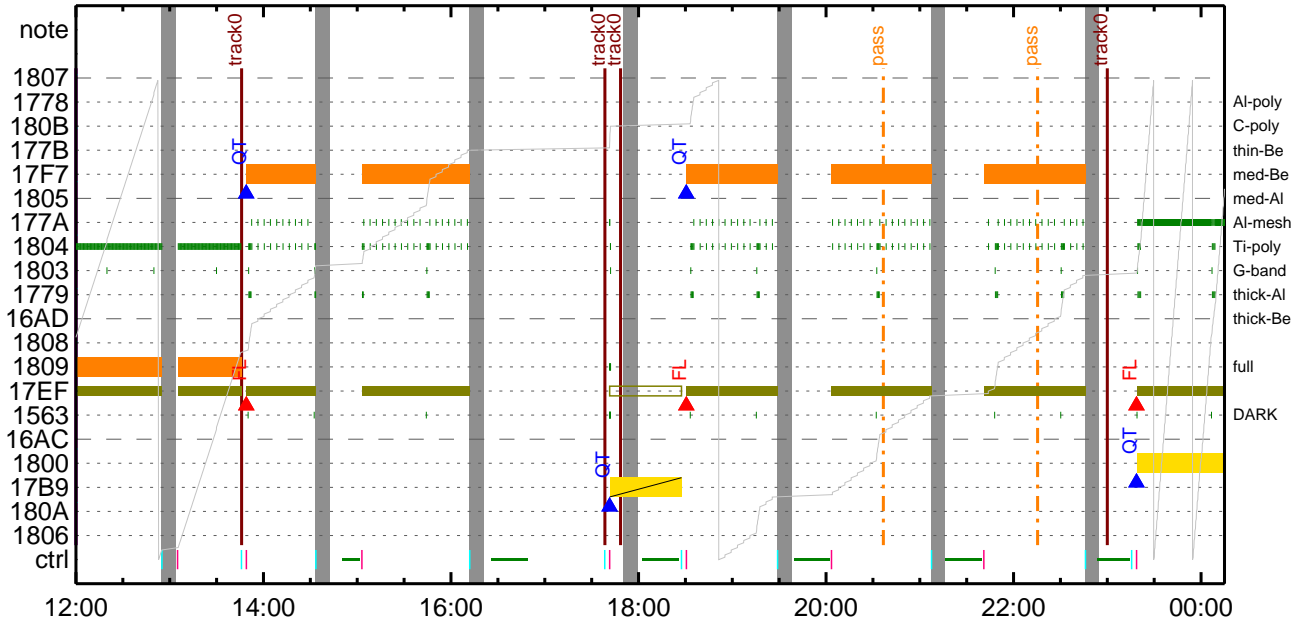
CMDI #0454 2010/08/26



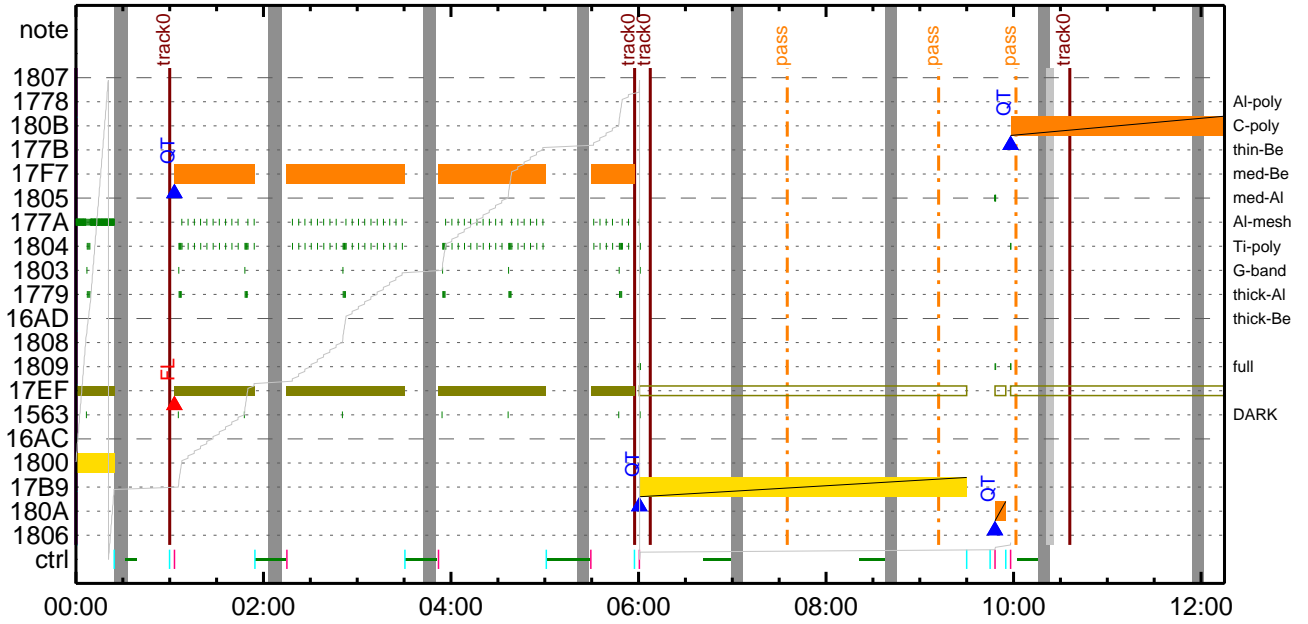
CMDI #0454 2010/08/27



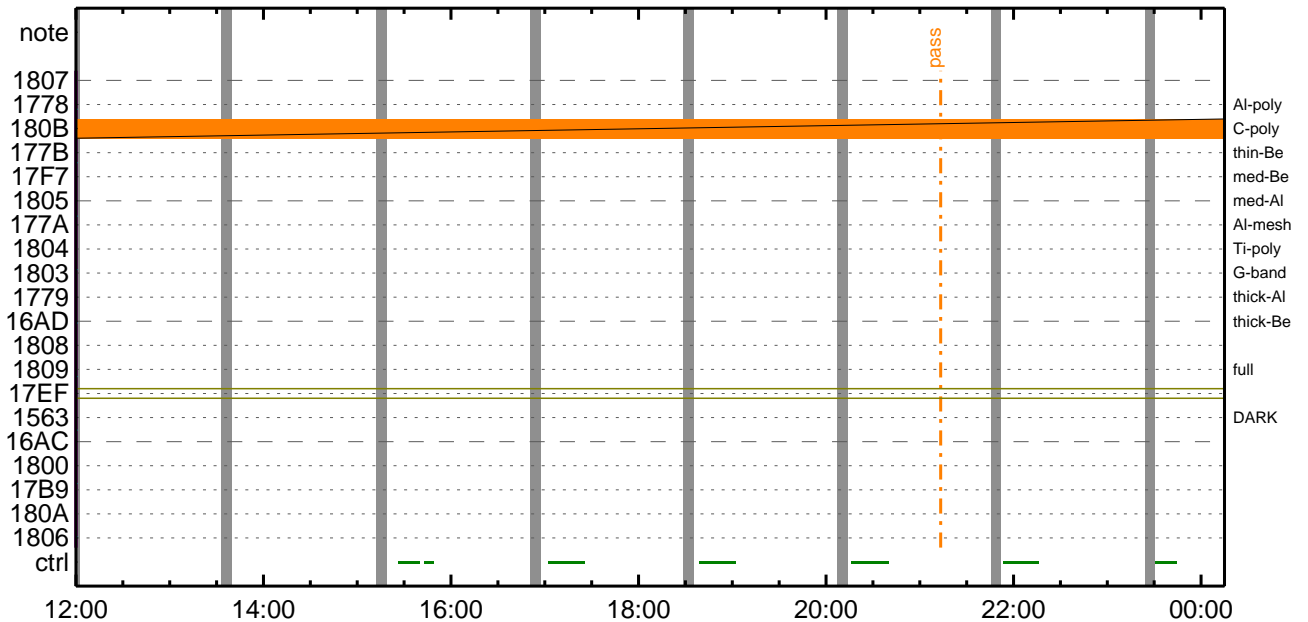
CMDI #0454 2010/08/27



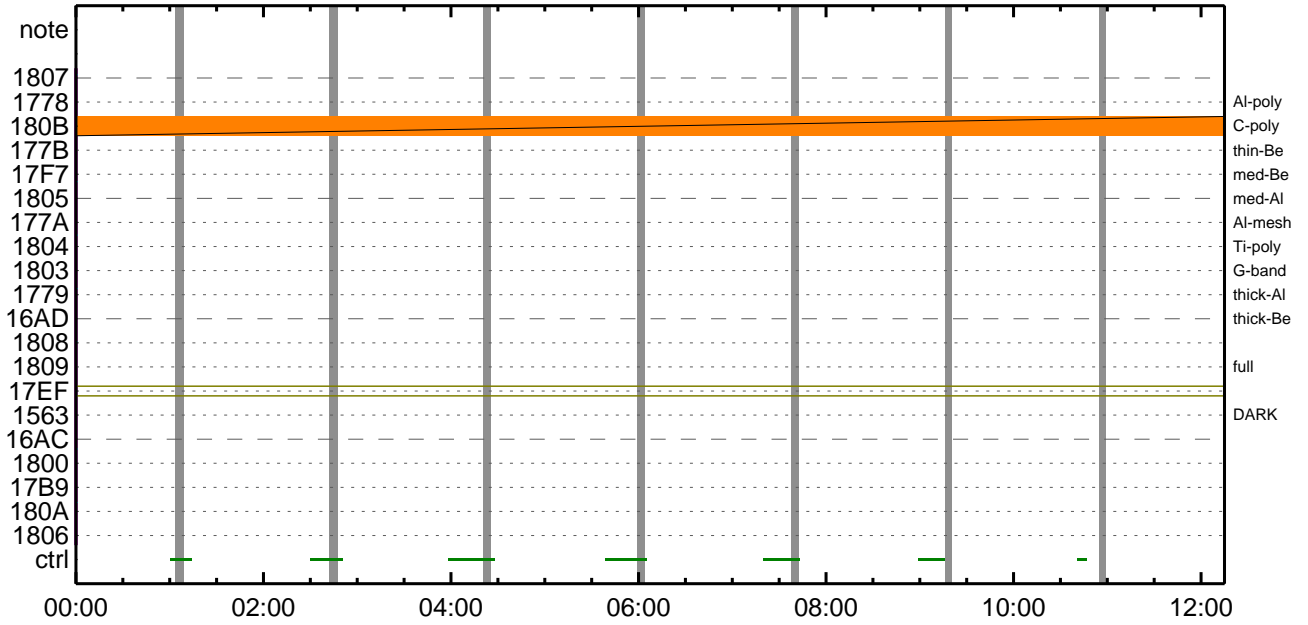
CMDI #0454 2010/08/28



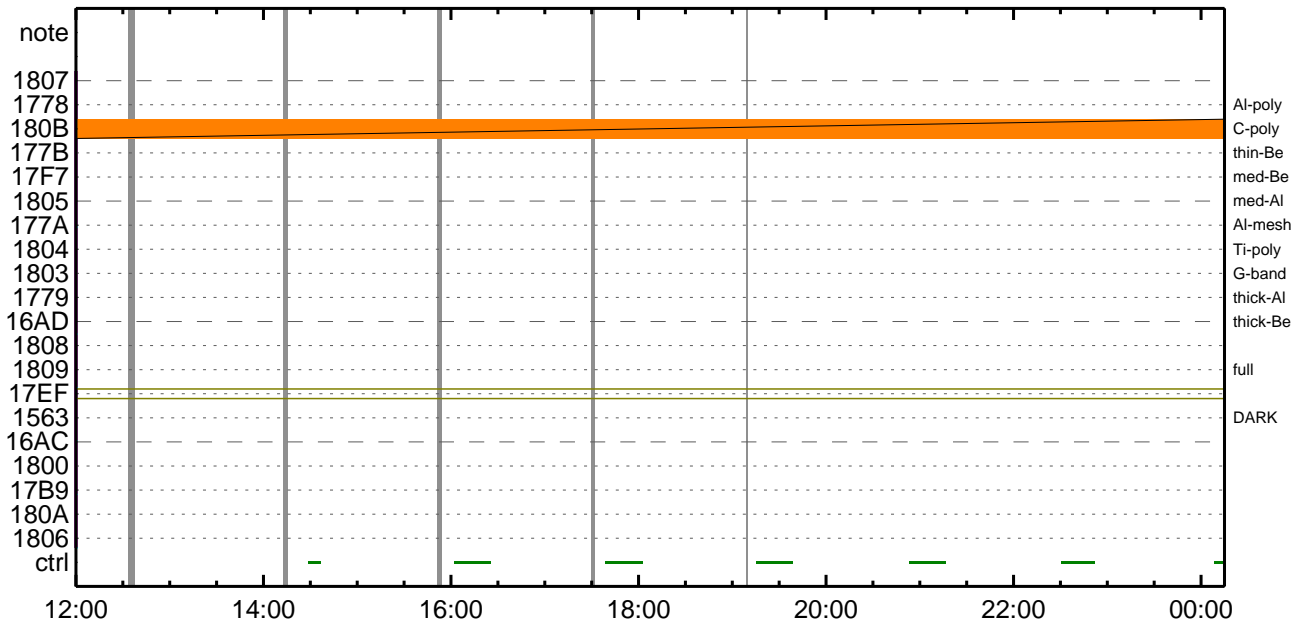
CMDI #0454 2010/08/28



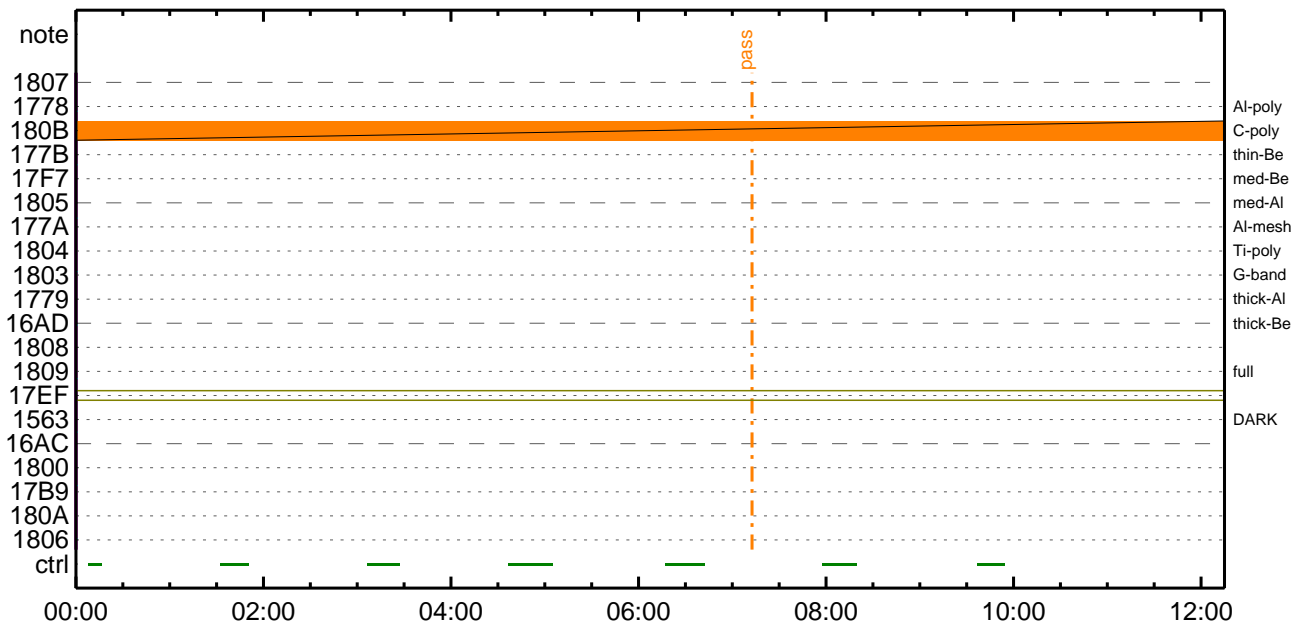
CMDI #0454 2010/08/29



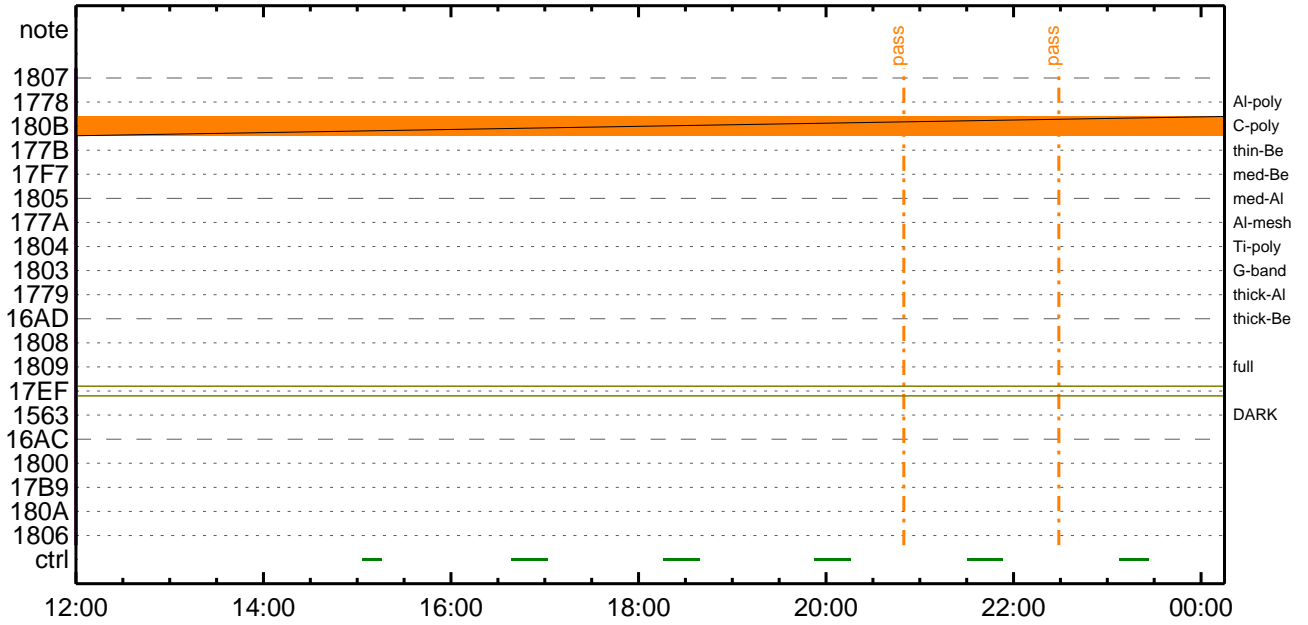
CMDI #0454 2010/08/29



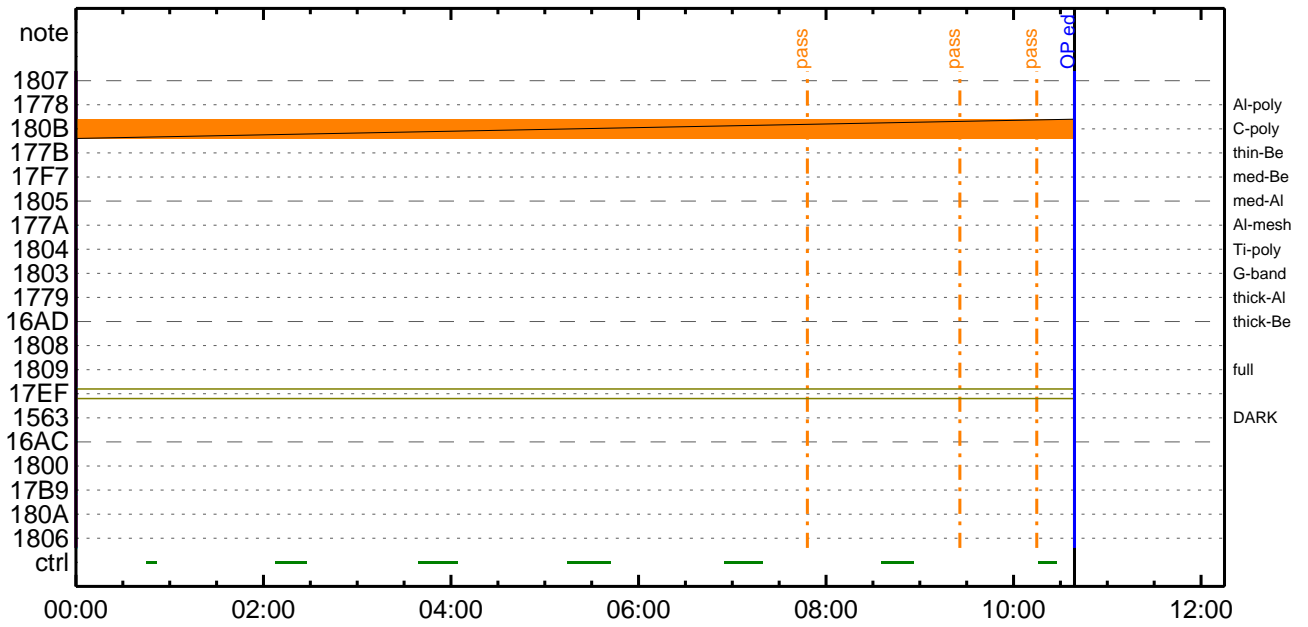
CMDI #0454 2010/08/30



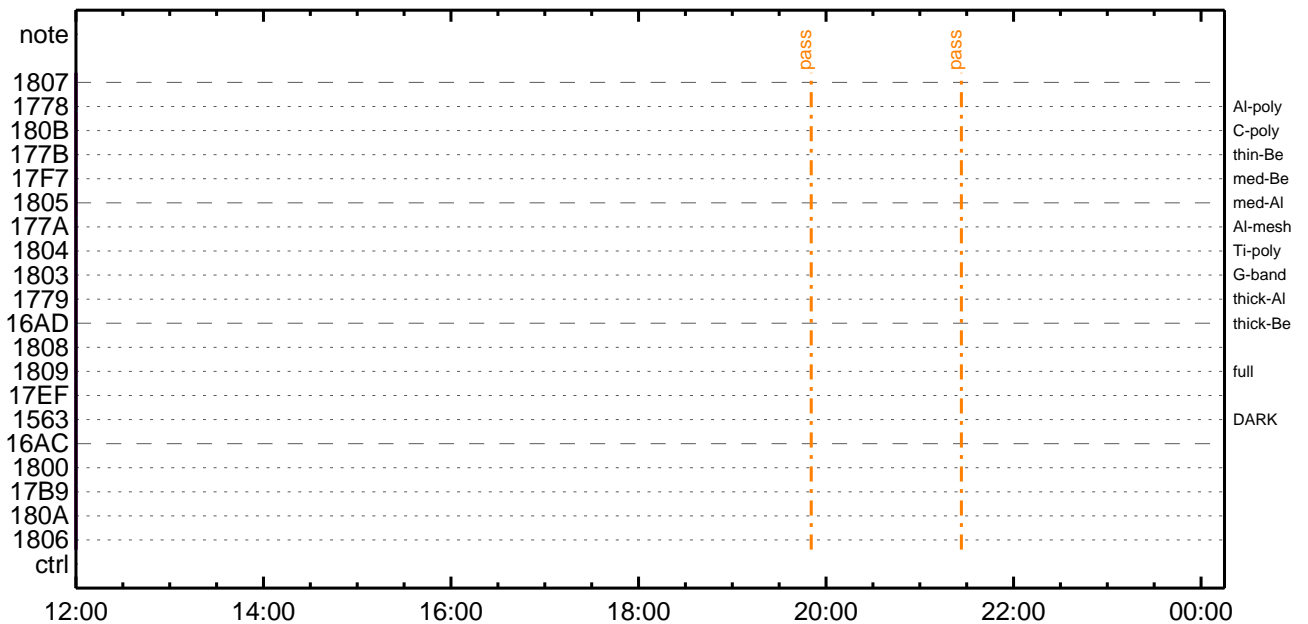
CMDI #0454 2010/08/30



CMDI #0454 2010/08/31



CMDI #0454 2010/08/31




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0096 C.          ρβρζ;çSETρÈDUMPρĪĬ±°iŸŸ¹ρÇ¹Ōρ|ρ³ρÈ;£
0097 C.
0098 . C. TIŸ³ŸβŸŌŸĒρδĀĐĪζ(UT)
0099 +. TI 2010-08-26 10:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2010-08-26 10:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2010-08-26 10:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2010-08-26 10:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °È²¼ρĪĀê%îĪŃρĪŸĀŸ§ŸĀŸ-¹àĪÛ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ          ENA
0117 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0118 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIĪĪ°èŸĀŸŌŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]       EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]       EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]      EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]         EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]      EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0140 C.
0141 . C. ŸĀŸŌŸ×½ªĪ»ρδ³ĪÇ§
0142 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBLρĪÈ¹ç•è²ĪOKρδ³ĪÇ§
0145 C.
0146 . C. DHUŸâ;¼ŸĒ;Ē¼Ÿ½,Ÿi;¼ŸÈ;ĒρδĪĀρ¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]         EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]      EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2010-08-26 10:48:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2010-08-26 10:48:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2010-08-26 10:48:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-08-26 10:48:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´ĪĀĪĪ»ð¾ŸρĒĀĐρ¹ρèDCBC•×²è *****
0192 C. (%Ā°ĪŸŌŸĀŸĒŸĒŸĀŸçŸèρÈ¾¼ρ¼Ā»Ÿρ¹ρè)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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(a) Spacecraft Operation Procedure (real-commands)

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main-549 2010-08-26 13:00:33 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÄY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¼äâ•µ°È»Í×ÁÇöÍYçYÄY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈòÈ¼°ÇÖä•á¿¼í¹çäí;çÀ®;ùá¹äâââçÁ+¿®•âÈäââ³äÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ç“ °ÈÀ,Í×ÈYäâLOSâççäí»p`Öäð¹íí,ä•;çÉÖÍ×äÈXÁÖONäí¹ÖäÈäíäÈäââ³äÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÖYÉYíYÄY-¾ÖÀÖä-°ÈÁÈä•ä¿äÉ;ç°È²¼äí°ÈÀ,¼È¼çäðð¼Á¹Öä¹äÉ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èä•ä¿äâ¼í¹çäí;ç°È²¼äí¼Á¹Öäâ°;çDCBC-150äð¿Èäà;f
0035 C.
0036 . C. ;ãPT1°ÈÀ,³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö,¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö,¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö,¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÆYÈÁÚÄ0;ÈÁ•Ä°²óÈð;È,äâí°ÈÀ,°È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö,¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö,¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö,¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ,ä-¼«È°Áä»ßä•ä¿ä,ä;ç°È²¼äðð¼Á¹Öä¹äÉ;f
0055 C. YçYÖYÆYÈÁÚÄ0äÁ•Ä°²óÈðä-¶äâ¼í¹çäí`°í»ä¹äÈâççÁÖä;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èä•ä¿äâ¼í¹çäí;ç°È²¼äí¼Á¹Öäâ°;çDCBC-151äð¿Èäà;f
0061 C.
0062 . C. ;ãPT2°ÈÀ,³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö,¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö,¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö,¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÆYÈÁÚÄ0;ÈÁ•Ä°²óÈð;È,äâí°ÈÀ,°È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö,¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö,¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö,¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ,Áä»ß;çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ,Áä»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
  
```

```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 17s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2010-08-26 10:48:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP ĩÛİİ»ö¼ÿāĒāā¹āēDCBC•x²è *****
0153 C. (¼ā°İÿÖÿĀÿËÿPÿËÿĀÿçÿËāē¼ā¼ā¼Ā»Ū¹āē)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¼Dÿ¹•İ Daily±;İŃāĒ´Øā¹āēDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ăLOŠÿĀÿŞÿĀÿ-¼Ā»Ū;ă
0164 C.
0165 . C. ***** LOS *****
0166 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-550 2010-08-26 13:00:33 122 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿ðÀð•µ°È»Í×ÁÇðÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•íÍÈ;ÈðÈ¼°ÇÒð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2010-08-26 10:48:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 08 08)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 80 80 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 85 83 08 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 60 20 18)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c a0 80 18 20)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 . C. ----- Success Verify ? OK / NG ____
0095 C.

```

```
0096 C.
0097 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0098 C.
0099 +. DC 07-F0 MDP_XRT_MODE_OBSV
0100 BC (c2)
0101 +. TI 2010-08-26 10:48:02.0
0102 DC 07-F0 MDP_XRT_MODE_OBSV
0103 BC (c2)
0104 . C. ----- Success Verify ? OK / NG ____
0105 C.
0106 C. ***** XRT END *****
0107 C.
0108 . C. ***** MDP `úÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0109 C. (¼á°îÿÓÿÃÿÈÿËÿËÿÁÿçÿèñ¼ññ¼Á»Ûñ¹ñè)
0110 . S. DC-BC dcbc-402:DCBC
0111 (MDP_known_event)
0112 C.
0113 C.
0114 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0115 . S. DC-BC dcbc-153:DCBC
0116 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0117 C.
0118 C.
0119 . C. ¡ãLOSÿÁÿSÿÃÿ¬¼Á»Ûñ¹ñè
0120 C.
0121 . C. ***** LOS *****
0122 C.
```

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*** OP Sequence for XRT ***

2010/08/26	10:58:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	10:58:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/26	10:59:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 b0 e6 00 fd
2010/08/26	10:59:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/26	10:59:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/26	10:59:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/26	10:59:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/26	11:01:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	11:01:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/08/26	11:02:00.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/26	11:02:02.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	12:16:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	12:16:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	12:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	12:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	12:27:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	12:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	13:55:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	13:55:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	13:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	13:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	14:06:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	14:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	14:16:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	14:16:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	14:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	14:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	14:22:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	14:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	15:33:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	15:33:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	15:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	15:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	15:44:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	15:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	15:49:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	15:49:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	15:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	15:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	16:12:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	16:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	17:12:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	17:12:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	17:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	17:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	17:49:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/26	17:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	18:51:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	18:51:02.0	XRT_FLD_RESET_412_OG [0x19c]			

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2010/08/26	18:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	18:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/26	19:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/26	19:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	19:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/08/26	19:30:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00
2010/08/26	19:30:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/26	19:30:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/08/26	19:32:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/26	19:33:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	19:33:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/08/26	22:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	22:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/26	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00
2010/08/26	23:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/26	23:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/26	23:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/26	23:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/26	23:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	23:03:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/08/26	23:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/26	23:46:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/26	23:46:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/26	23:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/26	23:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	00:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	00:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/08/27	01:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	01:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	01:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	01:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	01:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	01:29:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	01:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/27	01:30:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]	AOCU_NM	5	02-76	00 ac 00 00
2010/08/27	01:30:18.0	XRT_FLD_DIS_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2010/08/27	01:30:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/27	01:30:22.0	XRT_ARS_DIS_430_OG [0x1ae]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/08/27	01:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/27	03:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	03:29:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	03:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/27	03:30:16.0	XRT_QT_PROG_SET_431_OG [0x1af]	AOCU_NM	5	02-76	00 00 00 54
2010/08/27	03:30:18.0	XRT_FLD_DIS_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2010/08/27	03:30:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/27	03:30:22.0	XRT_ARS_DIS_430_OG [0x1ae]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

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2010/08/27	03:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	05:35:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	05:35:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/08/27	05:36:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2010/08/27	05:36:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/27	05:36:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/08/27	05:36:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/27	05:38:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	05:39:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/08/27	05:39:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	05:45:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	05:45:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/27	05:46:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 b0 e6 00 fd
2010/08/27	05:46:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/27	05:46:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/27	05:46:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/27	05:46:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/27	05:48:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	05:48:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/08/27	05:49:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/27	05:49:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	06:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	06:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	06:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	06:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	06:30:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/27	06:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	07:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	07:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	07:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	07:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	08:09:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/27	08:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	09:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	09:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	09:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	09:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	09:47:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/27	09:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	11:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	11:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	11:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/27	11:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/27	11:25:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/27	11:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/27	12:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/27	12:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/27	12:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2010/08/27	12:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/27	13:04:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/27	13:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/27	13:45:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	13:45:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/27	13:46:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 27 33 b7 8e				
2010/08/27	13:46:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/27	13:46:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/27	13:46:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/27	13:46:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/27	13:48:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	13:48:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2010/08/27	13:49:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/27	13:49:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/27	14:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	14:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	14:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/27	14:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/27	15:02:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/27	15:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/27	16:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	16:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	16:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/27	16:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/27	17:38:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	17:38:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/27	17:38:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/27	17:38:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/27	17:38:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/27	17:38:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/27	17:41:28.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	17:41:30.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/27	17:41:32.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/27	17:48:30.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 27 33 b7 8e				
2010/08/27	18:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	18:27:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/27	18:27:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/27	18:27:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/27	18:27:56.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/27	18:27:58.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/27	18:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	18:30:34.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2010/08/27	18:30:36.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/27	18:30:38.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/27	19:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/27	19:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/27	19:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/27	19:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2010/08/27	20:02:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/27	20:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/27	21:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/27	21:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/27	21:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/27	21:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/27	21:40:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/27	21:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/27	22:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/27	22:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/27	22:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/27	22:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/27	23:00:00.0	AOCs_OrE-point_Start_7_OG [0x09d]							
			AOCU_NM	5	02-76	00	3f	e7	ca 4e
2010/08/27	23:15:35.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/27	23:15:37.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/08/27	23:15:57.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/08/27	23:15:59.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/08/27	23:16:01.0	XRT_AEC_RESET_441_OG [0x1b9]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2010/08/27	23:16:03.0	XRT_ARS_DIS_420_OG [0x1a4]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/08/27	23:18:37.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/27	23:18:39.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2010/08/27	23:18:41.0	XRT_FL_PROG_SET_405_OG [0x195]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2010/08/27	23:18:43.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/28	00:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/28	00:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/28	00:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/28	00:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/28	00:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/28	00:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/08/28	01:00:00.0	AOCs_OrE-point_Start_6_OG [0x09c]							
			AOCU_NM	5	02-76	00	27	33	b7 8e
2010/08/28	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/08/28	01:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/08/28	01:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2010/08/28	01:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/08/28	01:02:56.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/28	01:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2010/08/28	01:03:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2010/08/28	01:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/28	01:54:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/28	01:54:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/28	01:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/28	01:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/08/28	02:14:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/28	02:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/08/28	03:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/28	03:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/08/28	03:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/08/28	03:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

2010/08/28	03:51:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/28	03:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/28	05:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/28	05:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/28	05:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/28	05:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/28	05:28:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/28	05:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/28	05:57:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/28	05:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/28	05:57:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/08/28	05:57:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/08/28	05:57:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/08/28	05:57:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/08/28	06:00:28.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/28	06:00:30.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/28	06:00:32.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/08/28	06:07:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/28	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 27 33 b7 8e	
2010/08/28	09:40:00.0	XRT_STANDBY_438_OG [0x1b6]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/28	09:40:02.0	XRT_DIAGNOSTIC_447_OG [0x1bf]	XRT_STBY	2	07-F8	03 01	
2010/08/28	09:40:04.0	XRT_FSA_CONTROL_417_OG [0x1a1]	XRT_DIAGNOSTIC	2	07-F8	03 07	
2010/08/28	09:40:06.5	XRT_FSA_CONTROL_410_OG [0x19a]	XRT_FW_1_STATUS_LSB	4	07-F8	51 e0 00 00	
2010/08/28	09:40:09.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_RESET	4	07-F8	51 40 00 00	
2010/08/28	09:40:11.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:14.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:16.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:19.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:21.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:24.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:26.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:29.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:31.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:34.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:36.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:39.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:41.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:44.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:46.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:49.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:51.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:54.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:56.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:40:59.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:41:01.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:41:04.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	
2010/08/28	09:41:06.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00	

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2010/08/28	09:41:09.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:11.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:14.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:16.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:19.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:21.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:24.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:26.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:29.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:31.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:34.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:36.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:39.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:41.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:44.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:46.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:49.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:51.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:54.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:56.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:41:59.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:01.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:04.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:06.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:09.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:11.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:14.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:16.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:19.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:21.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:24.0	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:26.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:29.0	XRT_FSA_CONTROL_417_OG [0x1a1] XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/08/28	09:42:31.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:34.0	XRT_FSA_CONTROL_417_OG [0x1a1] XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/08/28	09:42:36.5	XRT_FSA_CONTROL_414_OG [0x19e] XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/08/28	09:42:39.0	XRT_FSA_CONTROL_417_OG [0x1a1] XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/08/28	09:42:41.5	XRT_FSA_CONTROL_415_OG [0x19f] XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/08/28	09:42:44.0	XRT_FSA_CONTROL_410_OG [0x19a] XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/08/28	09:42:46.0	XRT_STANDBY_438_OG [0x1b6] XRT_STBY	2	07-F8	03	01		
2010/08/28	09:42:48.0	XRT_OPERATE_421_OG [0x1a5] XRT_OPERATE	2	07-F8	03	02		
2010/08/28	09:45:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/08/28	09:45:02.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/08/28	09:45:22.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/08/28	09:45:24.0	XRT_FLRCTRL_DIS_403_OG [0x193] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/08/28	09:45:26.0	XRT_ARS_DIS_404_OG [0x194] MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/08/28	09:48:04.0	XRT_FLD_RESET_412_OG [0x19c]						

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2010/08/28	09:48:06.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/28	09:48:08.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2010/08/28	09:50:00.0	XRT_STANDBY_438_OG [0x1b6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/28	09:50:02.0	XRT_DIAGNOSTIC_447_OG [0x1bf]	XRT_STBY	2	07-F8	03 01
2010/08/28	09:50:04.0	XRT_FSA_CONTROL_417_OG [0x1a1]	XRT_DIAGNOSTIC	2	07-F8	03 07
2010/08/28	09:50:06.5	XRT_FSA_CONTROL_410_OG [0x19a]	XRT_FW1_STATUS_LSB	4	07-F8	51 e0 00 00
2010/08/28	09:50:09.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_RESET	4	07-F8	51 40 00 00
2010/08/28	09:50:11.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00
2010/08/28	09:50:14.0	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00
2010/08/28	09:50:16.5	XRT_FSA_CONTROL_414_OG [0x19e]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00
2010/08/28	09:50:19.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00
2010/08/28	09:50:21.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:24.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:26.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:29.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:31.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:34.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:36.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:39.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:41.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:44.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:46.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:49.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:51.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:54.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:56.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:50:59.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:01.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:04.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:06.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:09.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:11.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:14.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:16.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:19.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:21.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:24.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:26.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:29.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:31.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:34.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:36.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:39.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:41.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:44.0	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00
2010/08/28	09:51:46.5	XRT_FSA_CONTROL_425_OG [0x1a9]	XRT_FW1_STEP_CW	4	07-F8	51 46 00 00

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2010/08/28	09:51:49.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:51:51.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:51:54.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:51:56.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:51:59.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:01.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:04.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:06.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:09.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:11.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:14.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:16.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:19.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:21.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:24.0	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:26.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:29.0	XRT_FSA_CONTROL_417_OG [0x1a1]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/08/28	09:52:31.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:34.0	XRT_FSA_CONTROL_417_OG [0x1a1]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/08/28	09:52:36.5	XRT_FSA_CONTROL_425_OG [0x1a9]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/08/28	09:52:39.0	XRT_FSA_CONTROL_417_OG [0x1a1]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/08/28	09:52:41.5	XRT_FSA_CONTROL_415_OG [0x19f]							
		XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00	
2010/08/28	09:52:44.0	XRT_FSA_CONTROL_410_OG [0x19a]							
		XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/08/28	09:52:46.0	XRT_STANDBY_438_OG [0x1b6]							
		XRT_STBY	2	07-F8	03	01			
2010/08/28	09:52:48.0	XRT_OPERATE_421_OG [0x1a5]							
		XRT_OPERATE	2	07-F8	03	02			
2010/08/28	09:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/28	09:55:02.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/08/28	09:55:22.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/28	09:55:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/28	09:55:26.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/28	09:58:04.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/28	09:58:06.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2010/08/28	09:58:08.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/28	10:36:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00