

# XRT Timeline to be uploaded on 2010/08/31

Period: 2010/08/31 10:39:00 - 2010/09/04 09:52:00

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## Normal mode

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### XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
08/31 18:43:02 - 08/31 18:52:00	Fixed ( 0.0, 0.0)	synoptic, shifted 40.0 min
09/01 03:05:00 - 09/01 03:09:54	Fixed ( 0.0, 0.0)	synoptic; request from HOP173
09/01 06:32:02 - 09/01 06:38:54	Fixed ( 0.0, 0.0)	synoptic, shifted 29.0 min
09/01 18:18:32 - 09/01 18:25:24	Fixed ( 0.0, 0.0)	synoptic, shifted 15.5 min
09/02 03:03:02 - 09/02 03:09:54	Fixed ( 0.0, 0.0)	synoptic: request from HOP173
09/02 06:04:02 - 09/02 06:10:54	Fixed ( 0.0, 0.0)	synoptic, shifted 1.0 min

**PROG= 03 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 7 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 8 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 4 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

### XOB #180D: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 2min-cad

Term	Pointing (x, y)	Comment
08/31 23:03:02 - 09/01 02:43:00	Track ( 275.9, 85.1) <sup>Ⓜ</sup> 08/31 23:00:00	HOP173 track AR11101 until 3:00
09/01 03:13:02 - 09/01 06:28:54	Track ( 536.1, 317.5) <sup>Ⓜ</sup> 09/01 03:10:00	HOP173 track AR11102 until 5:00
09/01 06:42:02 - 09/01 10:55:30	Track ( 557.9, 319.7) <sup>Ⓜ</sup> 09/01 06:39:00	Track AR11102
09/01 17:03:02 - 09/01 17:52:00	Track ( 619.0, 326.7) <sup>Ⓜ</sup> 09/01 17:00:00	Track AR11102
09/01 18:28:32 - 09/02 02:59:54	Track ( 626.9, 327.8) <sup>Ⓜ</sup> 09/01 18:25:30	Cont
09/02 03:13:02 - 09/02 06:00:54	Track ( 673.3, 334.3) <sup>Ⓜ</sup> 09/02 03:10:00	Track AR11102
09/02 06:14:02 - 09/02 10:56:54	Track ( 688.2, 336.7) <sup>Ⓜ</sup> 09/02 06:11:00	# Cont

**PROG= 01 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 17 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 6 4-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 86 20-time(s) 120.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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## Flare mode

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### XOB #17EF: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
08/31 23:03:02 - 09/01 02:43:00	Track ( 275.9, 85.1) <sup>Ⓜ</sup> 08/31 23:00:00	HOP173 track AR11101 until 3:00
09/01 03:13:02 - 09/01 06:28:54	Track ( 536.1, 317.5) <sup>Ⓜ</sup> 09/01 03:10:00	HOP173 track AR11102 until 5:00
09/01 06:42:02 - 09/01 10:55:30	Track ( 557.9, 319.7) <sup>Ⓜ</sup> 09/01 06:39:00	Track AR11102
09/01 17:03:02 - 09/01 17:52:00	Track ( 619.0, 326.7) <sup>Ⓜ</sup> 09/01 17:00:00	Track AR11102
09/01 18:28:32 - 09/02 02:59:54	Track ( 626.9, 327.8) <sup>Ⓜ</sup> 09/01 18:25:30	Cont
09/02 03:13:02 - 09/02 06:00:54	Track ( 673.3, 334.3) <sup>Ⓜ</sup> 09/02 03:10:00	Track AR11102
09/02 06:14:02 - 09/02 10:56:54	Track ( 688.2, 336.7) <sup>Ⓜ</sup> 09/02 06:11:00	# Cont

**PROG= 07 1-time(s)**

- Subr= 1 30-time(s) 20.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 91 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3    30-time(s)    60.0sec</b>												
<b>Seqn= 87    1-time(s)    2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2    1-time(s)    2.0sec</b>												
<b>Seqn= 90    1-time(s)    2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3    30-time(s)    60.0sec</b>												
<b>Seqn= 87    1-time(s)    2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2    1-time(s)    2.0sec</b>												
<b>Seqn= 90    1-time(s)    2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3    30-time(s)    60.0sec</b>												
<b>Seqn= 87    1-time(s)    2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4    24-time(s)    600.0sec</b>												
<b>Seqn= 89    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

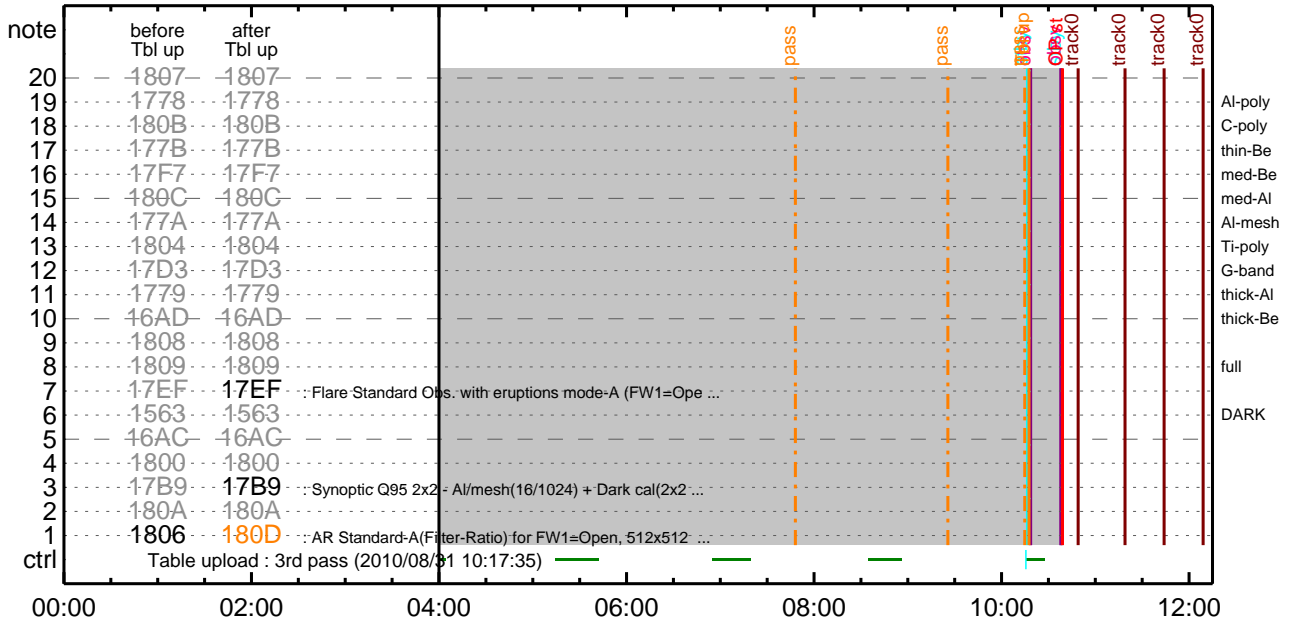
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### Flare Detection

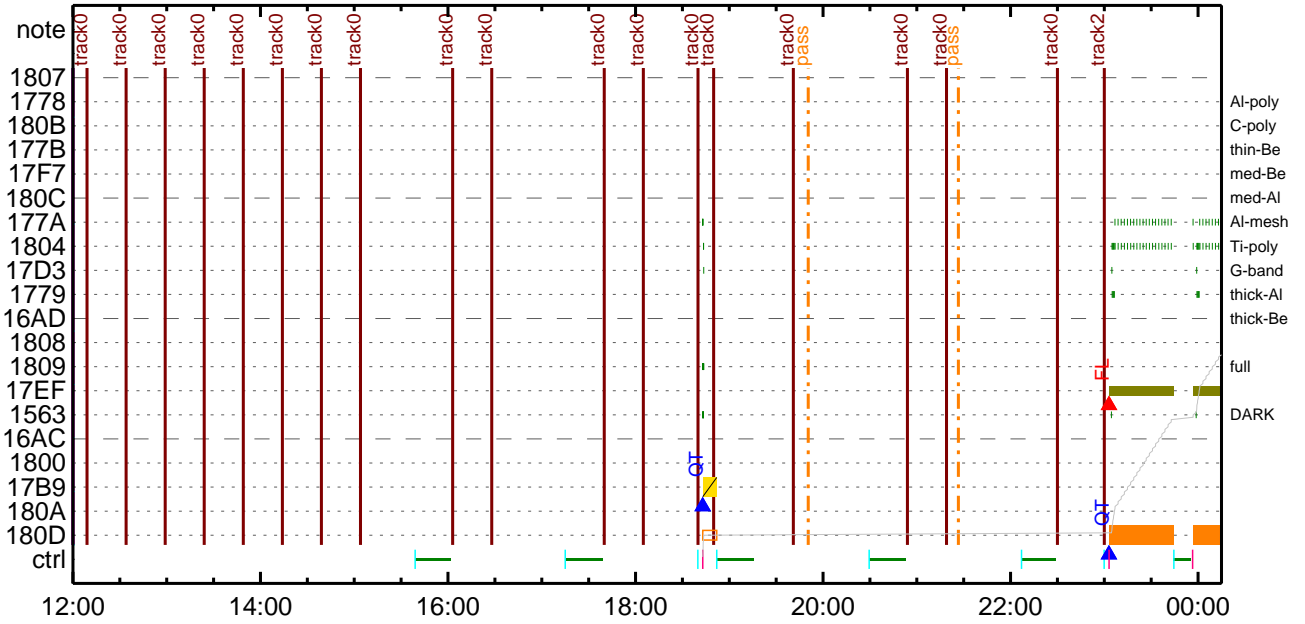
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<b>FLD Patrol</b>												
Term	Pointing (x, y)				Comment							
08/31 23:00:16 - 09/01 03:00:16	Track (	275.9,	85.1)	@ 08/31 23:00:00	HOP173 track AR11101 until 3:00							
09/01 03:10:16 - 09/01 06:29:16	Track (	536.1,	317.5)	@ 09/01 03:10:00	HOP173 track AR11102 until 5:00							
09/01 06:39:16 - 09/01 18:15:46	Track (	557.9,	319.7)	@ 09/01 06:39:00	Track AR11102							
09/01 18:25:46 - 09/02 03:00:16	Track (	626.9,	327.8)	@ 09/01 18:25:30	Cont							
09/02 03:10:16 - 09/02 06:01:16	Track (	673.3,	334.3)	@ 09/02 03:10:00	Track AR11102							
09/02 06:11:16 - 09/04 09:52:00	Track (	688.2,	336.7)	@ 09/02 06:11:00	# Cont							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

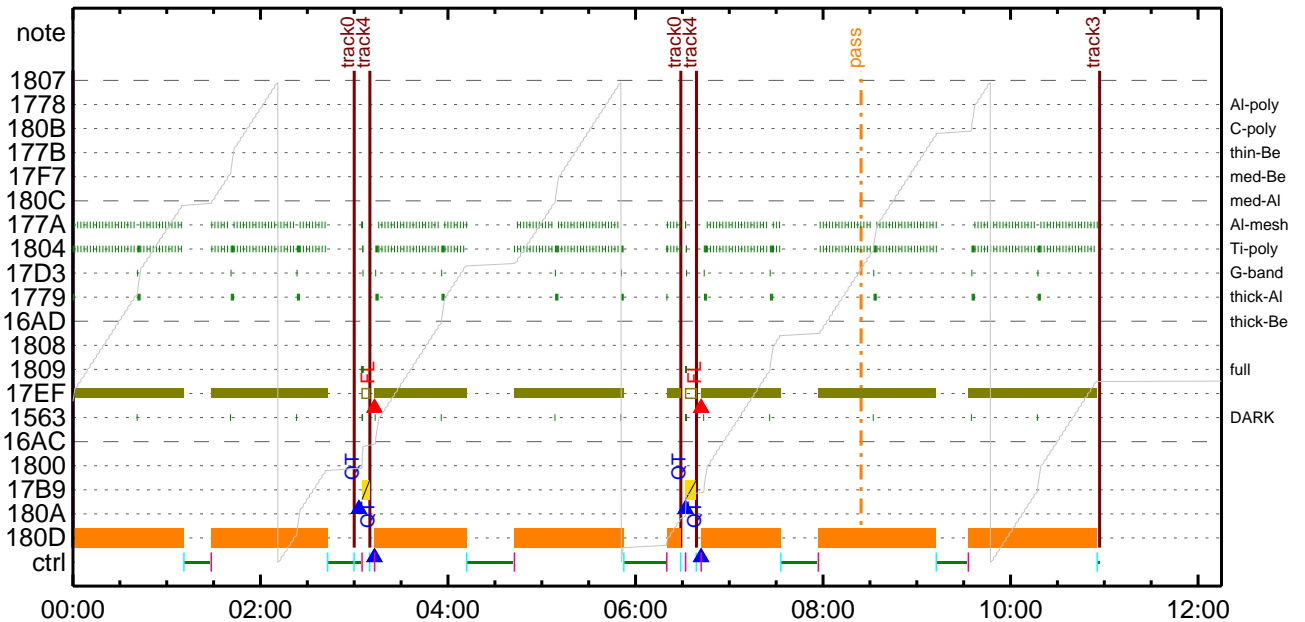
CMDI #0463 2010/08/31



CMDI #0463 2010/08/31

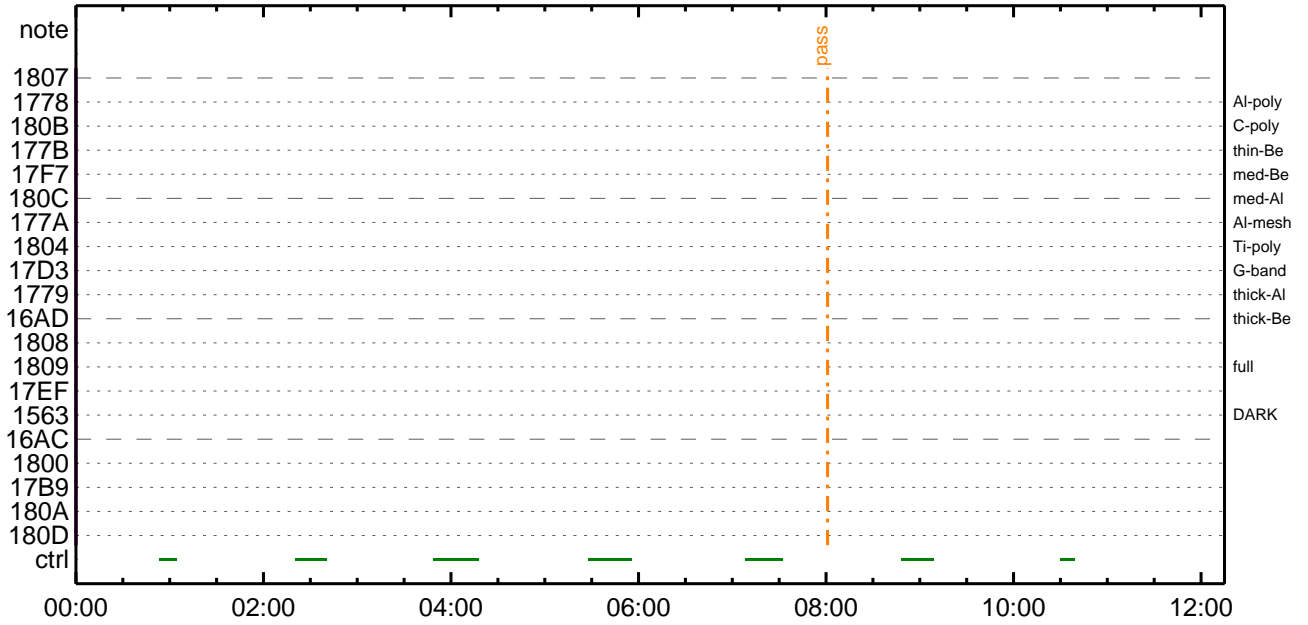


CMDI #0463 2010/09/01

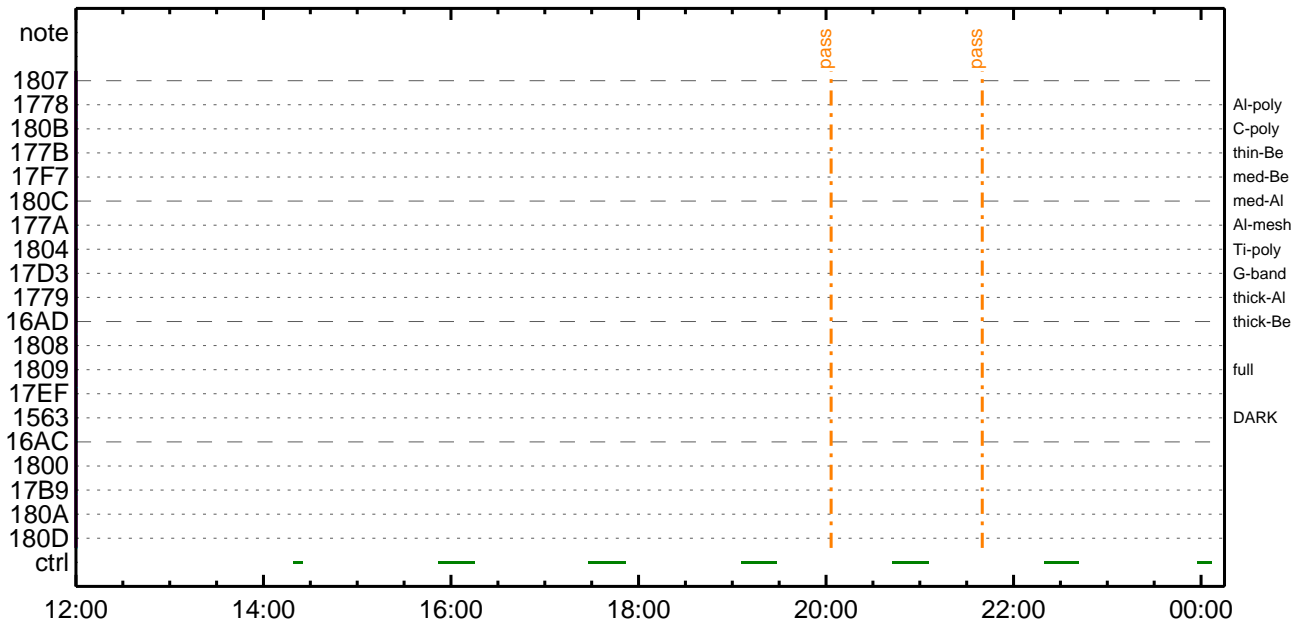




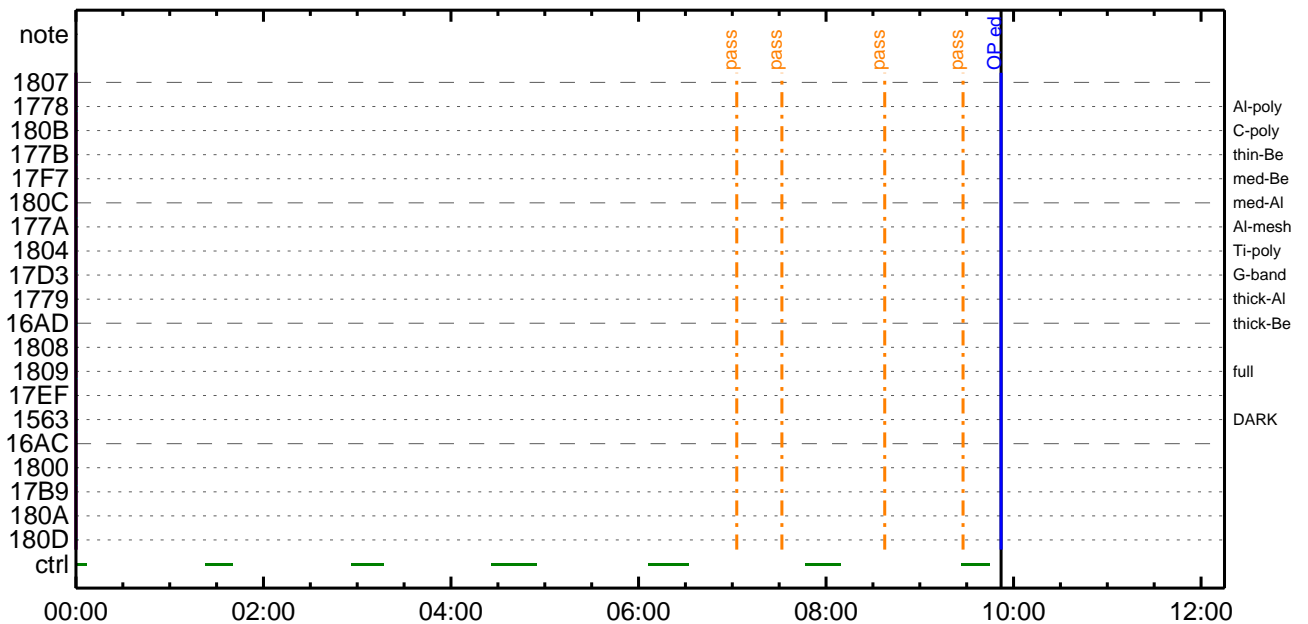
CMDI #0463 2010/09/03



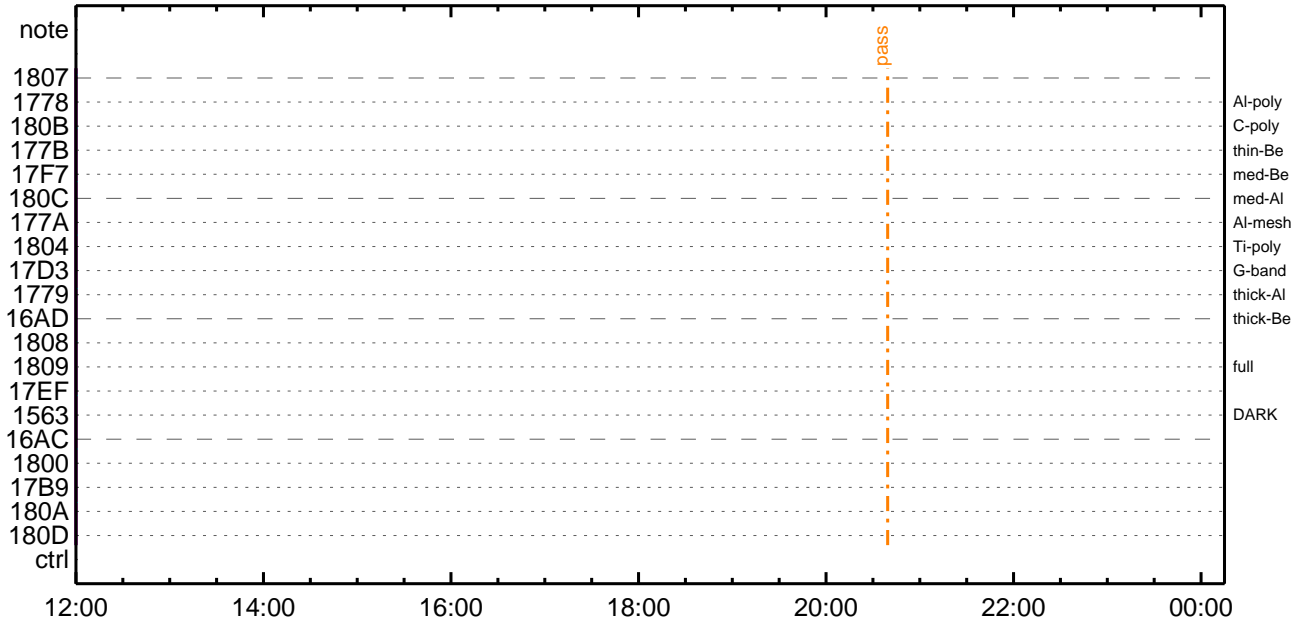
CMDI #0463 2010/09/03



CMDI #0463 2010/09/04



CMDI #0463 2010/09/04





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-557:OP
0104 ( )
0105 S. OG og-557:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬²è¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ô²•²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î½±²î½Y¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²YóYÉ²òðÁDî¿(UT)
0183 +. TI 2010-08-31 10:34:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-08-31 10:34:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-08-31 10:34:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-08-31 10:38:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïãîÄè%îíññîîŷÄŷ§ŷÄŷ-¹âiÛ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.      çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñî¼È¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èòðîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-08-31 10:38:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-08-31 10:38:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2010-08-31 10:38:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C. *****
0264 C. SOT TI command set
0265 C. *****
0266 C. Execute, after the success of OP upload.
0267 +. TI 2010-08-31 10:38:16.0
0268 DC 07-F0 MDP_SOT_MODE_STBY
0269 BC      (41)
0270 C. -----
0271 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0272 C. -----
0273 C. ***** SOT END *****
0274 C.
0275 C. ***** MDP `ûÃîãî»ö¼ŷñÈÄðñ¹ñèDCBC•x²è *****
0276 C. (¼ã°îŷÖŷÄŷÈŷŷŷÄŷçŷèè%¼ññ¼Ä»Ûñ¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîññè'Øñ¹ñèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```





(a) Spacecraft Operation Procedure (real-commands)

```
main-559 2010-08-31 13:04:20 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿ðÀð•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•íÍÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 20 20)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 08 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 85 83 08 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0f 80 80 06 06)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 10 80 80 08 08)
0052 + DC 07-F0 MDP_XRT_AEC_RESET
0053 BC (d0)
0054 . C. ----- Success Verify ? OK / NG ____
0055 C.
0056 C.
0057 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0058 C.
0059 +. DC 07-F0 MDP_XRT_MODE_OBSV
0060 BC (c2)
0061 +. TI 2010-08-31 10:38:02.0
0062 DC 07-F0 MDP_XRT_MODE_OBSV
0063 BC (c2)
0064 . C. ----- Success Verify ? OK / NG ____
0065 C.
0066 C. ***** XRT END *****
0067 . C. *****
0068 C. SOT table upload
0069 C. *****
0070 . C. < Stop FG table >
0071 +. DC 07-F0 MDP_FG_CTRL_MANU
0072 BC (51)
0073 . C. -----
0074 C. MDP_FG_CTRL_MODE = MANU [ ]
0075 C. -----
0076 C.
0077 . C. <Upload FG Observation Table>
0078 . S. RAM ram-262:MDP_OBS_F
0079 ( )
0080 C.
0081 . C. < Dump RAMID=MDP_OBS_F >
0082 +. DC 07-F0 MDP_DUMP_FGTBL
0083 BC (82 07 00 00 00 38 b8)
0084 C. -----
0085 C. MDP_OBS_F verify = OK/NG [ ]
0086 C. -----
0087 C.
0088 . C. < Stop SP table >
0089 +. DC 07-F0 MDP_SP_CTRL_MANU
0090 BC (61)
0091 C. -----
0092 C. MDP_SP_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload SP Observation Table>
```



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\*\*\* OP Sequence for XRT \*\*\*

2010/08/31	10:49:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00	54	00	00 00
2010/08/31	11:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00	4b	1a	00 00
2010/08/31	11:44:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00	42	35	00 00
2010/08/31	12:09:00.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00	39	58	00 00
2010/08/31	12:34:00.0	AOCS_ORe-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00	30	72	00 00
2010/08/31	12:59:00.0	AOCS_ORe-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00	27	8d	00 00
2010/08/31	13:24:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00	1e	a7	00 00
2010/08/31	13:49:00.0	AOCS_ORe-point_Start_8_OG [0x09e]						
		AOCU_NM	5	02-76	00	15	ca	00 00
2010/08/31	14:14:00.0	AOCS_ORe-point_Start_9_OG [0x09f]						
		AOCU_NM	5	02-76	00	0c	e5	00 00
2010/08/31	14:39:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]						
		AOCU_NM	5	02-76	00	04	00	00 00
2010/08/31	15:04:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]						
		AOCU_NM	5	02-76	00	fc	00	00 00
2010/08/31	15:39:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	15:39:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0		da		
2010/08/31	15:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2010/08/31	15:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2010/08/31	16:03:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]						
		AOCU_NM	5	02-76	00	f3	1b	00 00
2010/08/31	16:28:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]						
		AOCU_NM	5	02-76	00	ea	36	00 00
2010/08/31	17:15:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	17:15:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0		da		
2010/08/31	17:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2010/08/31	17:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2010/08/31	17:40:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]						
		AOCU_NM	5	02-76	00	e1	59	00 00
2010/08/31	18:05:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]						
		AOCU_NM	5	02-76	00	d8	73	00 00
2010/08/31	18:39:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	18:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/08/31	18:40:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	00	00	00	00 00
2010/08/31	18:40:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0		d9		
2010/08/31	18:40:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2010/08/31	18:40:20.0	XRT_ARS_DIS_404_OG [0x194]						
		MDP_XRT_ARS_DIS	1	07-F0		d5		
2010/08/31	18:42:58.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0		da		
2010/08/31	18:43:00.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	03	
2010/08/31	18:43:02.5	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2010/08/31	18:50:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]						
		AOCU_NM	5	02-76	00	cf	8e	00 00
2010/08/31	18:52:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	18:52:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0		da		
2010/08/31	18:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2010/08/31	18:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2010/08/31	19:41:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]						
		AOCU_NM	5	02-76	00	c6	a8	00 00
2010/08/31	20:29:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	20:29:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0		da		
2010/08/31	20:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2010/08/31	20:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2010/08/31	20:54:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]						
		AOCU_NM	5	02-76	00	bd	cb	00 00
2010/08/31	21:19:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]						
		AOCU_NM	5	02-76	00	b4	e6	00 00
2010/08/31	22:07:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/08/31	22:07:02.0	XRT_FLD_RESET_412_OG [0x19c]						

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2010/08/31	22:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/08/31	22:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/08/31	22:30:00.0	AOCs_OrE-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 ac 00 00 00		
2010/08/31	22:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/08/31	22:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/08/31	23:00:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 00 00 00 00		
2010/08/31	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/08/31	23:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/08/31	23:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/08/31	23:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/08/31	23:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/08/31	23:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2010/08/31	23:03:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2010/08/31	23:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/08/31	23:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/08/31	23:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/08/31	23:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/08/31	23:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/08/31	23:55:30.0	XRT_Custom_418_OG [0x1a2]						
2010/08/31	23:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/01	01:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/01	01:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/01	01:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/01	01:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/01	01:27:30.0	XRT_Custom_418_OG [0x1a2]						
2010/09/01	01:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/01	02:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/01	02:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/01	02:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/01	02:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/01	02:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/01	02:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/09/01	03:00:00.0	AOCs_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 00 00 00 00		
2010/09/01	03:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/09/01	03:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/09/01	03:00:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/01	03:02:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/01	03:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/09/01	03:04:00.5	XRT_Custom_418_OG [0x1a2]						
2010/09/01	03:05:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/01	03:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/01	03:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/01	03:10:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00 00		
2010/09/01	03:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/01	03:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/01	03:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/09/01	03:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/01	03:12:56.0	XRT_FLD_RESET_412_OG [0x19c]						

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2010/09/01	03:12:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2010/09/01	03:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/09/01	03:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	04:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	04:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	04:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	04:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	04:41:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/01	04:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	05:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	05:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	05:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	05:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	06:19:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/01	06:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	06:28:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	06:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/09/01	06:29:00.5	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 00 00 00	00
2010/09/01	06:29:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/09/01	06:29:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/09/01	06:29:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/01	06:31:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	06:32:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2010/09/01	06:32:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	06:38:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	06:38:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2010/09/01	06:39:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00	00
2010/09/01	06:39:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/01	06:39:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/01	06:39:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/01	06:39:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/01	06:41:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	06:41:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2010/09/01	06:42:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/09/01	06:42:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	07:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	07:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	07:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	07:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	07:56:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/01	07:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	09:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	09:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	09:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	09:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	09:32:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/01	09:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	10:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	



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2010/09/01	10:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	10:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	10:57:00.0	AOCS_OrE-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	03 00 00 00 00	
2010/09/01	10:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	14:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	14:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	14:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	14:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	16:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	16:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	16:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	16:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	16:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	16:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/09/01	17:00:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00 00	
2010/09/01	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/01	17:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/01	17:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/01	17:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/01	17:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	17:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2010/09/01	17:03:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2010/09/01	17:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	17:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	17:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	17:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/01	17:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/01	18:15:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	18:15:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/09/01	18:15:30.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/09/01	18:15:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/09/01	18:15:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/09/01	18:15:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/01	18:18:28.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	18:18:30.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/09/01	18:18:32.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/01	18:25:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/01	18:25:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/09/01	18:25:30.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00 00	
2010/09/01	18:25:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/01	18:25:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/01	18:25:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/01	18:25:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/01	18:28:26.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/01	18:28:28.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2010/09/01	18:28:30.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2010/09/01	18:28:32.5	XRT_CTRL_AUTO_406_OG [0x196]					

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2010/09/01	19:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/01	19:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/01	19:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/01	19:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/01	19:52:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/01	19:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/01	21:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/01	21:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/01	21:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/01	21:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/01	21:29:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/01	21:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/01	22:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/01	22:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/01	22:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/01	22:44:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/01	22:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/01	23:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/01	23:05:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/02	00:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/02	00:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/02	00:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/02	00:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/02	00:29:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/02	00:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/02	01:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/02	01:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/02	01:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/02	01:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/02	02:04:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/02	02:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/02	02:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION_401_OG [0x191]	4	07-F8	22 ff aa 00
2010/09/02	02:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/02	03:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 00 00 00 00
2010/09/02	03:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/09/02	03:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/09/02	03:00:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/02	03:02:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/02	03:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/09/02	03:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/02	03:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/02	03:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/02	03:10:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00 00
2010/09/02	03:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/09/02	03:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/09/02	03:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/09/02	03:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/02	03:12:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/02	03:12:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2010/09/02	03:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]				

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2010/09/02	03:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	03:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	03:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	03:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/02	03:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/02	03:41:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/02	03:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	04:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	04:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	04:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/02	04:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/02	05:18:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/02	05:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	06:00:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	06:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/09/02	06:01:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 00 00 00	00
2010/09/02	06:01:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/09/02	06:01:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/09/02	06:01:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/02	06:03:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	06:04:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2010/09/02	06:04:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	06:10:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	06:10:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2010/09/02	06:11:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	04 00 00 00	00
2010/09/02	06:11:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/02	06:11:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/02	06:11:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/02	06:11:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/02	06:13:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	06:13:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2010/09/02	06:14:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/09/02	06:14:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	06:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	06:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	06:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/02	06:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/02	06:56:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/02	06:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	08:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	08:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	08:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/02	08:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/02	08:32:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/02	08:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/02	09:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/02	09:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/02	09:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

