

XRT Timeline to be uploaded on 2010/09/21

Period: 2010/09/21 10:14:00 - 2010/09/25 10:19:00

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Normal mode

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XOB #1813: AR Standard-B(Morphology) with PFB FW1=Open Ti/Poly 384x384 at 1064 1048-30sec-cad											
Term	Pointing (x, y)					Comment					
09/21 10:27:00 - 09/21 13:00:00	Track (725.5, -398.1) @ 09/21 10:24:00					# OP start + 10min HOP166 at AR11106					
09/22 06:37:00 - 09/22 13:00:00	Track (-39.0, -592.0) @ 09/22 06:34:00					# HOP126 and HOP166 at AR11108					
09/23 06:10:30 - 09/23 10:51:35	Track (138.3, -590.5) @ 09/23 06:07:30					# HOP126 and HOP166 at AR11108					
PROG= 11 Inf.-time(s)											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Seqn= 20 4-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 71 90-time(s) 30.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec											
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>											

XOB #1812: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 225s cad											
Term	Pointing (x, y)					Comment					
09/21 13:03:06 - 09/21 18:04:54	Track (725.5, -398.1) @ 09/21 10:24:00					# OP start + 10min HOP166 at AR11106					
09/21 18:18:00 - 09/22 06:23:54	Track (-253.5, -580.1) @ 09/21 18:15:00					# Track AR11108					
09/22 13:03:06 - 09/22 14:59:54	Track (-39.0, -592.0) @ 09/22 06:34:00					# HOP126 and HOP166 at AR11108					
09/22 18:23:30 - 09/23 05:57:24	Track (-74.3, -584.5) @ 09/22 18:20:30					# Track AR11108					
PROG= 17 Inf.-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Seqn= 20 4-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 63 20-time(s) 225.0sec											
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec											
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec											
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec											
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec											
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec											
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec											
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>											

XOB #1715: Synoptic Q95 2x2 - Al/mesh(128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) + Ti-poly(256/2897) + G-band(1											
Term	Pointing (x, y)					Comment					
09/21 18:08:00 - 09/21 18:14:54	Fixed (0.0, 0.0)					synoptic, shifted 5.0 min					
09/22 06:27:00 - 09/22 06:33:54	Fixed (0.0, 0.0)					synoptic, shifted 24.0 min					
09/22 18:13:30 - 09/22 18:20:24	Fixed (0.0, 0.0)					synoptic, shifted 10.5 min					
09/23 06:00:30 - 09/23 06:07:24	Fixed (0.0, 0.0)					synoptic, shifted -2.5 min					
PROG= 16 1-time(s)											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 69 1-time(s) 4.0sec											
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Seqn= 5 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec											
└─ Seqn= 68 1-time(s) 4.0sec											
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Seqn= 4 1-time(s) 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>											

XOB #1809: HOP101 - Ti/poly 8s, G-band - 384x384-AEC1-ROI centered												
Term	Pointing (x, y)		Comment									
09/22 15:03:00 - 09/22 17:47:00	Fixed (0.0, 0.0)		# HOP169 on disk center									
PROG= 08 Inf.-time(s)												
┌ Subr= 1 1-time(s) 2.0sec												
└─┬ Seqn= 23 1-time(s) 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec												
└─┬ Subr= 2 30-time(s) 2.0sec												
└─┬ Seqn= 34 2-time(s) 30.0sec												
└─┬ Open/Ti-poly Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) Q=90 1 0 2.0sec												
└─┬ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

* * * * * **Flare mode** * * * * *

XOB #1817: Flare Obs for use with Ti-poly 1x1 PFB images (FW1=Open)												
Term	Pointing (x, y)		Comment									
09/21 10:27:00 - 09/21 13:00:00	Track (725.5, -398.1) @ 09/21 10:24:00		# OP start + 10min HOP166 at AR11106									
09/21 13:03:06 - 09/21 18:04:54	Track (725.5, -398.1) @ 09/21 10:24:00		# OP start + 10min HOP166 at AR11106									
09/21 18:18:00 - 09/22 06:23:54	Track (-253.5, -580.1) @ 09/21 18:15:00		# Track AR11108									
09/22 06:37:00 - 09/22 13:00:00	Track (-39.0, -592.0) @ 09/22 06:34:00		# HOP126 and HOP166 at AR11108									
09/22 13:03:06 - 09/22 14:59:54	Track (-39.0, -592.0) @ 09/22 06:34:00		# HOP126 and HOP166 at AR11108									
09/22 15:03:00 - 09/22 17:47:00	Fixed (0.0, 0.0)		# HOP169 on disk center									
09/22 18:23:30 - 09/23 05:57:24	Track (-74.3, -584.5) @ 09/22 18:20:30		# Track AR11108									
09/23 06:10:30 - 09/23 10:51:35	Track (138.3, -590.5) @ 09/23 06:07:30		# HOP126 and HOP166 at AR11108									
PROG= 01 1-time(s)												
┌ Subr= 1 2-time(s) 620.0sec												
└─┬ Seqn= 67 30-time(s) 20.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Seqn= 90 1-time(s) 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Subr= 2 8-time(s) 600.0sec												
└─┬ Seqn= 67 5-time(s) 120.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬ Seqn= 90 1-time(s) 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Subr= 3 1-time(s) 2.0sec												
└─┬ Seqn= 89 40-time(s) 600.0sec												
└─┬ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec												
└─┬ Open/Ti-poly Open/thick-Be close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/Al-mesh Open/Al-mesh close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

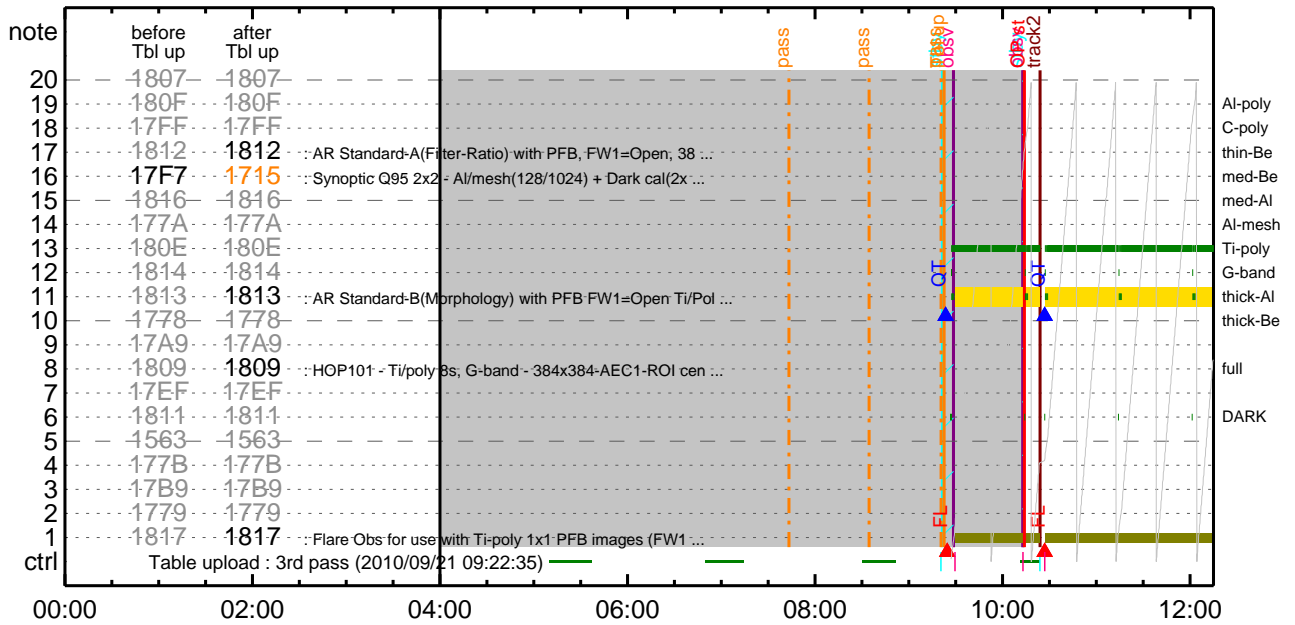
* * * * * **Active Region Search** * * * * *

NOT USED

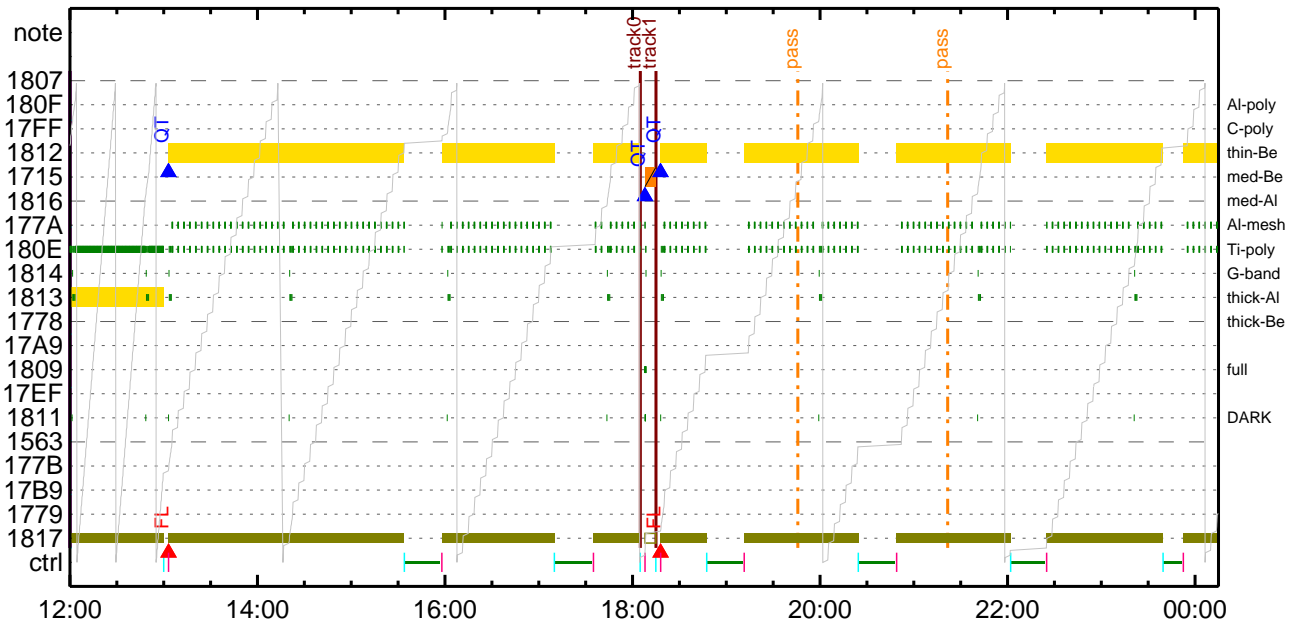
* * * * * **Flare Detection** * * * * *

FLD Patrol												
Term	Pointing (x, y)		Comment									
09/21 18:15:16 - 09/22 06:24:16	Track (-253.5, -580.1) @ 09/21 18:15:00		# Track AR11108									
09/22 06:34:16 - 09/22 18:10:46	Track (-39.0, -592.0) @ 09/22 06:34:00		# HOP126 and HOP166 at AR11108									
09/22 18:20:46 - 09/23 05:57:46	Track (-74.3, -584.5) @ 09/22 18:20:30		# Track AR11108									
09/23 06:07:46 - 09/25 10:19:00	Track (138.3, -590.5) @ 09/23 06:07:30		# HOP126 and HOP166 at AR11108									
Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 Q=50 30sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

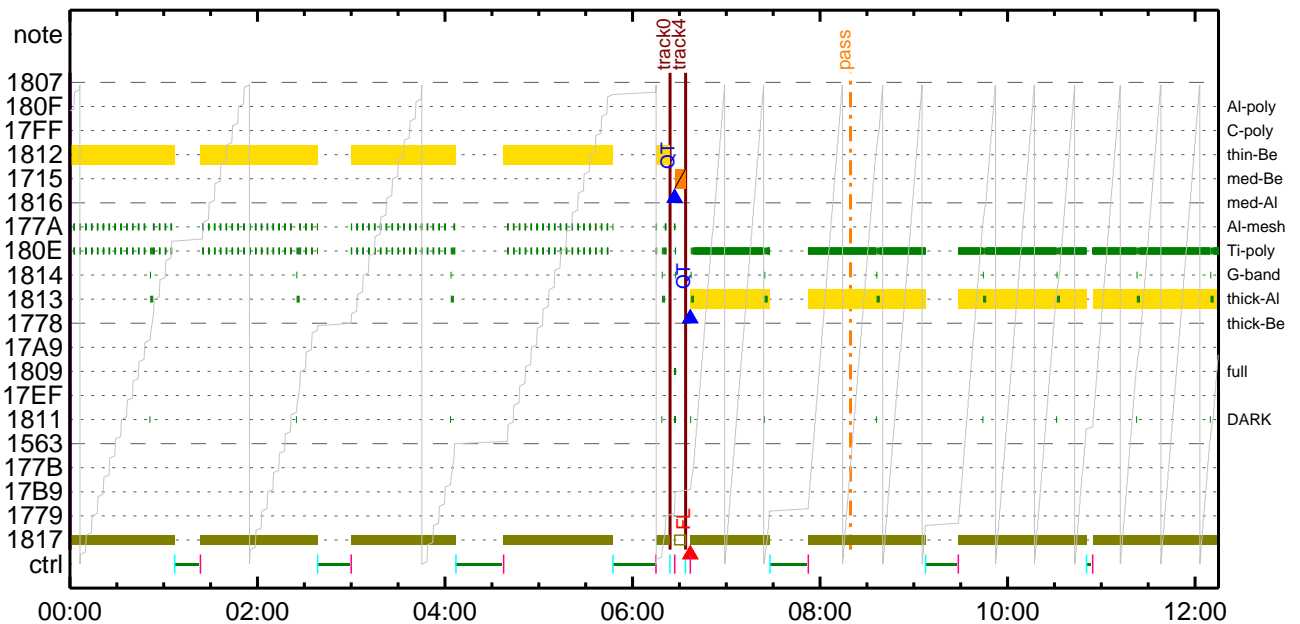
CMDI #0498 2010/09/21



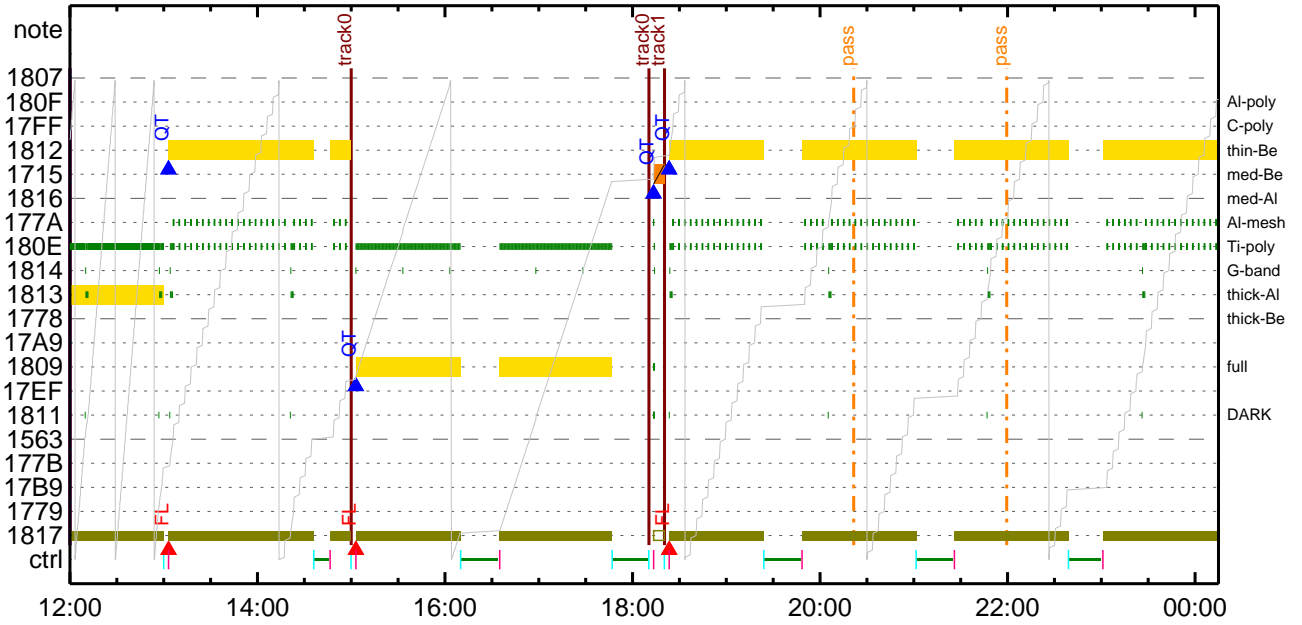
CMDI #0498 2010/09/21



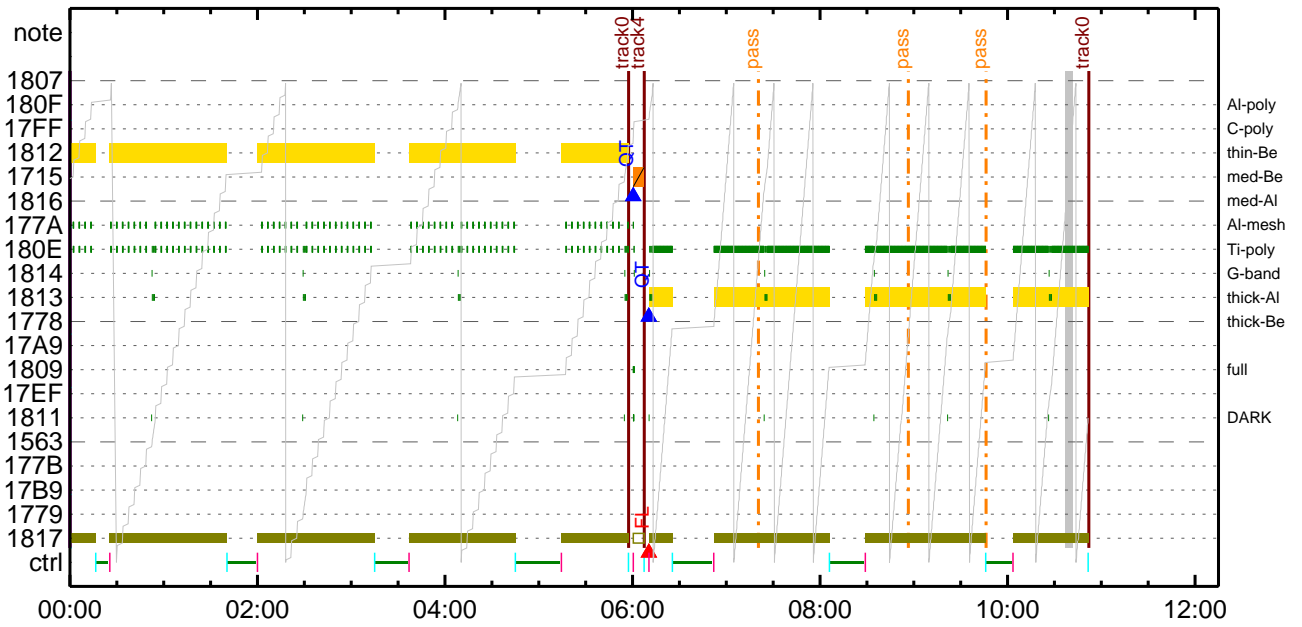
CMDI #0498 2010/09/22



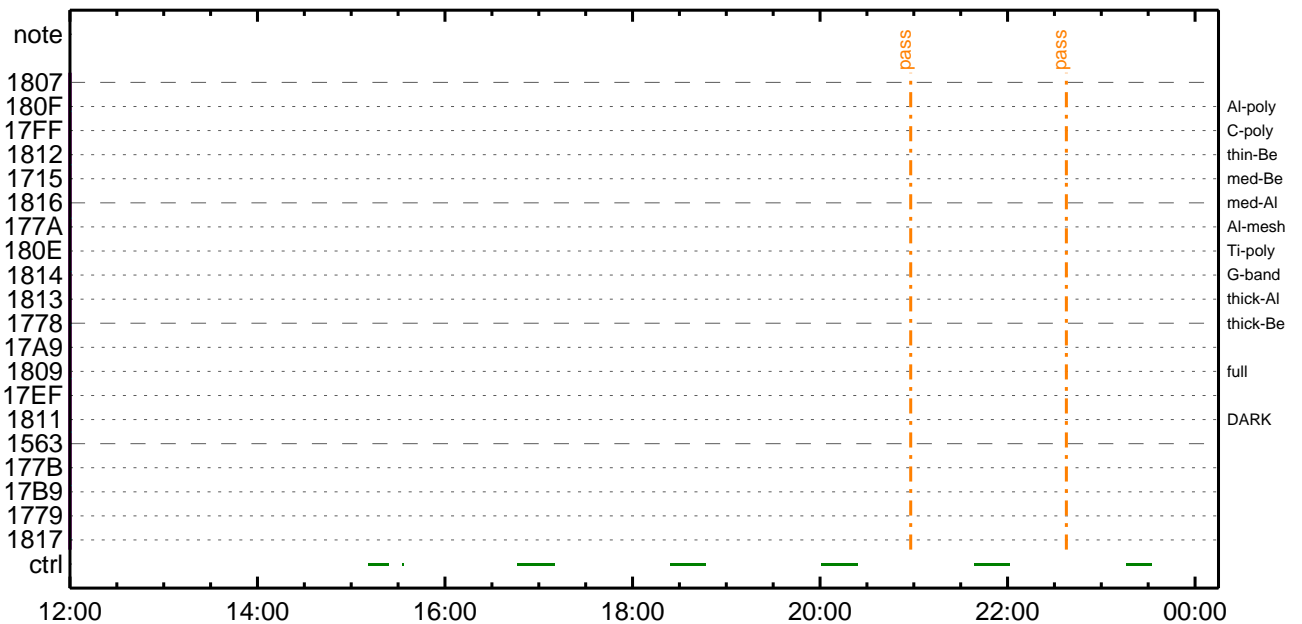
CMDI #0498 2010/09/22



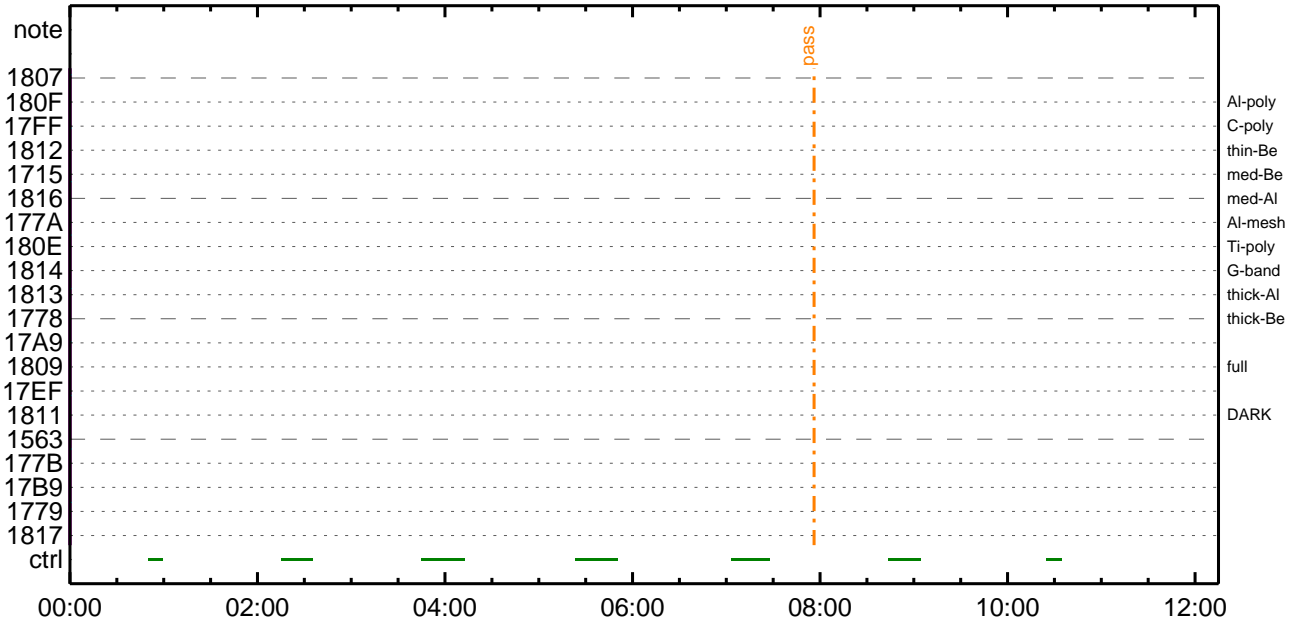
CMDI #0498 2010/09/23



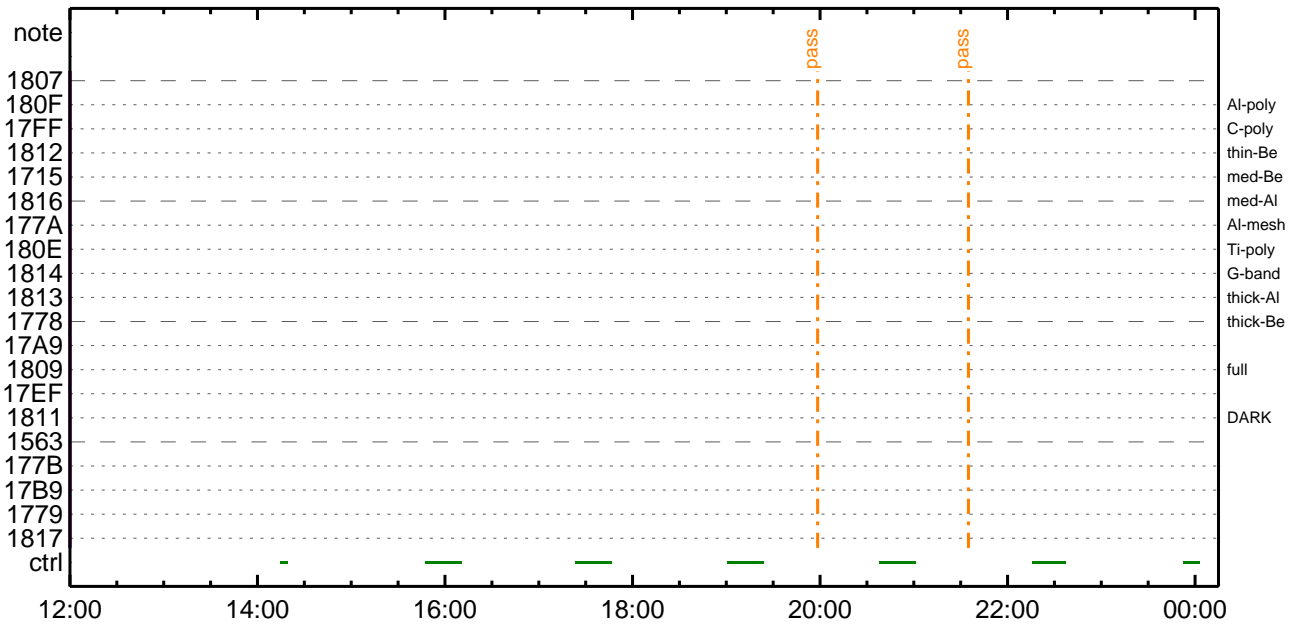
CMDI #0498 2010/09/23



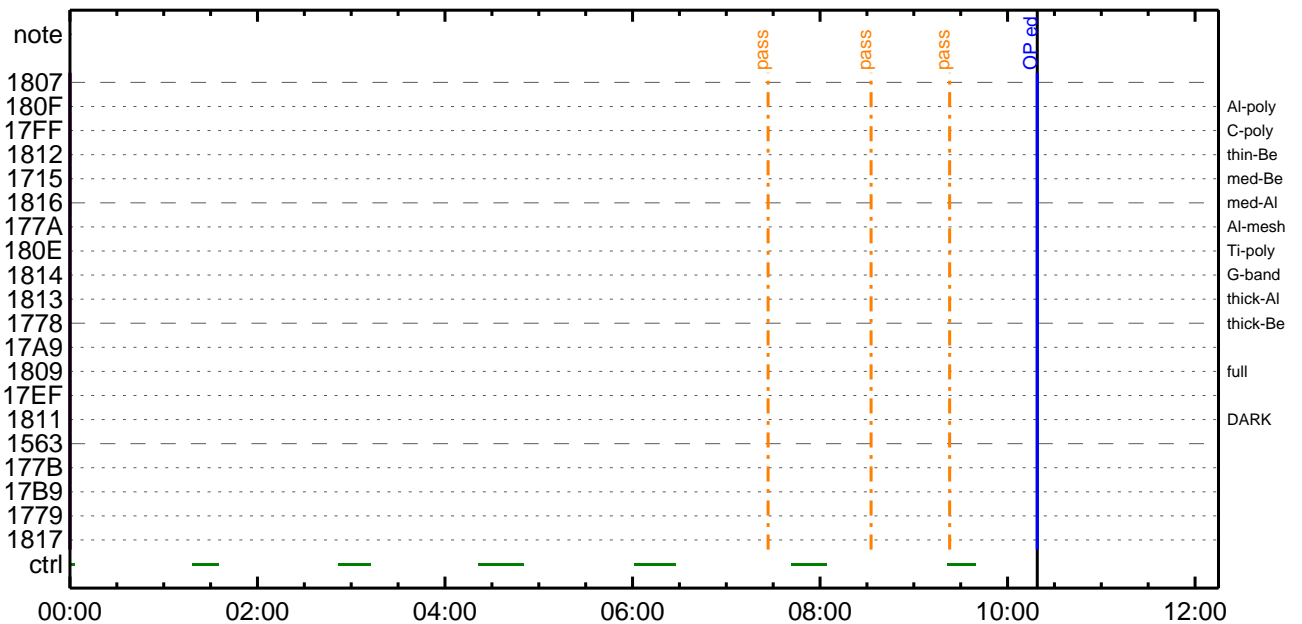
CMDI #0498 2010/09/24



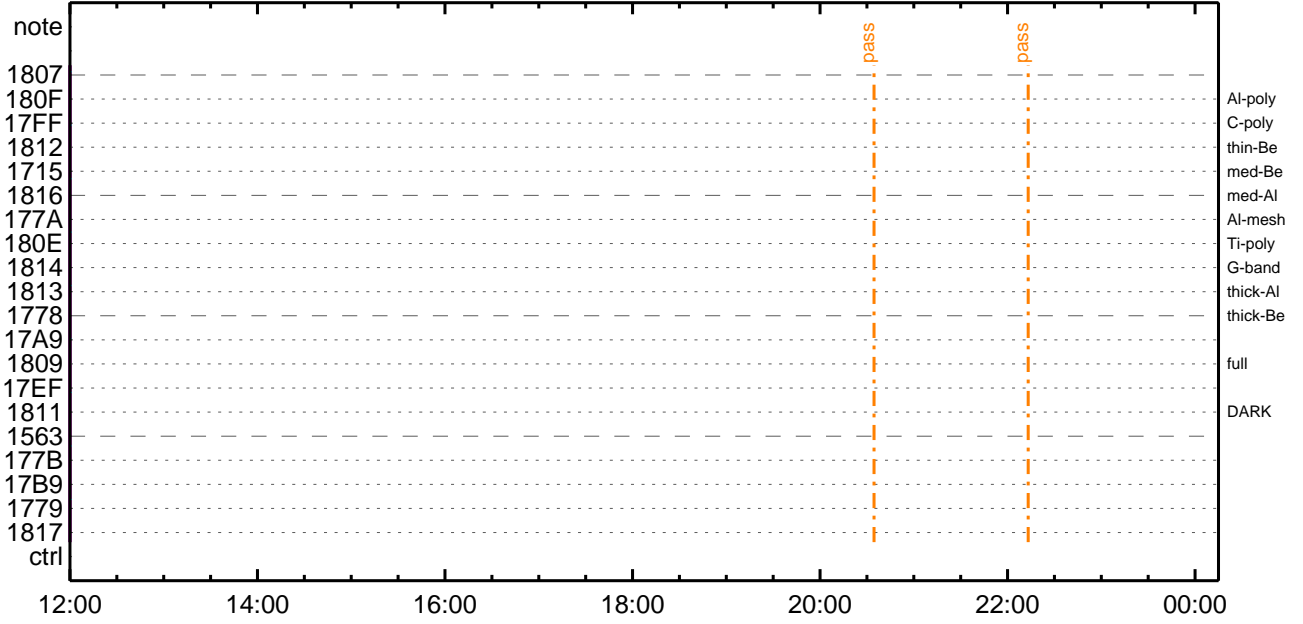
CMDI #0498 2010/09/24



CMDI #0498 2010/09/25



CMDI #0498 2010/09/25




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;Y4E;YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;Y4E;ã
0103 S. OP op-598:OP
0104 ( )
0105 S. OG og-598:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî½E¹ç·è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî½E¹ç·è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî½E¹ç·è²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî½Ä´¶Á°òEÈ¬ò°Á÷¿@ (½åµ-YAY6Yx½ê½çòðÁÔÆòÇ½ª°¬òE½i¹çòçòâ) *****
0167 C. DHUYâ;Y4E;E½Y½;Yi;Y4E;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿@NGñî½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@ñî½Á¹Ôª°¬Eòòò³òE;f
0180 C. òEò¿;çSETòEDUMPrñî½±°iYNY¹ç¹Ôª|ò³òE;f
0181 C.
0182 C. TIY³Y½Y6Y4EòðÁDî¿¿(UT)
0183 +. TI 2010-09-21 10:09:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-09-21 10:09:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-09-21 10:09:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-09-21 10:13:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C.      °Ê²¼¤íÀè%íîñ¤îŷÁŷ$ŷÁŷ~¹àîŷ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM] EQ 4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C.      *****
0206 C.      Tîîî°èŷÁŷöŷx
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM] EQ 3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM] EQ 0
0216 C.      ÷÷[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO] EQ 7
0220 C.      ÷÷[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C.      ŷÁŷöŷx½ªî»¤³îçš
0226 C.      ÷÷[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C.      RAM ID=TI_TBL¤îî°è²¼¤î»¤³îçš
0229 C.
0230 . C.      DHUŷª;¼ŷÈ;Ê¼ŷ¼. ŷî;¼ŷÈ;È¤¤î¤ª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO] EQ 2
0234 C.      ÷÷[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 . C.      Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.      ***** Start EIS operation (TI set) *****
0242 C.      Execute, after the success of OP upload.
0243 C.      Set EIS TI-commands
0244 +. TI 2010-09-21 10:13:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-09-21 10:13:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 . C.      [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C.      ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.      *****
0255 C.      SOT TI command set
0256 C.      *****
0257 C.      Execute, after the success of OP upload.
0258 +. TI 2010-09-21 10:13:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 . C.      -----
0262 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C.      -----
0264 C.      ***** SOT END *****
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2010-09-21 10:13:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 . C.      ***** MDP ´ûÃî¤î»ö¼ŷ¤¤ÈÀ¤¤ª¹¤èDCBC•x²è *****
0276 C.      (¼ª°îŷöŷÁŷÈŷŷŷÁŷçŷè¤¼¤¤ª¹¤¤ª»Û¤ªè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C.      ***** ŷ¤ŷ¹•î Daily±çîñ¤¤È´¤¤ª¹¤èDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C.      ;ãLOSŷÁŷ$ŷÁŷ~¼ª»Û;ã
0287 C.
0288 . C.      ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-599 2010-09-21 11:57:30 91 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÅY$YÅY-¼Ä»Ü;ã  
0005 C.  
0006 C. YÅYB;¼Y³YF¥ÓYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 C. Åí;Èó¿ðÅó•µ°È»Í×ÁÇóÍYçYÅY×YÍ;¼YÉ;ÈÈè%µ•fÍÉ;ÈóÈ¼°ÇÔó•ó¿¼i¹çóÍ;çÄ®, ùó¹óÈóÈóÇÁ+¿®ó•óÈóóó³óÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 ( )  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCs Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. ***** AOCs Commands (Orbital Element Update) *****  
0046 C. Update the orbital element  
0047 +. DC 02-50 AOCU_ORB_PRPGT_START  
0048 BC (16)  
0049 + DC 02-8E AOCU_ORB_UPD  
0050 C.  
0051 C. <A_ORB>[ORBIT] EPC = 8287440.7 +- 1.0 (s) [ ]  
0052 C.  
0053 C.  
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0055 +. DC 07-FC EIS_MODE_MANU  
0056 BC (21 02)  
0057 . C. Verify EIS in MANUAL mode  
0058 C. Estimated OBSTBL upload time is 31s  
0059 C. *****  
0060 C. EIS START OBSTBL LOAD  
0061 C. *****  
0062 . S. RAM ram-820:EIS_OBSTBL  
0063 ( )  
0064 +. DC 07-FC EIS_DUMP_OBSTBL  
0065 BC (07 07 07 00 00 70 00)  
0066 C.  
0067 C. Execute, after the success of OBSTBL upload.  
0068 C. Set EIS TI-commands  
0069 +. TI 2010-09-21 10:13:50.0  
0070 DC 07-FC EIS_MODE_CHG_ENA  
0071 BC (20)  
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0073 C. *****  
0074 C. EIS END OBSTBL LOAD  
0075 C. *****  
0076 C.  
0077 . C. ***** MDP `ôÄÎóÎ`ò¼YóÈÅðó¹óèDCBC•x²è *****  
0078 C. (¾á°ÍYÓYÅYÈYbYÉYáYçYèó¼ó¼ã¼Ä»Üó¹óè)  
0079 . S. DC-BC dcbc-402:DCBC  
0080 (MDP_known_event)  
0081 C.  
0082 C.  
0083 . C. ***** YDY¹•Ï Daily±¿ÍÑóÈ´Øó¹óèDCBC•x²è *****  
0084 . S. DC-BC dcbc-153:DCBC  
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0086 C.  
0087 C.  
0088 . C. ;ãLOS¥ÅY$YÅY-¼Ä»Ü;ã  
0089 C.  
0090 . C. ***** LOS *****  
0091 C.
```



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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ()
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0141 BC (c4 0b)
0142 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0143 BC (c5 01)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_FLD_ENA
0147 BC (d8)
0148 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0149 BC (c8)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0158 BC (c0)
0159 +. TI 2010-09-21 10:13:02.0
0160 DC 07-F0 MDP_XRT_MODE_OBSV
0161 BC (c2)
0162 +. TI 2010-09-21 10:13:04.0
0163 DC 07-F0 MDP_XRT_CTRL_AUTO
0164 BC (c0)
0165 . C. ----- Success Verify ? OK / NG ____
0166 C.
0167 C. ***** XRT END *****
0168 . C. *****
0169 C. SOT table upload
0170 C. *****
0171 . C. < Stop FG table >
0172 +. DC 07-F0 MDP_FG_CTRL_MANU
0173 BC (51)
0174 . C. -----
0175 C. MDP_FG_CTRL_MODE = MANU [ ]
0176 C. -----
0177 C.
0178 . C. <Upload FG Observation Table>
0179 . S. RAM ram-261:MDP_OBS_F
0180 ()
0181 C.
0182 . C. < Dump RAMID=MDP_OBS_F >
0183 +. DC 07-F0 MDP_DUMP_FGTBL
0184 BC (82 07 00 00 00 38 b8)
0185 C. -----
0186 C. MDP_OBS_F verify = OK/NG [ ]
0187 C. -----
0188 C.
0189 . C. < Upload DPL table >
0190 C.
0191 C. ¥ç¥Ã¥×¥í;¼¥É¥î¥°¥ESTS_CHK¥ð¥OFF¥È¥¹¥è
0192 C.
0193 . S. RAM ram-271:MDP_DPL

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0194      ( )
0195      C.
0196      . C. < Dump RAMID=MDP_DPL >
0197 +. DC 07-F0 MDP_DUMP_FGTBL
0198      BC      (82 07 00 38 b8 00 40)
0199      C. -----
0200      C. MDP_DPL verify          = OK      [ ]
0201      C. -----
0202      C.
0203      C. STS_CHKαδONαÈα¹αè
0204      C.
0205      . C. < Update MDP DSC PAR1 >
0206 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0207      BC      (4c)
0208      C. MDP_CMD_CODE          = F04C0700[ ]
0209      C. MDP_CMD_CNT          (count-up 1) [ ]
0210      C. -----
0211      C.
0212      . C.
0213      C. *****
0214      C. SOT TI command set
0215      C. *****
0216      C. Execute, after the success of TBL upload.
0217 +. TI 2010-09-21 10:13:18.0
0218      DC 07-F0 MDP_SOT_MODE_OBSV
0219      BC      (40)
0220      . C. -----
0221      C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0222      C. -----
0223      C.
0224      C.
0225      . C. ***** MDP `ûÃÎαî»ò¼ÝαÈÃΔα¹αèDCBC•x²è *****
0226      C. (¼ã°îÿÓÿÃÿÈÿPÿËÿãÿçÿèαÈ¼αα¼Ã»Ûα¹αè)
0227      . S. DC-BC dcbc-402:DCBC
0228      (MDP_known_event)
0229      C.
0230      C.
0231      . C. ***** ÿDÿ¹•Ï Daily±;îÑαÈ´Øα¹αèDCBC•x²è *****
0232      . S. DC-BC dcbc-153:DCBC
0233      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0234      C.
0235      C.
0236      . C. ;ãLOSÿÃÿSÿÿÃÿ`¼Ã»Û;ã
0237      C.
0238      . C. ***** LOS *****
0239      C.

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*** OP Sequence for XRT ***

2010/09/21	10:23:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	10:23:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/21	10:24:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2010/09/21	10:24:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/21	10:24:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/21	10:24:20.0	XRT_ARS_DIS_415_OG [0x19f]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/21	10:26:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2010/09/21	10:26:58.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2010/09/21	10:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/21	13:00:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	13:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/21	13:00:22.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/21	13:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/21	13:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/09/21	13:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/21	13:03:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2010/09/21	13:03:04.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2010/09/21	13:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/21	15:34:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	15:34:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/21	15:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/21	15:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/21	15:57:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/21	15:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/21	17:10:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	17:10:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/21	17:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/21	17:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/21	17:34:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/21	17:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/21	18:04:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	18:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/09/21	18:05:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/09/21	18:05:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/09/21	18:05:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/09/21	18:05:20.0	XRT_ARS_DIS_415_OG [0x19f]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/21	18:07:56.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/21	18:07:58.0	XRT_QT_PROG_SET_405_OG [0x195]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2010/09/21	18:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/21	18:14:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/21	18:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/21	18:15:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2010/09/21	18:15:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/21	18:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/21	18:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/09/21	18:15:22.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/21	18:17:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]					

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2010/09/21	18:17:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2010/09/21	18:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/21	18:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/21	18:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/21	18:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/21	18:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/21	19:10:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/21	19:11:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/21	20:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/21	20:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/21	20:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/21	20:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/21	20:48:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/21	20:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/21	22:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/21	22:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/21	22:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/21	22:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/21	22:24:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/21	22:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/21	23:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/21	23:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/21	23:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/21	23:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/21	23:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/21	23:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/22	01:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/22	01:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/22	01:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/22	01:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/22	01:22:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/22	01:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/22	02:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/22	02:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/22	02:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/22	02:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/22	02:59:00.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/22	03:00:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/22	04:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/22	04:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/22	04:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/22	04:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/22	04:36:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/22	04:37:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/22	05:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/22	05:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/22	05:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/22	05:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/22	06:14:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CUSTOM_418_OG	1	07-F0	da	
2010/09/22	06:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2010/09/22	06:23:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	06:23:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/09/22	06:24:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2010/09/22	06:24:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/09/22	06:24:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/09/22	06:24:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/22	06:26:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/22	06:26:58.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2010/09/22	06:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	06:33:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	06:33:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/22	06:34:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00
2010/09/22	06:34:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/09/22	06:34:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/09/22	06:34:20.0	XRT_AEC_RESET_442_OG [0x1ba]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/09/22	06:34:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/22	06:36:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2010/09/22	06:36:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/09/22	06:37:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	07:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	07:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/22	07:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/22	07:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/22	07:51:30.0	XRT_Custom_418_OG [0x1a2]				
2010/09/22	07:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	09:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	09:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/22	09:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/22	09:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/22	09:27:30.0	XRT_Custom_418_OG [0x1a2]				
2010/09/22	09:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	10:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	10:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/22	10:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/22	10:53:30.0	XRT_Custom_418_OG [0x1a2]				
2010/09/22	10:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/22	10:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	13:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	13:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/22	13:00:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/09/22	13:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/09/22	13:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/09/22	13:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/22	13:03:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2010/09/22	13:03:04.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/09/22	13:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/22	14:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/22	14:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da

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2010/09/22	14:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/09/22	14:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/09/22	14:45:30.0	XRT_Custom_418_OG [0x1a2]							
2010/09/22	14:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/22	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	14:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/09/22	15:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/09/22	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/09/22	15:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/09/22	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/09/22	15:00:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/09/22	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/09/22	15:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/09/22	15:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2010/09/22	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/22	16:10:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	16:10:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/09/22	16:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/09/22	16:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/09/22	16:34:00.0	XRT_Custom_418_OG [0x1a2]							
2010/09/22	16:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/22	17:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	17:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/09/22	17:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/09/22	17:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/09/22	18:10:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	18:10:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/09/22	18:10:30.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/09/22	18:10:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/09/22	18:10:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/09/22	18:10:50.0	XRT_ARS_DIS_415_OG [0x19f]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/09/22	18:13:26.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/09/22	18:13:28.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2010/09/22	18:13:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/22	18:20:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	18:20:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/09/22	18:20:30.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2010/09/22	18:20:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/09/22	18:20:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/09/22	18:20:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/09/22	18:20:52.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/09/22	18:23:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2010/09/22	18:23:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2010/09/22	18:23:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/22	19:24:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/22	19:24:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/09/22	19:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/09/22	19:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

2010/09/22	19:47:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/22	19:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/22	21:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/22	21:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/22	21:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/22	21:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/22	21:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/22	21:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/22	22:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/22	22:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/22	22:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/22	22:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/22	22:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/22	23:00:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/22	23:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/23	00:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	00:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	00:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/23	00:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/23	00:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/23	00:24:30.0	XRT_Custom_418_OG [0x1a2]						
2010/09/23	00:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/23	01:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	01:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	01:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/23	01:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/23	01:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/23	01:59:00.0	XRT_Custom_418_OG [0x1a2]						
2010/09/23	02:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/23	03:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	03:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	03:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/23	03:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/23	03:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/23	03:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/23	03:36:00.0	XRT_Custom_418_OG [0x1a2]						
2010/09/23	03:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/23	04:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	04:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	04:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/23	04:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/23	04:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/23	05:13:30.0	XRT_Custom_418_OG [0x1a2]						
2010/09/23	05:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/09/23	05:57:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	05:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	05:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/09/23	05:57:30.0	AOCS_Ore-point_Start_2_OG [0x098]		5	02-76	00 00 00 00 00		
2010/09/23	05:57:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM					
2010/09/23	05:57:48.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/09/23	05:57:48.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/09/23	05:57:50.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/09/23	05:57:50.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/23	06:00:26.0	XRT_FLD_RESET_412_OG [0x19c]						
2010/09/23	06:00:26.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/23	06:00:28.0	XRT_QT_PROG_SET_405_OG [0x195]						
2010/09/23	06:00:28.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2010/09/23	06:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
2010/09/23	06:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/23	06:07:24.0	XRT_CTRL_MANU_400_OG [0x190]						
2010/09/23	06:07:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/23	06:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
2010/09/23	06:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/23	06:07:30.0	AOCS_Ore-point_Start_4_OG [0x09a]						

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2010/09/23	06:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2010/09/23	06:07:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2010/09/23	06:07:50.0	XRT_AEC_RESET_442_OG [0x1ba]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2010/09/23	06:07:52.0	XRT_ARS_DIS_420_OG [0x1a4]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/09/23	06:10:26.0	XRT_QT_PROG_SET_446_OG [0x1be]		2	07-F0	c4	0b			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0b			
2010/09/23	06:10:28.0	XRT_FL_PROG_SET_431_OG [0x1af]		2	07-F0	c5	01			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	01			
2010/09/23	06:10:30.5	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/23	06:25:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/23	06:25:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/23	06:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/23	06:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/23	06:51:00.0	XRT_Custom_418_OG [0x1a2]								
2010/09/23	06:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/23	08:06:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/23	08:06:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/23	08:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/23	08:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/23	08:28:00.0	XRT_Custom_418_OG [0x1a2]								
2010/09/23	08:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/23	09:46:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/23	09:46:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/23	09:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/23	09:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/23	10:02:30.0	XRT_Custom_418_OG [0x1a2]								
2010/09/23	10:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/23	10:51:35.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/23	10:52:00.0	AOCS_Ore-point_Start_2_OG [0x098]		5	02-76	00	00	00	00	00
		AOCU_NM		5	02-76	00	00	00	00	00