

XRT Timeline to be uploaded on 2010/09/23

Period: 2010/09/23 10:42:00 - 2010/09/28 10:32:00

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Normal mode

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XOB #1813: AR Standard-B(Morphology) with PFB FW1=Open Ti/Poly 384x384 at 1064 1048-30sec-cad										
Term	Pointing (x, y)	Comment								
09/23 10:55:00 - 09/23 12:00:01	Track (173.5, -589.6) ^{Ⓢ 09/23 10:52:00}	# OP start + 10min, HOP 126 and 166 over about 8 -12 officially. AR 11108.								
09/24 06:13:00 - 09/24 12:00:00	Track (312.5, -584.0) ^{Ⓢ 09/24 06:10:00}	# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.								
09/25 05:46:30 - 09/25 07:41:30	Track (468.5, -573.2) ^{Ⓢ 09/25 05:43:30}	# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.								
PROG= 11 Inf.-time(s)										
└─ Subr= 2 1-time(s) 2.0sec										
└─ Seqn= 19 1-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 20 4-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Subr= 1 1-time(s) 2.0sec										
└─ Seqn= 71 90-time(s) 30.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 1 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 2 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 3 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1812: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 225s cad										
Term	Pointing (x, y)	Comment								
09/23 12:03:07 - 09/23 17:59:54	Track (173.5, -589.6) ^{Ⓢ 09/23 10:52:00}	# OP start + 10min, HOP 126 and 166 over about 8 -12 officially. AR 11108.								
09/24 02:13:00 - 09/24 05:59:54	Track (166.7, -582.5) ^{Ⓢ 09/24 02:10:00}	* AR 11108								
09/24 12:03:06 - 09/24 14:59:54	Track (312.5, -584.0) ^{Ⓢ 09/24 06:10:00}	# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.								
09/25 02:16:00 - 09/25 05:33:24	Track (340.5, -575.2) ^{Ⓢ 09/25 02:13:00}	* AR 11108								
PROG= 17 Inf.-time(s)										
└─ Subr= 1 1-time(s) 2.0sec										
└─ Seqn= 19 1-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 20 4-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec										
└─ Seqn= 63 20-time(s) 225.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 1 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 1 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 2 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 2 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95 3 3 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95 3 3 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)										
Term	Pointing (x, y)	Comment								
09/23 18:03:00 - 09/23 18:09:54	Fixed (0.0, 0.0)	synoptic								
09/24 06:03:00 - 09/24 06:09:54	Fixed (0.0, 0.0)	synoptic								
09/24 18:06:00 - 09/24 18:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min								
09/25 05:36:30 - 09/25 05:43:24	Fixed (0.0, 0.0)	synoptic, shifted -26.5 min								
PROG= 03 1-time(s)										
└─ Subr= 1 1-time(s) 12.0sec										
└─ Seqn= 7 1-time(s) 4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM 0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1818: HOP101 - Ti/poly 5.8s, G-band - 512x512-AEC0-ROI centered-60 sec												
Term	Pointing (x, y)		Comment									
09/23 18:13:00 - 09/24 02:09:54	Fixed (-11.0, 890.0)		# HOP 101, N pole helioseismology, and HOP 175, waves in polar holes.									
09/24 18:16:00 - 09/25 00:58:15	Fixed (-11.0, 890.0)		# HOP 101 and 175, N pole.									
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 23 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec												
└─ Subr= 2 15-time(s) 2.0sec												
└─ Seqn= 58 2-time(s) 60.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 5.66s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #181A: QS waves - Ti/poly 5.8s, G-band - 384x384-AEC0-ROI centered-40 sec												
Term	Pointing (x, y)		Comment									
09/24 15:03:00 - 09/24 18:02:54	Fixed (0.0, 0.0)		* HOP 169, MHD waves with Sac Peak.									
PROG= 19 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 23 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec												
└─ Subr= 2 40-time(s) 2.0sec												
└─ Seqn= 65 1-time(s) 40.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 5.66s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1819: Test images for FW1 move - Ti/poly (2 sec)- 2048x2048 - 2x2												
Term	Pointing (x, y)		Comment									
09/25 08:33:00 - 09/25 08:36:52	Track (468.5, -573.2) @ 09/25 05:43:30		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
09/25 08:48:10 - 09/25 09:00:30	Track (468.5, -573.2) @ 09/25 05:43:30		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 59 2-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1802: FW1 Test, med-Al+Ti/poly, 16s AEC0, 384FOV												
Term	Pointing (x, y)		Comment									
09/25 08:40:02 - 09/25 08:45:00	Track (468.5, -573.2) @ 09/25 05:43:30		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
PROG= 13 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 1 5-time(s) 25.0sec												
└─ med-Al/Ti-poly med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

* * * * * **Flare mode** * * * * *

XOB #1817: Flare Obs for use with Ti-poly 1x1 PFB images (FW1=Open)												
Term	Pointing (x, y)		Comment									
09/23 10:55:00 - 09/23 12:00:01	Track (173.5, -589.6) @ 09/23 10:52:00		# OP start + 10min, HOP 126 and 166 over about 8 -12 officially. AR 11108.									
09/23 12:03:07 - 09/23 17:59:54	Track (173.5, -589.6) @ 09/23 10:52:00		# OP start + 10min, HOP 126 and 166 over about 8 -12 officially. AR 11108.									
09/23 18:13:00 - 09/24 02:09:54	Fixed (-11.0, 890.0)		# HOP 101, N pole helioseismology, and HOP 175, waves in polar holes.									
09/24 02:13:00 - 09/24 05:59:54	Track (166.7, -582.5) @ 09/24 02:10:00		* AR 11108									
09/24 06:13:00 - 09/24 12:00:00	Track (312.5, -584.0) @ 09/24 06:10:00		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
09/24 12:03:06 - 09/24 14:59:54	Track (312.5, -584.0) @ 09/24 06:10:00		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
09/24 15:03:00 - 09/24 18:02:54	Fixed (0.0, 0.0)		* HOP 169, MHD waves with Sac Peak.									
09/24 18:16:00 - 09/25 00:58:15	Fixed (-11.0, 890.0)		# HOP 101 and 175, N pole.									
09/25 02:16:00 - 09/25 05:33:24	Track (340.5, -575.2) @ 09/25 02:13:00		* AR 11108									
09/25 05:46:30 - 09/25 07:41:30	Track (468.5, -573.2) @ 09/25 05:43:30		# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.									
PROG= 01 1-time(s)												
└─ Subr= 1 2-time(s) 620.0sec												
└─ Seqn= 67 30-time(s) 20.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└─ Subr= 2 8-time(s) 600.0sec												
└─ Seqn= 67 5-time(s) 120.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3		1-time(s)	2.0sec									
Seqn= 89		40-time(s)	600.0sec									
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

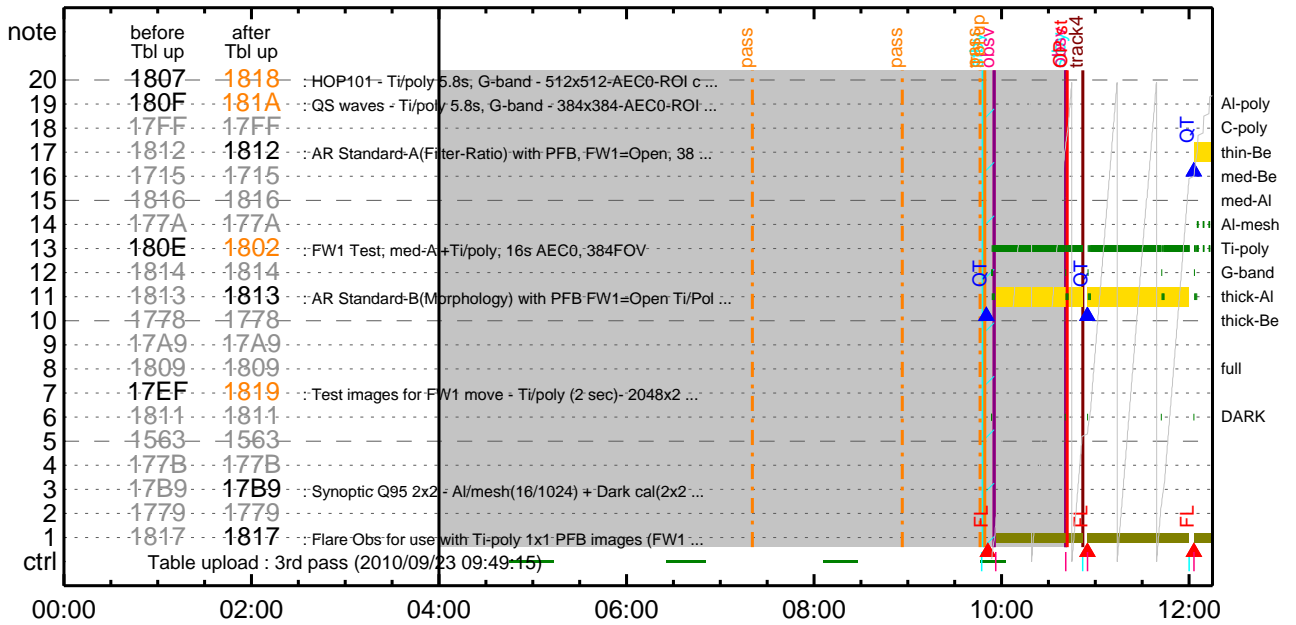
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Flare Detection

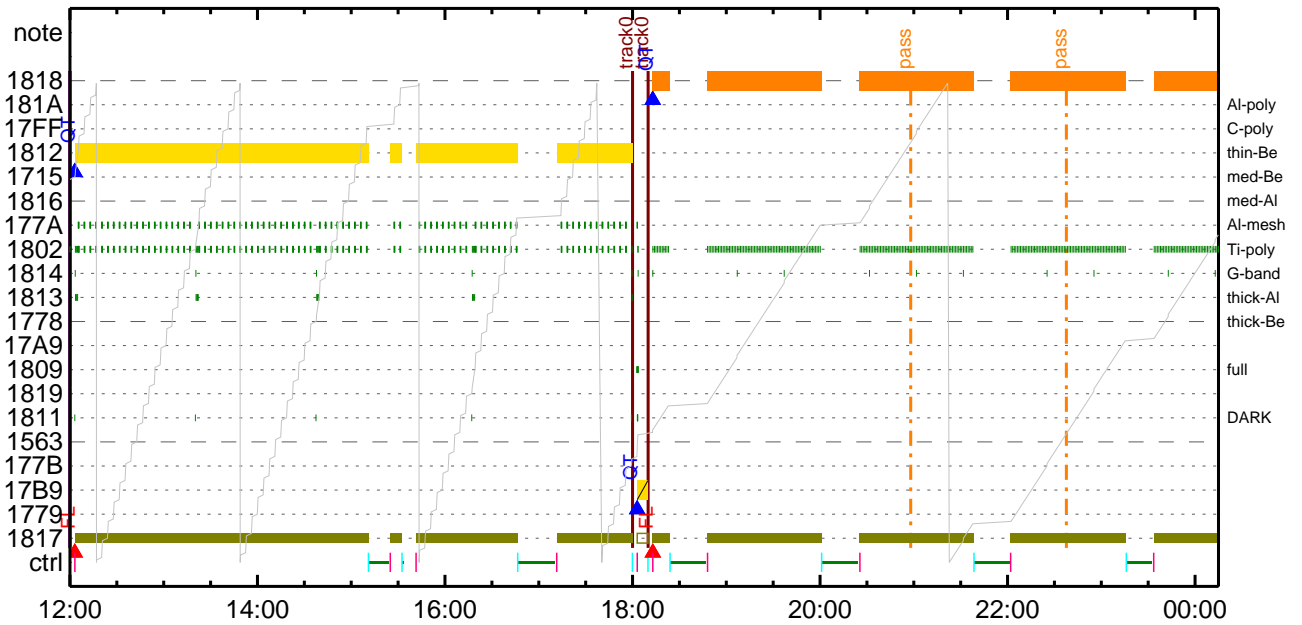
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FLD Patrol												
Term		Pointing (x, y)					Comment					
09/23 18:10:16 - 09/24 06:00:16		Fixed (-11.0, 890.0)					# HOP 101, N pole helioseismology, and HOP 175, waves in polar holes.					
09/24 06:10:16 - 09/24 18:03:16		Track (312.5, -584.0) ^{® 09/24 06:10:00}					# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.					
09/24 18:13:16 - 09/25 05:33:46		Fixed (-11.0, 890.0)					# HOP 101 and 175, N pole.					
09/25 05:43:46 - 09/25 08:30:22		Track (468.5, -573.2) ^{® 09/25 05:43:30}					# AR 11108, with HOPs 126 and 166 over about 8 - 12 UT.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

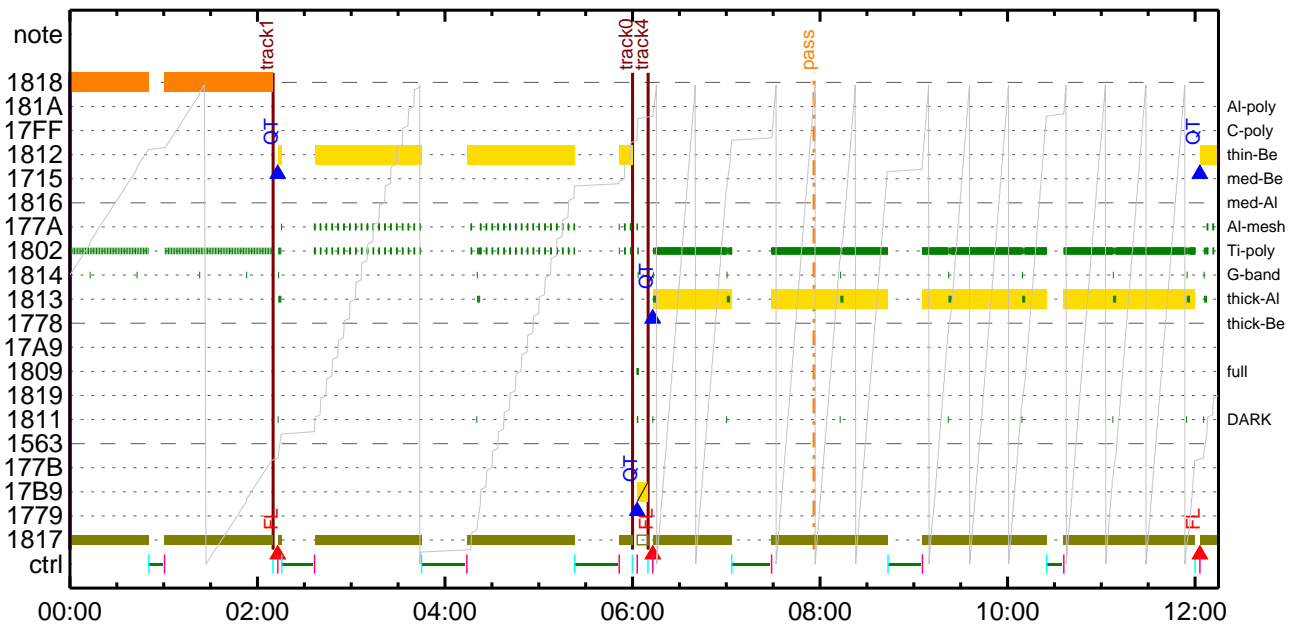
CMDI #0504 2010/09/23



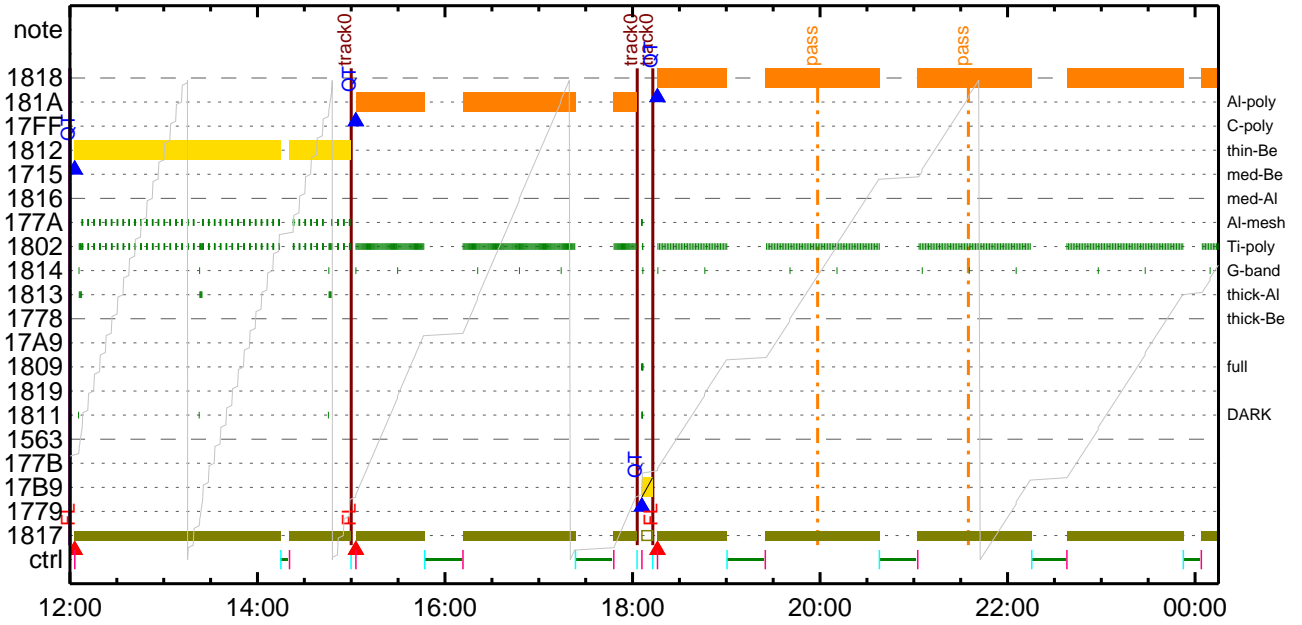
CMDI #0504 2010/09/23



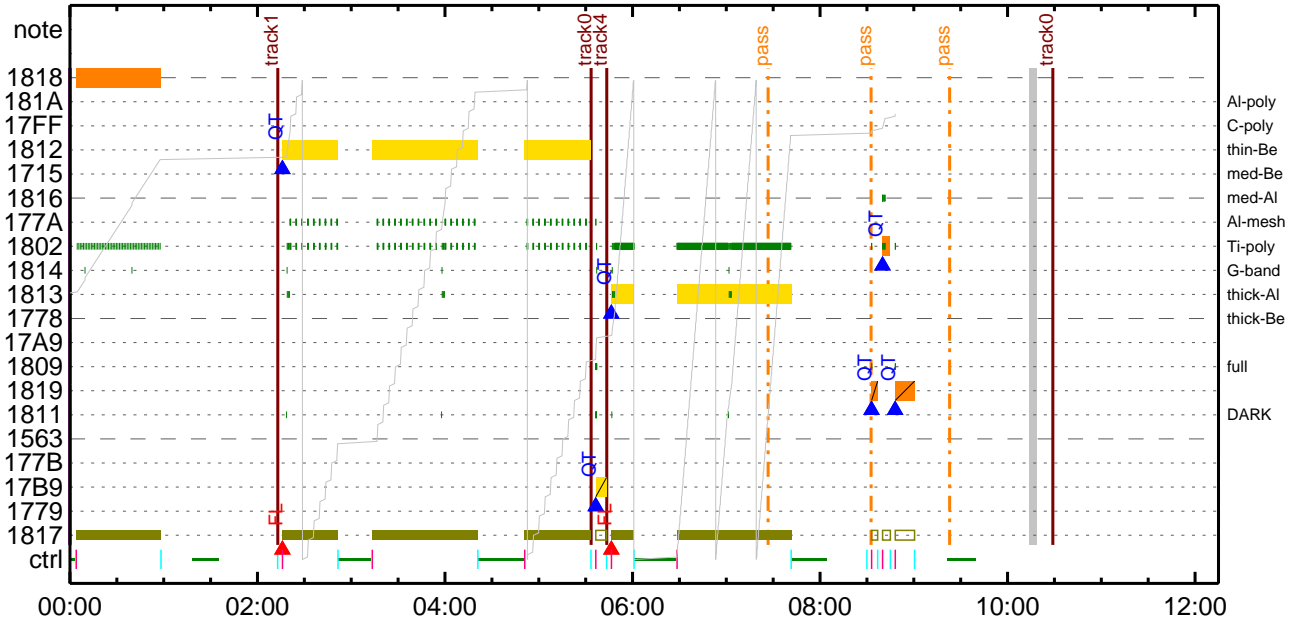
CMDI #0504 2010/09/24



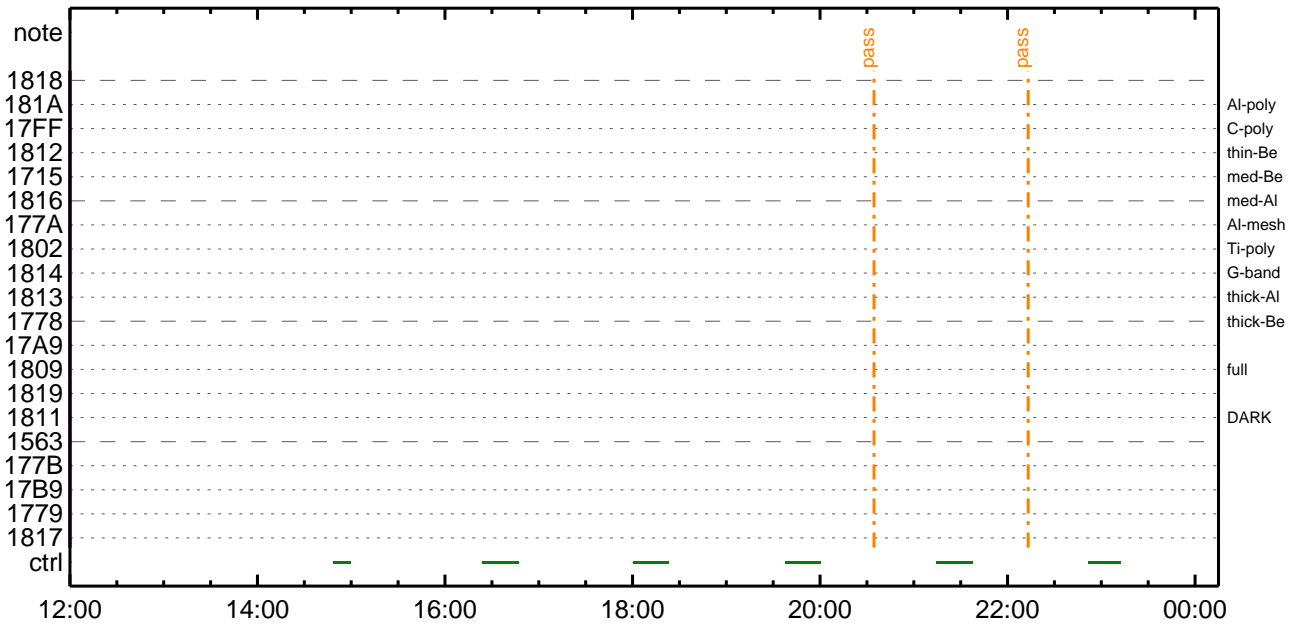
CMDI #0504 2010/09/24



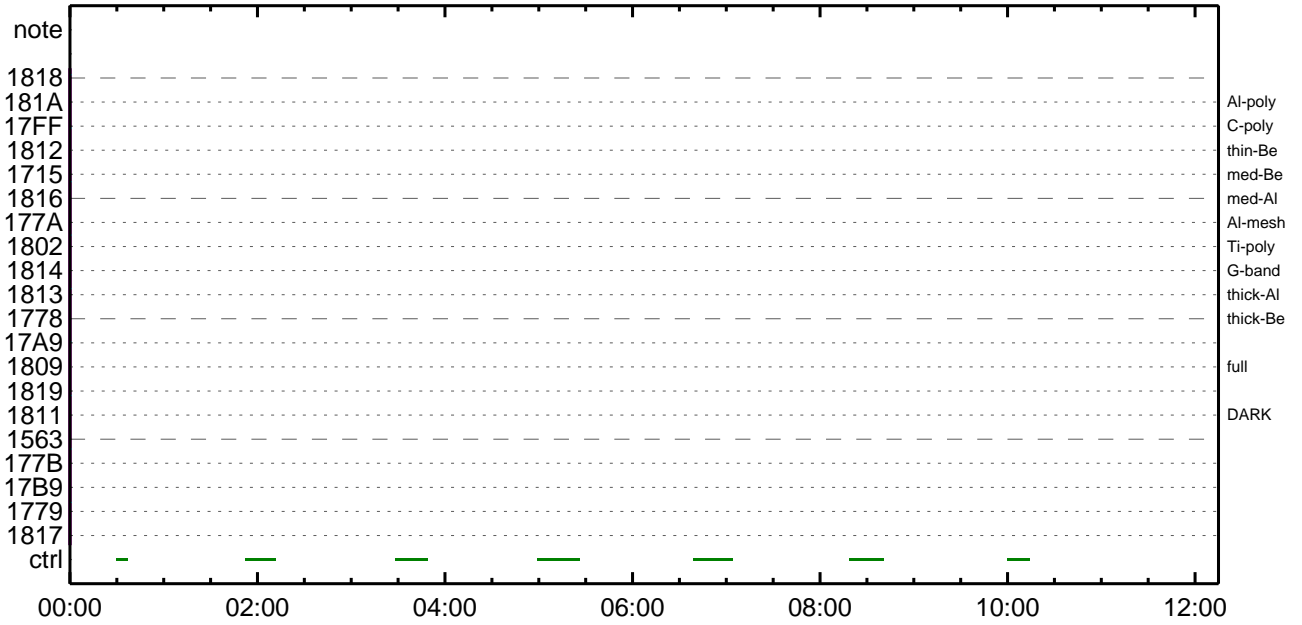
CMDI #0504 2010/09/25



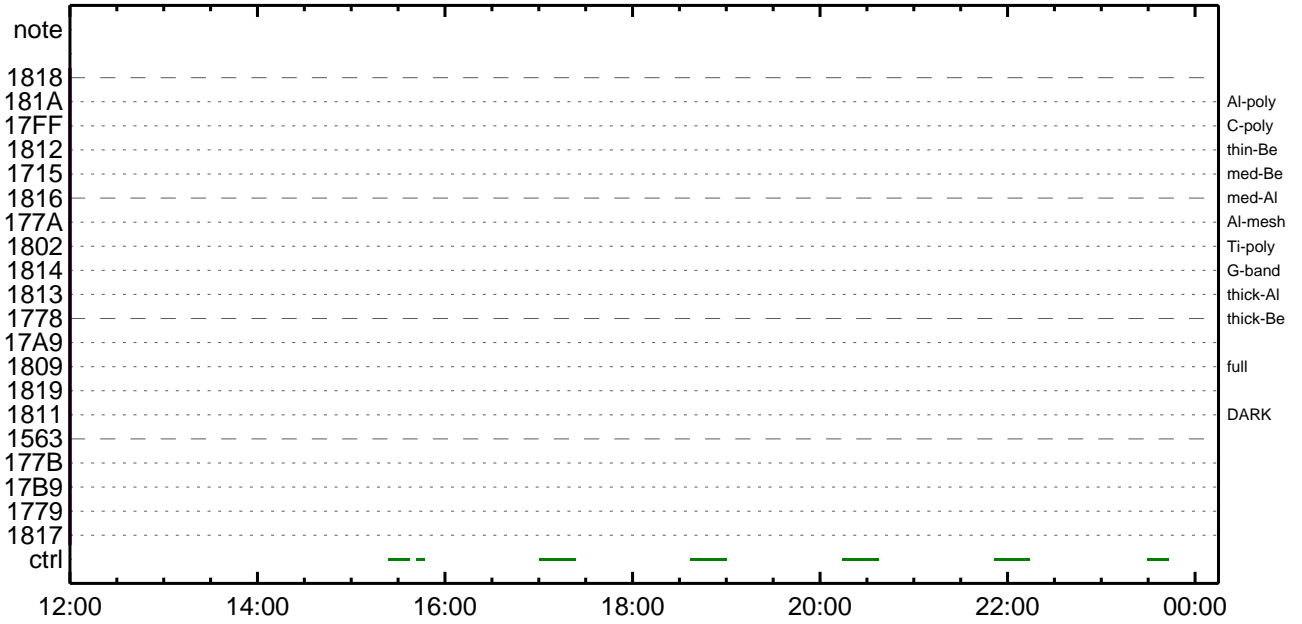
CMDI #0504 2010/09/25



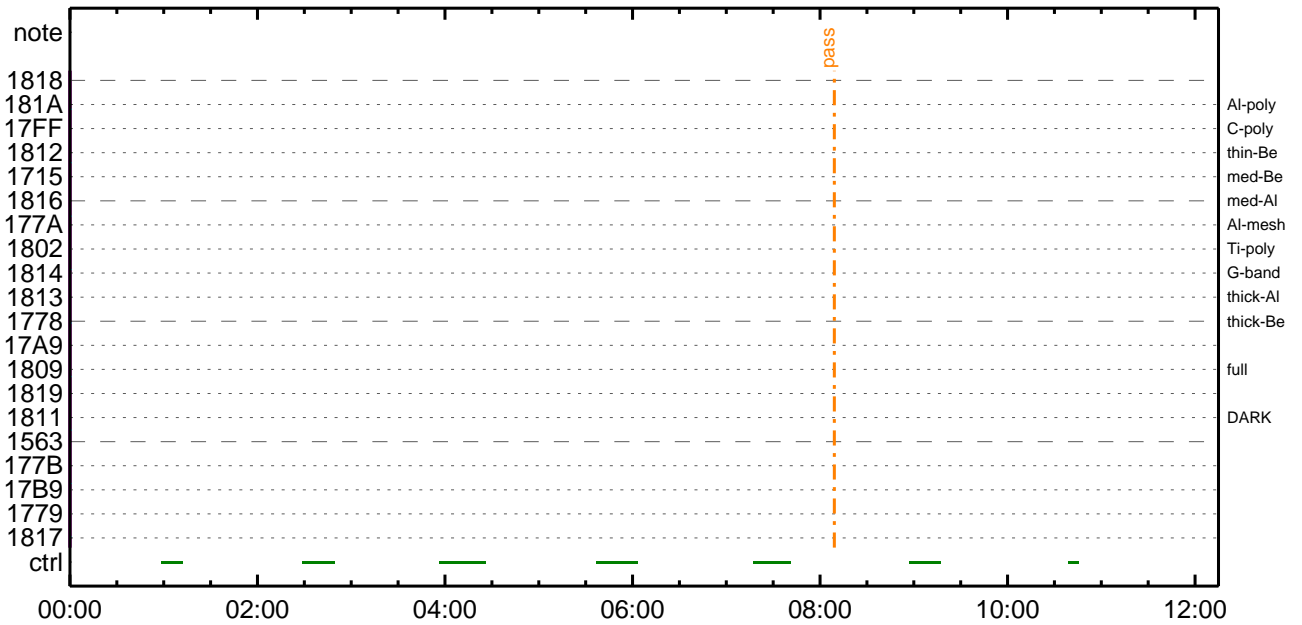
CMDI #0504 2010/09/26



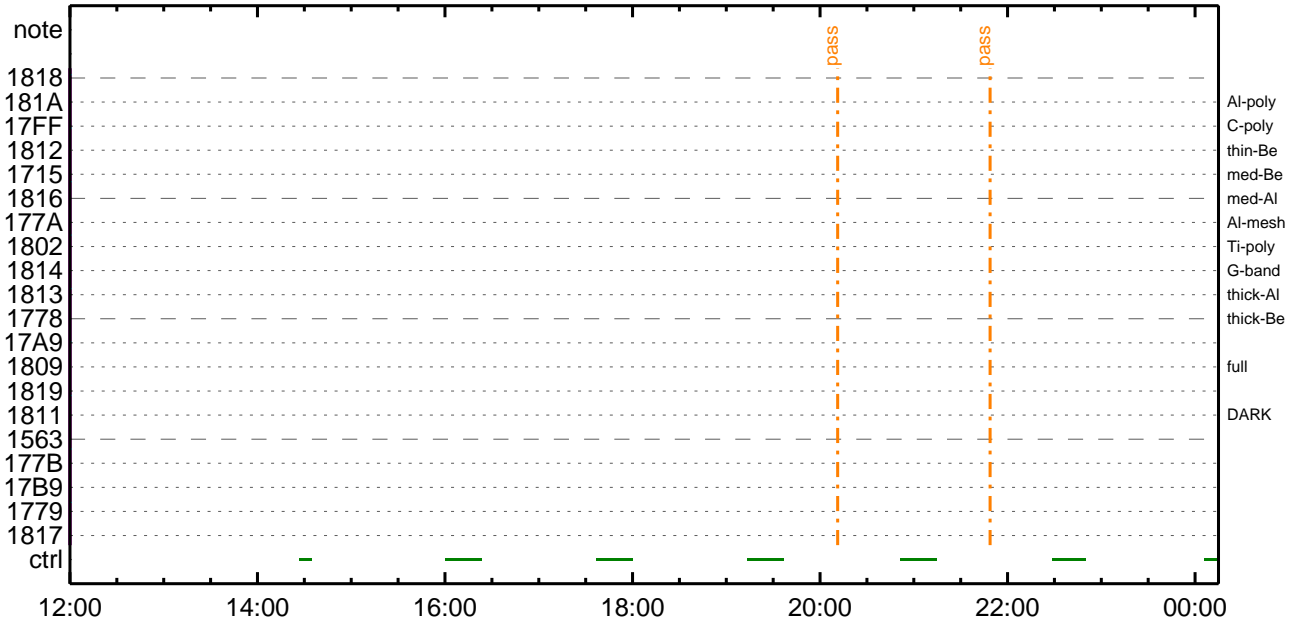
CMDI #0504 2010/09/26



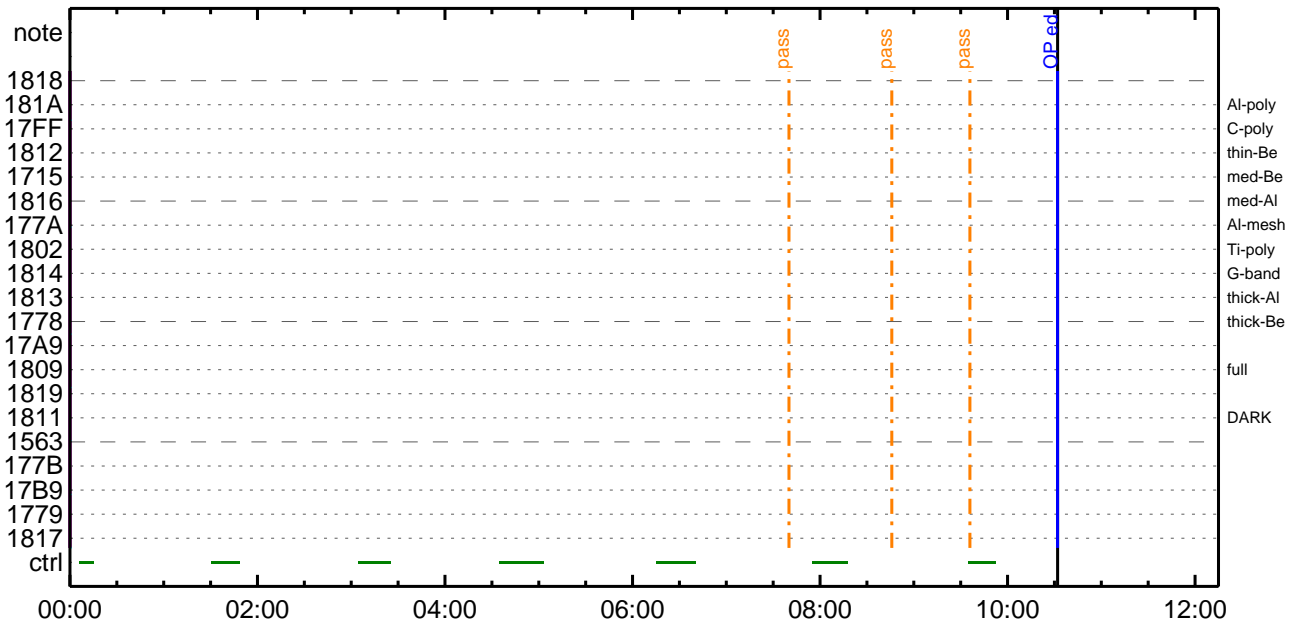
CMDI #0504 2010/09/27



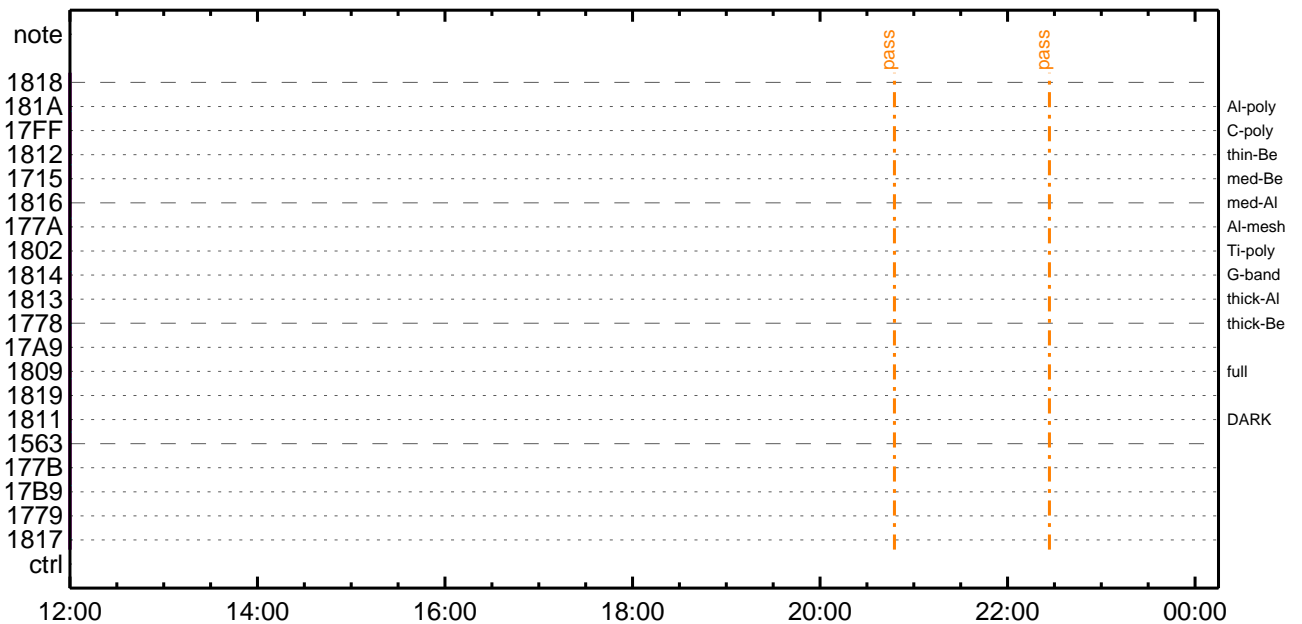
CMDI #0504 2010/09/27



CMDI #0504 2010/09/28



CMDI #0504 2010/09/28



(a) Spacecraft Operation Procedure (real-commands)

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main-602 2010-09-23 13:23:55 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY§YÁY~¼Á»Ü;ã
0005 C.
0006 C. YÁY§;¼Y³YFÝÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÁð•μ°È»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈ%μ•íÍÈ;ÈÈÈ%ÇÖñ•ñç¼l¹çñÍ;çÁ®, ùñ¹ñÈñðñÇÁ+ç®ñ•ñÈñññ³ñÈ; f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®μ;ON
0016 C. *****
0017 C. ç" °ÈÀ, Í×ÈYñáLOSñðçñÍ»p´Öñð¹íí, ñ•; çÉÖÍ×ñÈXÁÓONñí¹ÖñÈñ¹ñÈñññ³ñÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÁY~¼ÓÁÖñ-ñÁÁÈñ•ñç; ç°È²¼ñÍ°ÈÀ, ¼È%çñð¼Á¹Öñ¹ñÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç" RESTART; ÈPT1; Èñ•ñç¼ñ¼l¹çñÍ; ç°È²¼ñÍ¼Á¹Öññ°; çDCBC-150ñçÈñà; f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÙÁØ; ÈÁ•Á°²óÈð; È, áñí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ñ-¼«È°Áá»ññ•ñç; á; ç°È²¼ñð¼Á¹Öñ¹ñÈ; f
0055 C. YçYÓYÉYÈÁÙÁØñáÁ•Á°²óÈðñ-¼áñ¼l¹çñÍ´°Í»ñ¹ñÈñðñÇÁÖñÁ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç" RESTART; ÈPT2; Èñ•ñç¼ñ¼l¹çñÍ; ç°È²¼ñÍ¼Á¹Öññ°; çDCBC-151ñçÈñà; f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÙÁØ; ÈÁ•Á°²óÈð; È, áñí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ñ; çXÁ+ç®μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ñ;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

```



```

0194 C.
0195 +. TI 2010-09-23 10:41:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄè%íññîîŷÄŷ§ŷÄŷ-¹àiÛ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]        EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî°è¹çª²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Ëòðîã¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]        EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-09-23 10:41:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-09-23 10:41:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2010-09-23 10:41:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîîî»ò¼ŷòÈÄð¹òèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèòÈ¼¼ª¼Ä»Ûò¹òè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îññîîË´Ø¹òèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ðäLOSŷÄŷŷŷÄŷ-¼ª»Û;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-604 2010-09-23 13:23:55 103 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøøøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 85 83 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0061 BC (c4 0b)
0062 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0063 BC (c5 01)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_ENA
0067 BC (d8)
0068 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0069 BC (c8)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0078 BC (c0)
0079 +. TI 2010-09-23 10:41:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 +. TI 2010-09-23 10:41:04.0
0083 DC 07-F0 MDP_XRT_CTRL_AUTO
0084 BC (c0)
0085 . C. ----- Success Verify ? OK / NG ____
0086 C.
0087 C. ***** XRT END *****
0088 C.
0089 . C. ***** MDP `ûÃíøí»ò¼YøÈÁøø¹øèDCBC•x²è *****
0090 C. (¼á°íYÓYÁYÈYB¥YÉYáYçYèøÈ¼øø¼Á»Ûø¹øè)
0091 . S. DC-BC dcbc-402:DCBC
0092 (MDP_known_event)
0093 C.
0094 C.
0095 . C. ***** YDY¹.İ Daily±¿ÍÑøÈ´Øø¹øèDCBC•x²è *****
```

```
0096 . S. DC-BC dcbc-153:DCBC
0097 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0098 C.
0099 C.
0100 . C. ;ãLOS¥Á¥$¥Ã¥-¼Á»Û;ä
0101 C.
0102 . C. ***** LOS *****
0103 C.
```

*** OP Sequence for XRT ***

2010/09/23	10:51:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	10:51:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/23	10:52:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2010/09/23	10:52:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/09/23	10:52:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/09/23	10:52:20.0	XRT_ARS_DIS_415_OG [0x19f]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/23	10:54:56.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2010/09/23	10:54:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/09/23	10:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	12:00:01.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	12:00:03.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/09/23	12:00:23.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/09/23	12:00:25.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/09/23	12:00:27.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/09/23	12:00:29.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/23	12:03:03.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2010/09/23	12:03:05.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/09/23	12:03:07.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	15:11:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	15:11:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/23	15:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/23	15:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/23	15:24:00.0	XRT_Custom_418_OG [0x1a2]			
2010/09/23	15:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	15:32:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	15:32:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/23	15:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/23	15:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/23	15:40:30.0	XRT_Custom_418_OG [0x1a2]			
2010/09/23	15:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	16:46:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	16:46:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/23	16:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/09/23	16:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/09/23	17:10:30.0	XRT_Custom_418_OG [0x1a2]			
2010/09/23	17:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/09/23	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/09/23	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/09/23	18:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/09/23	18:00:20.0	XRT_ARS_DIS_415_OG [0x19f]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/09/23	18:02:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/09/23	18:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/09/23	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/09/23	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/09/23	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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XRT_OGLIST_0504.chk

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2010/09/23	18:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	b0	e6	00	fd
2010/09/23	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/09/23	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/09/23	18:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2010/09/23	18:10:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/09/23	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/23	18:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	14		
2010/09/23	18:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		
2010/09/23	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/23	18:24:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/23	18:24:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/23	18:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/09/23	18:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/09/23	18:47:00.0	XRT_Custom_418_OG [0x1a2]							
2010/09/23	18:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/23	20:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/23	20:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/23	20:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/09/23	20:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/09/23	20:24:30.0	XRT_Custom_418_OG [0x1a2]							
2010/09/23	20:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/23	21:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/23	21:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/23	21:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/09/23	21:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/09/23	22:01:00.0	XRT_Custom_418_OG [0x1a2]							
2010/09/23	22:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/23	23:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/23	23:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/23	23:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/09/23	23:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/09/23	23:32:30.0	XRT_Custom_418_OG [0x1a2]							
2010/09/23	23:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/24	00:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/24	00:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/24	00:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/09/24	00:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/09/24	00:59:30.0	XRT_Custom_418_OG [0x1a2]							
2010/09/24	01:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/09/24	02:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/09/24	02:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/09/24	02:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/09/24	02:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/09/24	02:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/09/24	02:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2010/09/24	02:10:22.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/09/24	02:10:24.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/09/24	02:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	11		
2010/09/24	02:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		

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2010/09/24	02:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	02:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	02:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	02:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/24	02:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/24	02:35:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/24	02:36:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	03:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	03:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	03:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/24	03:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/24	04:13:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/24	04:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	05:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	05:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	05:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/24	05:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/24	05:50:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/24	05:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/09/24	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/09/24	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/09/24	06:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/09/24	06:00:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/24	06:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/09/24	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/09/24	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2010/09/24	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/24	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/24	06:10:20.0	XRT_AEC_RESET_442_OG [0x1ba]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/24	06:10:22.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	06:10:24.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/24	06:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2010/09/24	06:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2010/09/24	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	07:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	07:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	07:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/24	07:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/24	07:28:00.5	XRT_Custom_418_OG [0x1a2]					
2010/09/24	07:29:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/24	08:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/24	08:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/24	08:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/24	08:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					

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2010/09/24	09:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/24	09:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	10:25:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	10:25:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/24	10:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/24	10:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/24	10:35:00.0	XRT_Custom_418_OG [0x1a2]						
2010/09/24	10:36:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	12:00:00.5	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	12:00:02.5	XRT_FOCUS_POSITION_409_OG [0x199]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/24	12:00:22.5	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/24	12:00:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/24	12:00:26.5	XRT_AEC_RESET_441_OG [0x1b9]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/09/24	12:00:28.5	XRT_ARS_DIS_420_OG [0x1a4]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/24	12:03:02.5	XRT_QT_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2010/09/24	12:03:04.5	XRT_FL_PROG_SET_431_OG [0x1af]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2010/09/24	12:03:06.5	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	14:15:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	14:15:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/24	14:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/24	14:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/24	14:19:30.0	XRT_Custom_418_OG [0x1a2]						
2010/09/24	14:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	14:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/09/24	15:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2010/09/24	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/09/24	15:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/09/24	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/09/24	15:00:22.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/09/24	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/24	15:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2010/09/24	15:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2010/09/24	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	15:47:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	15:47:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/24	15:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/24	15:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/24	16:10:30.0	XRT_Custom_418_OG [0x1a2]						
2010/09/24	16:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	17:23:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	17:23:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/09/24	17:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/09/24	17:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/09/24	17:47:00.0	XRT_Custom_418_OG [0x1a2]						
2010/09/24	17:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/09/24	18:02:54.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/09/24	18:02:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/09/24	18:03:00.0	AOCS_ORe-point_Start_2_OG [0x098]						

2010/09/24	18:03:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2010/09/24	18:03:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2010/09/24	18:03:20.0	XRT_ARS_DIS_415_OG [0x19f]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/09/24	18:05:56.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	18:05:58.0	XRT_QT_PROG_SET_407_OG [0x197]		2	07-F0	c4	03			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	03			
2010/09/24	18:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	18:12:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/24	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2010/09/24	18:13:00.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	00	b0	e6	00	fd
		AOCU_NM		5	02-76	00	b0	e6	00	fd
2010/09/24	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2010/09/24	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2010/09/24	18:13:20.0	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2010/09/24	18:13:22.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/09/24	18:15:54.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	18:15:56.0	XRT_QT_PROG_SET_410_OG [0x19a]		2	07-F0	c4	14			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	14			
2010/09/24	18:15:58.0	XRT_FL_PROG_SET_431_OG [0x1af]		2	07-F0	c5	01			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	01			
2010/09/24	18:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	19:00:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/24	19:00:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	19:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/24	19:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/24	19:24:00.0	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	19:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	20:38:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/24	20:38:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	20:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/24	20:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/24	21:01:30.0	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	21:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	22:15:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/24	22:15:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	22:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/24	22:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/24	22:37:00.5	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	22:38:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/24	23:52:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/24	23:52:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/09/24	23:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/09/24	23:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/09/25	00:03:00.0	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/25	00:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/09/25	00:58:15.5	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/25	02:12:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/09/25	02:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2010/09/25	02:13:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		5	02-76	01	00	00	00	00
		AOCU_NM		5	02-76	01	00	00	00	00
2010/09/25	02:13:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2010/09/25	02:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2010/09/25	02:13:20.0	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				

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2010/09/25	02:13:22.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	02:13:24.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/25	02:15:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2010/09/25	02:15:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2010/09/25	02:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	02:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	02:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	02:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/25	02:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/25	03:12:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/25	03:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	04:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	04:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	04:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/25	04:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/25	04:50:00.0	XRT_Custom_418_OG [0x1a2]					
2010/09/25	04:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	05:33:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	05:33:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/09/25	05:33:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2010/09/25	05:33:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/09/25	05:33:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/09/25	05:33:50.5	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/25	05:36:26.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	05:36:28.5	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2010/09/25	05:36:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	05:43:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	05:43:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2010/09/25	05:43:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00	00
2010/09/25	05:43:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/09/25	05:43:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/09/25	05:43:50.0	XRT_AEC_RESET_442_OG [0x1ba]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/09/25	05:43:52.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	05:43:54.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/09/25	05:46:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2010/09/25	05:46:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2010/09/25	05:46:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	06:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	06:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	06:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/25	06:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/25	06:27:30.0	XRT_Custom_418_OG [0x1a2]					
2010/09/25	06:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/09/25	07:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/09/25	07:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/09/25	07:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/09/25	07:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/09/25	08:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2010/09/25	08:30:02.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/09/25	08:30:22.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/09/25	08:30:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/09/25	08:30:26.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/09/25	08:32:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/09/25	08:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/09/25	08:36:52.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/09/25	08:36:54.0	XRT_STANDBY_425_OG [0x1a9]							
		XRT_STBY	2	07-F8	03	01			
2010/09/25	08:36:56.0	XRT_DIAGNOSTIC_426_OG [0x1aa]							
		XRT_DIAGNOSTIC	2	07-F8	03	07			
2010/09/25	08:36:58.0	XRT_FSA_CONTROL_427_OG [0x1ab]							
		XRT_FW1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/09/25	08:37:00.5	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/09/25	08:37:03.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:05.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:08.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:10.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:13.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:15.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:18.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:20.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:23.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:25.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:28.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:30.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:33.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:35.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:38.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:40.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:43.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:45.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:48.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:50.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:53.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:55.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:37:58.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:00.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:03.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:05.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:08.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:10.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:13.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:15.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:18.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:20.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:23.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:25.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:28.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/09/25	08:38:30.5	XRT_FSA_CONTROL_429_OG [0x1ad]							

2010/09/25	08:38:33.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:35.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:38.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:40.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:43.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:45.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:48.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:50.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:53.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:55.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:38:58.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:00.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:03.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:05.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:08.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:10.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:13.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:15.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:18.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:20.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:23.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:25.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/09/25	08:39:28.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:30.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/09/25	08:39:33.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:39:35.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/09/25	08:39:38.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/09/25	08:39:40.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/09/25	08:39:40.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/09/25	08:40:00.0	XRT_QT_PROG_SET_433_OG [0x1b1]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/09/25	08:40:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2010/09/25	08:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/09/25	08:45:02.0	XRT_FSA_CONTROL_427_OG [0x1ab]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/09/25	08:45:04.5	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/09/25	08:45:07.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/09/25	08:45:09.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:45:12.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:45:14.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:45:17.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/09/25	08:45:19.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:22.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:24.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:27.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:29.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:32.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:34.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/09/25	08:45:37.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00

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2010/09/25	08:47:37.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
			XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/09/25	08:47:39.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/09/25	08:47:42.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/09/25	08:47:44.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/09/25	08:48:04.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2010/09/25	08:48:06.0	XRT_STANDBY_425_OG [0x1a9]	XRT_STBY	2	07-F8	03	01		
2010/09/25	08:48:08.0	XRT_OPERATE_435_OG [0x1b3]	XRT_OPERATE	2	07-F8	03	02		
2010/09/25	08:48:10.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/09/25	09:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/09/25	10:29:00.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00