

XRT Timeline to be uploaded on 2010/10/02

Period: 2010/10/02 10:30:00 - 2010/10/07 10:40:00

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Normal mode

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XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384												
Term	Pointing (x, y)							Comment				
10/02 10:43:00 - 10/02 18:39:54	Fixed (-11.0, 925.0)	# OP start + 10min HOP81 @ N-Pole										
10/03 10:03:00 - 10/03 17:39:24	Fixed (-11.0, 925.0)	HOP81 N-Pole										
10/04 10:23:00 - 10/04 17:59:54	Fixed (-11.0, 925.0)	HOP81 @ N-Pole										
PROG= 12 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 94 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─┬─ Subr= 2 30-time(s) 2.0sec												
└─┬─ Seqn= 97 2-time(s) 30.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)							Comment				
10/02 18:42:58 - 10/02 18:49:54	Fixed (0.0, 0.0)	synoptic										
10/03 05:39:28 - 10/03 09:59:54	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min										
10/03 17:42:28 - 10/03 23:15:00	Fixed (0.0, 0.0)	synoptic, shifted -20.5 min										
10/04 06:00:58 - 10/04 10:19:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min										
10/04 18:02:58 - 10/04 23:00:00	Fixed (0.0, 0.0)	synoptic										
10/05 06:18:58 - 10/05 06:30:00	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min										
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 7 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─┬─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─┬─ Seqn= 8 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─┬─ Seqn= 4 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17F7: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 4min-cad												
Term	Pointing (x, y)							Comment				
10/02 18:53:02 - 10/02 23:00:00	Track (795.7, 272.9) ^{Ⓢ 10/02 18:50:00}	Track AR11109; HOP171 23-01UT										
10/03 01:03:06 - 10/03 05:36:24	Track (795.7, 272.9) ^{Ⓢ 10/02 18:50:00}	Track AR11109; HOP171 23-01UT										
PROG= 15 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 17 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─┬─ Seqn= 6 4-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 86 10-time(s) 240.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1821: AR Standard-B(Morphology) no PFB FW1=Open + Ti/Poly 512x512 at 1064 1048-30sec-cad												
Term	Pointing (x, y)							Comment				
10/02 23:03:04 - 10/03 01:00:00	Track (795.7, 272.9) ^{Ⓢ 10/02 18:50:00}	Track AR11109; HOP171 23-01UT										
PROG= 16 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 17 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─┬─ Seqn= 6 4-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec												

L Seqn= 98 90-time(s) 30.0sec													
L Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1813: AR Standard-B(Morphology) with PFB FW1=Open + Ti/Poly 384x384 at 1064 1048-30sec-cad

Term	Pointing (x, y)	Comment
10/03 23:18:06 - 10/04 01:00:00	Track (-144.1, 266.8) @ 10/03 23:00:00	HOP171 AR11111 (23-01UT)
10/04 23:03:06 - 10/05 01:00:30	Track (16.2, 266.3) @ 10/04 18:10:00	Continue to track AR11111; HOP171 23-01 UT

PROG= 11 Inf.-time(s)													
L Subr= 2 1-time(s) 2.0sec													
L Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
L Seqn= 20 4-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
L Subr= 1 1-time(s) 2.0sec													
L Seqn= 71 90-time(s) 30.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1812: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 225s cad

Term	Pointing (x, y)	Comment
10/04 01:03:06 - 10/04 05:57:54	Track (-144.1, 266.8) @ 10/03 23:00:00	HOP171 AR11111 (23-01UT)

PROG= 17 Inf.-time(s)													
L Subr= 1 1-time(s) 2.0sec													
L Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
L Seqn= 20 4-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
L Subr= 2 1-time(s) 2.0sec													
L Seqn= 63 20-time(s) 225.0sec													
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1819: Test images for FW1 move - Ti/poly (2 sec)- 2048x2048 - 2x2

Term	Pointing (x, y)	Comment
10/05 06:33:00 - 10/05 06:33:19	Track (118.9, 267.6) @ 10/05 06:26:00	Continue to track AR11111
10/05 06:41:54 - 10/05 06:56:30	Track (118.9, 267.6) @ 10/05 06:26:00	Continue to track AR11111

PROG= 07 1-time(s)													
L Subr= 1 1-time(s) 2.0sec													
L Seqn= 59 2-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1802: FW1 Test, med-Al+Ti/poly, 16s AEC0, 384FOV

Term	Pointing (x, y)	Comment
10/05 06:36:29 - 10/05 06:38:43	Track (118.9, 267.6) @ 10/05 06:26:00	Continue to track AR11111

PROG= 13 1-time(s)													
L Subr= 1 1-time(s) 2.0sec													
L Seqn= 1 5-time(s) 25.0sec													
med-Al/Ti-poly		med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1817: Flare Obs for use with Ti-poly 1x1 PFB images (FW1=Open)

Term	Pointing (x, y)	Comment
10/02 10:43:00 - 10/02 18:39:54	Fixed (-11.0, 925.0)	# OP start + 10min HOP81 @ N-Pole
10/02 18:53:02 - 10/02 23:00:00	Track (795.7, 272.9) @ 10/02 18:50:00	Track AR11109; HOP171 23-01UT
10/02 23:03:04 - 10/03 01:00:00	Track (795.7, 272.9) @ 10/02 18:50:00	Track AR11109; HOP171 23-01UT
10/03 01:03:06 - 10/03 05:36:24	Track (795.7, 272.9) @ 10/02 18:50:00	Track AR11109; HOP171 23-01UT
10/03 10:03:00 - 10/03 17:39:24	Fixed (-11.0, 925.0)	HOP81 N-Pole

10/03 23:18:06 - 10/04 01:00:00 Track (-144.1, 266.8) @ 10/03 23:00:00 HOP171 AR11111 (23-01UT)
 10/04 01:03:06 - 10/04 05:57:54 Track (-144.1, 266.8) @ 10/03 23:00:00 HOP171 AR11111 (23-01UT)
 10/04 10:23:00 - 10/04 17:59:54 Fixed (-11.0, 925.0) HOP81 @ N-Pole
 10/04 23:03:06 - 10/05 01:00:30 Track (16.2, 266.3) @ 10/04 18:10:00 Continue to track AR11111; HOP171 23-01 UT

PROG= 01 1-time(s)

Subr= 1 2-time(s) 620.0sec		Seqn= 67 30-time(s) 20.0sec		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 90 1-time(s) 2.0sec				Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2 8-time(s) 600.0sec		Seqn= 67 5-time(s) 120.0sec		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 90 1-time(s) 2.0sec				Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 1-time(s) 2.0sec		Seqn= 89 40-time(s) 600.0sec		Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
				Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
				Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

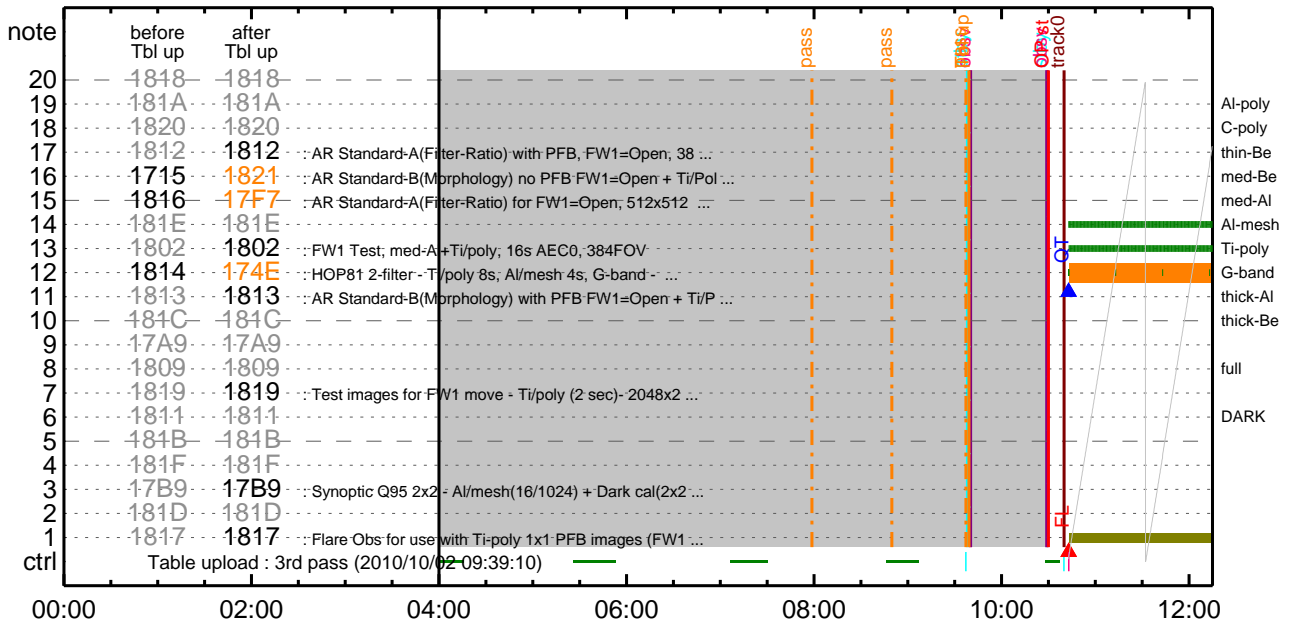
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Flare Detection

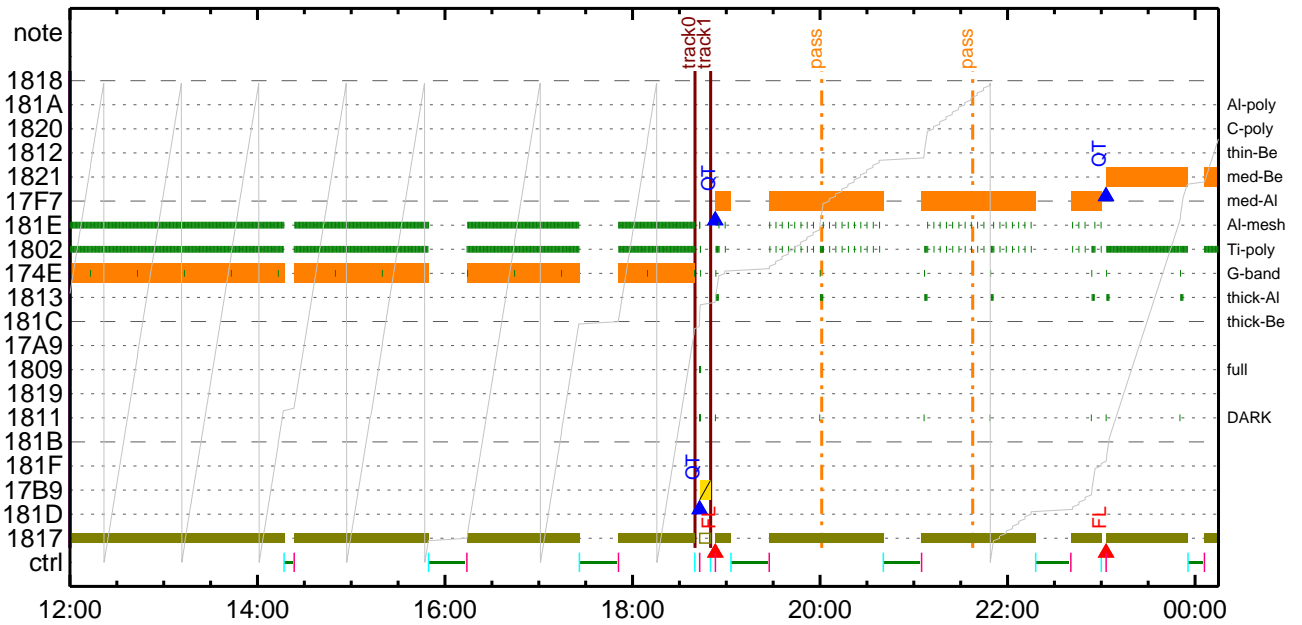
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FLD Patrol																
Term		Pointing (x, y)					Comment									
10/02 10:40:16 - 10/02 18:40:16		Fixed (-11.0, 925.0)					# OP start + 10min HOP81 @ N-Pole									
10/02 18:50:16 - 10/03 05:36:46		Track (795.7, 272.9) @ 10/02 18:50:00					Track AR11109; HOP171 23-01UT									
10/03 10:00:16 - 10/03 17:39:46		Fixed (-11.0, 925.0)					HOP81 N-Pole									
10/03 23:15:22 - 10/04 05:58:16		Track (-144.1, 266.8) @ 10/03 23:00:00					HOP171 AR11111 (23-01UT)									
10/04 10:20:16 - 10/04 18:00:16		Fixed (-11.0, 925.0)					HOP81 @ N-Pole									
10/04 23:00:22 - 10/05 06:16:16		Track (16.2, 266.3) @ 10/04 18:10:00					Continue to track AR11111; HOP171 23-01 UT									
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8ms	Obs	8x8				Q=50	30sec		
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

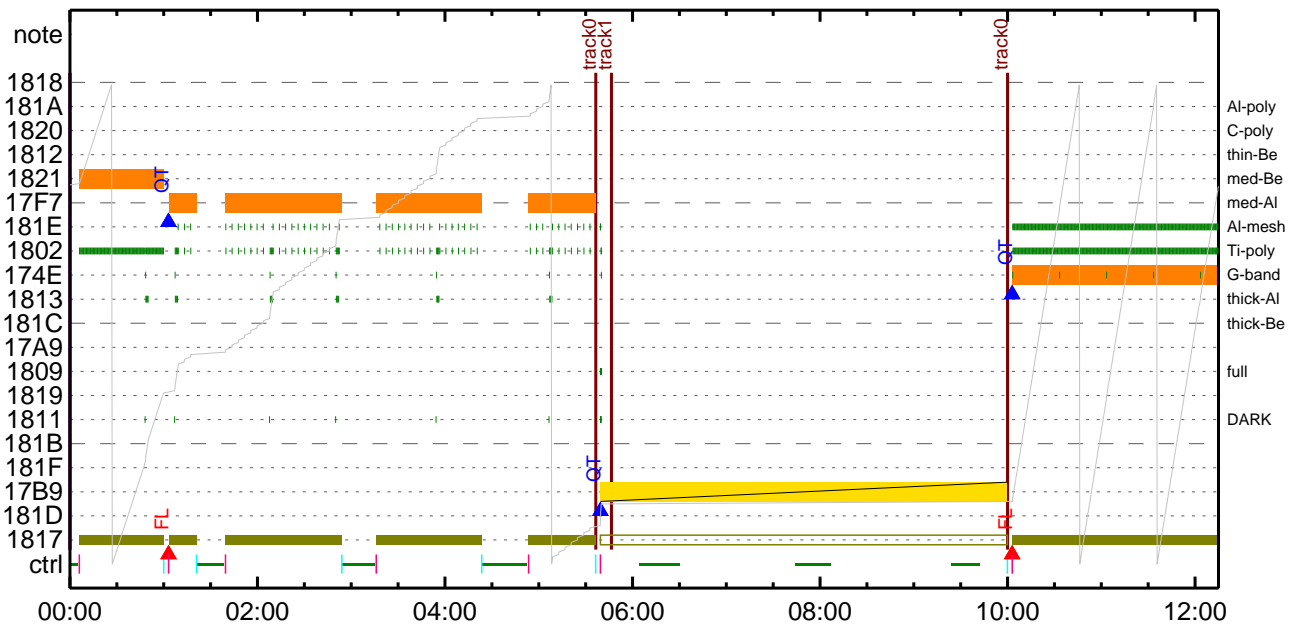
CMDI #0523 2010/10/02



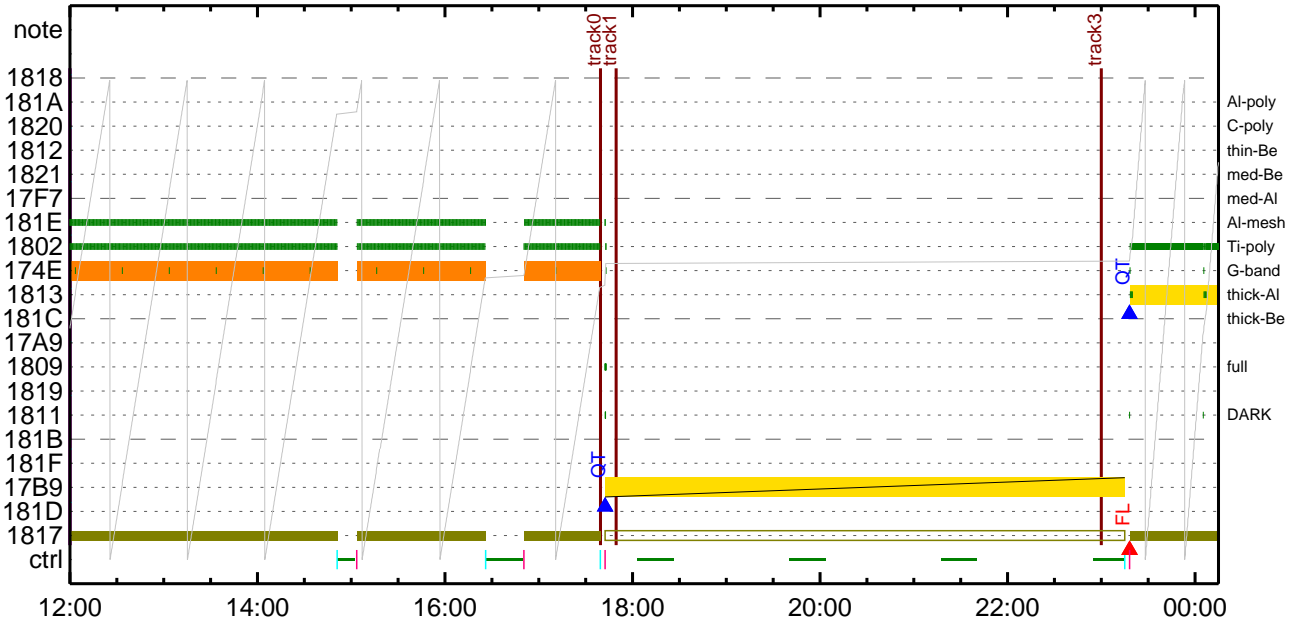
CMDI #0523 2010/10/02



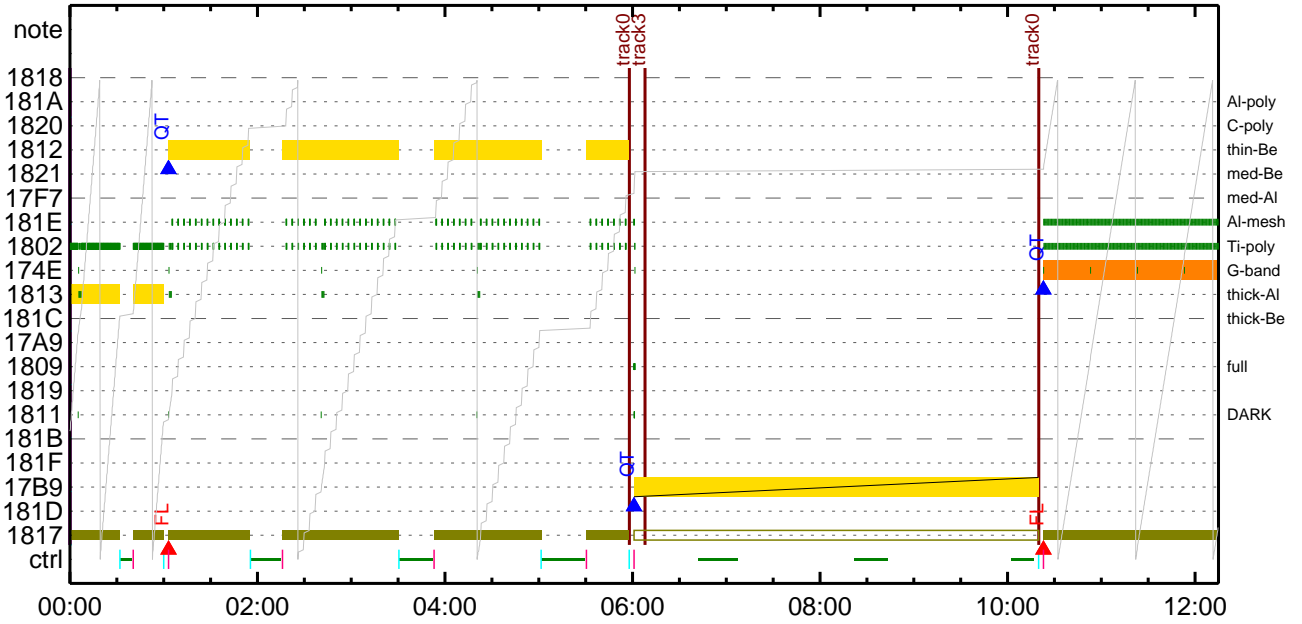
CMDI #0523 2010/10/03



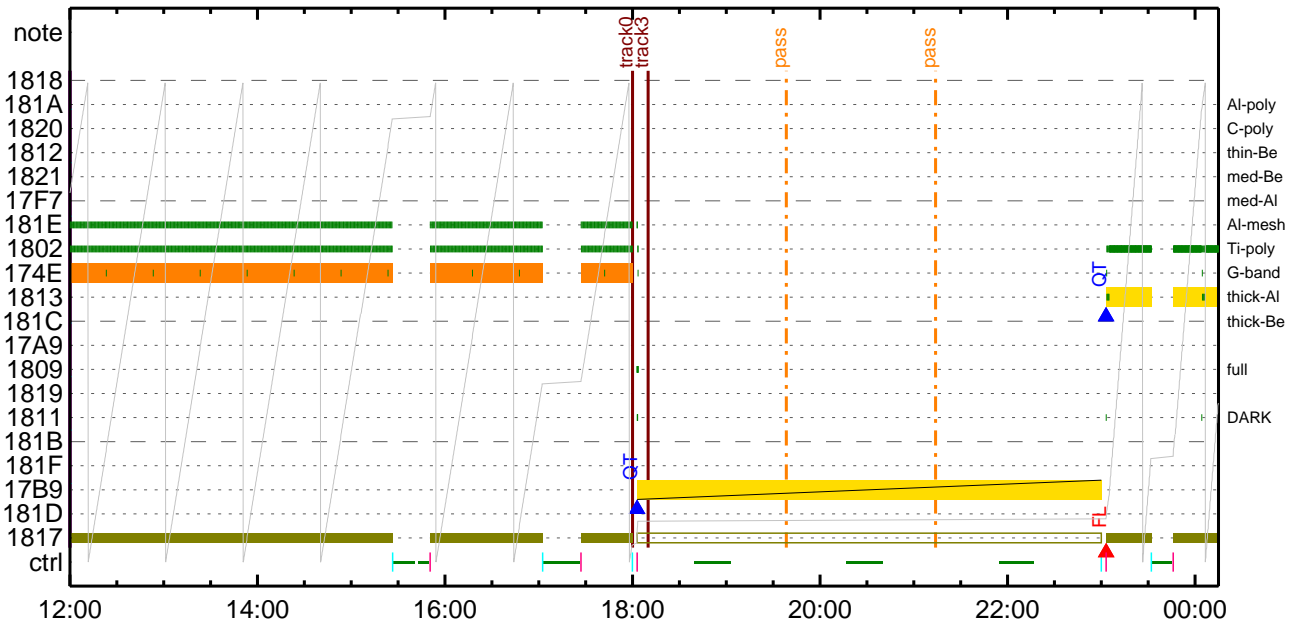
CMDI #0523 2010/10/03



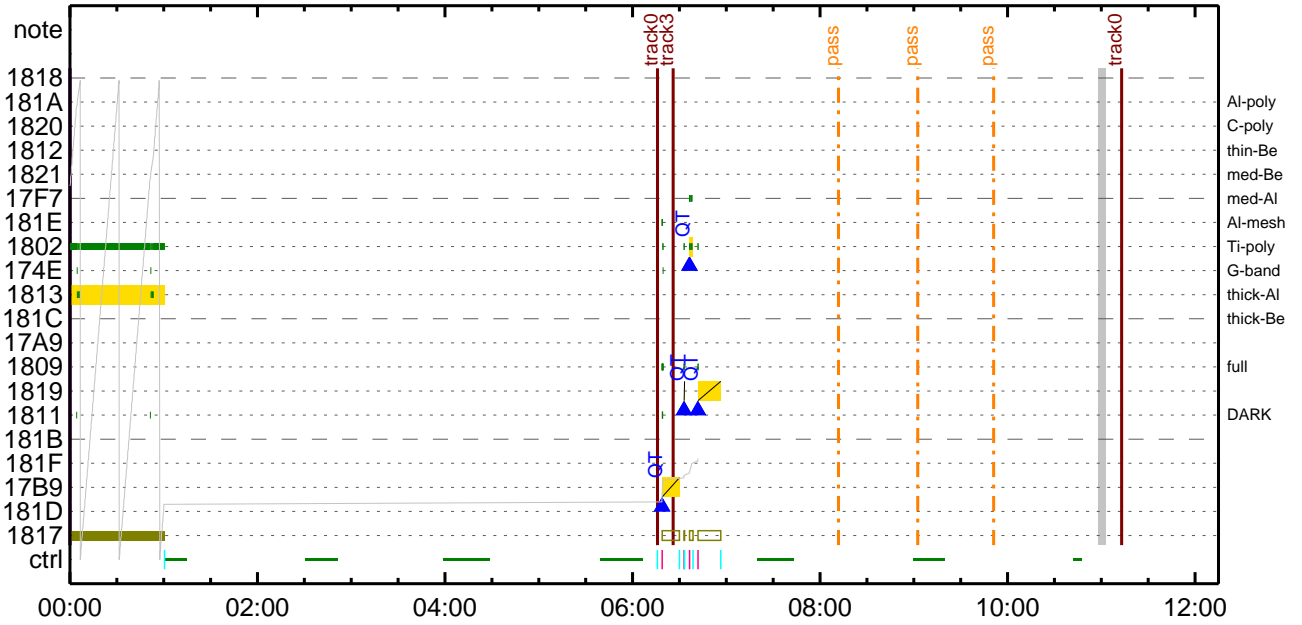
CMDI #0523 2010/10/04



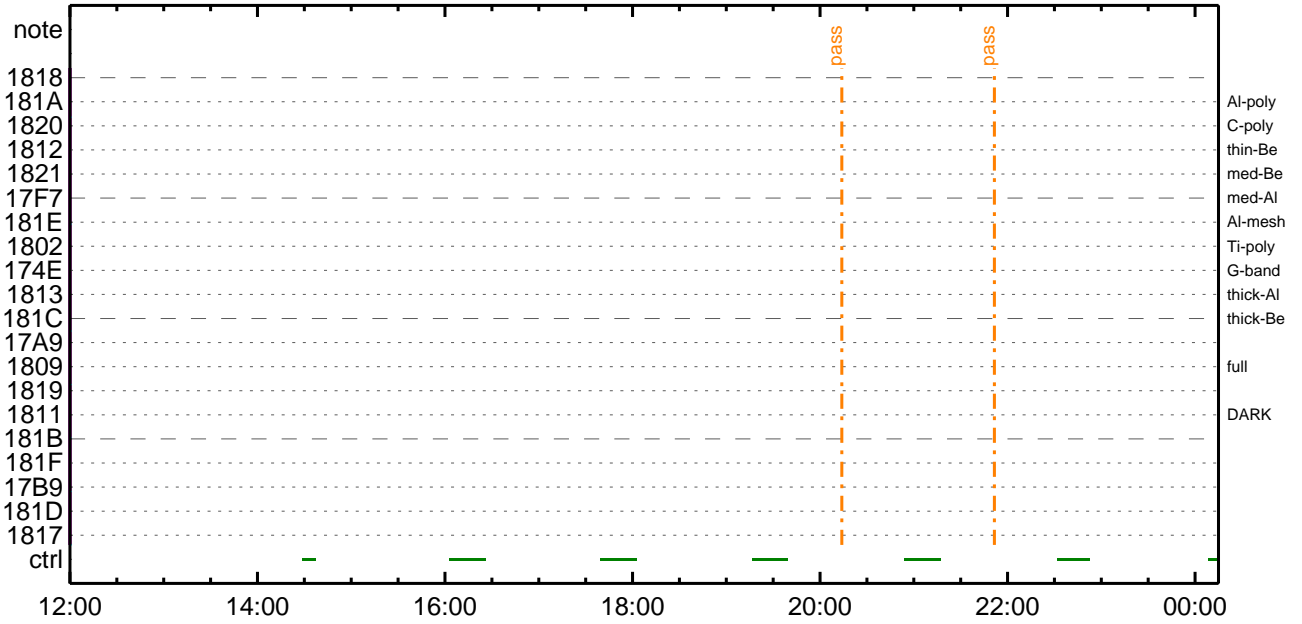
CMDI #0523 2010/10/04



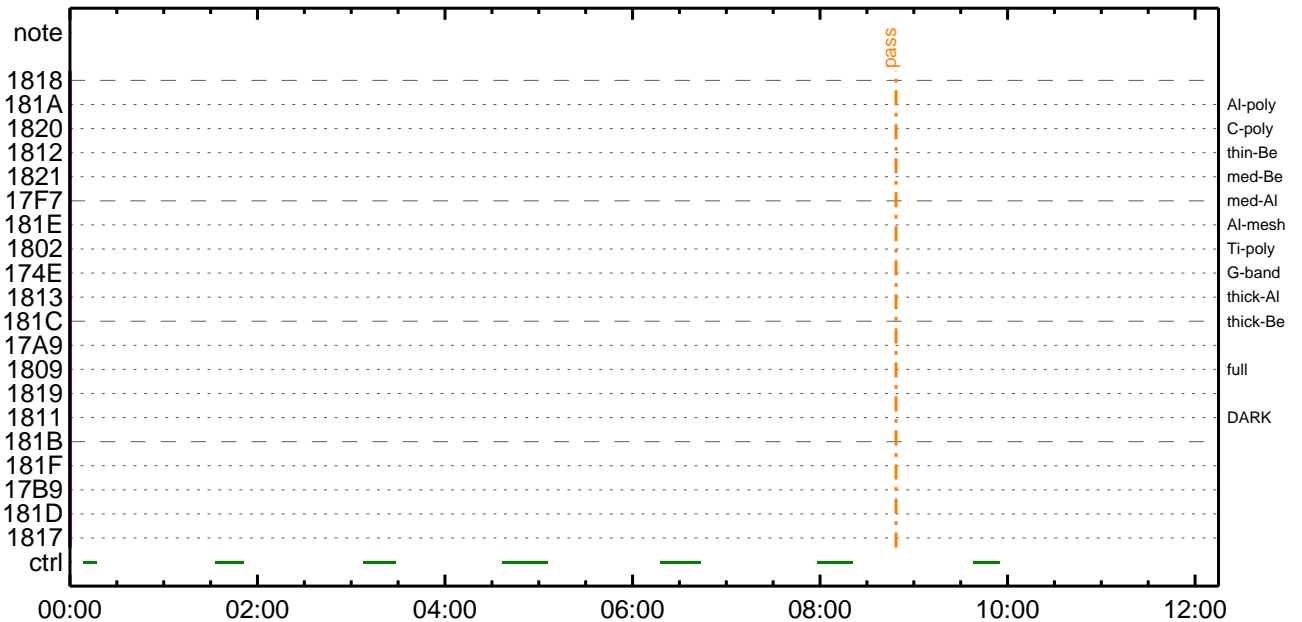
CMDI #0523 2010/10/05



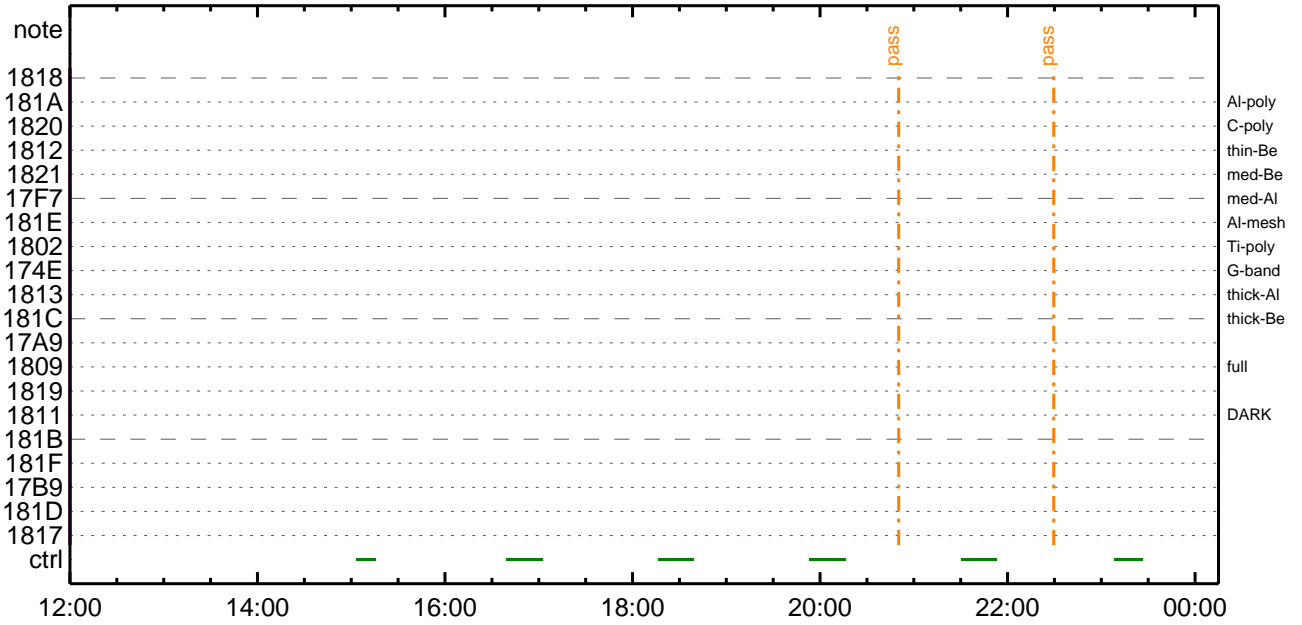
CMDI #0523 2010/10/05



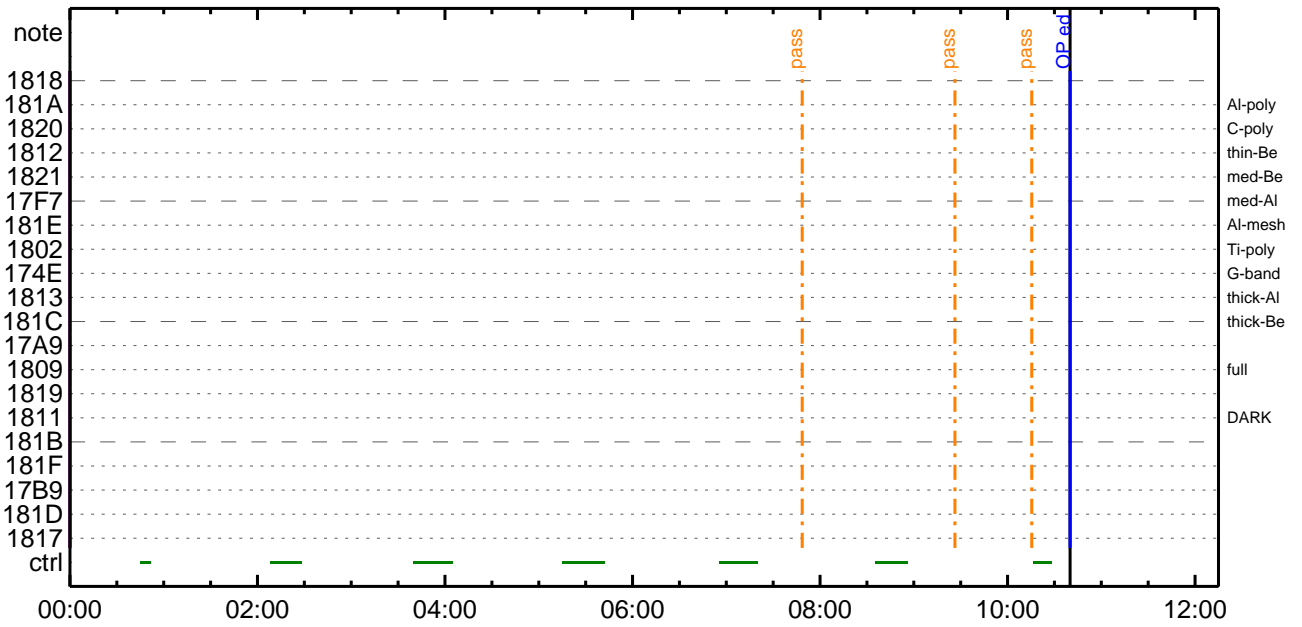
CMDI #0523 2010/10/06



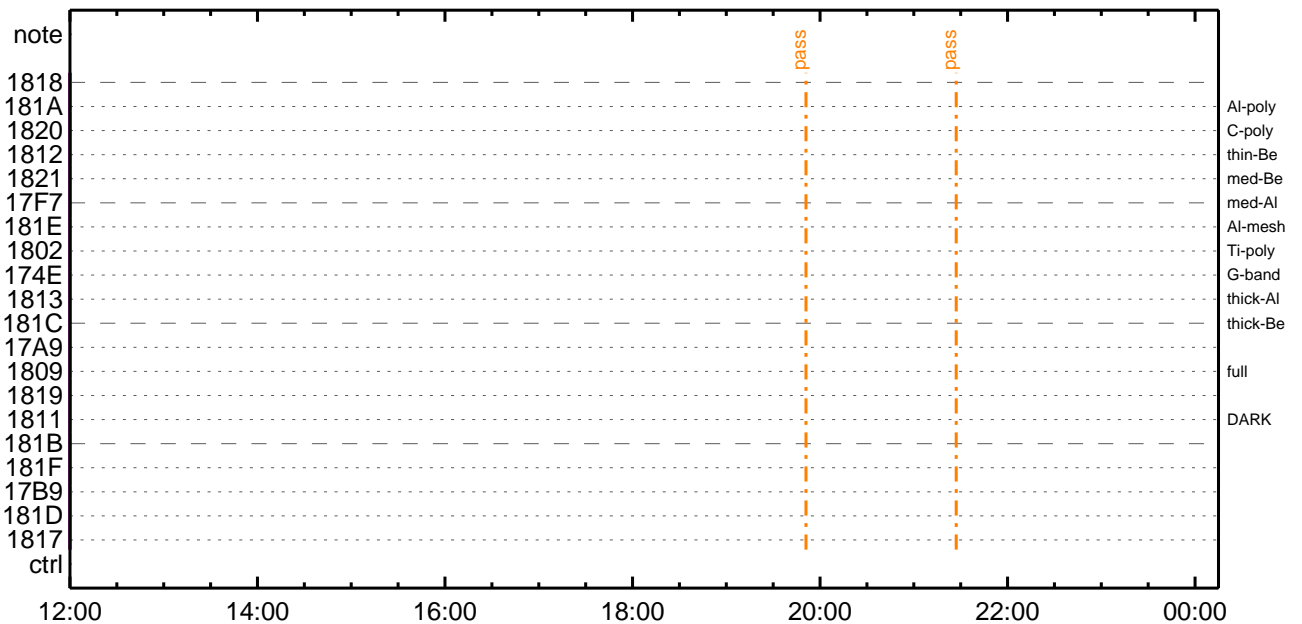
CMDI #0523 2010/10/06



CMDI #0523 2010/10/07



CMDI #0523 2010/10/07




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0096 C.          0300; SET EDUMP 0100 0100 0100 0100 0100 0100
0097 C.
0098 C. TI 2010-10-02 10:25:00.0
0099 +. TI 2010-10-02 10:25:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0102 C.
0103 +. TI 2010-10-02 10:25:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0106 C.
0107 +. TI 2010-10-02 10:25:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0110 C.
0111 +. TI 2010-10-02 10:29:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0114 C.
0115 C.          0100 0100 0100 0100 0100 0100
0116 C.          [ ] [HK1_TI_CMD_ENA/DIS]    EQ      ENA
0117 C.          [ ] [HK1_TI_CMD_NUM]      EQ      4
0118 C.          [ ] [HK1_NEXT_EXEC_PIM]    EQ      DHU
0119 C.          [ ] [HK1_NEXT_EXEC_DC]    EQ      0xB3
0120 C.
0121 C. *****
0122 C. TI 2010-10-02 10:29:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          [ ] [HK1_DMP_TOP_ADRS_1]    EQ      07
0129 C.          [ ] [HK1_DMP_TOP_ADRS_0]    EQ      2B
0130 C.          [ ] [HK1_DMP_BLOCK_NUM]    EQ      3
0131 C.          [ ] [HK1_DMP_REPEAT_NUM]   EQ      0
0132 C.          [ ] [HK1_DMA_DMP_PIM]     EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          [ ] [HK1_PKT_FORM_NO]      EQ      7
0136 C.          [ ] [HK1_PKT_GEN_TIME]     EQ      0.25 s
0137 C.          [ ] [HK1_S_TLM_BIT_RATE]   EQ      32k
0138 C.          [ ] [HK1_X_TLM_BIT_RATE]   EQ      4M
0139 C.          [ ] [HK1_DMP_CHK_FLG]     EQ      EXEC
0140 C.
0141 C.          0100 0100 0100 0100 0100 0100
0142 C.          [ ] [HK1_DMP_CHK_FLG]     EQ      NON
0143 C.
0144 C. RAM ID=TI_TBL 0100 0100 0100 0100 0100 0100
0145 C.
0146 C. DHU 0100 0100 0100 0100 0100 0100
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          [ ] [HK1_PKT_FORM_NO]      EQ      2
0150 C.          [ ] [HK1_PKT_GEN_TIME]     EQ      0.5S
0151 C.          [ ] [HK1_S_TLM_BIT_RATE]   EQ      32K
0152 C.          [ ] [HK1_X_TLM_BIT_RATE]   EQ      4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2010-10-02 10:29:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2010-10-02 10:29:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2010-10-02 10:29:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC (41)
0177 C. -----
0178 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-10-02 10:29:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0100 0100 0100 0100 0100 0100 *****
0192 C. (0100 0100 0100 0100 0100 0100)
0193 S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-620 2010-10-02 13:51:52 114 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°È»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•íÉ;ÈÈÈ¼°ÇÔα•α¿¼í¹çαÍ;çÀ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. SOT table upload
0047 C. *****
0048 . C. < Stop SP table >
0049 +. DC 07-F0 MDP_SP_CTRL_MANU
0050 BC (61)
0051 C.
0052 C. MDP_SP_CTRL_MODE = MANU [ ]
0053 C.
0054 C.
0055 . C. <Upload SP Observation Table>
0056 . S. RAM ram-282:MDP_OBS_S
0057 ( )
0058 C.
0059 . C. < Dump RAMID=MDP_OBS_S >
0060 +. DC 07-F0 MDP_DUMP_SPTBL
0061 BC (83 07 00 00 00 38 b8)
0062 C.
0063 C. MDP_OBS_S verify = OK/NG [ ]
0064 C.
0065 C.
0066 C. *****
0067 C. SOT TI command set
0068 C. *****
0069 C. Execute, after the success of TBL upload.
0070 +. TI 2010-10-02 10:29:18.0
0071 DC 07-F0 MDP_SOT_MODE_OBSV
0072 BC (40)
0073 C.
0074 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0075 C.
0076 C.
0077 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0078 +. DC 07-FC EIS_MODE_MANU
0079 BC (21 02)
0080 . C. Verify EIS in MANUAL mode
0081 . C. Estimated OBSTBL upload time is 28s
0082 C. *****
0083 C. EIS START OBSTBL LOAD
0084 C. *****
0085 . S. RAM ram-820:EIS_OBSTBL
0086 ( )
0087 +. DC 07-FC EIS_DUMP_OBSTBL
0088 BC (07 07 07 00 00 70 00)
0089 C.
0090 C. Execute, after the success of OBSTBL upload.
0091 C. Set EIS TI-commands
0092 +. TI 2010-10-02 10:29:50.0
0093 DC 07-FC EIS_MODE_CHG_ENA
0094 BC (20)
0095 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
```



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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2010-10-02 10:29:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C. ***** XRT END *****
0155 C.
0156 . C. ***** MDP `úãîñï»ö%ýðéâð¹ñèDCBC•x²è *****
0157 C. (%á°îÿÓÿÄÿÈÿÏÿËÿáÿçÿèè%¼ð¼Á»Û¹è)
0158 . S. DC-BC dcbc-402:DCBC
0159 (MDP_known_event)
0160 C.
0161 C.
0162 . C. ***** ÿDÿ¹•Ï Daily±¿ÍññÉ´Ø¹ñèDCBC•x²è *****
0163 . S. DC-BC dcbc-153:DCBC
0164 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0165 C.
0166 C.
0167 . C. ¡ãLOSÿÁÿ$ÿÃÿ-¼Á»Û;ä
0168 C.
0169 . C. ***** LOS *****
0170 C.

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*** OP Sequence for XRT ***

2010/10/02	10:39:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	10:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/10/02	10:40:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 ad cb 00 fd		
2010/10/02	10:40:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/10/02	10:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/10/02	10:40:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/10/02	10:40:22.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/02	10:42:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/02	10:42:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2010/10/02	10:42:58.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2010/10/02	10:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/02	14:17:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	14:17:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/02	14:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/02	14:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/02	14:22:30.0	XRT_Custom_418_OG [0x1a2]					
2010/10/02	14:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/02	15:49:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	15:49:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/02	15:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/02	15:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/02	16:13:00.0	XRT_Custom_418_OG [0x1a2]					
2010/10/02	16:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/02	17:26:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	17:26:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/02	17:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/02	17:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/02	17:50:00.5	XRT_Custom_418_OG [0x1a2]					
2010/10/02	17:51:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/02	18:39:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	18:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/10/02	18:40:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/10/02	18:40:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/10/02	18:40:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/10/02	18:40:20.0	XRT_ARS_DIS_415_OG [0x19f]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/02	18:42:56.0	XRT_QT_PROG_SET_407_OG [0x197]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/10/02	18:42:58.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/02	18:49:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/02	18:49:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/10/02	18:50:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2010/10/02	18:50:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/10/02	18:50:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/10/02	18:50:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2010/10/02	18:50:22.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/02	18:52:56.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/02	18:52:58.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2010/10/02	18:53:00.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		

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2010/10/02	18:53:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/02	19:03:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/02	19:03:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/02	19:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/02	19:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/02	19:26:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/02	19:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/02	20:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/02	20:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/02	20:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/02	20:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/02	21:04:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/02	21:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/02	22:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/02	22:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/02	22:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/02	22:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/02	22:39:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/02	22:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/02	23:00:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/02	23:00:02.5	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/02	23:00:22.5	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/02	23:00:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/02	23:00:26.5	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/02	23:00:28.5	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/02	23:03:00.5	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2010/10/02	23:03:02.5	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2010/10/02	23:03:04.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/02	23:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/02	23:55:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/02	23:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/02	23:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/03	00:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/03	00:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/03	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/03	01:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/03	01:00:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/03	01:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/03	01:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/03	01:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/03	01:03:02.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2010/10/03	01:03:04.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2010/10/03	01:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/03	01:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/03	01:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/03	01:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/03	01:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/03	01:38:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/03	01:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2010/10/03	02:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	02:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/03	02:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/03	02:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/03	03:15:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/03	03:16:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	04:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	04:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/03	04:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/03	04:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/03	04:52:30.5	XRT_Custom_418_OG [0x1a2]							
2010/10/03	04:53:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	05:36:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	05:36:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/10/03	05:36:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2010/10/03	05:36:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/03	05:36:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/10/03	05:36:50.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/03	05:39:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2010/10/03	05:39:28.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	05:46:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00			
2010/10/03	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	09:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/10/03	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 ad cb 00 fd			
2010/10/03	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/10/03	10:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/10/03	10:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2010/10/03	10:00:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/03	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/03	10:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2010/10/03	10:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01			
2010/10/03	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	14:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	14:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/03	14:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/03	14:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/03	15:02:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/03	15:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	16:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	16:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/03	16:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/03	16:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/03	16:49:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/03	16:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/03	17:39:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/03	17:39:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/10/03	17:39:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2010/10/03	17:39:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/03	17:39:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							

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2010/10/03	17:39:50.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/03	17:42:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/10/03	17:42:28.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/03	17:49:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00
2010/10/03	23:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2010/10/03	23:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/03	23:15:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/03	23:15:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/03	23:15:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/03	23:15:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/03	23:15:28.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/03	23:18:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/03	23:18:02.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2010/10/03	23:18:04.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/10/03	23:18:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	00:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	00:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	00:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	00:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	00:39:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/04	00:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	01:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/04	01:00:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/04	01:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/04	01:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/04	01:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/04	01:03:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2010/10/04	01:03:04.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/10/04	01:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	01:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	01:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	01:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	01:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	02:15:00.0	XRT_Custom_418_OG [0x1a2]				
2010/10/04	02:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	03:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	03:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	03:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	03:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	03:52:00.0	XRT_Custom_418_OG [0x1a2]				
2010/10/04	03:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	05:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	05:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	05:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	05:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	05:29:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/04	05:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2010/10/04	05:57:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	05:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/10/04	05:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/10/04	05:58:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/10/04	05:58:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/10/04	05:58:20.0	XRT_ARS_DIS_415_OG [0x19f]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/04	06:00:56.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/10/04	06:00:58.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	06:08:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/10/04	10:19:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	10:19:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/04	10:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 ad cb 00 fd
2010/10/04	10:20:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/04	10:20:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/04	10:20:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/04	10:20:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/04	10:22:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	10:22:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2010/10/04	10:22:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2010/10/04	10:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	15:26:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	15:26:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	15:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	15:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	15:49:30.0	XRT_Custom_418_OG [0x1a2]			
2010/10/04	15:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	17:02:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	17:02:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/04	17:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/04	17:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/04	17:26:00.0	XRT_Custom_418_OG [0x1a2]			
2010/10/04	17:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/10/04	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/10/04	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/10/04	18:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/10/04	18:00:20.0	XRT_ARS_DIS_415_OG [0x19f]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/04	18:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/10/04	18:02:58.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/04	18:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/10/04	23:00:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/04	23:00:02.5	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/04	23:00:22.5	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/04	23:00:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/04	23:00:26.5	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/04	23:00:28.5	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/04	23:03:00.5	XRT_FLD_RESET_412_OG [0x19c]			

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2010/10/04	23:03:02.5	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/04	23:03:04.5	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2010/10/04	23:03:06.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/04	23:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/04	23:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/04	23:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/04	23:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/04	23:45:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/04	23:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2010/10/05	01:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	01:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	01:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	01:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	06:15:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	06:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
2010/10/05	06:16:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/05	06:16:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
2010/10/05	06:16:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/05	06:16:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/05	06:18:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	06:18:58.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/10/05	06:26:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	06:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03	00	00	00	00
2010/10/05	06:30:02.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	06:30:22.0	XRT_FLD_DIS_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/05	06:30:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/05	06:30:26.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/05	06:32:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	06:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/10/05	06:33:19.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	06:33:21.0	XRT_STANDBY_425_OG [0x1a9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	06:33:23.0	XRT_DIAGNOSTIC_426_OG [0x1aa]	XRT_STBY	2	07-F8	03	01			
2010/10/05	06:33:25.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_DIAGNOSTIC	2	07-F8	03	07			
2010/10/05	06:33:27.5	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/05	06:33:30.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/10/05	06:33:32.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:35.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:37.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:40.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:42.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:45.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:47.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:50.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:52.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:55.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:33:57.5	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:34:00.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	

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2010/10/05	06:35:57.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:36:00.0	XRT_FSA_CONTROL_427_OG [0x1ab]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/05	06:36:02.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00	
2010/10/05	06:36:05.0	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/10/05	06:36:07.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/05	06:36:27.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2010/10/05	06:36:29.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	06:38:43.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	06:38:45.0	XRT_FSA_CONTROL_427_OG [0x1ab]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/05	06:38:47.5	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/10/05	06:38:50.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:38:52.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:38:55.0	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:38:57.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/05	06:39:00.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:02.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:05.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:07.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:10.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:12.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:15.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:17.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:20.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:22.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:25.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:27.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:30.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:32.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:35.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:37.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:40.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:42.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:45.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:47.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:50.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:52.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:55.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:39:57.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:00.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:02.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:05.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:07.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:10.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:12.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:15.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:17.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/05	06:40:20.0	XRT_FSA_CONTROL_434_OG [0x1b2]							

2010/10/05	06:40:22.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:25.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:27.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:30.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:32.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:35.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:37.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:40.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:42.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:45.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:47.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:50.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:52.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:55.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:40:57.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:00.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:02.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:05.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:07.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:10.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:12.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/05	06:41:15.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:17.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/05	06:41:20.0	XRT_FSA_CONTROL_427_OG [0x1ab]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/05	06:41:22.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/05	06:41:25.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/10/05	06:41:28.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/10/05	06:41:48.0	XRT_QT_PROG_SET_423_OG [0x1a7]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/10/05	06:41:50.0	XRT_STANDBY_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2010/10/05	06:41:52.0	XRT_OPERATE_435_OG [0x1b3]	XRT_STBY	2	07-F8	03	01		
2010/10/05	06:41:54.0	XRT_CTRL_AUTO_406_OG [0x196]	XRT_OPERATE	2	07-F8	03	02		
2010/10/05	06:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/05	11:13:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
		AOCU_NM		5	02-76	00	00	00	00