

# XRT Timeline to be uploaded on 2010/10/05

Period: 2010/10/05 11:03:00 - 2010/10/09 10:16:00

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Normal mode

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XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384												
Term	Pointing (x, y)							Comment				
10/05 11:16:00 - 10/05 18:44:54	Fixed ( -11.0, -955.0)	# OP start + 10min; HOP81 S-Pole										
10/06 10:03:00 - 10/06 12:00:00	Fixed ( -11.0, 925.0)	HOP 81 N-Pole (XRT CCD bakeout from 12UT)										
<b>PROG= 12 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 94 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ Subr= 2 30-time(s) 2.0sec												
└─┬─ Seqn= 97 2-time(s) 30.0sec												
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)							Comment				
10/05 18:48:00 - 10/05 19:39:30	Fixed ( 0.0, 0.0)	synoptic, shifted 3.0 min										
10/06 05:53:00 - 10/06 09:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted -10.0 min										
10/07 06:03:00 - 10/07 06:09:54	Fixed ( 0.0, 0.0)	synoptic										

<b>PROG= 03 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 7 1-time(s) 4.0sec												
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 5 1-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 8 1-time(s) 4.0sec												
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 4 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1822: Support EIS DEM with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad												
Term	Pointing (x, y)							Comment				
10/05 19:42:38 - 10/05 20:53:30	Track ( 221.7, 270.4) <sup>@ 10/05 18:55:00</sup>	Track AR11111; HOP171 for 23UT-01UT										

<b>PROG= 19 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 19 1-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Subr= 2 3-time(s) 2.0sec												
└─┬─ Seqn= 99 2-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─ Open/thick-Al Open/thick-Be close Safe Norm 32.0s Obs 1x1 384x384 (1064, 1048) Q=95 0 0 2.0sec												
└─┬─ Seqn= 29 10-time(s) 60.0sec												
└─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1812: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 225s cad												
Term	Pointing (x, y)							Comment				
10/05 21:20:06 - 10/05 23:00:00	Track ( 221.7, 270.4) <sup>@ 10/05 18:55:00</sup>	Track AR11111; HOP171 for 23UT-01UT										
10/06 01:03:06 - 10/06 05:49:54	Track ( 221.7, 270.4) <sup>@ 10/05 18:55:00</sup>	Track AR11111; HOP171 for 23UT-01UT										

<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 19 1-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Seqn= 20 4-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												

Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 63 20-time(s) 225.0sec</b>												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1813: AR Standard-B(Morphology) with PFB FW1=Open + Ti/Poly 384x384 at 1064 1048-30sec-cad</b>												
Term			Pointing (x, y)				Comment					
10/05 23:03:04 - 10/06 01:00:00			Track ( 221.7, 270.4) @ 10/05 18:55:00				Track AR11111; HOP171 for 23UT-01UT					
<b>PROG= 11 Inf.-time(s)</b>												
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 20 4-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 71 90-time(s) 30.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV</b>												
Term			Pointing (x, y)				Comment					
10/07 00:00:30 - 10/07 05:59:54			Track ( 387.7, 278.0) @ 10/06 16:00:00				Continue to track AR11111 (HOP171 from 23UT-01UT)					
<b>PROG= 08 Inf.-time(s)</b>												
<b>Subr= 1 1-time(s) 600.0sec</b>												
<b>Seqn= 37 1-time(s) 4.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long</b>												
Term			Pointing (x, y)				Comment					
10/07 06:13:00 - 10/07 06:19:54			Fixed ( -528.4, -528.4)				XRT Post-baekout Quadrant #1					
<b>PROG= 20 1-time(s)</b>												
<b>Subr= 1 1-time(s) 12.0sec</b>												
<b>Seqn= 38 1-time(s) 12.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 93 2-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long</b>												
Term			Pointing (x, y)				Comment					
10/07 06:23:00 - 10/07 06:29:54			Fixed ( 528.4, -528.4)				XRT Post-bakeout Quadrant #2					
<b>PROG= 06 1-time(s)</b>												
<b>Subr= 1 1-time(s) 12.0sec</b>												
<b>Seqn= 36 1-time(s) 12.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 93 2-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long</b>												
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Term	Pointing (x, y)	Comment
10/07 06:33:00 - 10/07 06:39:54	Fixed ( 528.4, 528.4)	XRT Post-bakeout Quadrant #3
<b>PROG= 05 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 39 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open Safe Norm 44ms	Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 44ms	Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Dark 44ms	Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Dark 44ms	Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close Safe Norm 4.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Norm 8.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

Term	Pointing (x, y)	Comment
10/07 06:43:00 - 10/09 10:16:00	Fixed ( -528.4, 528.4)	XRT Post-bakeout Quadrant #4
<b>XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long</b>		
<b>PROG= 09 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 40 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open Safe Norm 44ms	Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 44ms	Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Dark 44ms	Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Dark 44ms	Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close Safe Norm 4.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Norm 8.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

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### Flare mode

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Term	Pointing (x, y)	Comment
10/05 11:16:00 - 10/05 18:44:54	Fixed ( -11.0, -955.0)	# OP start + 10min; HOP81 S-Pole
10/05 19:42:38 - 10/05 20:53:30	Track ( 221.7, 270.4) @ 10/05 18:55:00	Track AR111111; HOP171 for 23UT-01UT
10/05 21:20:06 - 10/05 23:00:00	Track ( 221.7, 270.4) @ 10/05 18:55:00	Track AR111111; HOP171 for 23UT-01UT
10/05 23:03:04 - 10/06 01:00:00	Track ( 221.7, 270.4) @ 10/05 18:55:00	Track AR111111; HOP171 for 23UT-01UT
10/06 01:03:06 - 10/06 05:49:54	Track ( 221.7, 270.4) @ 10/05 18:55:00	Track AR111111; HOP171 for 23UT-01UT
10/06 10:03:00 - 10/06 12:00:00	Fixed ( -11.0, 925.0)	HOP 81 N-Pole (XRT CCD bakeout from 12UT)
<b>PROG= 01 1-time(s)</b>		
└─ <b>Subr= 1 2-time(s) 620.0sec</b>		
└─ <b>Seqn= 67 30-time(s) 20.0sec</b>		
Open/Ti-poly	Open/thick-Al close Safe Norm 250ms	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Norm 2.00s	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 90 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s	Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
└─ <b>Subr= 2 8-time(s) 600.0sec</b>		
└─ <b>Seqn= 67 5-time(s) 120.0sec</b>		
Open/Ti-poly	Open/thick-Al close Safe Norm 250ms	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Norm 2.00s	Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 90 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s	Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
└─ <b>Subr= 3 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 89 40-time(s) 600.0sec</b>		
Open/Al-mesh	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 500ms	Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Dark 1.00s	Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

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### Active Region Search

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NOT USED

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### Flare Detection

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Term	Pointing (x, y)	Comment
10/05 11:15:46 - 10/05 18:45:16	Fixed ( -11.0, -955.0)	# OP start + 10min; HOP81 S-Pole
<b>FLD Patrol</b>		

10/05 19:39:52 - 10/06 05:50:16 Track ( 221.7, 270.4) @ 10/05 18:55:00 Track AR11111; HOP171 for 23UT-01UT

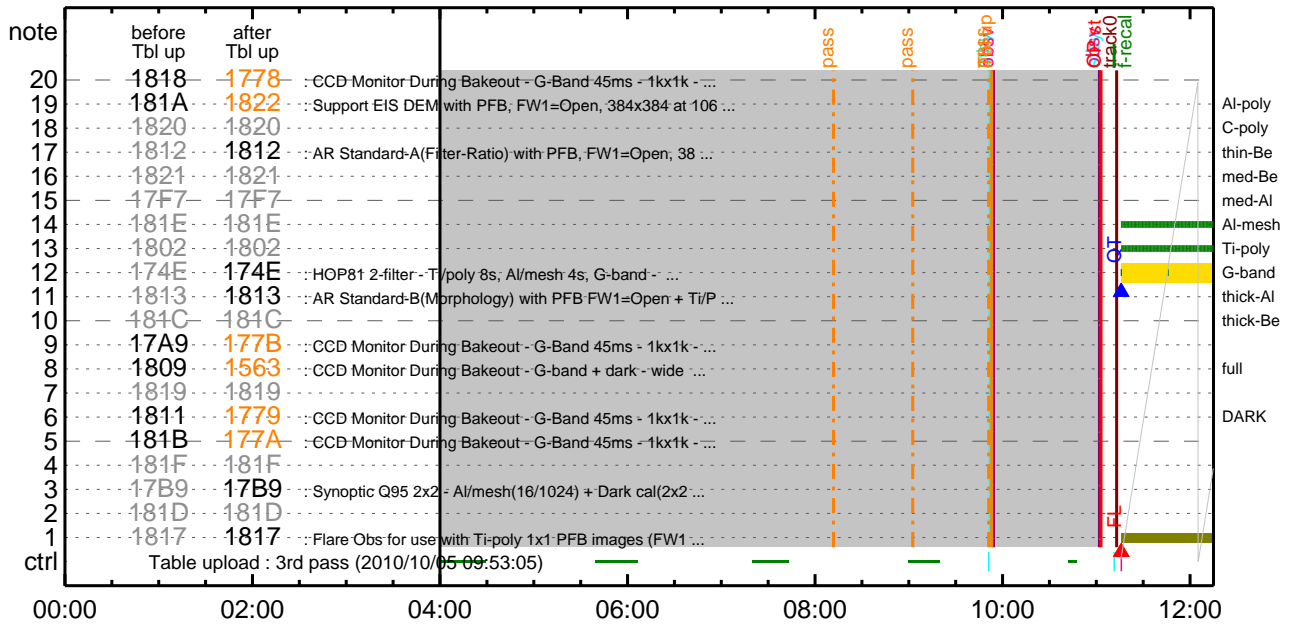
10/06 10:00:16 - 10/07 00:00:26 Fixed ( -11.0, 925.0) HOP 81 N-Pole (XRT CCD bakeout from 12UT)

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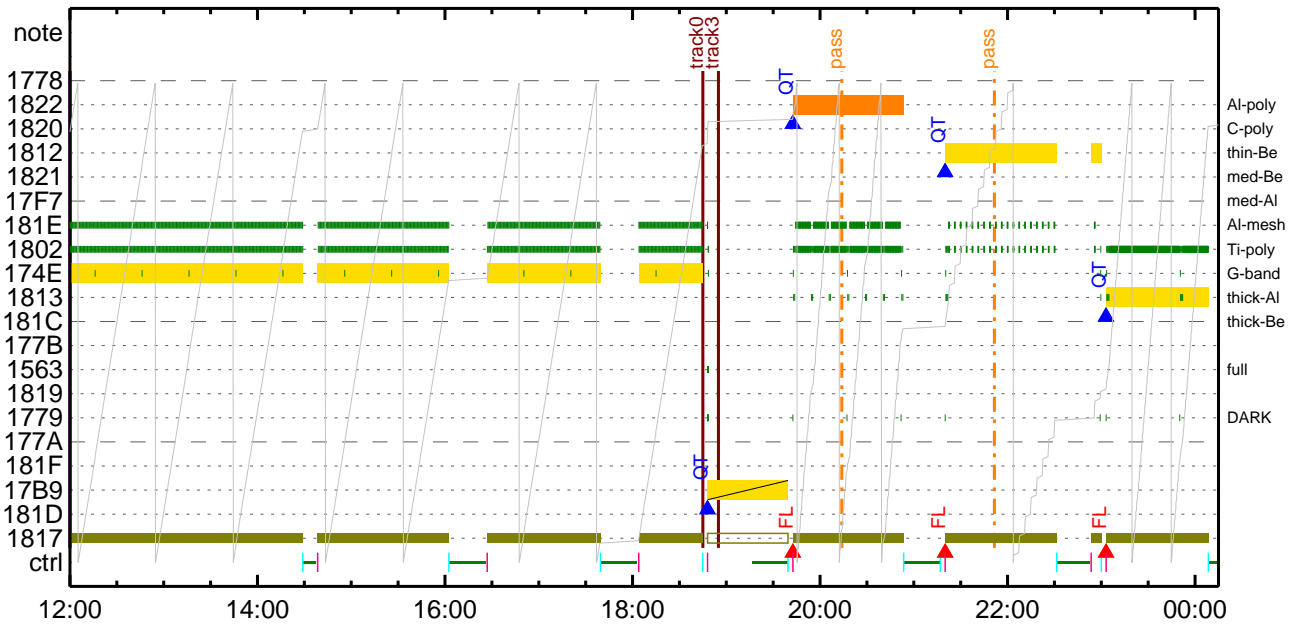
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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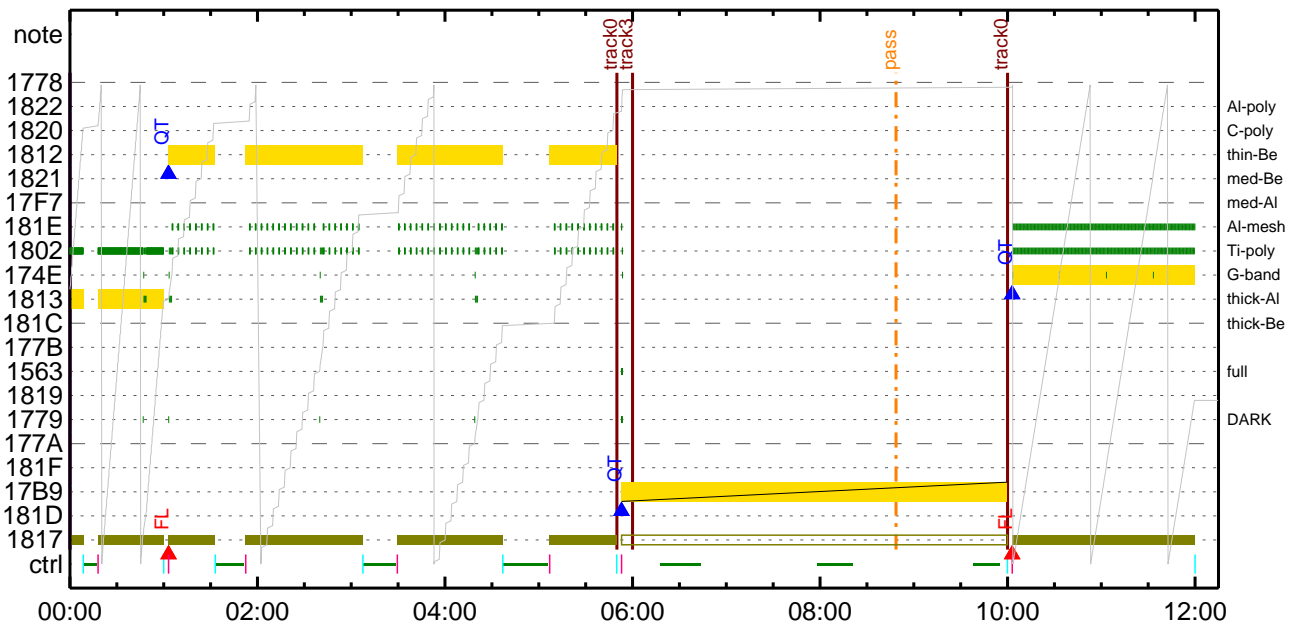
### CMDI #0528 2010/10/05



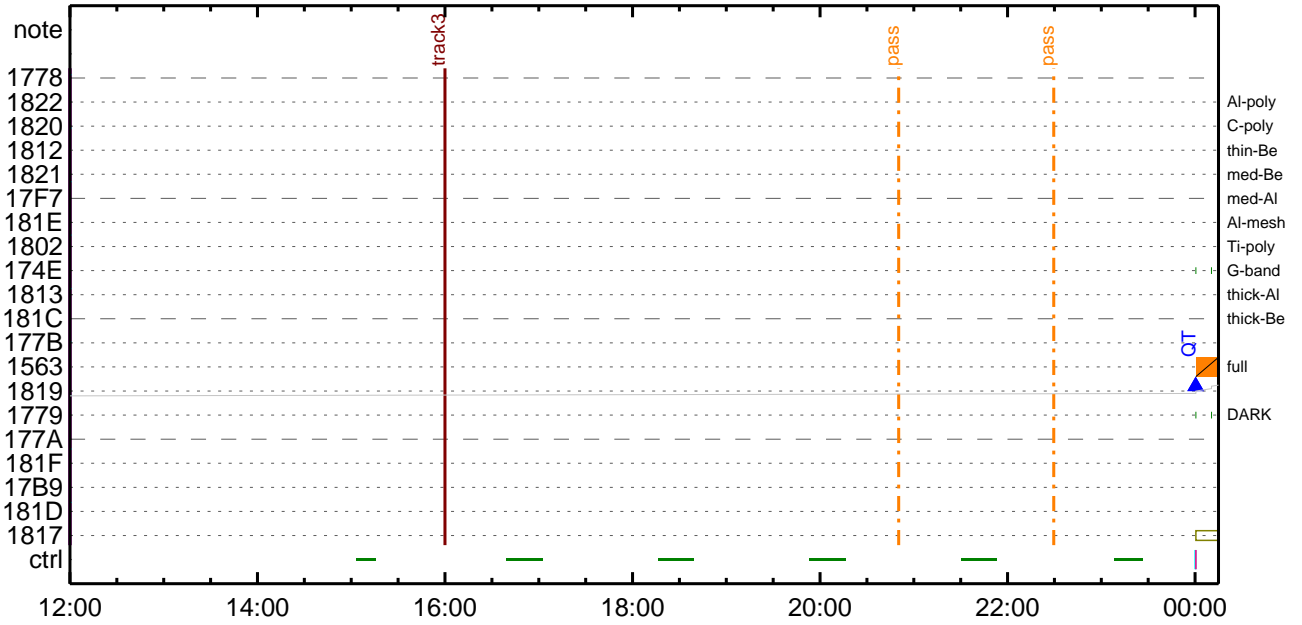
### CMDI #0528 2010/10/05



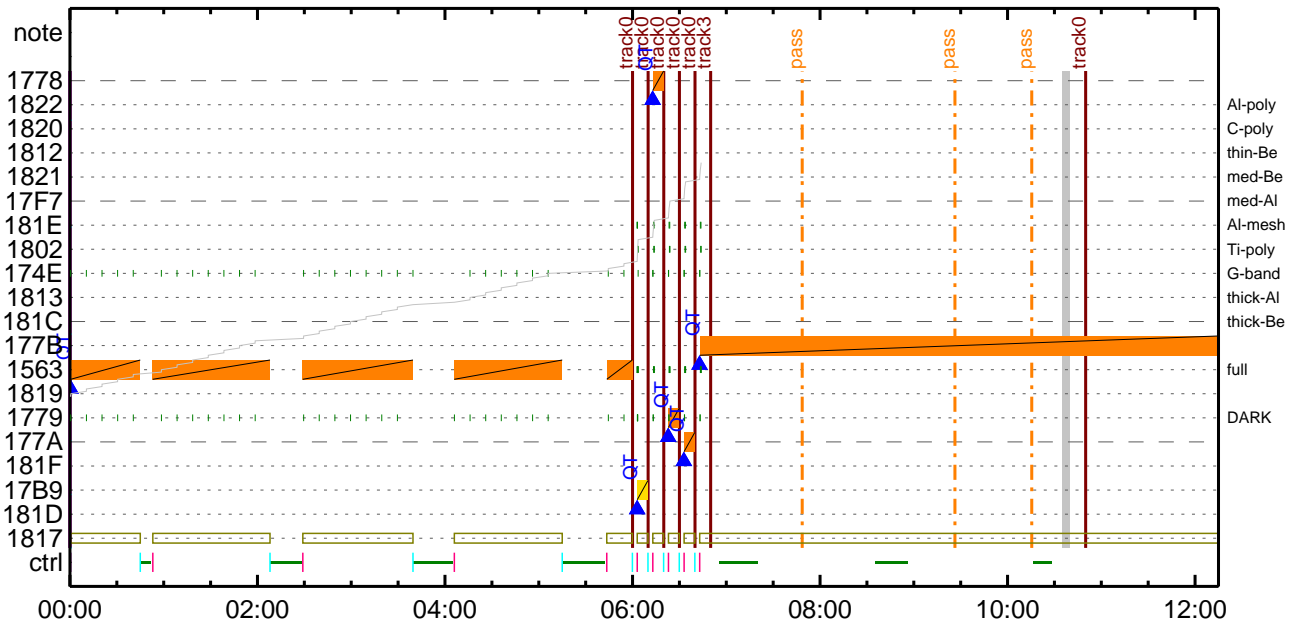
### CMDI #0528 2010/10/06



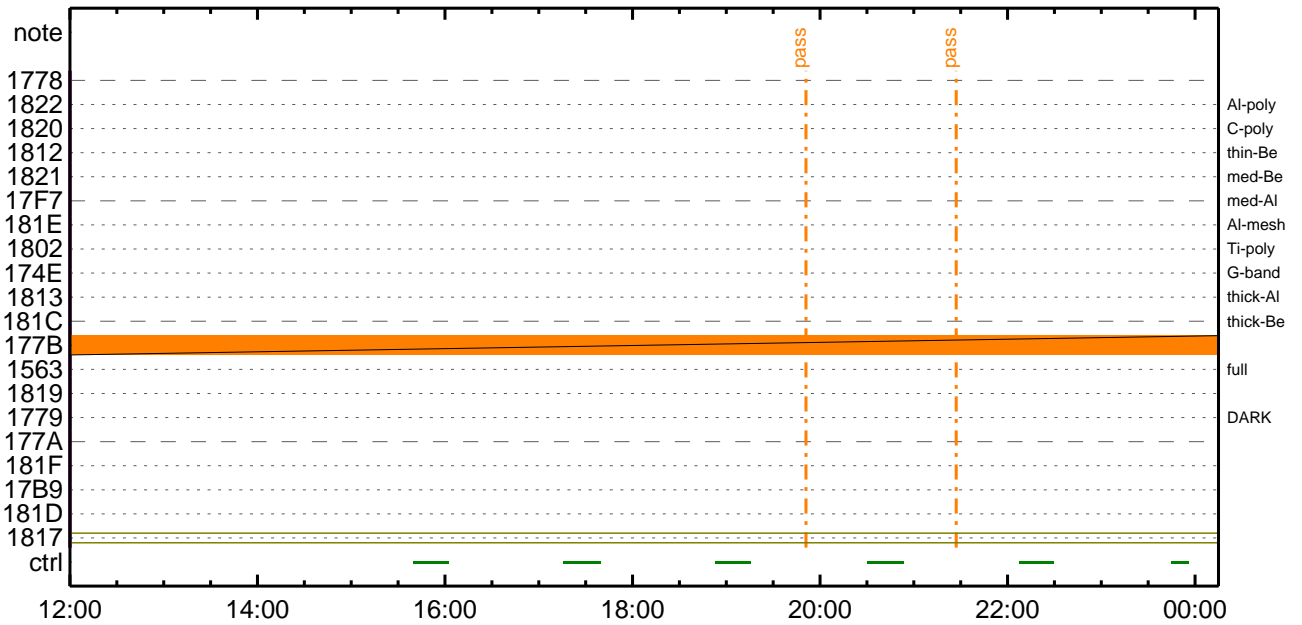
CMDI #0528 2010/10/06



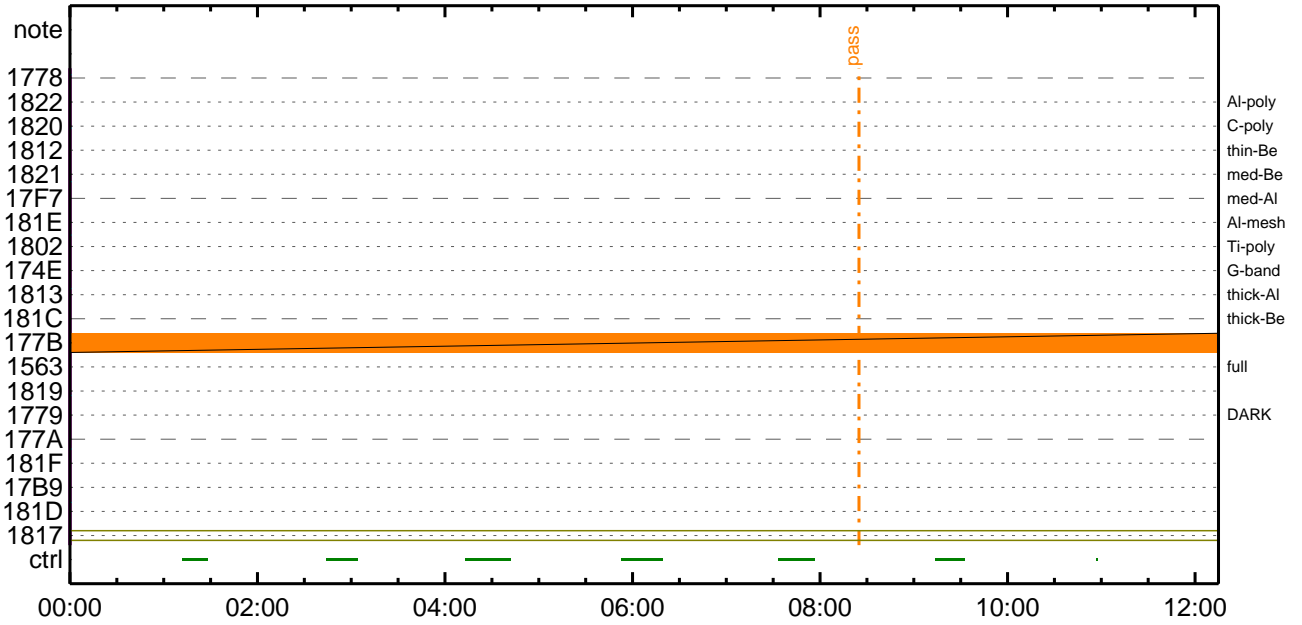
CMDI #0528 2010/10/07



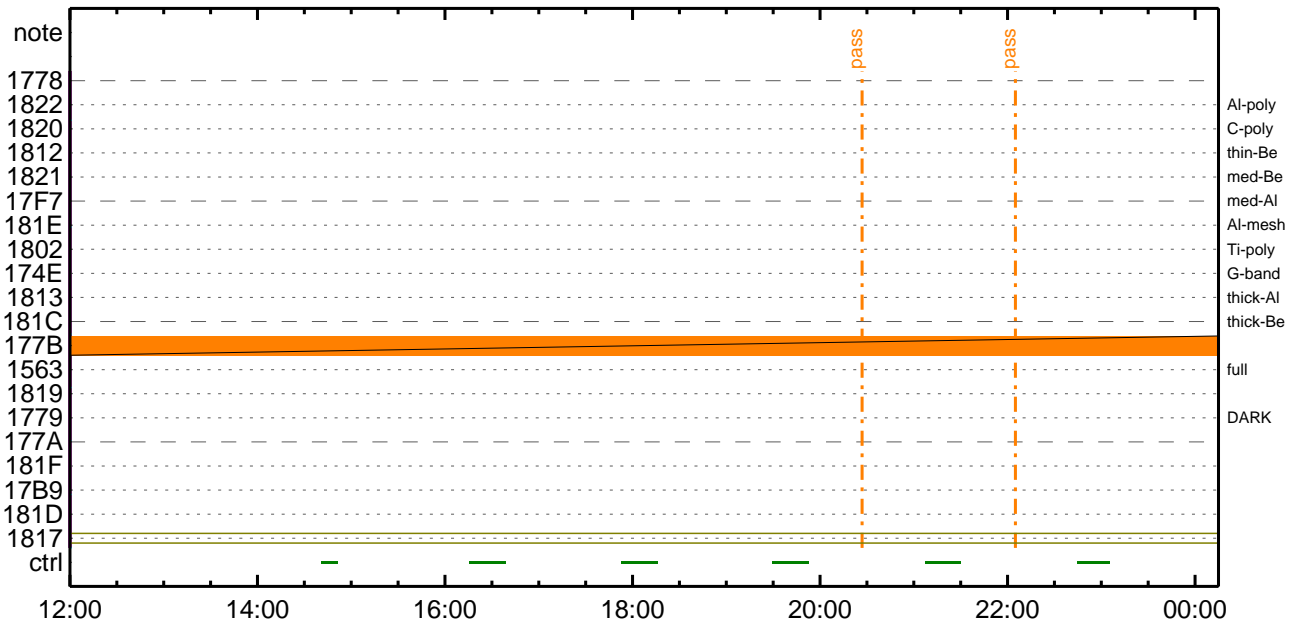
CMDI #0528 2010/10/07



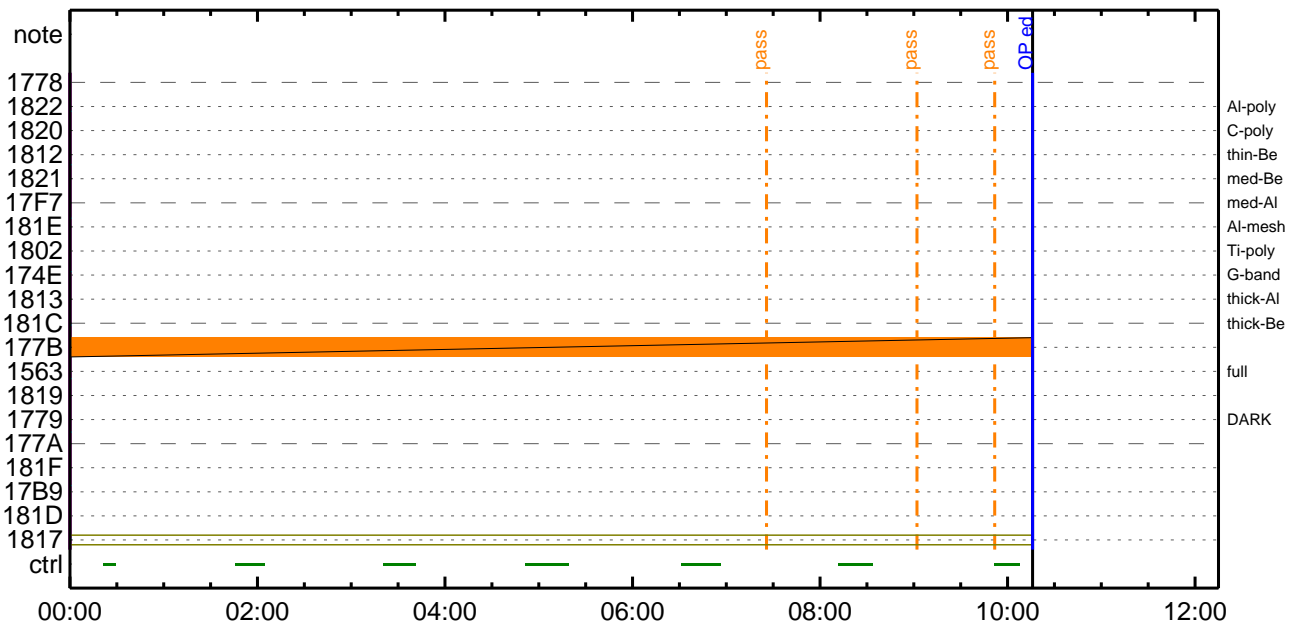
### CMDI #0528 2010/10/08



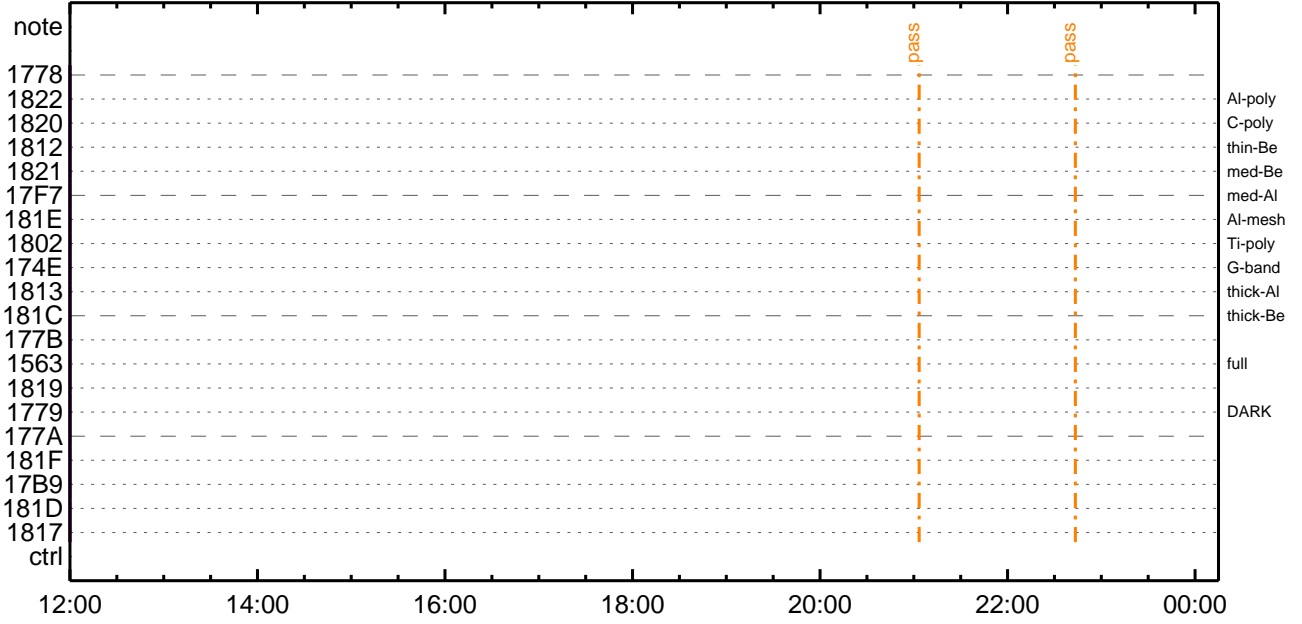
### CMDI #0528 2010/10/08



### CMDI #0528 2010/10/09



CMDI #0528 2010/10/09





(a) Spacecraft Operation Procedure (real-commands)

```
main-622 2010-10-05 13:14:40 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+z@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈðzðÁð•µºÈ>Í×ÁÇøÍYÇYÁY×YÍ;¼YÉ;ÈÈÈµ•íÍÈ;ÈðÈ¼ºÇðð•ðz¼l¹çðí;çÁ®, ùð¹ðÈððÇÁ+z@ð•ðÈððð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+z@µ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, Í×ÈÝðÄLOSððÇøÍ»p´ððð¹ÍÍ, ð•; çÈÒÍ×ðÈXÁðONðí¹ðÈðíðÈððð³ðÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÒÄð-ºÁÄÈð•ðzðé; çºÈ²¼ðíºÈÀ, ¼Èzçðð¼Á¹ðð¹ðé; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼iºÈÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èð•ðzð¼¼l¹çðí; çºÈ²¼ðí¼Á¹ððº; çDCBC-150ðØzÈðà; f
0035 C.
0036 . C. ;ãPT1ºÈÀ, ³«»Û;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ù)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ù)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ù)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÄÜÁØ;ÈÄ•ÁººðÈð;È, ãðíºÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ù)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ù)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ù)
0052 C.
0053 C.
0054 . C. PT1ºÈÀ, ð-¼«ÈºÁ»ßð•ðz, ã; çºÈ²¼ðð¼Á¹ðð¹ðé; f
0055 C. YçYÓYÉYÈÄÜÁØðÄÄ•ÁººðÈðð-¾áð¼¼l¹çðí´ºÈÀ, ð¹ðÈðððÇÄððÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼iºÈÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èð•ðzð¼¼l¹çðí; çºÈ²¼ðí¼Á¹ððº; çDCBC-151ðØzÈðà; f
0061 C.
0062 . C. ;ãPT2ºÈÀ, ³«»Û;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ù)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ù)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ù)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÄÜÁØ;ÈÄ•ÁººðÈð;È, ãðíºÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ù)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ù)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ù)
0078 C.
0079 . C. *****
0080 C. DRºÈÀ, Áã»ß;çXÁ+z@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDRºÈÀ, Áã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+z@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-622:OP
0104 ( )
0105 S. OG og-622:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î½±°î½Y¹ç¹Ôª°¬²ò³òE;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2010-10-05 10:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-10-05 10:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-10-05 10:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-10-05 11:02:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□îÄë%îîñ□îŷÄŷ§ŷÄŷ-¹âîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»□ð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□îŷ°èŷÄŷÖŷ×½ªî»□ð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Ê□ðîä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2010-10-05 11:02:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2010-10-05 11:02:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2010-10-05 11:02:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-10-05 11:02:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄî□î»ö¼ŷ□èÄð□¹□èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷ¼ŷçŷè□è¼□□¼Ä»Û□¹□è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñ□è´Ø□¹□èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-623 2010-10-05 13:14:40 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YFÿÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿ðÁð•µ°È»Í×ÁÇøÍY¿Y×Yí;¼YÉ;ÈÈèµ•íÉ;ÈøÈ¼°ÇÔø•ø¿¼í¹çøÍ;çÄ®, ùø¹øÈøBøÇÄ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 + DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 1108441.9 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. *****
0055 C. SOT table upload
0056 C. *****
0057 . C. < Upload DPL table >
0058 +. DC 07-F0 MDP_FG_CTRL_MANU
0059 BC (51)
0060 . C. -----
0061 C. MDP_FG_CTRL_MODE = MANU [ ]
0062 C. -----
0063 C.
0064 C. Y¿YÄY×Yí;¼YÉøÍÄ°øÈSTS_CHKøðOFFøÈø¹øÈ
0065 C.
0066 . S. RAM ram-271:MDP_DPL
0067 ( )
0068 C.
0069 . C. < Dump RAMID=MDP_DPL >
0070 +. DC 07-F0 MDP_DUMP_FGTBL
0071 BC (82 07 00 38 b8 00 40)
0072 C. -----
0073 C. MDP_DPL verify = OK [ ]
0074 C. -----
0075 C.
0076 C. STS_CHKøðONøÈø¹øÈ
0077 C.
0078 . C. < Update MDP DSC PAR1 >
0079 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0080 BC (4c)
0081 C. MDP_CMD_CODE = F04C0700[ ]
0082 C. MDP_CMD_CNT (count-up 1) [ ]
0083 C. -----
0084 C.
0085 . C.
0086 C. *****
0087 C. SOT TI command set
0088 C. *****
0089 C. Execute, after the success of TBL upload.
0090 +. TI 2010-10-05 11:02:18.0
0091 DC 07-F0 MDP_SOT_MODE_OBSV
0092 BC (40)
0093 . C. -----
0094 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0095 C. -----
```

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0096 C.
0097 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0098 +. DC 07-FC EIS_MODE_MANU
0099 BC (21 02)
0100 . C. Verify EIS in MANUAL mode
0101 . C. Estimated OBSTBL upload time is 3s
0102 C. *****
0103 C. EIS START OBSTBL LOAD
0104 C. *****
0105 . S. RAM ram-820:EIS_OBSTBL
0106 ( )
0107 +. DC 07-FC EIS_DUMP_OBSTBL
0108 BC (07 07 07 00 00 70 00)
0109 C.
0110 C. Execute, after the success of OBSTBL upload.
0111 C. Set EIS TI-commands
0112 +. TI 2010-10-05 11:02:50.0
0113 DC 07-FC EIS_MODE_CHG_ENA
0114 BC (20)
0115 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0116 C. *****
0117 C. EIS END OBSTBL LOAD
0118 C. *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ò%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝÁÝÇÝèñÊ½¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ¥DÝ¹•Ï Daily±¿ÍññÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ;ãLOS¥Á¥$¥Ã¥´¼Ã»Û;ã
0132 C.
0133 . C. ***** LOS *****
0134 C.

```



0096 C.  
0097 C.  
0098 C.  
0099 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0100 C.  
0101 +. DC 07-F0 MDP\_XRT\_CTRL\_MANU  
0102 BC (c1)  
0103 + DC 07-F0 MDP\_XRT\_MODE\_STBY  
0104 BC (c3)  
0105 . C. ----- Success Verify ? OK / NG\_\_\_\_  
0106 C.  
0107 C. XRT Obs. Table Upload  
0108 . S. RAM ram-291:MDP\_OBS\_X  
0109 (  
0110 C.  
0111 +. DC 07-F0 MDP\_DUMP\_XRTTBL  
0112 BC (84 07 00 00 00 3a d4)  
0113 . C. ----- Comparison Check ? OK / ERR \_\_\_\_  
0114 C.  
0115 C.  
0116 +. DC 07-F0 MDP\_XRT\_ROI\_SET  
0117 BC (cd 01 b1 b1 04 04)  
0118 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0119 BC (cd 02 b1 b1 08 08)  
0120 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0121 BC (cd 03 b1 b1 08 08)  
0122 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0123 BC (cd 04 b1 b1 06 06)  
0124 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0125 BC (cd 05 85 83 06 06)  
0126 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0127 BC (cd 06 85 83 06 06)  
0128 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0129 BC (cd 07 80 80 20 20)  
0130 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0131 BC (cd 08 80 80 20 08)  
0132 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0133 BC (cd 09 80 80 08 20)  
0134 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0135 BC (cd 0a 80 80 20 04)  
0136 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0137 BC (cd 0b c0 c0 10 10)  
0138 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0139 BC (cd 0c 40 c0 10 10)  
0140 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0141 BC (cd 0d 40 40 10 10)  
0142 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0143 BC (cd 0e c0 40 10 10)  
0144 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0145 BC (cd 0f 80 80 06 06)  
0146 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0147 BC (cd 10 80 80 08 08)  
0148 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0149 C.  
0150 C.  
0151 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0152 C.  
0153 +. DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0154 BC (c2)  
0155 +. TI 2010-10-05 11:02:02.0  
0156 DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0157 BC (c2)  
0158 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0159 C.  
0160 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0161 C.  
0162 . C. \*\*\*\*\* MDP ^úãîøî»ö%ýðÊãð¹²³DCBC•x²è \*\*\*\*\*  
0163 C. (%ã°îÿÓŸÁŸBŸFŸŸÁŸÇŸèøÉ%¼¼¼¼Ä»Û¹²è)  
0164 . S. DC-BC dcbc-402:DCBC  
0165 (MDP\_known\_event)  
0166 C.  
0167 C.  
0168 . C. \*\*\*\*\* ŸÐŸ¹•İ Daily±ġİñøĒ´Ø²³DCBC•x²è \*\*\*\*\*  
0169 . S. DC-BC dcbc-153:DCBC  
0170 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0171 C.  
0172 C.  
0173 . C. ĵãLOSŸÁŸŞŸÃŸ-¼¼Ä»Ûĵä  
0174 C.  
0175 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0176 C.

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\*\*\* OP Sequence for XRT \*\*\*

2010/10/05	11:11:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	11:11:26.0	XRT_FOCUS_RECALIBRATE_414_OG [0x19e]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2010/10/05	11:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	54	e5	00	fd
2010/10/05	11:15:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/05	11:15:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/05	11:15:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/05	11:15:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/05	11:15:52.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	11:15:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	11:15:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2010/10/05	11:15:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/05	11:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	14:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	14:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	14:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	14:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	14:37:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/05	14:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	16:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	16:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	16:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	16:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	16:26:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/05	16:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	17:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	17:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	17:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	17:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	18:03:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/05	18:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	18:44:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	18:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/05	18:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/10/05	18:45:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/05	18:45:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/05	18:45:20.0	XRT_ARS_DIS_442_OG [0x1ba]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	18:47:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/10/05	18:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	18:55:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/10/05	19:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	19:39:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/05	19:39:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/05	19:39:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/05	19:39:56.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/05	19:39:58.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	19:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	19:42:34.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			



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2010/10/05	19:42:36.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/05	19:42:38.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	20:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	20:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	20:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	20:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	21:17:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/05	21:17:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/05	21:17:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/05	21:17:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/05	21:17:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	21:20:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2010/10/05	21:20:04.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/05	21:20:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	22:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	22:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/05	22:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/05	22:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/05	22:52:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/05	22:53:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/05	23:00:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/05	23:00:02.5	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/05	23:00:22.5	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/05	23:00:24.5	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/05	23:00:26.5	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/05	23:00:28.5	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/05	23:03:00.5	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2010/10/05	23:03:02.5	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/05	23:03:04.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	00:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	00:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/06	00:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/06	00:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/06	00:17:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/06	00:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	01:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/06	01:00:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/06	01:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/06	01:00:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/06	01:00:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/06	01:03:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2010/10/06	01:03:04.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2010/10/06	01:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	01:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	01:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/06	01:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2010/10/06	01:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/06	01:51:30.0	XRT_Custom_418_OG [0x1a2]								
2010/10/06	01:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	03:07:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	03:07:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/06	03:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/06	03:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/06	03:28:30.0	XRT_Custom_418_OG [0x1a2]								
2010/10/06	03:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	04:37:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	04:37:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/06	04:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/06	04:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/06	05:06:00.0	XRT_Custom_418_OG [0x1a2]								
2010/10/06	05:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	05:49:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	05:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/06	05:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2010/10/06	05:50:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/06	05:50:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/06	05:50:20.0	XRT_ARS_DIS_442_OG [0x1ba]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/06	05:52:58.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/10/06	05:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	03 00 00 00 00				
2010/10/06	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	09:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/06	10:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]								
			AOCU_NM	5	02-76	00 ad cb 00 fd				
2010/10/06	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/06	10:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/06	10:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/06	10:00:22.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/06	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/06	10:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2010/10/06	10:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2010/10/06	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/06	12:00:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/06	12:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]								
			TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2010/10/06	14:00:30.0	XRT_Custom_436_OG [0x1b4]								
2010/10/06	16:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	03 00 00 00 00				
2010/10/06	16:00:30.0	XRT_Custom_436_OG [0x1b4]								
2010/10/06	18:00:30.0	XRT_Custom_436_OG [0x1b4]								
2010/10/06	20:00:30.0	XRT_Custom_437_OG [0x1b5]								
2010/10/06	20:00:40.0	XRT_Custom_436_OG [0x1b4]								
2010/10/06	22:00:40.0	XRT_Custom_447_OG [0x1bf]								
2010/10/07	00:00:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	00:00:02.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/07	00:00:18.0	XRT_Custom_437_OG [0x1b5]								
2010/10/07	00:00:22.0	XRT_QT_PROG_SET_439_OG [0x1b7]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/10/07	00:00:24.0	XRT_ARS_DIS_440_OG [0x1b8]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/07	00:00:26.0	XRT_FLD_DIS_443_OG [0x1bb]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/07	00:00:28.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2010/10/07	00:00:28.5	XRT_FLRCTRL_DIS_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/07	00:00:30.0	XRT_TCIB_XRT_S_HTR_A_DIS_416_OG [0x1a0]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2010/10/07	00:00:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	00:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	00:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/07	00:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/07	00:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/07	00:52:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	00:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	02:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	02:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/07	02:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/07	02:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/07	02:28:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	02:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	03:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	03:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/07	03:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/07	03:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/07	04:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	04:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	05:15:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	05:15:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/07	05:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/07	05:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/07	05:42:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	05:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/07	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/10/07	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/07	06:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/07	06:00:20.0	XRT_ARS_DIS_442_OG [0x1ba]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/07	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/10/07	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	06:09:54.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	06:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2010/10/07	06:12:32.0	XRT_FOCUS_POSITION_404_OG [0x194]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/07	06:12:52.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/10/07	06:12:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/07	06:12:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/07	06:12:58.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/07	06:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/07	06:19:54.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	06:20:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2010/10/07	06:22:32.0	XRT_FOCUS_POSITION_404_OG [0x194]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/07	06:22:52.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/10/07	06:22:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/07	06:22:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]							

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2010/10/07	06:22:58.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/07	06:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/07	06:29:54.0	XRT_CTRL_MANU_449_OG [0x1c1]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/07	06:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 d1 07 d1 07		
2010/10/07	06:32:32.0	XRT_FOCUS_POSITION_404_OG [0x194]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/10/07	06:32:52.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2010/10/07	06:32:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/10/07	06:32:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/10/07	06:32:58.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/07	06:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/07	06:39:54.0	XRT_CTRL_MANU_449_OG [0x1c1]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/07	06:40:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 d1 07 2e f9		
2010/10/07	06:42:32.0	XRT_FOCUS_POSITION_404_OG [0x194]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/10/07	06:42:52.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2010/10/07	06:42:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/10/07	06:42:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/10/07	06:42:58.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/07	06:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/07	06:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00		
2010/10/07	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		