

XRT Timeline to be uploaded on 2010/10/07

Period: 2010/10/07 10:40:00 - 2010/10/12 10:46:00

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Normal mode

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XOB #181C: Synoptic Q95 2x2 - Al/poly(33/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + (Al/Poly+Ti-poly)(64/4096)

Term	Pointing (x, y)	Comment
10/07 16:06:00 - 10/07 20:14:54	Fixed (-11.0, 30.0)	* XRT synoptic shifted manually, in addition to continued SOT HOP 79.
10/08 18:19:00 - 10/08 18:25:54	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min

PROG= 10 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 81 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 74 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1825: AR Al/Poly 384x384FOV at 1064 1048 60s cadence

Term	Pointing (x, y)	Comment
10/07 20:18:00 - 10/08 02:30:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 04:45:04 - 10/08 05:53:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 14:54:36 - 10/08 17:52:30	Track (702.0, 319.4) @ 10/08 14:40:00	back to AR 11111
10/08 18:29:00 - 10/09 05:51:30	Track (720.6, 322.3) @ 10/08 18:26:00	# AR 11111

PROG= 04 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└ Seqn= 2 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Subr= 2 60-time(s) 60.0sec												
└ Seqn=100 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	4.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1823: AR Al/Poly 384x384FOV at 1064 1048 30s cadence

Term	Pointing (x, y)	Comment
10/08 02:33:04 - 10/08 04:13:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.

PROG= 02 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└ Seqn=100 1-time(s) 30.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	4.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	4.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
10/08 06:32:30 - 10/08 06:39:24	Fixed (0.0, 0.0)	synoptic, shifted 29.5 min
10/09 06:04:30 - 10/09 06:05:50	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min

PROG= 03 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1826: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al (64s) - 2 loops

Term	Pointing (x, y)	Comment
10/08 06:42:30 - 10/08 12:30:00	Track (-31.0, -0.1) @ 10/08 06:39:30	# EIS sensitivity monitoring and spectral atlas, also XRT multi-filter synoptic, tracking near di
PROG= 18 1-time(s)		
Subr= 1 2-time(s) 300.0sec		
Seqn= 9 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Norm 64.0s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 12 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 14 1-time(s) 2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Dark 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #181E: Flare Standard Obs. with eruptions mode-A (FW1=Al/Poly)

Term	Pointing (x, y)	Comment
10/07 20:18:00 - 10/08 02:30:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 02:33:04 - 10/08 04:13:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 04:45:04 - 10/08 05:53:00	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 14:54:36 - 10/08 17:52:30	Track (702.0, 319.4) @ 10/08 14:40:00	back to AR 11111
10/08 18:29:00 - 10/09 05:51:30	Track (720.6, 322.3) @ 10/08 18:26:00	# AR 11111

PROG= 14 1-time(s)

Subr= 1 2-time(s) 620.0sec		
Seqn= 84 30-time(s) 20.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 177ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Al-poly/thick-Al	Al-poly/thick-Be close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Al-poly/thick-Be	Al-poly/thick-Be close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 2x2 384x384 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 8-time(s) 600.0sec		
Seqn= 84 5-time(s) 120.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 177ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Al-poly/thick-Al	Al-poly/thick-Be close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Al-poly/thick-Be	Al-poly/thick-Be close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 2x2 384x384 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 3 1-time(s) 2.0sec		
Seqn= 83 40-time(s) 600.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Al-poly/thick-Al	Al-poly/thick-Be close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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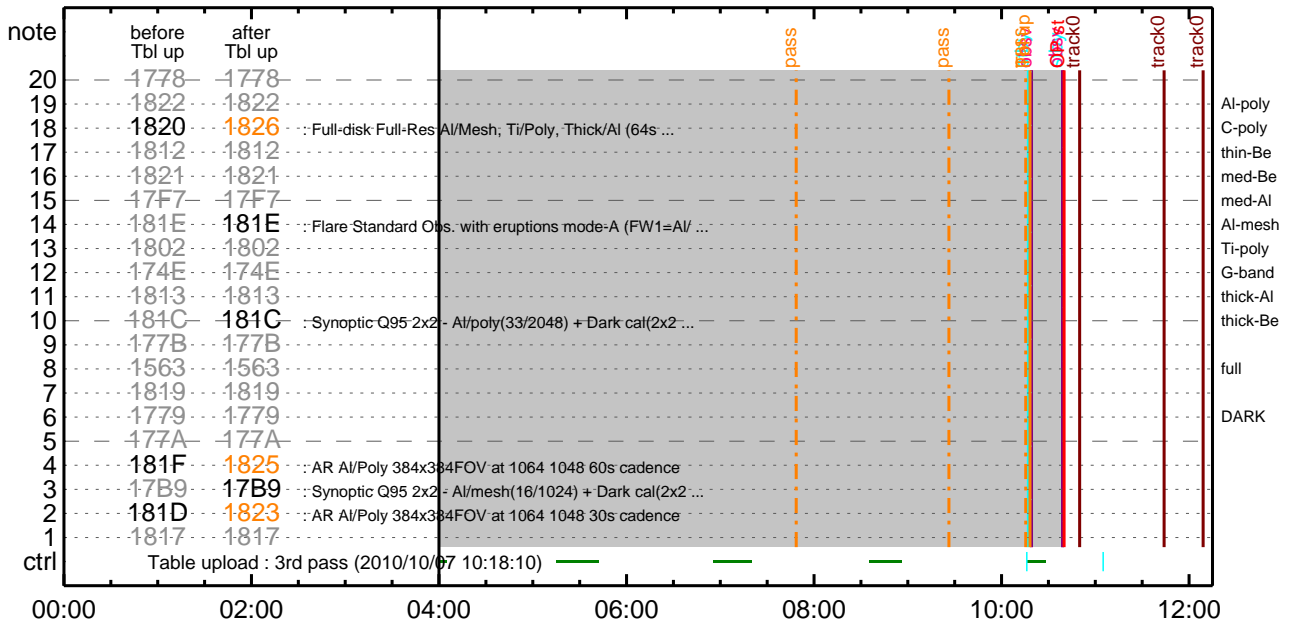
Flare Detection

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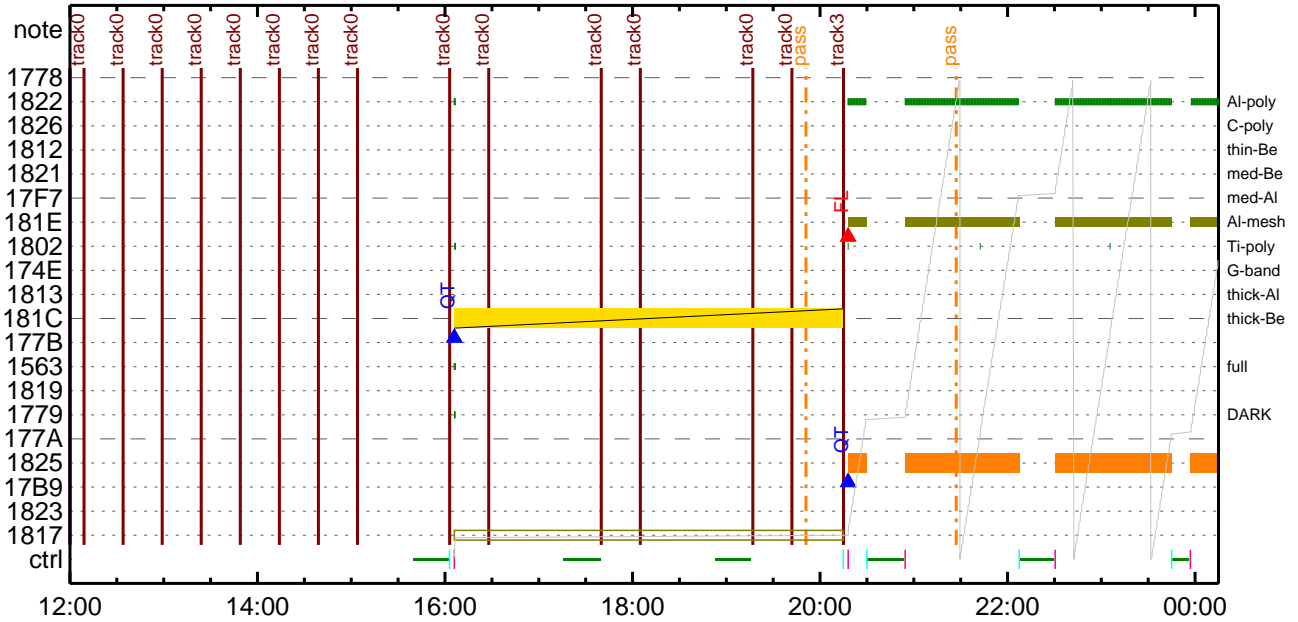
FLD Patrol

Term	Pointing (x, y)	Comment
10/07 20:15:16 - 10/08 06:29:46	Track (598.9, 306.5) @ 10/07 20:15:00	* HOP 171 - Track AR 11111.
10/08 14:51:52 - 10/08 18:16:16	Track (702.0, 319.4) @ 10/08 14:40:00	back to AR 11111
10/08 18:26:16 - 10/09 06:01:46	Track (720.6, 322.3) @ 10/08 18:26:00	# AR 11111
Al-poly/Open	Al-poly/Open close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

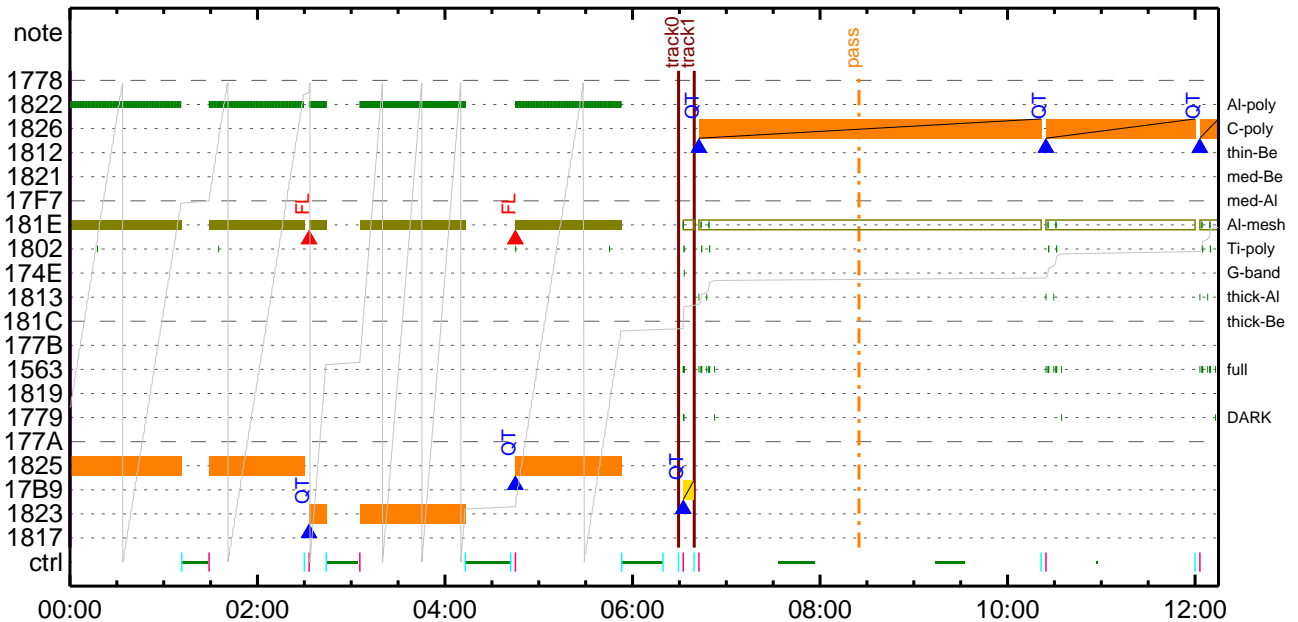
CMDI #0531 2010/10/07



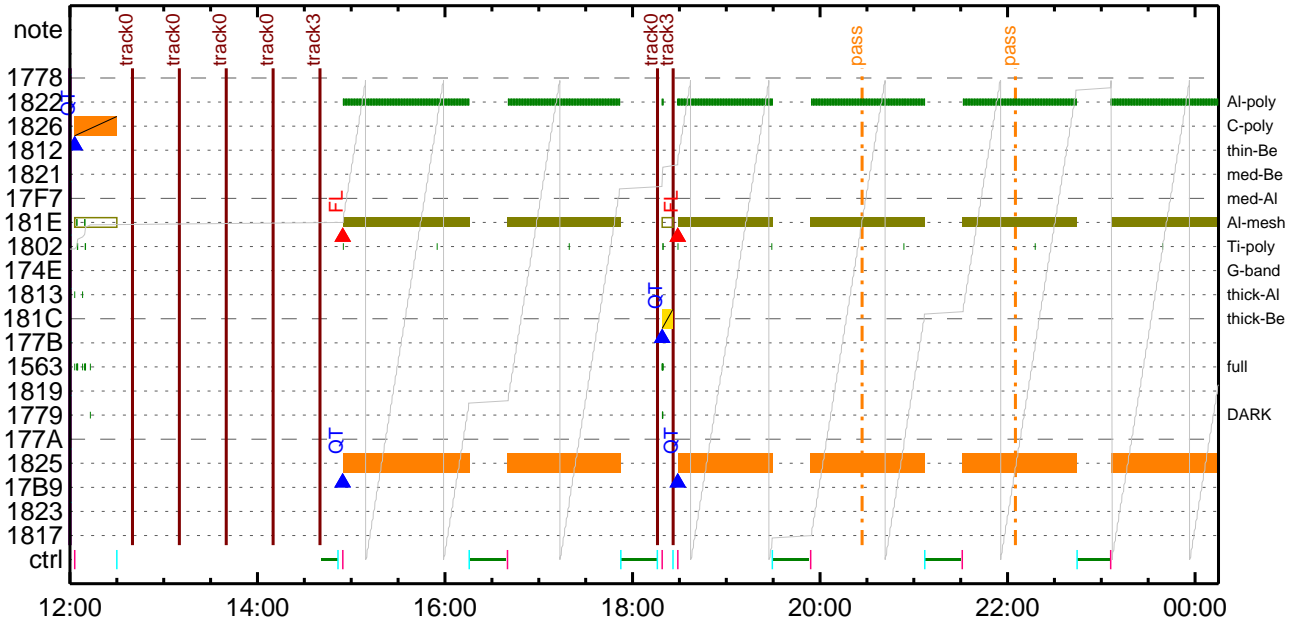
CMDI #0531 2010/10/07



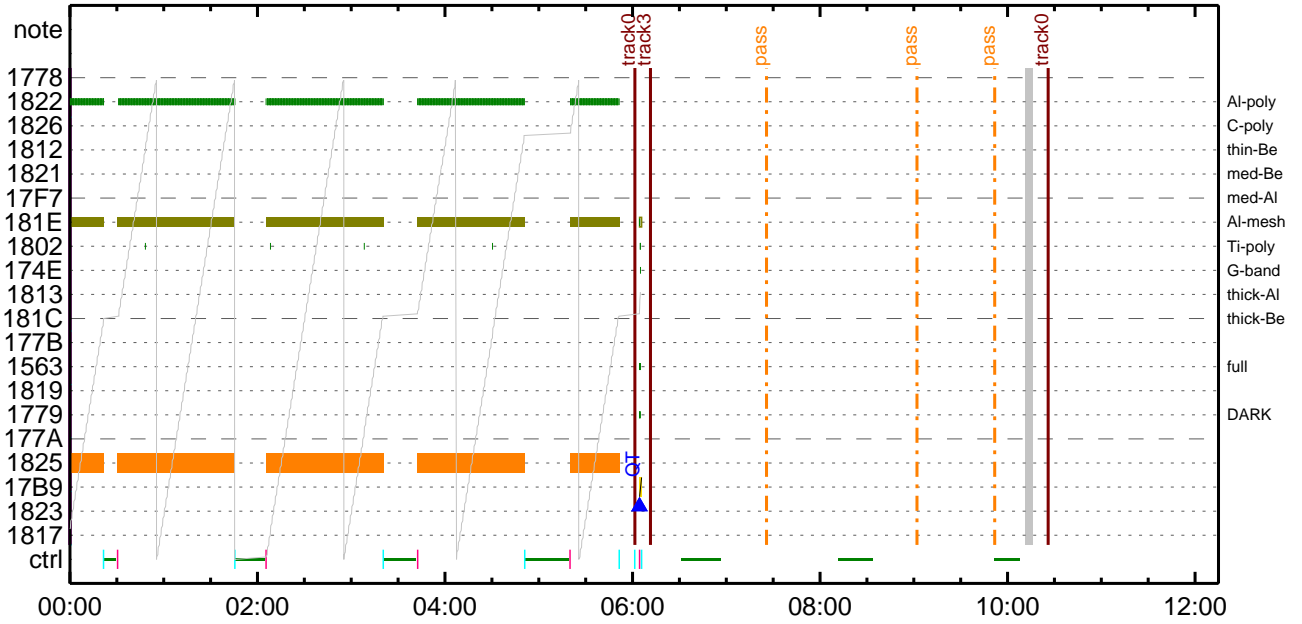
CMDI #0531 2010/10/08



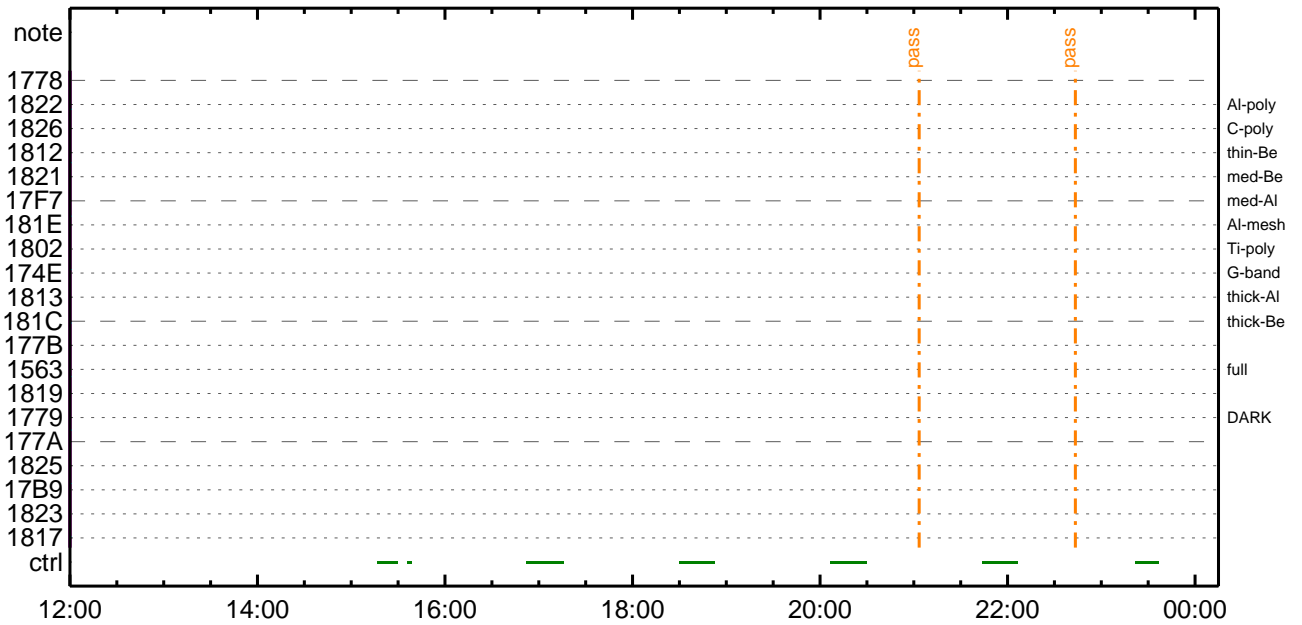
CMDI #0531 2010/10/08



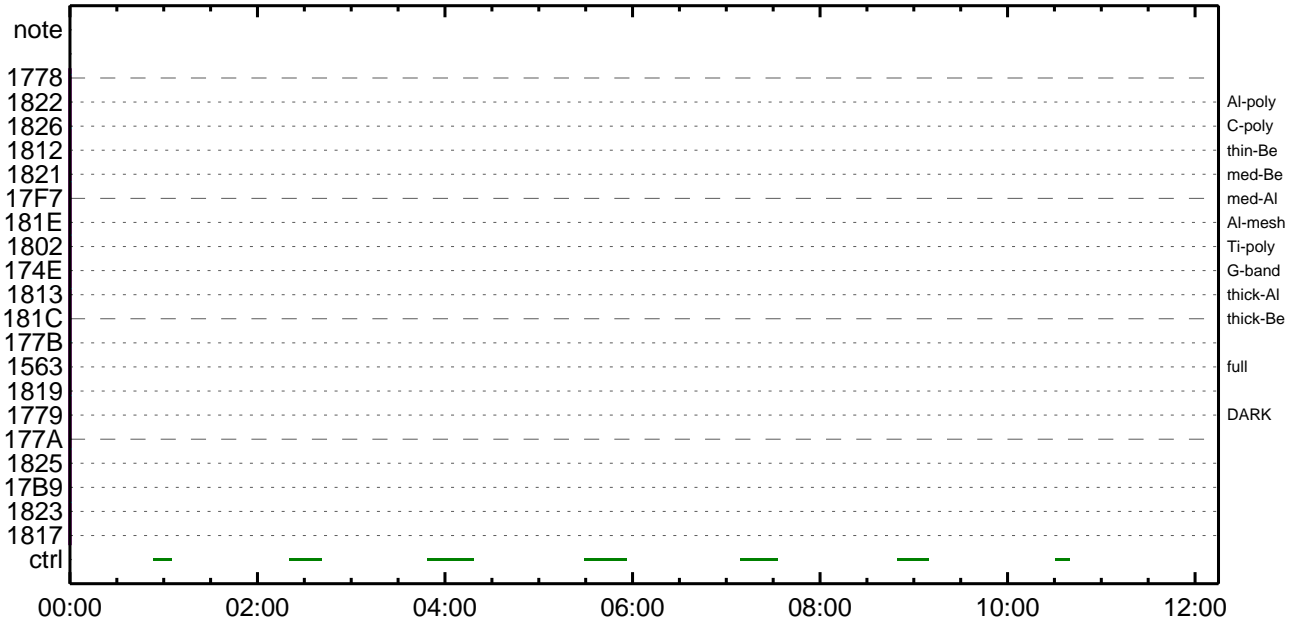
CMDI #0531 2010/10/09



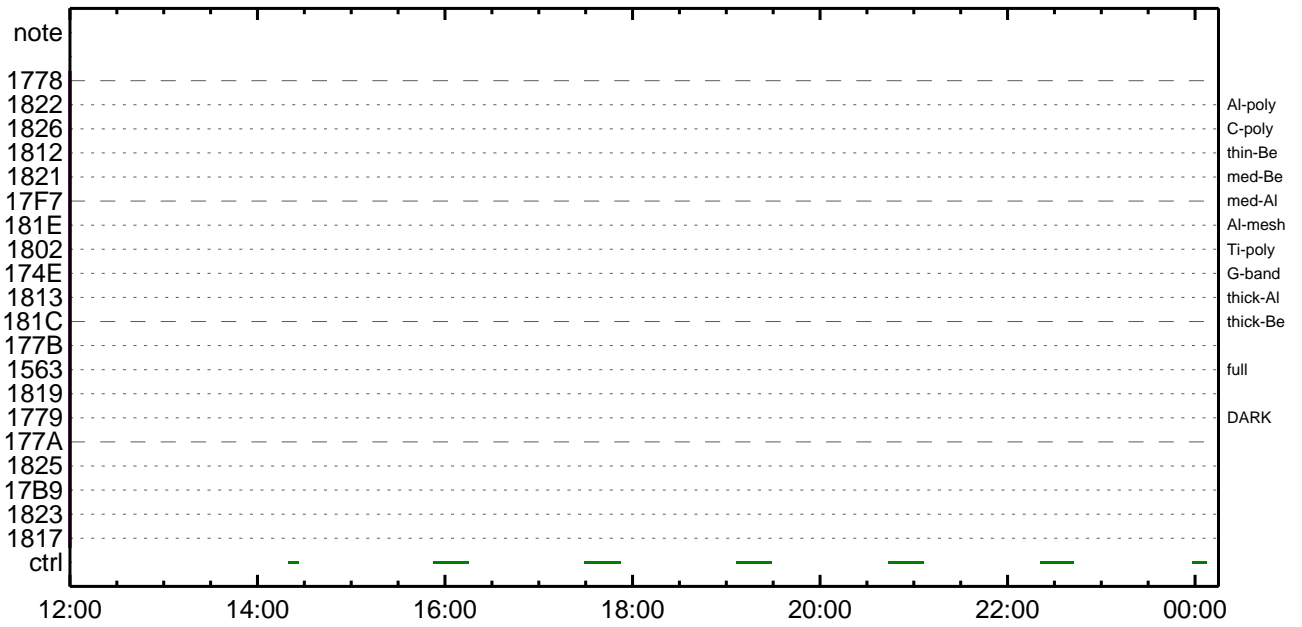
CMDI #0531 2010/10/09



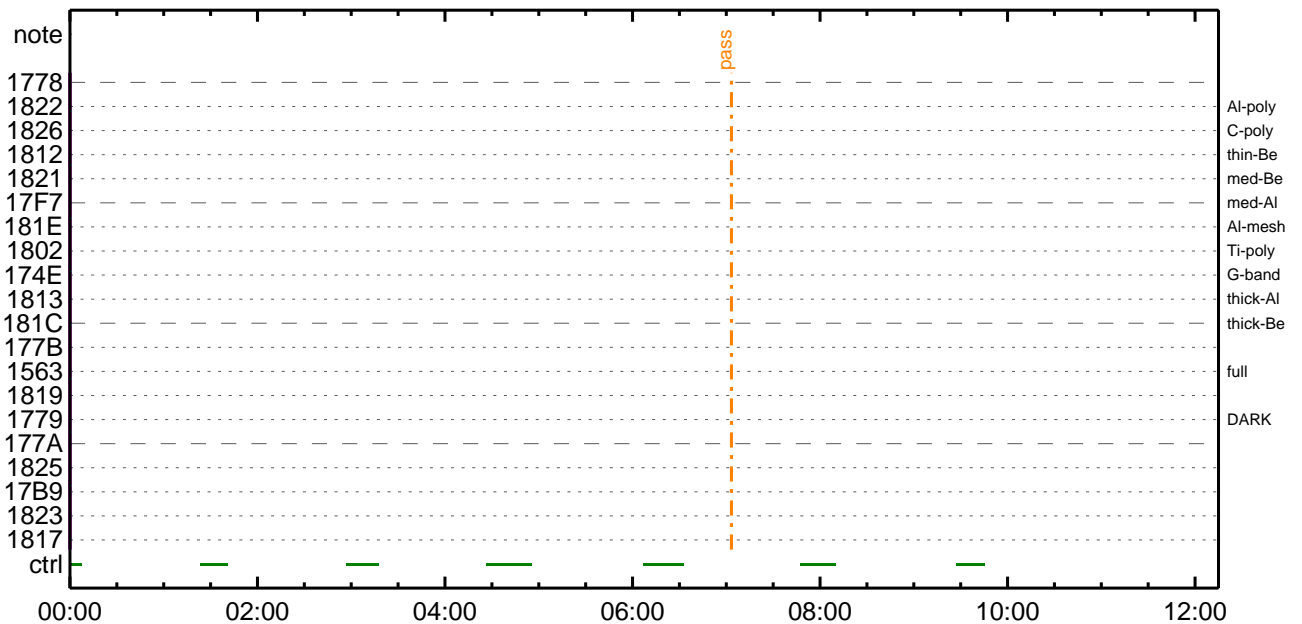
CMDI #0531 2010/10/10



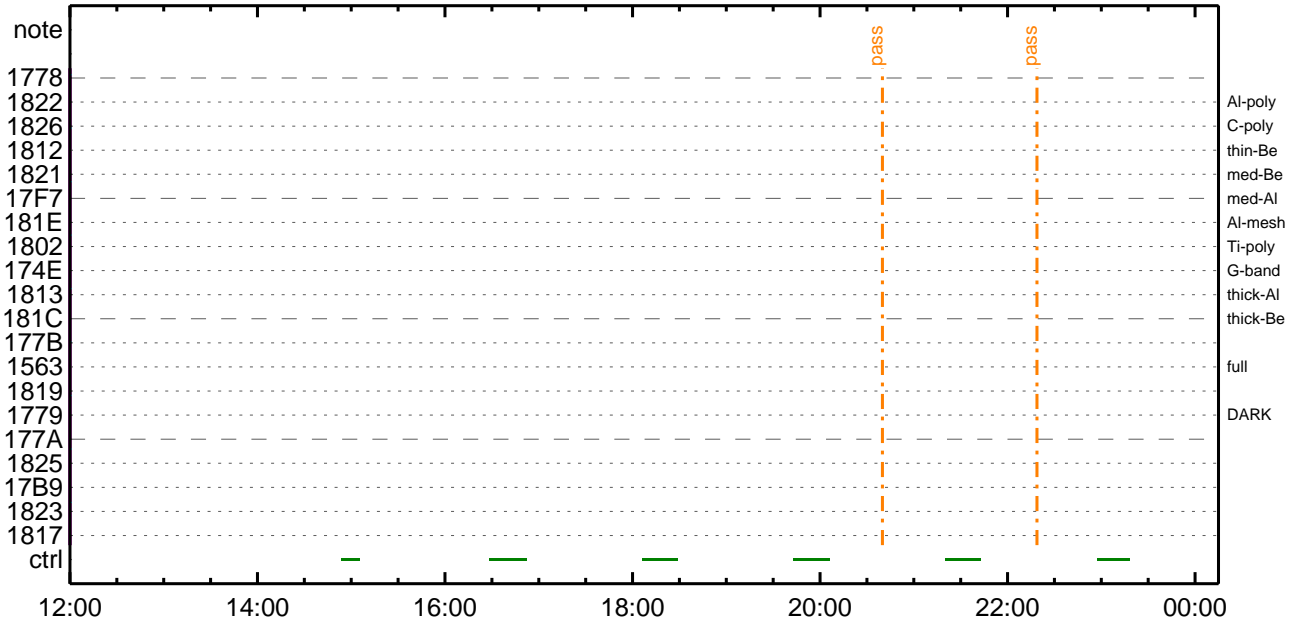
CMDI #0531 2010/10/10



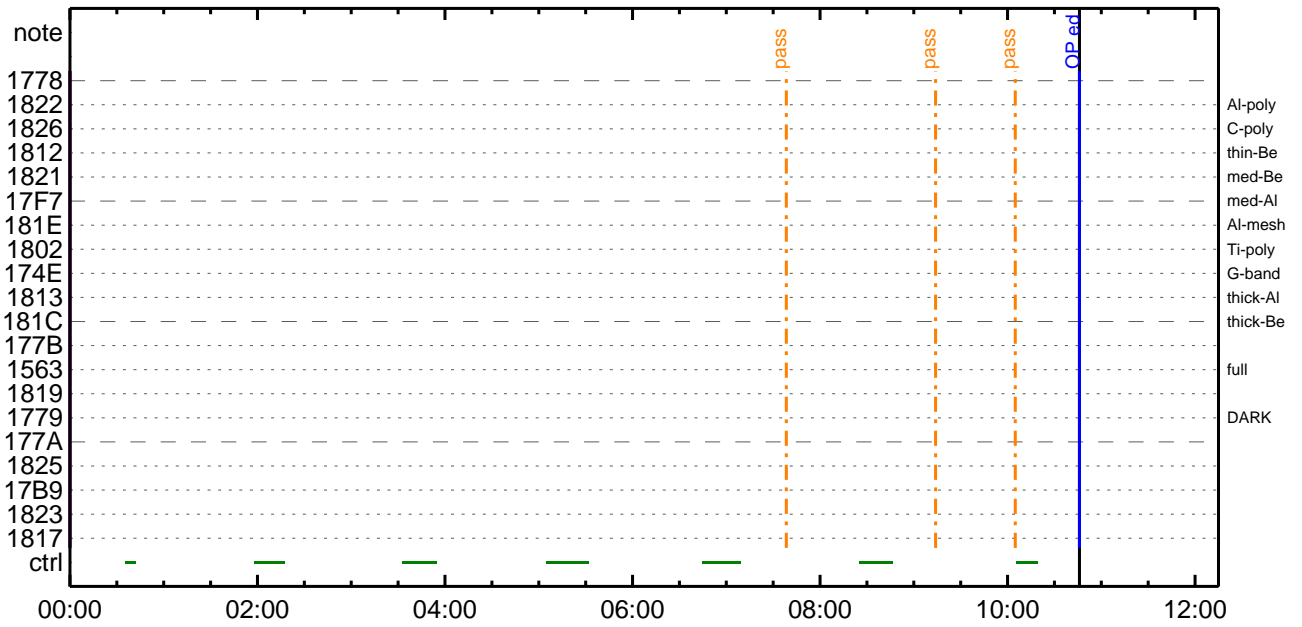
CMDI #0531 2010/10/11



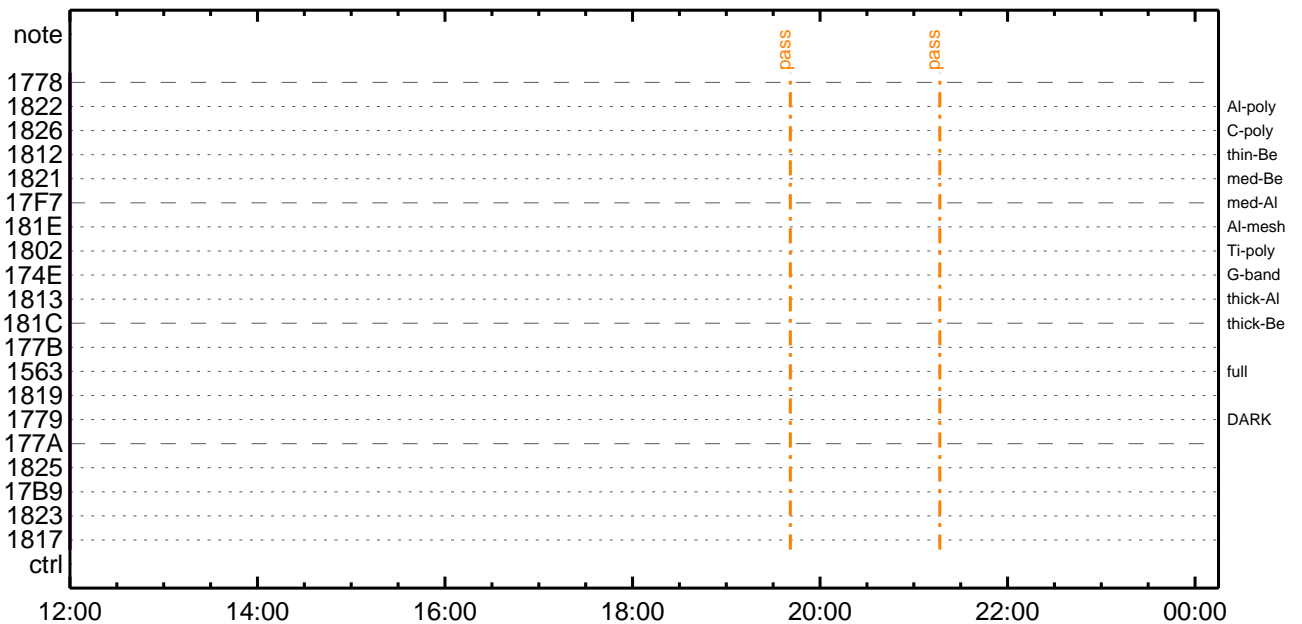
CMDI #0531 2010/10/11



CMDI #0531 2010/10/12



CMDI #0531 2010/10/12




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉ;ã
0103 S. OP op-626:OP
0104 ( )
0105 S. OG og-626:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÏÏ°eYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYÓYx½ªÏ»oð³ÏÇS
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãÏÏÏ¹Ç•ë²ÏOKoð³ÏÇS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYÓYx½ªÏ»oð³ÏÇS
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãÏÏÏ¹Ç•ë²ÏOKoð³ÏÇS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYÓYx½ªÏ»oð³ÏÇS
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPãÏÏÏ¹Ç•ë²ÏOKoð³ÏÇS
0165 C.
0166 C. ***** oË²¼oÏÏÏ¹Ï°ÏÏÏ¹-ã°Á÷ç®(½ãµ-YAYÓYx½ªÏ»oð³ÏÇSãOÁÏæç¼ªª°oð¾i¹çççãâ) *****
0167 C. DHUYã;YÉ;ËY½;Y;YÉ;ËoðÏã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;s OPOG UPLOADã-Á÷ç®NGuÏÏ¹¹ç;ç°Ë²¼oÏÏÏ¹Ï°ÏÏÏ¹-CMDÁ÷ç®ãÏÏÏ¹¹Ï°ã°ãÏão³oðE;f
0180 C. oðoç;çSETãEDUMPAÏÏ±°iYÑY¹ç¹Ïã|o³oðE;f
0181 C.
0182 C. TIY³YÞYÓYÉoðãÁÏÏ¹ç(UT)
0183 +. TI 2010-10-07 10:35:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-10-07 10:35:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-10-07 10:35:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCS_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 []
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK []
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 []
0126 C.
0127 +. DC 02-8E AOCS_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 39s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ()
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2010-10-07 10:39:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'uAÎpî»ö¼ÝpëÄðp¹pèDCBC•x²è *****
0153 C. (%ã°IÝÓYÁYÈYpYÈYáYçYèpE¼q¼Ä»Ûp¹pè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** YDÝ¹•İ Daily±;İÑpÈ'Øp¹pèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÝÁÝSÝÄÝ¹¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

*** OP Sequence for XRT ***

2010/10/07	10:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	54	00	00	fd
2010/10/07	11:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/07	11:05:02.0	XRT_STANDBY_426_OG [0x1aa]							
		XRT_STBY	2	07-F8	03	01			
2010/10/07	11:05:04.0	XRT_DIAGNOSTIC_427_OG [0x1ab]							
		XRT_DIAGNOSTIC	2	07-F8	03	07			
2010/10/07	11:05:06.0	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/07	11:05:08.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/10/07	11:05:11.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/07	11:05:13.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/07	11:05:16.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/07	11:05:18.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00	
2010/10/07	11:05:21.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:23.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:26.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:28.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:31.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:33.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:36.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:38.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:41.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:43.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:46.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:48.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:51.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:53.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:56.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:05:58.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:01.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:03.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:06.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:08.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:11.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:13.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:16.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:18.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:21.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:23.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:26.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:28.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:31.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:33.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:36.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:38.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:41.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:43.5	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:46.0	XRT_FSA_CONTROL_434_OG [0x1b2]							
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:48.5	XRT_FSA_CONTROL_434_OG [0x1b2]							

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2010/10/07	11:06:51.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:53.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:56.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:06:58.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:01.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:03.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:06.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:08.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:11.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:13.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:16.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:18.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:21.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:23.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:26.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:28.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:31.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/07	11:07:33.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:36.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
			XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/07	11:07:38.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
			XRT_FW1_STEP_CW	4	07-F8	51	46	00	00	
2010/10/07	11:07:41.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
			XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00	
2010/10/07	11:07:43.5	XRT_FSA_CONTROL_433_OG [0x1b1]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00	
			XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00	
2010/10/07	11:07:46.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_RESET	4	07-F8	51	40	00	00	
			XRT_FW1_RESET	4	07-F8	51	40	00	00	
2010/10/07	11:44:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	4c	72	00	fd
			AOCU_NM	5	02-76	00	43	8d	00	fd
2010/10/07	12:34:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	3a	a7	00	fd
			AOCU_NM	5	02-76	00	31	ca	00	fd
2010/10/07	12:59:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	31	ca	00	fd
			AOCU_NM	5	02-76	00	28	e5	00	fd
2010/10/07	13:24:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	28	e5	00	fd
			AOCU_NM	5	02-76	00	20	00	00	fd
2010/10/07	13:49:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	20	00	00	fd
			AOCU_NM	5	02-76	00	17	1a	00	fd
2010/10/07	14:14:00.0	AOCS_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	17	1a	00	fd
			AOCU_NM	5	02-76	00	0e	35	00	fd
2010/10/07	14:39:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	0e	35	00	fd
			AOCU_NM	5	02-76	00	05	58	00	fd
2010/10/07	15:04:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	05	58	00	fd
			AOCU_NM	5	02-76	00	05	58	00	fd
2010/10/07	16:02:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/07	16:02:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/07	16:03:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	fd	59	00	fd
			AOCU_NM	5	02-76	00	fd	59	00	fd
2010/10/07	16:03:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0		d9			
			MDP_XRT_FLD_DIS	1	07-F0		d9			
2010/10/07	16:03:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2010/10/07	16:03:20.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0		d5			
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/10/07	16:05:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0a		
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	0a		
2010/10/07	16:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/07	16:28:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	f4	73	00	fd
			AOCU_NM	5	02-76	00	f4	73	00	fd
2010/10/07	17:40:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	eb	8e	00	fd
			AOCU_NM	5	02-76	00	eb	8e	00	fd
2010/10/07	18:05:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	e2	a8	00	fd
			AOCU_NM	5	02-76	00	e2	a8	00	fd
2010/10/07	19:17:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	d9	cb	00	fd
			AOCU_NM	5	02-76	00	d9	cb	00	fd
2010/10/07	19:42:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	d0	e6	00	fd
			AOCU_NM	5	02-76	00	d0	e6	00	fd
2010/10/07	20:14:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			

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2010/10/07	20:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/07	20:15:00.0	AOCs_OrE-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/10/07	20:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/10/07	20:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/10/07	20:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2010/10/07	20:15:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/10/07	20:17:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/07	20:17:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	04		
2010/10/07	20:17:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	0e		
2010/10/07	20:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/07	20:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/07	20:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/07	20:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/10/07	20:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/10/07	20:53:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	20:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/07	22:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/07	22:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/07	22:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/10/07	22:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/10/07	22:29:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	22:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/07	23:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/07	23:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/07	23:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/10/07	23:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/10/07	23:56:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/07	23:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/08	01:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/08	01:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/08	01:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/10/08	01:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/10/08	01:28:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/08	01:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/08	02:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/08	02:30:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/08	02:30:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/10/08	02:30:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/10/08	02:30:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2010/10/08	02:30:28.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/10/08	02:33:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	02		
2010/10/08	02:33:02.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	0e		
2010/10/08	02:33:04.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/10/08	02:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/10/08	02:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/10/08	02:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/10/08	02:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/10/08	03:04:31.0	XRT_Custom_418_OG [0x1a2]							
2010/10/08	03:05:31.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			

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2010/10/08	04:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	04:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	04:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	04:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	04:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	04:42:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/08	04:42:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/08	04:42:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/08	04:42:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/08	04:42:28.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/08	04:45:00.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/10/08	04:45:02.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2010/10/08	04:45:04.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	05:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	05:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	05:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	05:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	06:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	06:19:32.0	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW1_STATUS_LSB	4	07-F8	51 e0 00 00				
2010/10/08	06:19:34.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_RESET	4	07-F8	51 40 00 00				
2010/10/08	06:19:37.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:39.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:42.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:44.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:47.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:49.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:52.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:54.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:57.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:19:59.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:02.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:04.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:07.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:09.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:12.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:14.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:17.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:19.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:22.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:24.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:27.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:29.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:32.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:34.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:37.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:39.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/08	06:20:42.0	XRT_FSA_CONTROL_430_OG [0x1ae]							

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2010/10/08	06:20:44.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:47.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:49.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:52.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:54.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:57.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:20:59.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:02.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:04.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:07.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:09.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:12.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:14.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:17.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:19.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:22.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:24.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:27.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:29.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:32.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:34.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:37.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:39.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:42.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:44.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:47.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:49.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:52.0	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:54.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:21:57.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	06:21:59.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:22:02.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	06:22:04.5	XRT_FSA_CONTROL_430_OG [0x1ae]	XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00
2010/10/08	06:22:07.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	06:22:09.5	XRT_FSA_CONTROL_433_OG [0x1b1]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/10/08	06:22:12.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/10/08	06:29:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/08	06:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/10/08	06:29:30.5	AOCs_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	00	00	00
2010/10/08	06:29:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/08	06:29:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/10/08	06:29:50.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/08	06:32:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2010/10/08	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/08	06:39:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/08	06:39:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00

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2010/10/08	06:39:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]								
		AOCU_NM	5	02-76	01	00	00	00	00	
2010/10/08	06:39:46.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2010/10/08	06:39:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2010/10/08	06:39:50.0	XRT_ARS_DIS_442_OG [0x1ba]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/10/08	06:42:28.0	XRT_QT_PROG_SET_414_OG [0x19e]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2010/10/08	06:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/10/08	10:21:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/10/08	10:21:32.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2010/10/08	10:21:52.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2010/10/08	10:21:54.0	XRT_FLRCTRL_DIS_403_OG [0x193]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2010/10/08	10:21:56.0	XRT_ARS_DIS_415_OG [0x19f]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/10/08	10:24:32.0	XRT_QT_PROG_SET_414_OG [0x19e]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2010/10/08	10:24:34.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/10/08	12:00:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/10/08	12:00:02.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2010/10/08	12:00:22.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2010/10/08	12:00:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2010/10/08	12:00:26.0	XRT_ARS_DIS_415_OG [0x19f]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/10/08	12:03:02.0	XRT_QT_PROG_SET_414_OG [0x19e]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2010/10/08	12:03:04.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/10/08	12:30:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/10/08	12:30:02.0	XRT_FSA_CONTROL_428_OG [0x1ac]								
		XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00		
2010/10/08	12:30:04.5	XRT_FSA_CONTROL_429_OG [0x1ad]								
		XRT_FW1_RESET	4	07-F8	51	40	00	00		
2010/10/08	12:30:07.0	XRT_FSA_CONTROL_430_OG [0x1ae]								
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00		
2010/10/08	12:30:09.5	XRT_FSA_CONTROL_430_OG [0x1ae]								
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00		
2010/10/08	12:30:12.0	XRT_FSA_CONTROL_430_OG [0x1ae]								
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00		
2010/10/08	12:30:14.5	XRT_FSA_CONTROL_430_OG [0x1ae]								
		XRT_FW1_STEP_CCW	4	07-F8	51	48	00	00		
2010/10/08	12:30:17.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:19.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:22.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:24.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:27.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:29.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:32.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:34.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:37.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:39.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:42.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:44.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:47.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:49.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:52.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:54.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:57.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:30:59.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:31:02.0	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		
2010/10/08	12:31:04.5	XRT_FSA_CONTROL_434_OG [0x1b2]								
		XRT_FW1_STEP_CW	4	07-F8	51	46	00	00		

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2010/10/08	12:31:07.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:09.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:12.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:14.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:17.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:19.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:22.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:24.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:27.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:29.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:32.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:34.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:37.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:39.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:42.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:44.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:47.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:49.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:52.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:54.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:57.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:31:59.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:02.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:04.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:07.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:09.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:12.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:14.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:17.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:19.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:22.0	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:24.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:27.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	12:32:29.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:32.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	12:32:34.5	XRT_FSA_CONTROL_434_OG [0x1b2]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/08	12:32:37.0	XRT_FSA_CONTROL_428_OG [0x1ac]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/08	12:32:39.5	XRT_FSA_CONTROL_433_OG [0x1b1]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/10/08	12:32:42.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/10/08	12:40:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	c8	01	00 fd
2010/10/08	13:10:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	bf	1b	00 fd
2010/10/08	13:40:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	b6	36	00 fd
2010/10/08	14:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	ad	59	00 fd
2010/10/08	14:40:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00	00	00 00
2010/10/08	14:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2010/10/08	14:51:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00

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2010/10/08	14:51:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/08	14:51:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/08	14:51:56.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/08	14:51:58.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/08	14:54:30.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	14:54:32.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2010/10/08	14:54:34.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2010/10/08	14:54:36.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	16:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	16:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	16:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	16:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	16:39:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/08	16:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	17:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	17:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	17:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	17:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	18:15:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	18:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/08	18:16:00.5	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/10/08	18:16:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/08	18:16:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/08	18:16:20.0	XRT_ARS_DIS_442_OG [0x1ba]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/08	18:18:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2010/10/08	18:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	18:25:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	18:25:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/08	18:26:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/10/08	18:26:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/08	18:26:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/08	18:26:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/08	18:26:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/08	18:28:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	18:28:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2010/10/08	18:28:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2010/10/08	18:29:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	19:53:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/08	19:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	21:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	21:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	21:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	21:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	21:30:00.5	XRT_Custom_418_OG [0x1a2]							

2010/10/08	21:31:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/08	22:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/08	22:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/08	22:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/08	22:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/08	23:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/08	23:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/09	00:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/09	00:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/09	00:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/09	00:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/09	00:29:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/09	00:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/09	01:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/09	01:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/09	01:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/09	01:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/09	02:04:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/09	02:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/09	03:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/09	03:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/09	03:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/09	03:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/09	03:41:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/09	03:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/09	04:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/09	04:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/09	04:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/09	04:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/09	05:19:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/09	05:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/09	05:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/09	05:51:32.0	XRT_FSA_CONTROL_428_OG [0x1ac]							
		XRT_FW1_STATUS_LSB	4	07-F8	51 e0 00 00				
2010/10/09	05:51:34.5	XRT_FSA_CONTROL_429_OG [0x1ad]							
		XRT_FW1_RESET	4	07-F8	51 40 00 00				
2010/10/09	05:51:37.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:39.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:42.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:44.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:47.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:49.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:52.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:54.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:57.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:51:59.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:52:02.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:52:04.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:52:07.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:52:09.5	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				
2010/10/09	05:52:12.0	XRT_FSA_CONTROL_430_OG [0x1ae]							
		XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00				

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2010/10/09	05:54:12.0	XRT_FSA_CONTROL_429_OG [0x1ad]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
			XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/10/09	06:01:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/09	06:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/10/09	06:01:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	00	00	00
2010/10/09	06:01:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/09	06:01:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/10/09	06:01:50.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/09	06:04:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2010/10/09	06:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/09	06:05:50.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/09	06:05:52.5	XRT_STANDBY_426_OG [0x1aa]	XRT_STBY	2	07-F8	03	01		
2010/10/09	06:05:54.5	XRT_OPERATE_416_OG [0x1a0]	XRT_OPERATE	2	07-F8	03	02		
2010/10/09	06:11:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00	00	00
2010/10/09	10:26:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	00	00	00