

XRT Timeline to be uploaded on 2010/10/21

Period: 2010/10/21 09:49:00 - 2010/10/26 10:25:00

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Normal mode

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XOB #182D: HOP80 N-pole and AR 2-filter -2x2 FOV1024 - Al-mesh(Ti/Poly) + Ti-poly(Thick-Al) -Q95															
Term		Pointing (x, y)					Comment								
10/21 10:02:02 - 10/21 13:59:54		Track (-164.1, 802.8) ^{Ⓜ 10/21 09:59:00}					# OP start + 10min, HOP 80, polar coronal hole study.								
PROG= 09 Inf.-time(s)															
┌ Subr= 1 1-time(s) 2.0sec															
└ Seqn= 96 1-time(s) 2.0sec															
└ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 1024x1024 (1024, 768) Q=95 0 0 2.0sec															
┌ Subr= 2 40-time(s) 2.0sec															
└ Seqn= 76 1-time(s) 60.0sec															
└ Open/Al-mesh Open/Ti-poly close Safe Norm 125ms Obs 2x2 1024x1024 (1024, 768) Q=95 1 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 2x2 1024x1024 (1024, 768) Q=95 1 0 2.0sec															
Default Filter		Thicker Filter		VLS		mode		image		Exp.		ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #182B: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 60sec-cad															
Term		Pointing (x, y)					Comment								
10/21 14:03:02 - 10/21 17:44:30		Fixed (831.0, -450.0)					* HOP 165, outflow studies based on EIS and SWAP, looking at AR11112 limb.								
10/21 18:21:02 - 10/22 05:55:24		Fixed (831.0, -450.0)					# AR11112 at W limb.								
10/22 06:08:32 - 10/22 11:59:54		Fixed (831.0, -450.0)					# Cont.								
PROG= 20 Inf.-time(s)															
┌ Subr= 2 1-time(s) 2.0sec															
└ Seqn= 19 1-time(s) 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec															
└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec															
└ Seqn= 95 4-time(s) 2.0sec															
└ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec															
└ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec															
┌ Subr= 1 1-time(s) 2.0sec															
└ Seqn= 18 45-time(s) 30.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec															
Default Filter		Thicker Filter		VLS		mode		image		Exp.		ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)															
Term		Pointing (x, y)					Comment								
10/21 18:10:58 - 10/21 18:17:54		Fixed (0.0, 0.0)					synoptic, shifted 8.0 min								
10/22 05:58:28 - 10/22 06:05:24		Fixed (0.0, 0.0)					synoptic, shifted -4.5 min								
10/22 18:01:28 - 10/22 18:08:24		Fixed (0.0, 0.0)					synoptic, shifted -1.5 min								
10/23 06:02:58 - 10/23 06:09:54		Fixed (0.0, 0.0)					synoptic								
PROG= 03 1-time(s)															
┌ Subr= 1 1-time(s) 12.0sec															
└ Seqn= 7 1-time(s) 4.0sec															
└ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec															
└ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec															
└ Seqn= 5 1-time(s) 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec															
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec															
└ Seqn= 8 1-time(s) 4.0sec															
└ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec															
└ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec															
└ Seqn= 4 1-time(s) 2.0sec															
└ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec															
Default Filter		Thicker Filter		VLS		mode		image		Exp.		ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1827: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 120s cad												
Term		Pointing (x, y)					Comment					
10/22 12:03:02 - 10/22 17:58:24		Track (515.7, 200.8) ^{Ⓜ 10/22 12:00:00}					* Track AR 11113.					
10/22 18:11:32 - 10/23 05:59:54		Track (560.1, 204.0) ^{Ⓜ 10/22 18:08:30}					# Cont.					
10/23 06:13:02 - 10/23 07:50:00		Track (641.2, 210.9) ^{Ⓜ 10/23 06:10:00}					# Cont.					
PROG= 15 Inf.-time(s)												
┌ Subr= 1 1-time(s) 2.0sec												
└ Seqn= 19 1-time(s) 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└ Seqn= 20 4-time(s) 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												

Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 15 20-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
10/21 10:02:02 - 10/21 13:59:54	Track (-164.1, 802.8) @ 10/21 09:59:00	# OP start + 10min, HOP 80, polar coronal hole study.
10/21 14:03:02 - 10/21 17:44:30	Fixed (831.0, -450.0)	* HOP 165, outflow studies based on EIS and SWAP, looking at AR11112 limb.
10/21 18:21:02 - 10/22 05:55:24	Fixed (831.0, -450.0)	# AR11112 at W limb.
10/22 06:08:32 - 10/22 11:59:54	Fixed (831.0, -450.0)	# Cont.
10/22 12:03:02 - 10/22 17:58:24	Track (515.7, 200.8) @ 10/22 12:00:00	* Track AR 11113.
10/22 18:11:32 - 10/23 05:59:54	Track (560.1, 204.0) @ 10/22 18:08:30	# Cont.
10/23 06:13:02 - 10/23 07:50:00	Track (641.2, 210.9) @ 10/23 06:10:00	# Cont.

PROG= 16 1-time(s)

Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

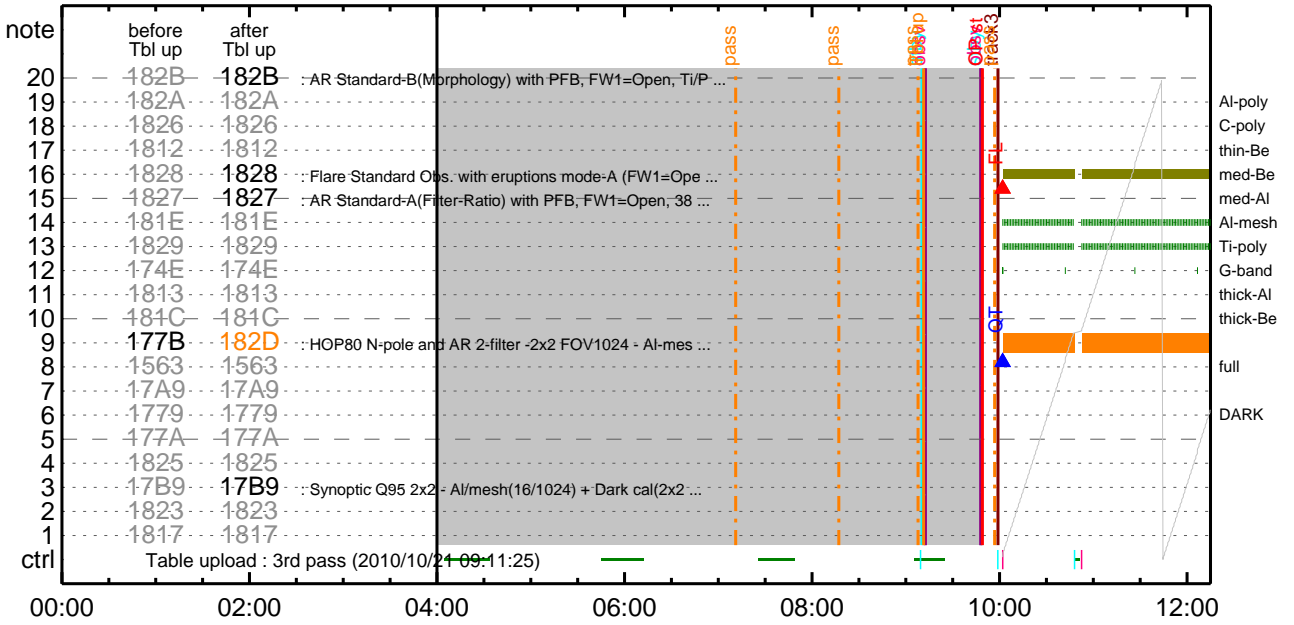
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Flare Detection

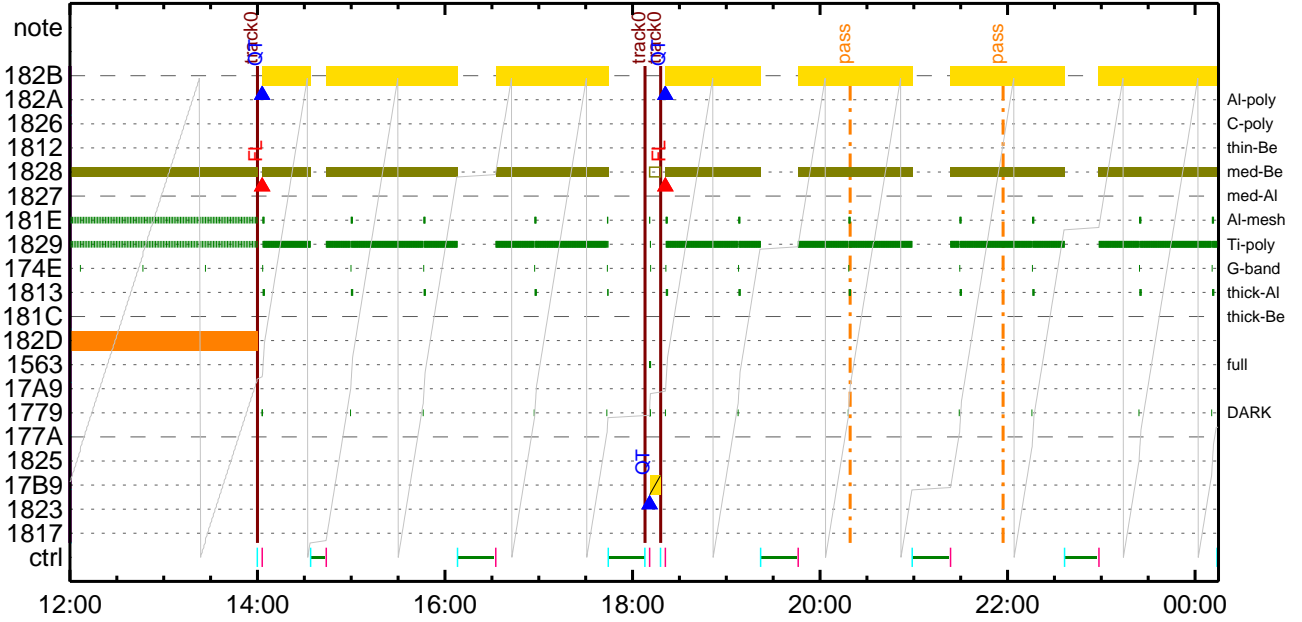
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FLD Patrol												
Term		Pointing (x, y)						Comment				
10/21 09:59:16 - 10/21 18:08:16		Track (-164.1, 802.8) @ 10/21 09:59:00						# OP start + 10min, HOP 80, polar coronal hole study.				
10/21 18:18:16 - 10/22 05:55:46		Fixed (831.0, -450.0)						# AR11112 at W limb.				
10/22 06:05:46 - 10/22 17:58:46		Fixed (831.0, -450.0)						# Cont.				
10/22 18:08:46 - 10/23 06:00:16		Track (560.1, 204.0) @ 10/22 18:08:30						# Cont.				
10/23 06:10:16 - 10/26 10:25:00		Track (641.2, 210.9) @ 10/23 06:10:00						# Cont.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

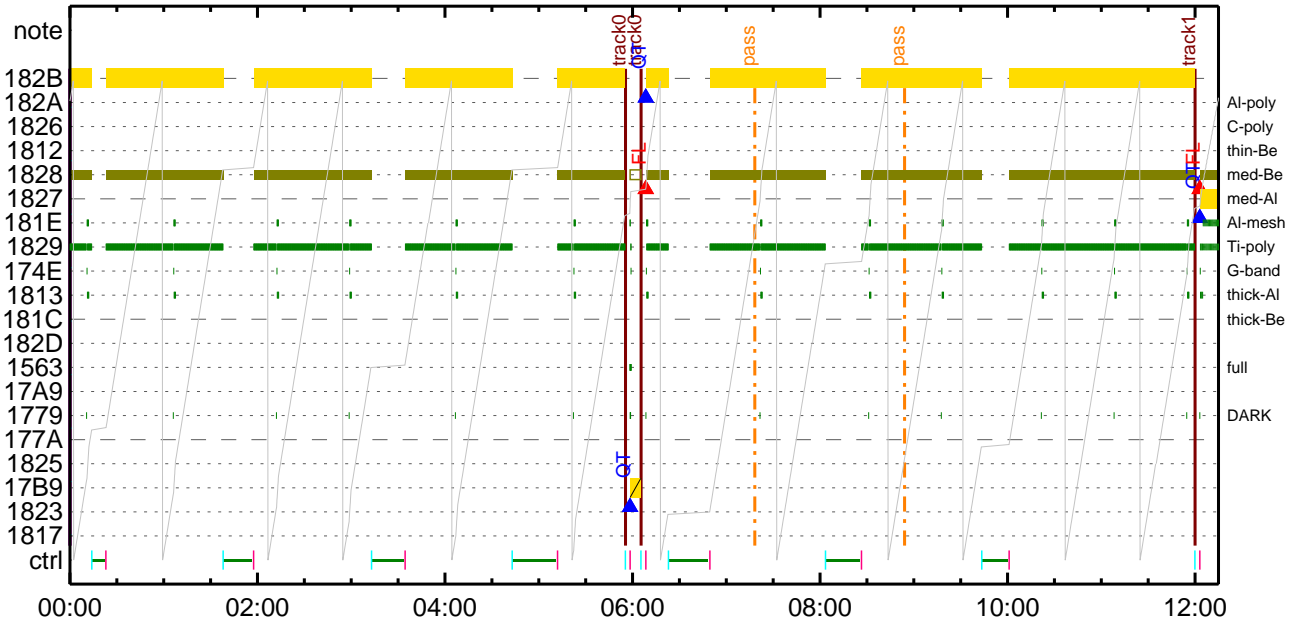
CMDI #0553 2010/10/21



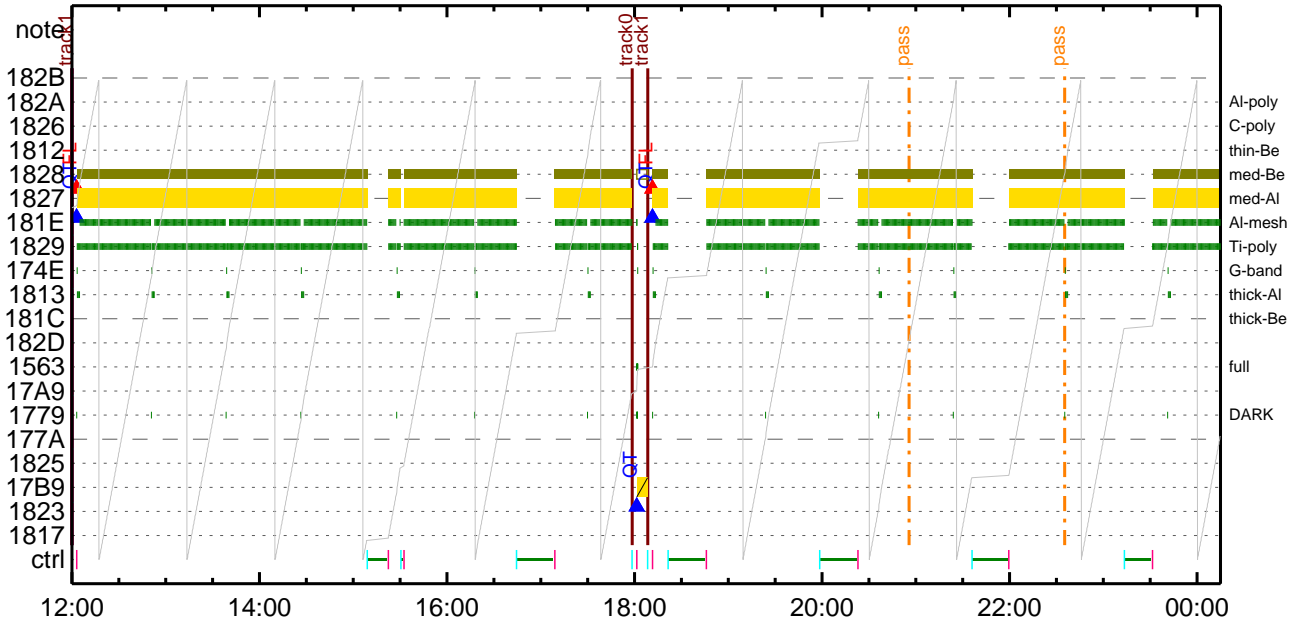
CMDI #0553 2010/10/21



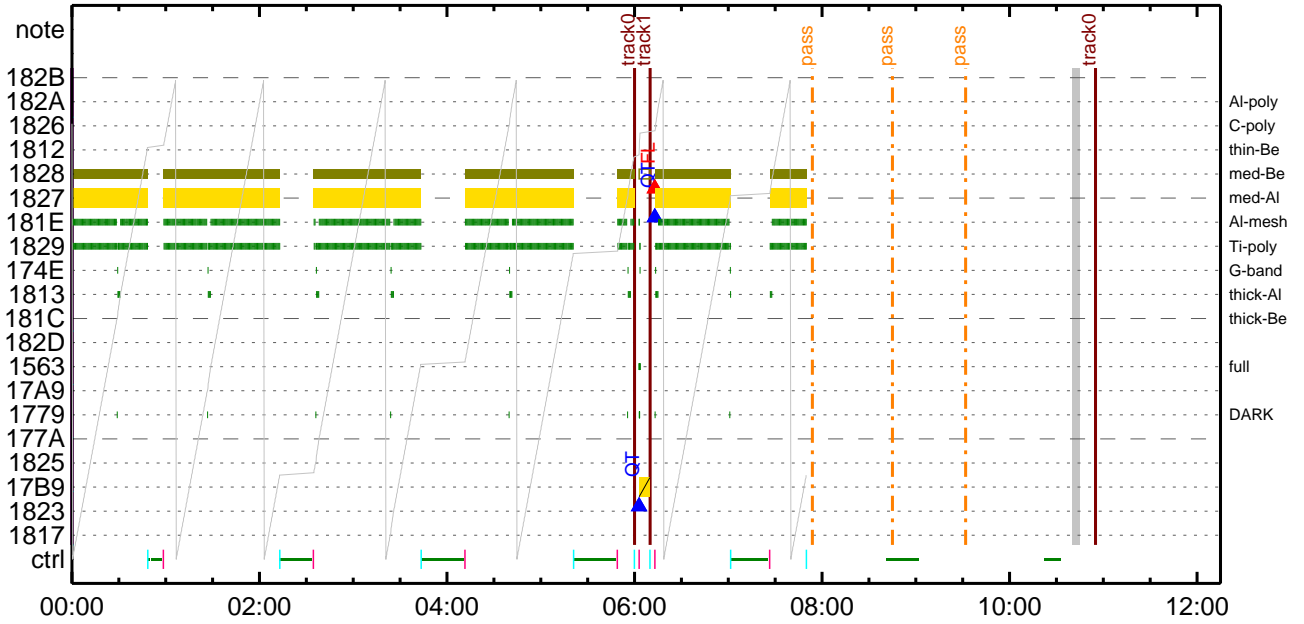
CMDI #0553 2010/10/22



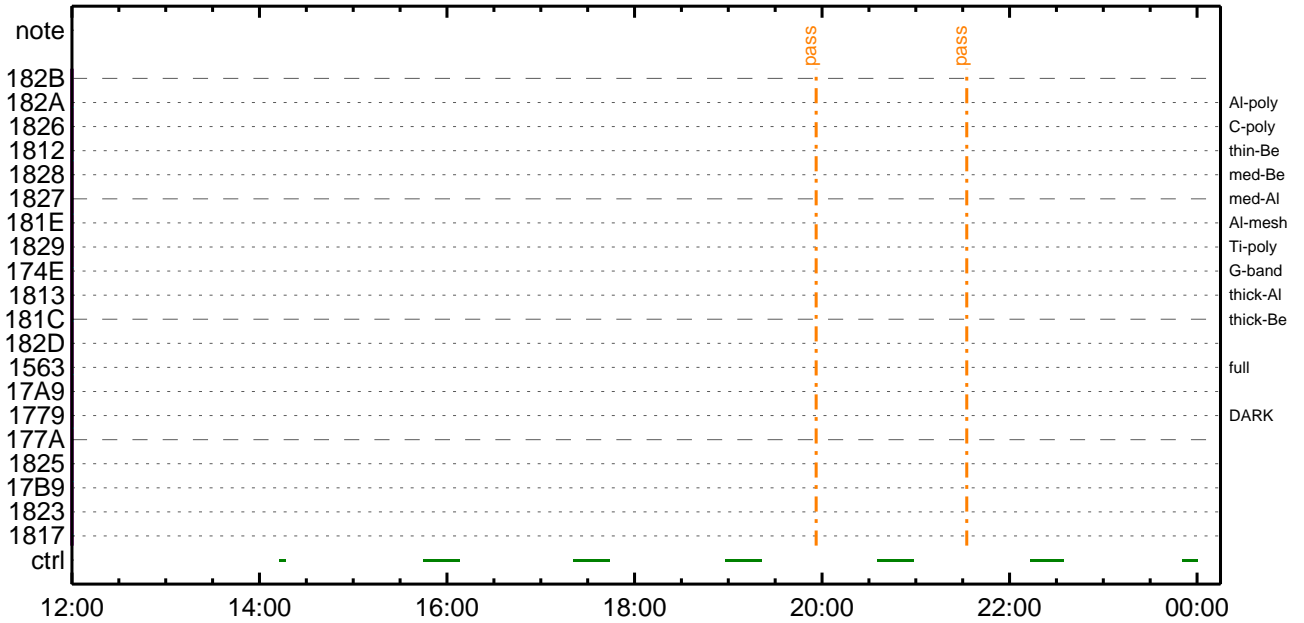
CMDI #0553 2010/10/22



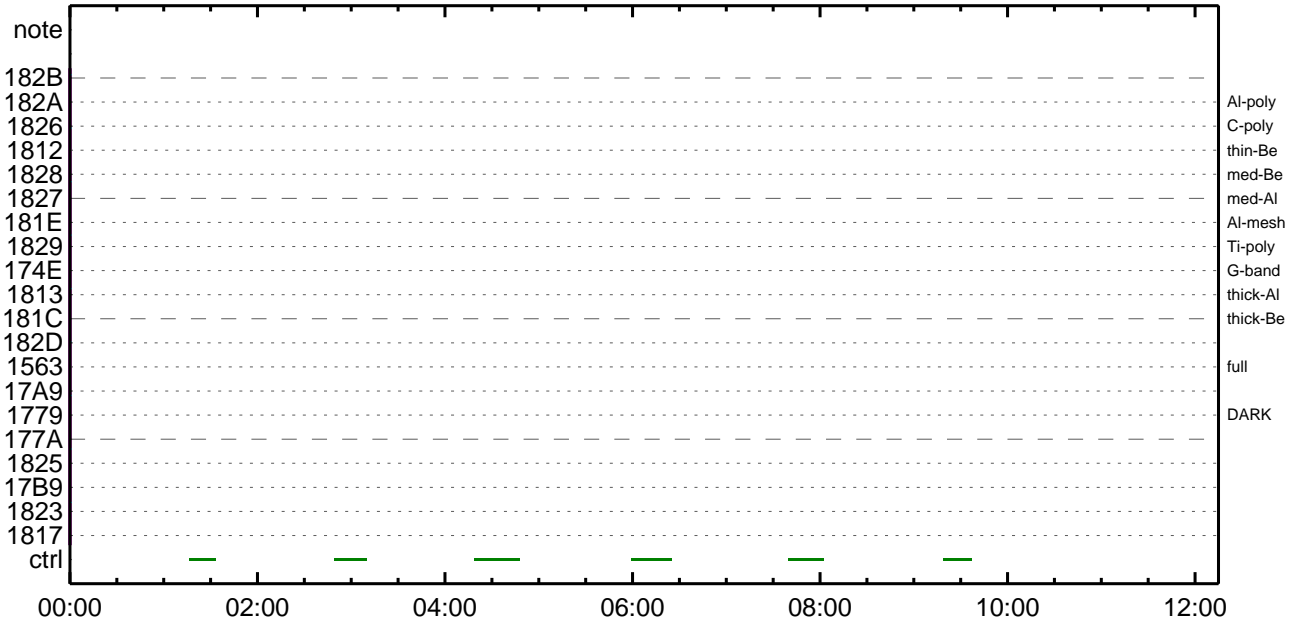
CMDI #0553 2010/10/23



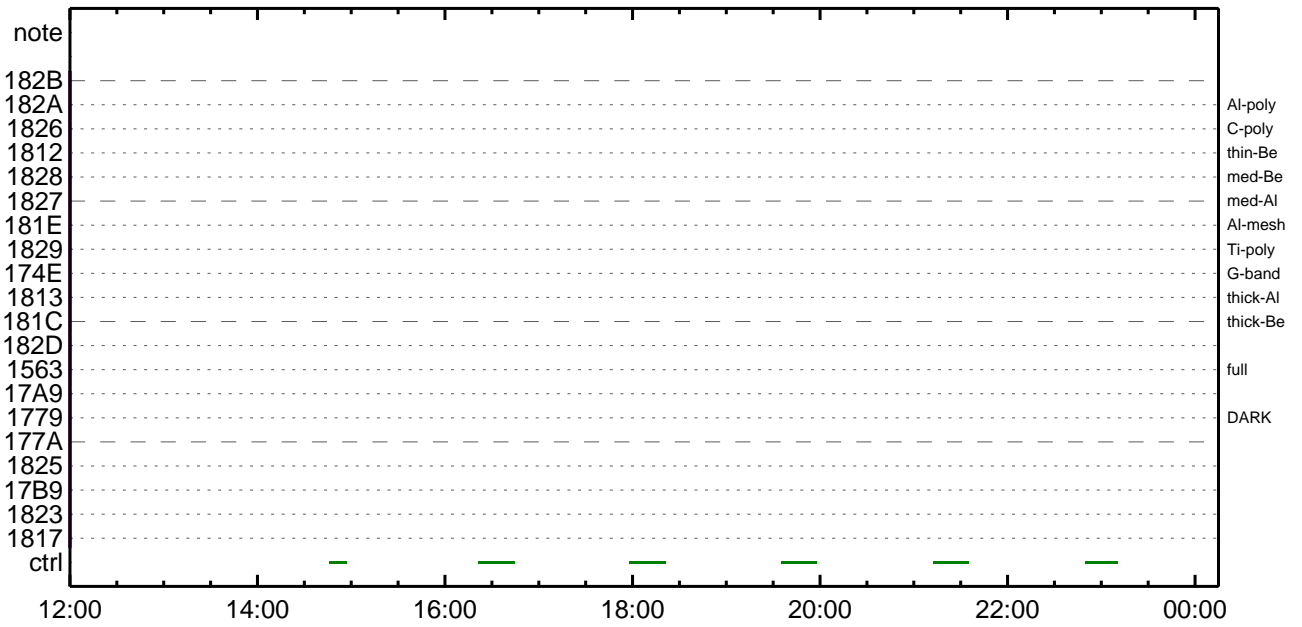
CMDI #0553 2010/10/23



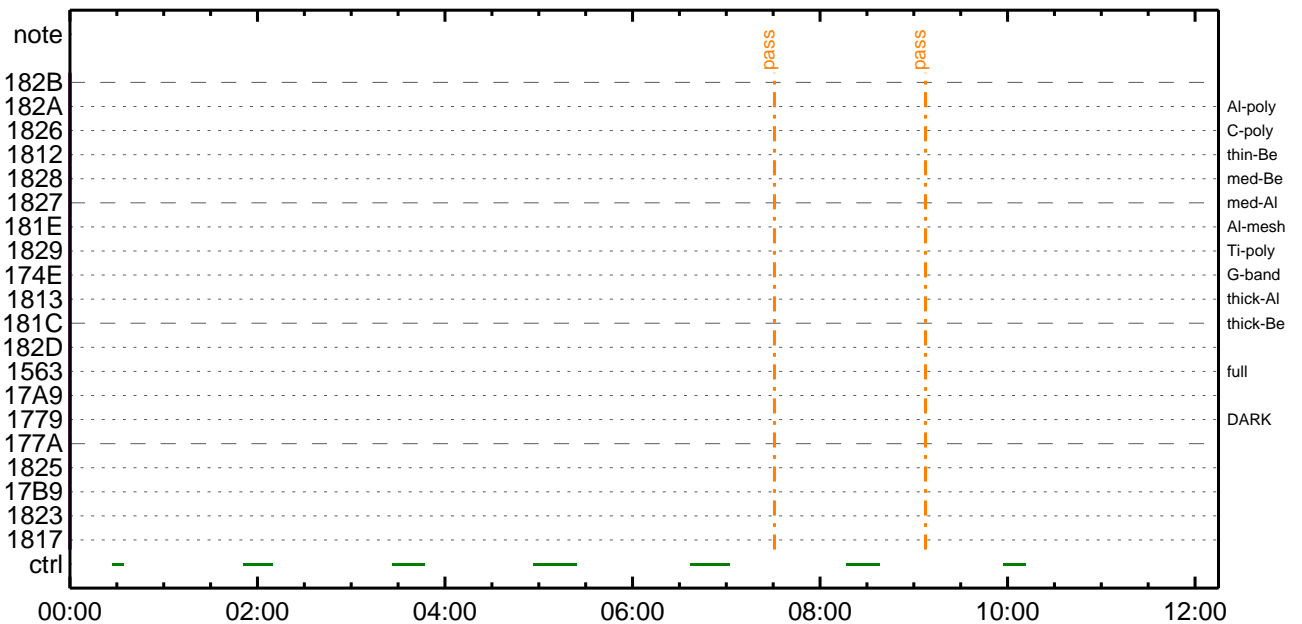
CMDI #0553 2010/10/24



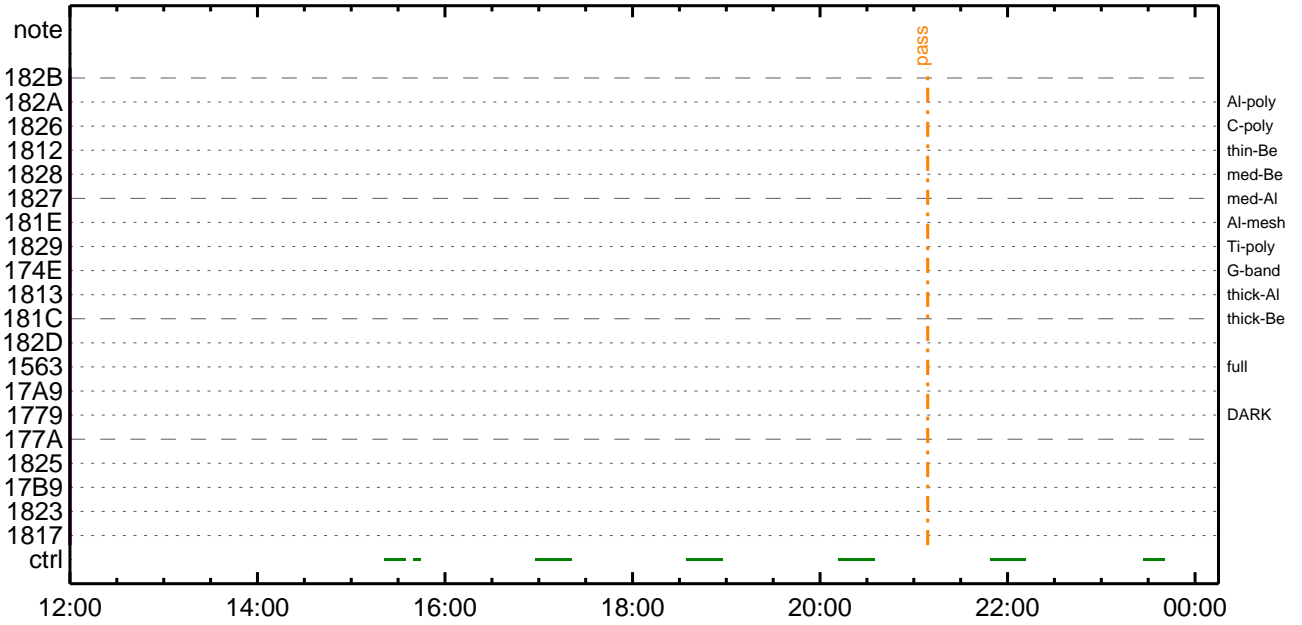
CMDI #0553 2010/10/24



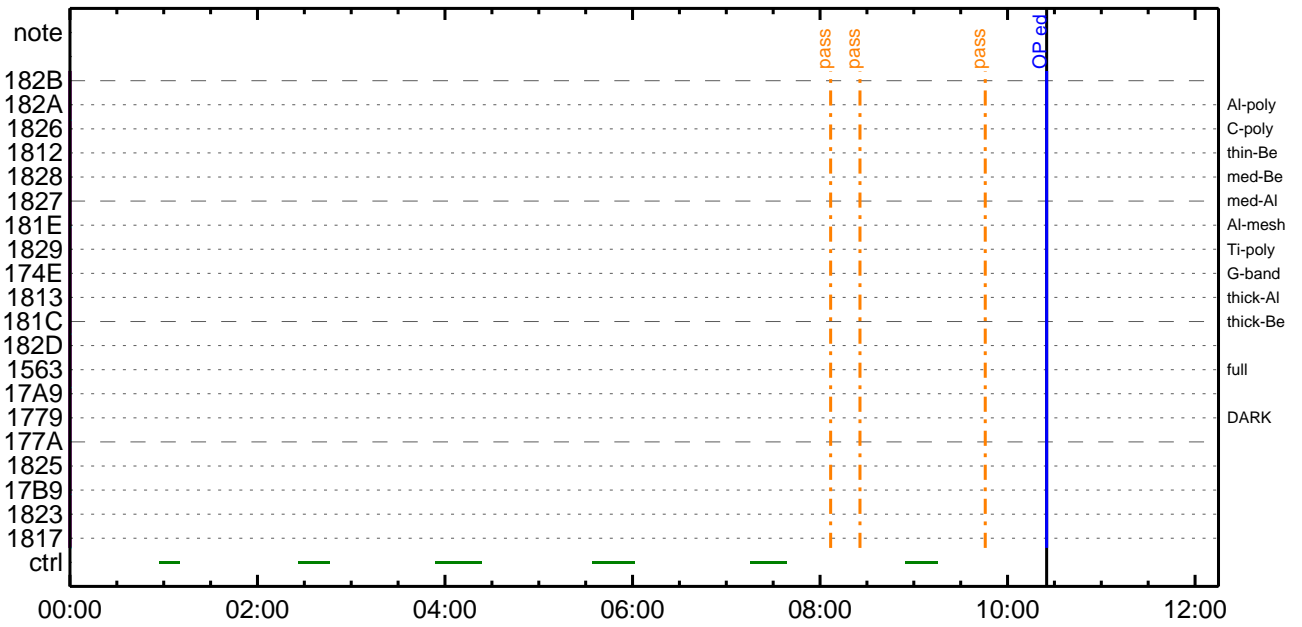
CMDI #0553 2010/10/25



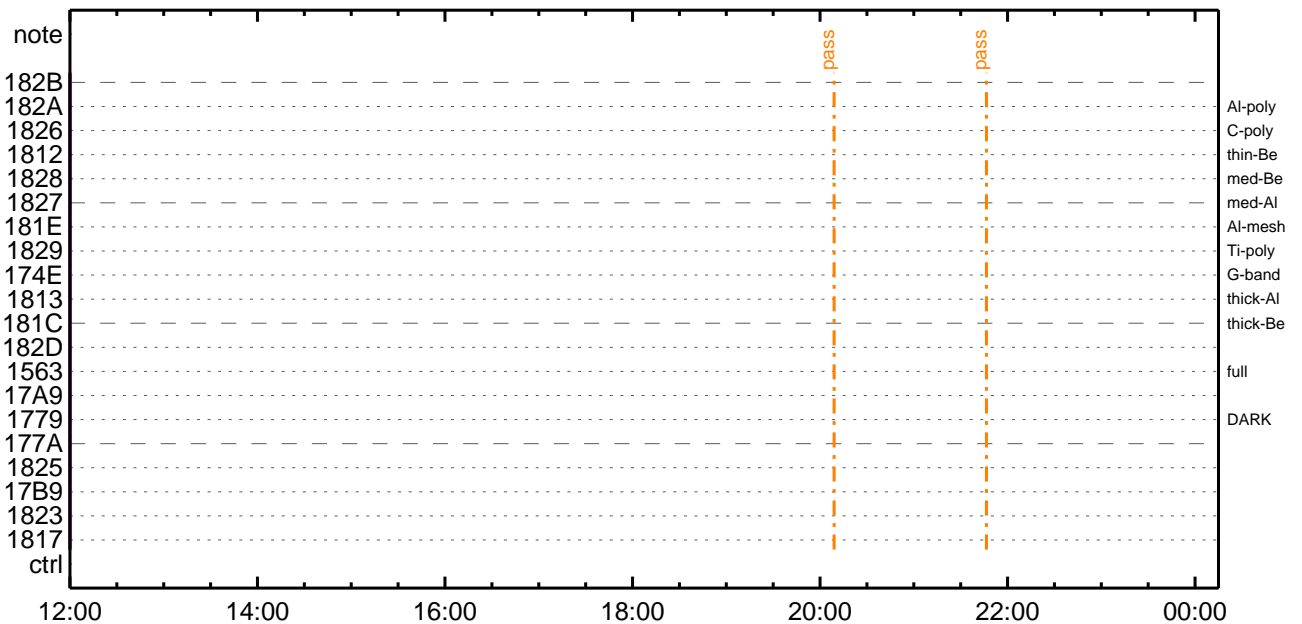
CMDI #0553 2010/10/25



CMDI #0553 2010/10/26



CMDI #0553 2010/10/26




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0096 C.                0x0000; SET0EDUMP01A±0iYÑY¹aÇ¹Ôa|0³0E;E
0097 C.
0098 . C. TIY³YFÿ6YÉ0ððÄDİ¿ (UT)
0099 +. TI 2010-10-21 09:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2010-10-21 09:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2010-10-21 09:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2010-10-21 09:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C.                0E²¼0İÄê%îíÑ0îYÁY§YÁY¹-àìÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]         EQ      ENA
0117 C.                çç[HK1_TI_CMD_NUM]           EQ      4
0118 C.                çç[HK1_NEXT_EXEC_PIM]         EQ      DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]         EQ      0xB3
0120 C.
0121 . C. *****
0122 C. TIİî°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]         EQ      07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]         EQ      2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]          EQ      3
0131 C.                çç[HK1_DMP_REPEAT_NUM]         EQ      0
0132 C.                çç[HK1_DMA_DMP_PIM]           EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]           EQ      7
0136 C.                çç[HK1_PKT_GEN_TIME]           EQ      0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]         EQ      32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0139 C.                çç[HK1_DMP_CHK_FLG]           EQ      EXEC
0140 C.
0141 . C. YÁYÖY×½³İ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]           EQ      NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yî;¼YÈ;Ë0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]           EQ      2
0150 C.                çç[HK1_PKT_GEN_TIME]           EQ      0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]         EQ      32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2010-10-21 09:48:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC                (21 02)
0163 +. TI 2010-10-21 09:48:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC                (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]         EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2010-10-21 09:48:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC                (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-10-21 09:48:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]         EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `úÃî0î»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°îYÖYÁYÈYÏYÉYÁYçYè0E¼00¼A»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOSŸÁŸSŸÃŸ~¼Â»Û;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-656 2010-10-21 12:04:57 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŸYÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀ•µ°È»ÍxÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈBÈ¼°ÇÓã•¿¼l¹çãÍ;çÀ®, ùã¹ãèãBãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG_____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 60 10 10)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 . C. ----- Success Verify ? OK / NG _____
0057 C.
0058 C.
0059 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0060 C.
0061 +. DC 07-F0 MDP_XRT_MODE_OBSV
0062 BC (c2)
0063 +. TI 2010-10-21 09:48:02.0
0064 DC 07-F0 MDP_XRT_MODE_OBSV
0065 BC (c2)
0066 . C. ----- Success Verify ? OK / NG _____
0067 C.
0068 C. ***** XRT END *****
0069 . C. *****
0070 C. SOT table upload
0071 C. *****
0072 . C. < Stop FG table >
0073 +. DC 07-F0 MDP_FG_CTRL_MANU
0074 BC (51)
0075 . C. -----
0076 C. MDP_FG_CTRL_MODE = MANU [ ]
0077 C. -----
0078 C.
0079 . C. <Upload FG Observation Table>
0080 . S. RAM ram-268:MDP_OBS_F
0081 ( )
0082 C.
0083 . C. < Dump RAMID=MDP_OBS_F >
0084 +. DC 07-F0 MDP_DUMP_FGTBL
0085 BC (82 07 00 00 00 38 b8)
0086 C. -----
0087 C. MDP_OBS_F verify = OK/NG [ ]
0088 C. -----
0089 C.
0090 . C. < Stop SP table >
0091 +. DC 07-F0 MDP_SP_CTRL_MANU
0092 BC (61)
0093 C. -----
0094 C. MDP_SP_CTRL_MODE = MANU [ ]
0095 C. -----
```

```

0096 C.
0097 . C. <Upload SP Observation Table>
0098 . S. RAM ram-285:MDP_OBS_S
0099 ( )
0100 C.
0101 . C. < Dump RAMID=MDP_OBS_S >
0102 +. DC 07-F0 MDP_DUMP_SPTBL
0103 BC (83 07 00 00 00 38 b8)
0104 C. -----
0105 C. MDP_OBS_S verify = OK/NG [ ]
0106 C. -----
0107 C.
0108 C. *****
0109 C. SOT TI command set
0110 C. *****
0111 C. Execute, after the success of TBL upload.
0112 +. TI 2010-10-21 09:48:18.0
0113 DC 07-F0 MDP_SOT_MODE_OBSV
0114 BC (40)
0115 . C. -----
0116 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0117 C. -----
0118 C.
0119 C.
0120 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0121 C. (¼á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ¥DÝ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.

```

*** OP Sequence for XRT ***

2010/10/21	09:58:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	09:58:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/21	09:59:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/10/21	09:59:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/21	09:59:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/21	09:59:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/21	09:59:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/21	10:01:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	10:01:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2010/10/21	10:02:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/10/21	10:02:02.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/21	10:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	10:48:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	10:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/21	10:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/21	10:51:30.0	XRT_Custom_418_OG [0x1a2]			
2010/10/21	10:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/21	13:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	13:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/21	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 28 00 b6 25
2010/10/21	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/21	14:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/21	14:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/21	14:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/21	14:02:56.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	14:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2010/10/21	14:03:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/10/21	14:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/21	14:34:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	14:34:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	14:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/21	14:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/21	14:43:00.0	XRT_Custom_418_OG [0x1a2]			
2010/10/21	14:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/21	16:08:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	16:08:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	16:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/21	16:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/21	16:31:30.0	XRT_Custom_418_OG [0x1a2]			
2010/10/21	16:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/21	17:44:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	17:44:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/21	17:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/21	17:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/21	18:07:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/21	18:07:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/10/21	18:08:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00

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2010/10/21	18:08:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/21	18:08:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/21	18:08:20.0	XRT_ARS_DIS_415_OG [0x19f]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/21	18:10:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/10/21	18:10:58.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/21	18:17:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/21	18:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/21	18:18:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 28 00 b6 25				
2010/10/21	18:18:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/21	18:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/21	18:18:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/21	18:18:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/21	18:20:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/21	18:20:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/10/21	18:21:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/10/21	18:21:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/21	19:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/21	19:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/21	19:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/21	19:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/21	19:45:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/21	19:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/21	20:59:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/21	20:59:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/21	20:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/21	21:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/21	21:22:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/21	21:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/21	22:36:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/21	22:36:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/21	22:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/21	22:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/21	22:57:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/21	22:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	00:14:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	00:14:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	00:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	00:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	00:22:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	00:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	01:38:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	01:38:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	01:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	01:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	01:56:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	01:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	03:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	03:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	03:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2010/10/22	03:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	03:33:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	03:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	04:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	04:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	04:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	04:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	05:11:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	05:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	05:55:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	05:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/22	05:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/10/22	05:55:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/22	05:55:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/22	05:55:50.0	XRT_ARS_DIS_415_OG [0x19f]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/22	05:58:26.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/10/22	05:58:28.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	06:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/22	06:05:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 28 00 b6 25				
2010/10/22	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/22	06:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/22	06:05:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/22	06:05:52.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/22	06:08:26.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	06:08:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/10/22	06:08:30.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/10/22	06:08:32.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	06:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	06:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	06:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	06:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	06:48:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	06:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	08:03:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	08:03:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	08:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	08:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	08:25:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	08:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	09:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	09:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	09:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	09:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	10:00:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	10:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/22	12:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				

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2010/10/22	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/22	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/22	12:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/22	12:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/22	12:02:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	12:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2010/10/22	12:03:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/10/22	12:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	15:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	15:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	15:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	15:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	15:21:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	15:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	15:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	15:31:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	15:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	16:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	16:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	16:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	16:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	17:08:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	17:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	17:58:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	17:58:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/10/22	17:58:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/10/22	17:58:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/22	17:58:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/22	17:58:50.0	XRT_ARS_DIS_415_OG [0x19f]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/22	18:01:26.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/10/22	18:01:28.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	18:08:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	18:08:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/10/22	18:08:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/10/22	18:08:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/22	18:08:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/22	18:08:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/22	18:08:52.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/22	18:11:26.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	18:11:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2010/10/22	18:11:30.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/10/22	18:11:32.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	18:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/22	18:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/22	18:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/22	18:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/22	18:45:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/22	18:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/22	19:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							

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2010/10/22	19:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/22	19:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/22	20:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/22	20:22:00.0	XRT_Custom_418_OG [0x1a2]				
2010/10/22	20:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/22	21:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/22	21:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/22	21:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/22	21:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/22	21:58:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/22	21:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/22	23:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/22	23:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/22	23:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/22	23:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/22	23:30:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/22	23:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	00:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	00:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	00:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/23	00:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/23	00:57:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/23	00:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	02:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	02:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	02:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/23	02:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/23	02:33:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/23	02:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	03:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	03:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	03:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/23	03:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/23	04:10:30.0	XRT_Custom_418_OG [0x1a2]				
2010/10/23	04:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	05:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	05:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	05:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/23	05:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/23	05:48:00.0	XRT_Custom_418_OG [0x1a2]				
2010/10/23	05:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/10/23	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2010/10/23	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/10/23	06:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/10/23	06:00:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/23	06:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/10/23	06:02:58.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]				

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2010/10/23	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/10/23	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2010/10/23	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/10/23	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/10/23	06:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0
2010/10/23	06:12:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/10/23	06:12:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	06:13:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2010/10/23	06:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/10/23	07:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	07:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/10/23	07:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/10/23	07:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/10/23	07:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/10/23	07:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
2010/10/23	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/10/23	10:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM	AOCU_NM	5	02-76	00 00 00 00 00