

# XRT Timeline to be uploaded on 2010/10/28

Period: 2010/10/28 11:05:00 - 2010/11/01 10:18:00

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Normal mode

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XOB #182B: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 60sec-cad													
Term	Pointing (x, y)						Comment						
10/28 11:18:00 - 10/28 18:47:30	Track ( 490.1, 276.6) @ 10/28 11:15:00						# OP start + 10min */AR obs and HOP178 (from 17UT) NOAA11117						
10/29 16:34:32 - 10/29 18:59:54	Track ( 620.9, 285.9) @ 10/29 06:34:30						# AR tracking NOAA 11117 and HOP178 (from 17UT)						
<b>PROG= 20 Inf.-time(s)</b>													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 18 45-time(s) 30.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term	Pointing (x, y)						Comment						
10/28 19:11:30 - 10/28 19:59:54	Track ( 0.0, 0.0) @ 10/28 19:00:00						# HOP 178 DC tracking (synoptic)						
10/29 06:27:30 - 10/29 06:34:24	Fixed ( 0.0, 0.0)						synoptic, shifted 24.5 min						
10/29 19:03:00 - 10/29 19:59:54	Track ( 0.0, -0.0) @ 10/29 19:00:00						# HOP178 DC tracking (synoptic)						
10/30 06:01:00 - 10/30 06:07:54	Fixed ( 0.0, 0.0)						synoptic, shifted -2.0 min						
<b>PROG= 03 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1830: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term	Pointing (x, y)						Comment						
10/28 20:03:00 - 10/28 22:02:30	Track ( 551.7, 280.6) @ 10/28 20:00:00						AR tracking NOAA 11117						
10/29 02:03:00 - 10/29 06:24:24	Track ( 591.7, 283.5) @ 10/29 02:00:00						AR tracking NOAA 11117						
10/29 06:37:30 - 10/29 16:10:30	Track ( 620.9, 285.9) @ 10/29 06:34:30						# AR tracking NOAA 11117 and HOP178 (from 17UT)						
10/29 20:03:02 - 10/30 05:57:54	Track ( 699.6, 293.2) @ 10/29 20:00:00						* AR tracking NOAA 11117						
10/30 06:11:02 - 10/30 11:23:00	Track ( 751.6, 299.2) @ 10/30 06:08:00						# AR tracking NOAA 11117						
<b>PROG= 04 Inf.-time(s)</b>													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 3 35-time(s) 80.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17A9: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al - 2 loops												
Term	Pointing (x, y)						Comment					

PROG= 07 1-time(s)													
Subr= 1 2-time(s) 300.0sec													
Seqn= 13 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 12 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 14 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1819: Test images for FW1 move - Ti/poly (2 sec)- 2048x2048 - 2x2**

Term	Pointing (x, y)	Comment
10/28 23:03:00 - 10/28 23:03:19	Track ( 32.8, 0.3) @ 10/28 22:30:00	EIS sensitivity monitoring
10/28 23:11:54 - 10/28 23:25:30	Track ( 32.8, 0.3) @ 10/28 22:30:00	EIS sensitivity monitoring

PROG= 02 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 59 2-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1802: FW1 Test, med-Al+Ti/poly, 16s AEC0, 384FOV**

Term	Pointing (x, y)	Comment
10/28 23:06:29 - 10/28 23:08:43	Track ( 32.8, 0.3) @ 10/28 22:30:00	EIS sensitivity monitoring

PROG= 14 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 1 5-time(s) 25.0sec													
	med-Al/Ti-poly	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
10/28 11:18:00 - 10/28 18:47:30	Track ( 490.1, 276.6) @ 10/28 11:15:00	# OP start + 10min */AR obs and HOP178 (from 17UT) NOAA11117
10/28 20:03:00 - 10/28 22:02:30	Track ( 551.7, 280.6) @ 10/28 20:00:00	AR tracking NOAA 11117
10/29 02:03:00 - 10/29 06:24:24	Track ( 591.7, 283.5) @ 10/29 02:00:00	AR tracking NOAA 11117
10/29 06:37:30 - 10/29 16:10:30	Track ( 620.9, 285.9) @ 10/29 06:34:30	# AR tracking NOAA 11117 and HOP178 (from 17UT)
10/29 16:34:32 - 10/29 18:59:54	Track ( 620.9, 285.9) @ 10/29 06:34:30	# AR tracking NOAA 11117 and HOP178 (from 17UT)
10/29 20:03:02 - 10/30 05:57:54	Track ( 699.6, 293.2) @ 10/29 20:00:00	* AR tracking NOAA 11117
10/30 06:11:02 - 10/30 11:23:00	Track ( 751.6, 299.2) @ 10/30 06:08:00	# AR tracking NOAA 11117

PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2    1-time(s)    2.0sec</b>												
<b>Seqn= 90    1-time(s)    2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3    30-time(s)    60.0sec</b>												
<b>Seqn= 87    1-time(s)    2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4    24-time(s)    600.0sec</b>												
<b>Seqn= 89    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

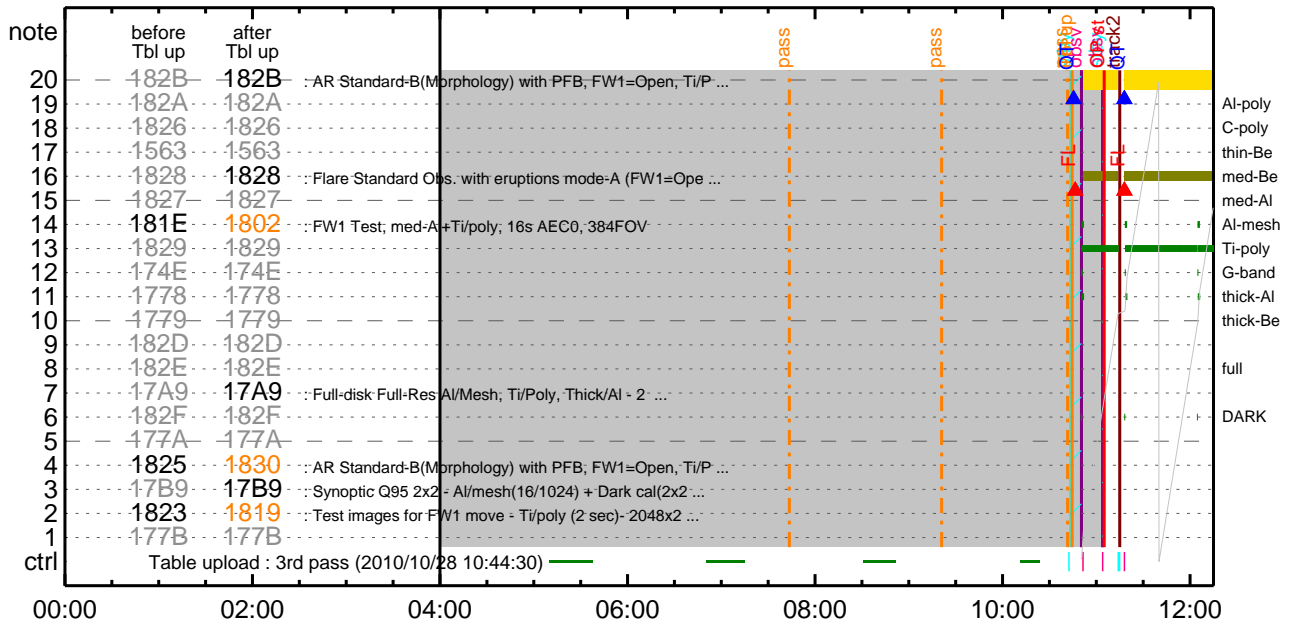
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### Flare Detection

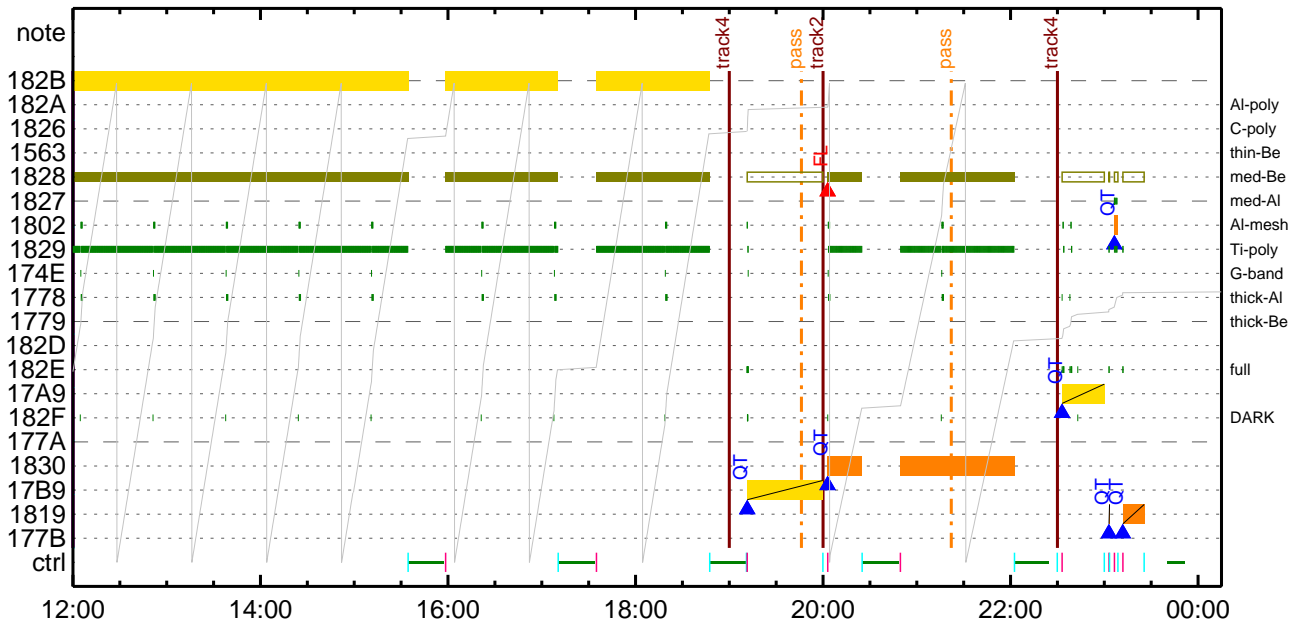
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<b>FLD Patrol</b>												
Term	Pointing (x, y)				Comment							
10/28 20:00:16 - 10/28 22:30:16	Track (	551.7,	280.6)	@ 10/28 20:00:00	AR tracking NOAA 11117							
10/29 02:00:16 - 10/29 06:24:46	Track (	591.7,	283.5)	@ 10/29 02:00:00	AR tracking NOAA 11117							
10/29 06:34:46 - 10/29 19:00:16	Track (	620.9,	285.9)	@ 10/29 06:34:30	# AR tracking NOAA 11117 and HOP178 (from 17UT)							
10/29 20:00:16 - 10/30 05:58:16	Track (	699.6,	293.2)	@ 10/29 20:00:00	* AR tracking NOAA 11117							
10/30 06:08:16 - 11/01 10:18:00	Track (	751.6,	299.2)	@ 10/30 06:08:00	# AR tracking NOAA 11117							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

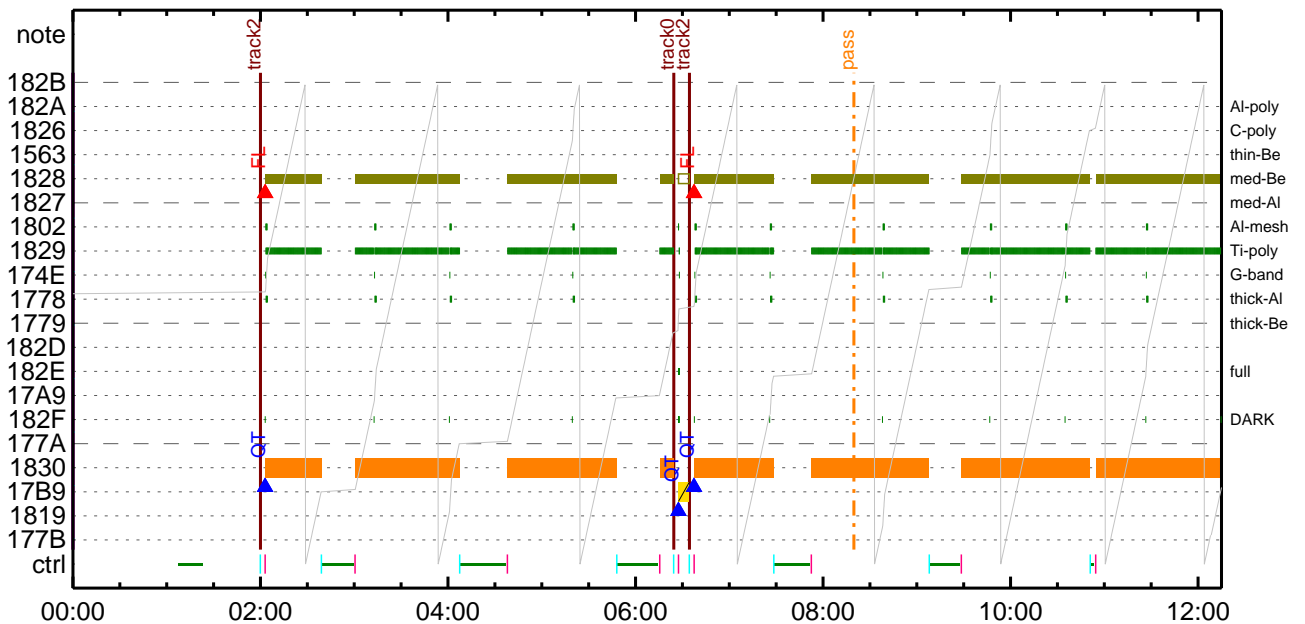
### CMDI #0563 2010/10/28



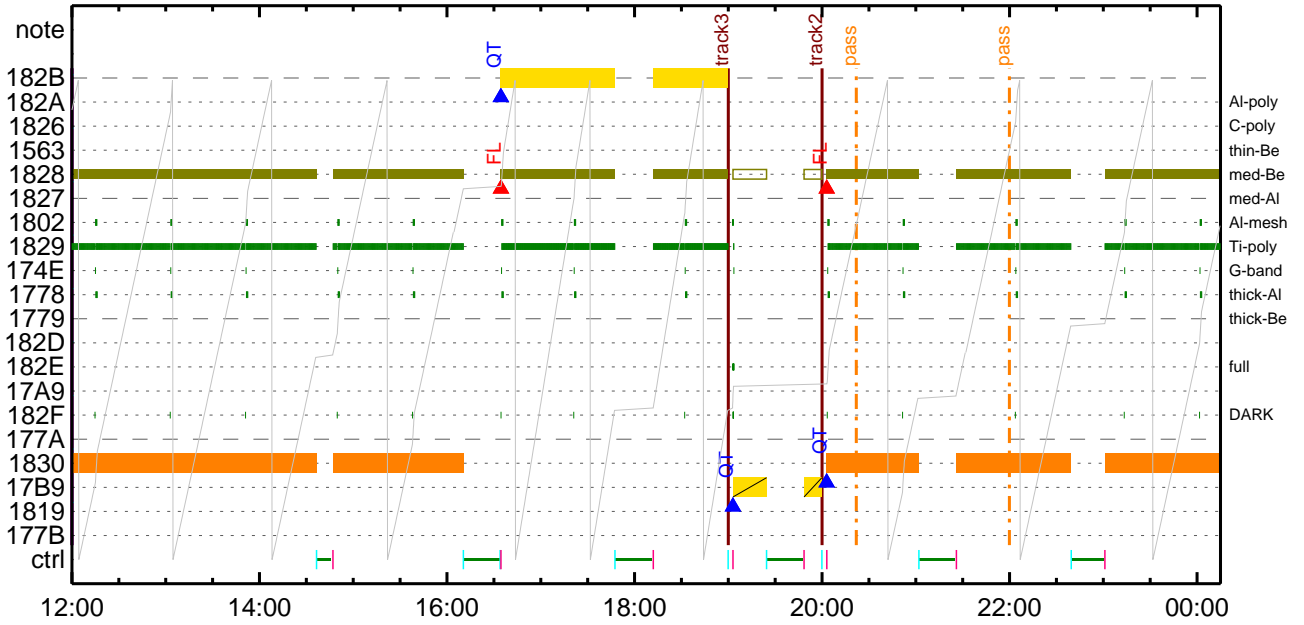
### CMDI #0563 2010/10/28



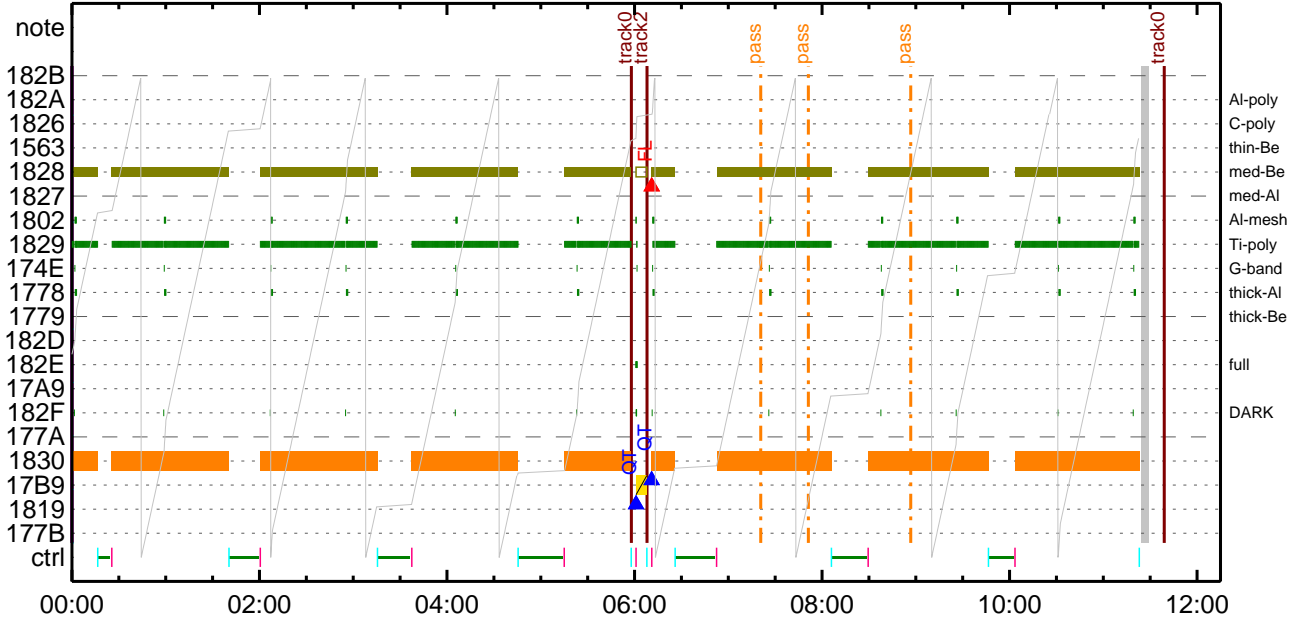
### CMDI #0563 2010/10/29



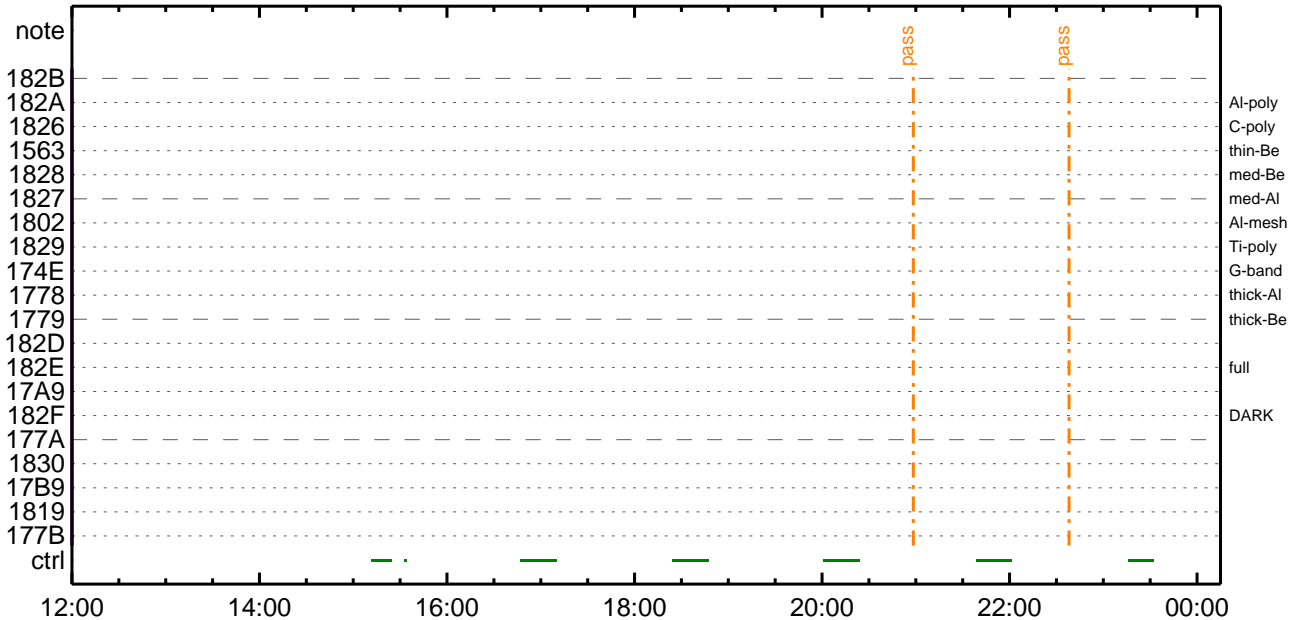
CMDI #0563 2010/10/29



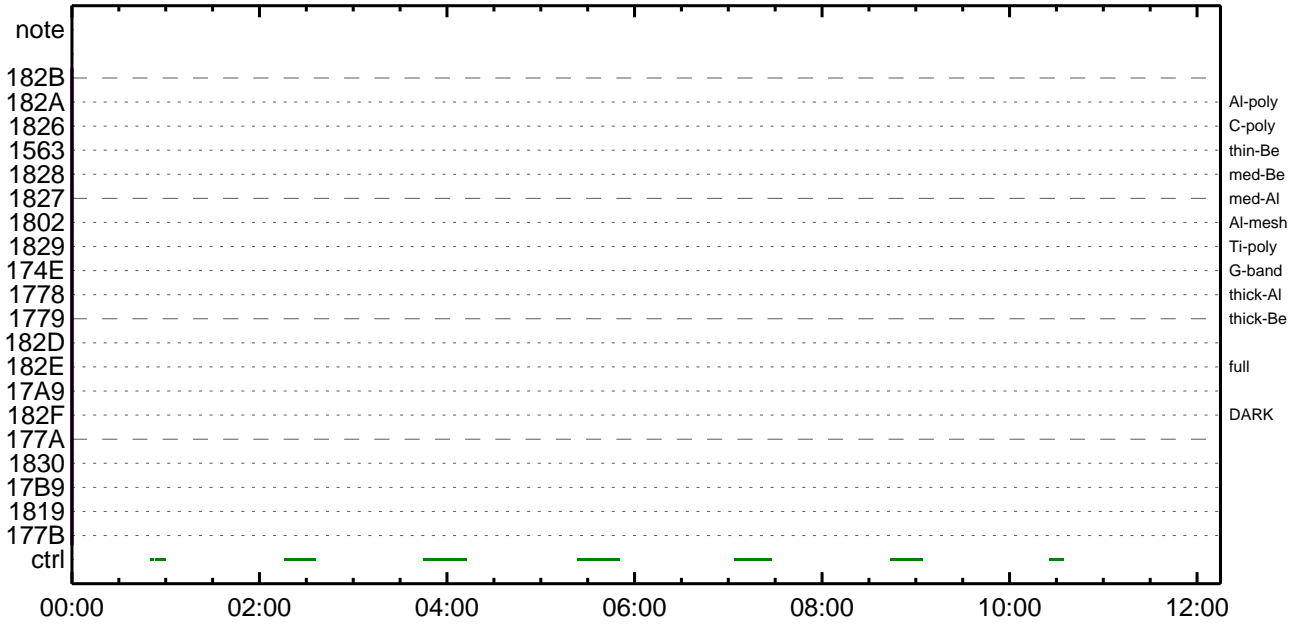
CMDI #0563 2010/10/30



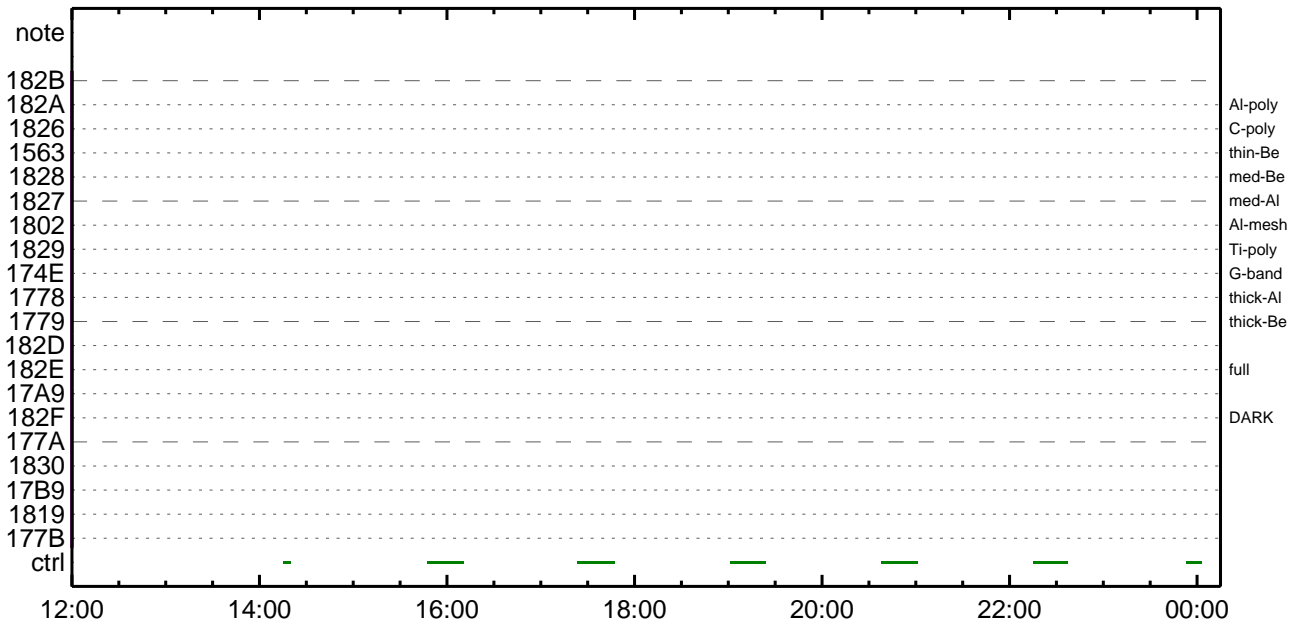
CMDI #0563 2010/10/30



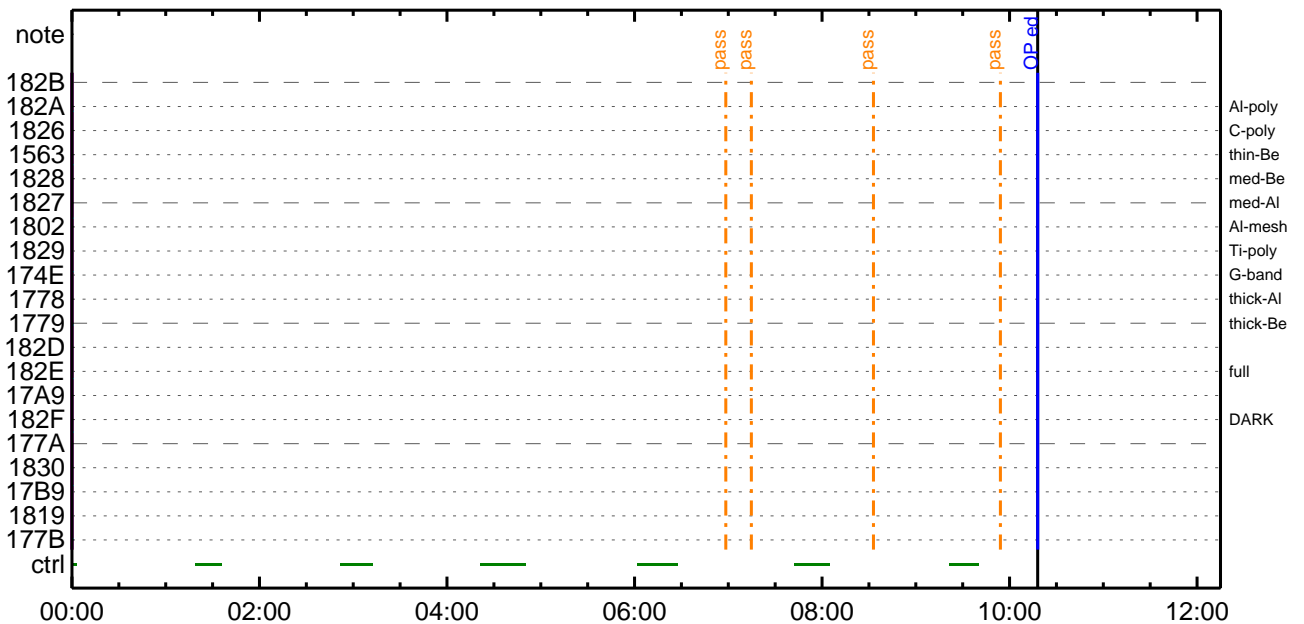
CMDI #0563 2010/10/31



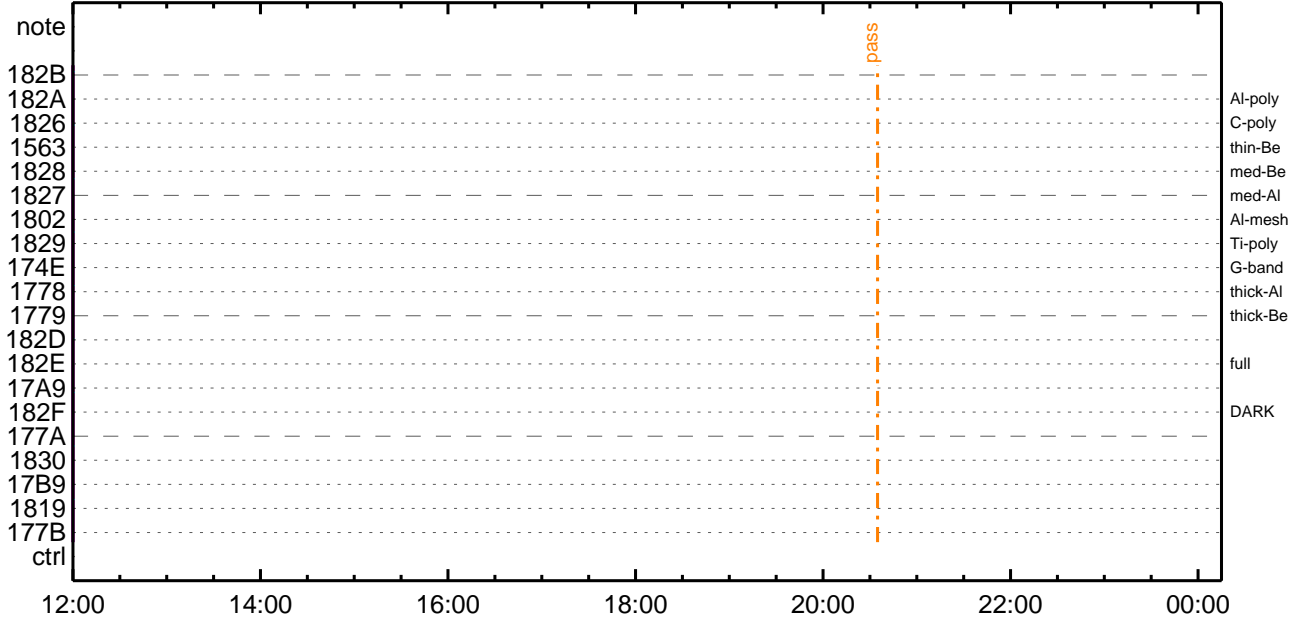
CMDI #0563 2010/10/31



CMDI #0563 2010/11/01



CMDI #0563 2010/11/01







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYóYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉ;ã
0103 S. OP op-668:OP
0104 ( )
0105 S. OG og-668:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPíî°èYAYóYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî½Ê¹Ç•è²ïOKαò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî½Ê¹Ç•è²ïOKαò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPαî½Ê¹Ç•è²ïOKαò³îÇ§
0165 C.
0166 C. ***** °Ê²¼αî½Ê¹Ç•è²ïOKαò³îÇ§ *****
0167 C. DHUYâ;½YÉ;Ê½Y½;Yî;½YÉ;Êòðîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADα-Á÷ç@NGUî½Ê¹Ç•è²ïOKαò³îÇ§ *****
0180 C. αPαç;çSETαEDUMPAî±±°îYÑY¹αÇ¹Òα|α³αE;£
0181 C.
0182 C. TIY³Y½YóYÉòðÁDîç(UT)
0183 +. TI 2010-10-28 11:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-10-28 11:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-10-28 11:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194      C.
0195 +. TI 2010-10-28 11:04:59.5
0196      DC 01-B2 DHU_OP_START
0197      C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198      C.
0199      C. °Ê²¼õİÄē%İİñõİŷÄŷ$ŷÄŷ-¹àİÜ
0200      C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201      C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202      C.          çç[HK1_NEXT_EXEC_PIM]             EQ      DHU
0203      C.          çç[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204      C.
0205      C. *****
0206      C. TIİİİ°ēŷÄŷÖŷ×
0207      C. *****
0208      C.
0209      C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211      BC      (03 ab 03 01 02)
0212      C.          çç[HK1_DMP_TOP_ADRS_1]           EQ      07
0213      C.          çç[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214      C.          çç[HK1_DMP_BLOCK_NUM]            EQ      3
0215      C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0216      C.          çç[HK1_DMA_DMP_PIM]              EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218      BC      (07 0b f8)
0219      C.          çç[HK1_PKT_FORM_NO]               EQ      7
0220      C.          çç[HK1_PKT_GEN_TIME]              EQ      0.25 s
0221      C.          çç[HK1_S_TLM_BIT_RATE]           EQ      32k
0222      C.          çç[HK1_X_TLM_BIT_RATE]           EQ      4M
0223      C.          çç[HK1_DMP_CHK_FLG]              EQ      EXEC
0224      C.
0225      C. ŷÄŷÖŷ×½ªİ»õ³İÇŞ
0226      C.          çç[HK1_DMP_CHK_FLG]              EQ      NON
0227      C.
0228      C. RAM ID=TI_TBLõİŷÊ¹ç•ē²İOKõ³İÇŞ
0229      C.
0230      C. DHUŷä;¼ŷÊ;Ê¼ŷ½. ŷİ;¼ŷÊ;Ëõðİäõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232      BC      (02 0a f8)
0233      C.          çç[HK1_PKT_FORM_NO]               EQ      2
0234      C.          çç[HK1_PKT_GEN_TIME]              EQ      0.5S
0235      C.          çç[HK1_S_TLM_BIT_RATE]           EQ      32K
0236      C.          çç[HK1_X_TLM_BIT_RATE]           EQ      4M
0237      C.
0238      C. *****
0239      C. SOT TI command set
0240      C. *****
0241      C. Execute, after the success of OP upload.
0242 +. TI 2010-10-28 11:04:16.0
0243      DC 07-F0 MDP_SOT_MODE_STBY
0244      BC      (41)
0245      C. -----
0246      C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247      C. -----
0248      C. ***** SOT END *****
0249      C. Stop EIS observation and temporarily disable EIS mode changes
0250      C.
0251      C.
0252      C. ***** Start EIS operation (TI set) *****
0253      C. Execute, after the success of OP upload.
0254      C. Set EIS TI-commands
0255 +. TI 2010-10-28 11:04:30.0
0256      DC 07-FC EIS_MODE_MANU
0257      BC      (21 02)
0258 +. TI 2010-10-28 11:04:40.0
0259      DC 07-FC EIS_MODE_CHG_DIS
0260      BC      (22)
0261      C.          [      ] [HK1_TI_CMD_NUM]        EQ      2 COUNTUP
0262      C. ***** End EIS operation (TI set) *****
0263      C.
0264      C.
0265      C.
0266      C. ***** XRT START *****
0267      C. Execute, after the success of OP upload.
0268 +. TI 2010-10-28 11:04:00.0
0269      DC 07-F0 MDP_XRT_MODE_STBY
0270      BC      (c3)
0271      C.          [      ] [HK1_TI_CMD_NUM]        EQ      1COUNTUP
0272      C.
0273      C. ***** XRT END *****
0274      C.
0275      C. ***** MDP `ûÃİñİ»õ¼ŷõËÃðõ¹ñēDCBC•x²ē *****
0276      C. (%ã°İŷÖŷÄŷËŷŷËŷÄŷçŷëõÊ¼õõ¼Ä»Ûõ¹ñē)
0277      S. DC-BC dcbc-402:DCBC
0278      (MDP_known_event)
0279      C.
0280      C.
0281      C. ***** ŷĐŷ¹•İ Daily±çİñõË'Øõ¹ñēDCBC•x²ē *****
0282      S. DC-BC dcbc-153:DCBC
0283      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284      C.
0285      C.
0286      C. ;ãLOSŷÄŷ$ŷÄŷ-¼Ä»Û;ä
0287      C.
0288      C. ***** LOS *****
0289      C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-669 2010-10-28 12:26:22 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËççðÁð•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËËÈ%µ•íÉ;ËðÈ¼°ÇÖð•çç¼í¹ççí;çÀ®, ùñ¹ðèððççÁ+ç®ð•ðÈððð³ðÈ; ð
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç” °ÆÀ, Í×ËYðáLOSððççí»p´Öðð¹íí, ð•; çÉÖÍ×çÈXÁÖONðí¹ÖðÈíðÈððð³ðÈ; ð
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¼ÖÁÖð-°ÁÀèð•çç; ç°Ê²¼ççí°ÆÀ, ¼è¼ççð¼Á¹Öçç; ð
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç” RESTART;ËPT1;Ëð•çççç¼í¹ççí; ç°Ê²¼ççí°ÆÀ¹Öçç»°; çDCBC-150çççÈçç; ð
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYËËÁÛÁÛ;ËÁ•Á°²óÈð;Ë, áççí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, çç¼«Æ°Áá»ðçç; á; ç°Ê²¼ççð¼Á¹Öçç; ð
0055 C. YçYÓYÉYËËÁÛÁÛ;ËÁ•Á°²óÈðçç¼í¹ççí°ÆÀ¹Öçç»°; çDCBC-150çççÈçç; ð
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç” RESTART;ËPT2;Ëð•çççç¼í¹ççí; ç°Ê²¼ççí°ÆÀ¹Öçç»°; çDCBC-151çççÈçç; ð
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYËËÁÛÁÛ;ËÁ•Á°²óÈð;Ë, áççí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ð; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ð;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-670 2010-10-28 12:26:22 133 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇòíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòí;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2010-10-28 11:04:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 06 06)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 85 83 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0091 BC (c4 14)
0092 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0093 BC (c5 10)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
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0096 + DC 07-F0 MDP\_XRT\_FLD\_ENA  
0097 BC (d8)  
0098 + DC 07-F0 MDP\_XRT\_FLRCTRL\_ENA  
0099 BC (c8)  
0100 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0101 C.  
0102 C.  
0103 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0104 C.  
0105 +. DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0106 BC (c2)  
0107 +. DC 07-F0 MDP\_XRT\_CTRL\_AUTO  
0108 BC (c0)  
0109 +. TI 2010-10-28 11:04:02.0  
0110 DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0111 BC (c2)  
0112 +. TI 2010-10-28 11:04:04.0  
0113 DC 07-F0 MDP\_XRT\_CTRL\_AUTO  
0114 BC (c0)  
0115 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0116 C.  
0117 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0118 C.  
0119 . C. \*\*\*\*\* MDP 'úÃîñî»ö¼ÝñÊÂðñ¹ñèDCBC•x²è \*\*\*\*\*  
0120 C. (¼ã°îÝÓÝÄÝÈÝÞÝËÝáÝçÝèñ¼ññ¼Ä»Ûñ¹ñè)  
0121 . S. DC-BC dcbc-402:DCBC  
0122 (MDP\_known\_event)  
0123 C.  
0124 C.  
0125 . C. \*\*\*\*\* ÝÐÝ¹•Ï Daily±¿ÎÑñÊ´Øñ¹ñèDCBC•x²è \*\*\*\*\*  
0126 . S. DC-BC dcbc-153:DCBC  
0127 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0128 C.  
0129 C.  
0130 . C. ;ãLOSÝÄÝ§ÝÄÝ¬¼Ä»Û;ä  
0131 C.  
0132 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0133 C.

Oct 28, 10 12:26

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\*\*\* OP Sequence for XRT \*\*\*

2010/10/28	11:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	11:14:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	11:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/28	11:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/10/28	11:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/28	11:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/28	11:15:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/28	11:17:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/28	11:17:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/10/28	11:17:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/10/28	11:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	15:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	15:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/28	15:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/28	15:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/28	15:57:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/28	15:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	17:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	17:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/28	17:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/28	17:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/28	17:34:00.0	XRT_Custom_418_OG [0x1a2]							
2010/10/28	17:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	18:47:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	18:47:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/28	18:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/28	18:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/28	19:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/10/28	19:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	19:11:02.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/10/28	19:11:22.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/10/28	19:11:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/10/28	19:11:26.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/28	19:11:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/10/28	19:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	19:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	19:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/10/28	20:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/10/28	20:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/10/28	20:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/10/28	20:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/10/28	20:02:52.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/10/28	20:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/28	20:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/10/28	20:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/10/28	20:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	20:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							

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2010/10/28	20:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/28	20:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/28	20:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/28	20:48:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/28	20:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/28	22:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/28	22:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/28	22:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/10/28	22:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/28	22:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/28	22:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/10/28	22:30:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00			
2010/10/28	22:30:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/28	22:30:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/10/28	22:30:20.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/28	22:32:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2010/10/28	22:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/28	23:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/28	23:00:02.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/10/28	23:00:22.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/10/28	23:00:24.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/10/28	23:00:26.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/28	23:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2010/10/28	23:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/28	23:03:19.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/28	23:03:21.0	XRT_STANDBY_417_OG [0x1a1]	XRT_STBY	2	07-F8	03 01			
2010/10/28	23:03:23.0	XRT_DIAGNOSTIC_439_OG [0x1b7]	XRT_DIAGNOSTIC	2	07-F8	03 07			
2010/10/28	23:03:25.0	XRT_FSA_CONTROL_440_OG [0x1b8]	XRT_FW1_STATUS_LSB	4	07-F8	51 e0 00 00			
2010/10/28	23:03:27.5	XRT_FSA_CONTROL_441_OG [0x1b9]	XRT_FW1_RESET	4	07-F8	51 40 00 00			
2010/10/28	23:03:30.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:32.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:35.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:37.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:40.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:42.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:45.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:47.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:50.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:52.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:55.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:03:57.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:00.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:02.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:05.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:07.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:10.0	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			
2010/10/28	23:04:12.5	XRT_FSA_CONTROL_444_OG [0x1bc]	XRT_FW1_STEP_CCW	4	07-F8	51 48 00 00			





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2010/10/28	23:06:27.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2010/10/28	23:06:29.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/28	23:08:43.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/28	23:08:45.0	XRT_FSA_CONTROL_440_OG [0x1b8]							
		XRT_FW1_STATUS_LSB	4	07-F8	51	e0 00 00			
2010/10/28	23:08:47.5	XRT_FSA_CONTROL_441_OG [0x1b9]							
		XRT_FW1_RESET	4	07-F8	51	40 00 00			
2010/10/28	23:08:50.0	XRT_FSA_CONTROL_444_OG [0x1bc]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48 00 00			
2010/10/28	23:08:52.5	XRT_FSA_CONTROL_444_OG [0x1bc]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48 00 00			
2010/10/28	23:08:55.0	XRT_FSA_CONTROL_444_OG [0x1bc]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48 00 00			
2010/10/28	23:08:57.5	XRT_FSA_CONTROL_444_OG [0x1bc]							
		XRT_FW1_STEP_CCW	4	07-F8	51	48 00 00			
2010/10/28	23:09:00.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:02.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:05.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:07.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:10.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:12.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:15.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:17.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:20.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:22.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:25.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:27.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:30.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:32.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:35.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:37.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:40.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:42.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:45.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:47.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:50.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:52.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:55.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:09:57.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:00.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:02.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:05.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:07.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:10.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:12.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:15.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:17.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:20.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:22.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:25.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:27.5	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:30.0	XRT_FSA_CONTROL_407_OG [0x197]							
		XRT_FW1_STEP_CW	4	07-F8	51	46 00 00			
2010/10/28	23:10:32.5	XRT_FSA_CONTROL_407_OG [0x197]							

2010/10/28	23:10:35.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:37.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:40.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:42.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:45.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:47.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:50.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:52.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:55.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:10:57.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:00.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:02.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:05.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:07.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:10.0	XRT_FSA_CONTROL_440_OG [0x1b8]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:12.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/28	23:11:15.0	XRT_FSA_CONTROL_440_OG [0x1b8]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:17.5	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/28	23:11:20.0	XRT_FSA_CONTROL_440_OG [0x1b8]	XRT_FW1_STEP_CW	4	07-F8	51	46	00	00
2010/10/28	23:11:22.5	XRT_FSA_CONTROL_404_OG [0x194]	XRT_FW_1_STATUS_LSB	4	07-F8	51	e0	00	00
2010/10/28	23:11:25.0	XRT_FSA_CONTROL_441_OG [0x1b9]	XRT_FW_1_STATUS_MSB	4	07-F8	51	e2	00	00
2010/10/28	23:11:28.0	XRT_FSA_CONTROL_407_OG [0x197]	XRT_FW1_RESET	4	07-F8	51	40	00	00
2010/10/28	23:11:48.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2010/10/28	23:11:50.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2010/10/28	23:11:52.0	XRT_STANDBY_417_OG [0x1a1]	XRT_STBY	2	07-F8	03	01		
2010/10/28	23:11:54.0	XRT_OPERATE_414_OG [0x19e]	XRT_OPERATE	2	07-F8	03	02		
2010/10/28	23:25:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/29	01:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/29	01:59:56.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/29	02:00:00.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/10/29	02:00:16.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00	00
2010/10/29	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/10/29	02:00:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/10/29	02:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/10/29	02:02:54.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2010/10/29	02:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/29	02:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2010/10/29	02:03:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2010/10/29	02:39:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/29	02:39:02.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/29	02:39:04.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/29	02:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/10/29	02:59:30.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_CUSTOM	1	07-F0	e8			
2010/10/29	03:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/10/29	04:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/10/29	04:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/10/29	04:07:34.0	XRT_PREFLR_STOP_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	da			

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2010/10/29	04:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2010/10/29	04:37:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	04:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	05:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	05:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	05:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	05:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	06:14:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	06:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	06:24:24.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/10/29	06:24:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/10/29	06:24:30.0	AOCs_OrE-point_Start_3_OG [0x099]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/10/29	06:24:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/10/29	06:24:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/10/29	06:24:50.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/10/29	06:27:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	06:27:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	06:34:24.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/10/29	06:34:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/10/29	06:34:30.0	AOCs_OrE-point_Start_1_OG [0x097]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/10/29	06:34:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/10/29	06:34:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/10/29	06:34:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	06:37:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2010/10/29	06:37:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2010/10/29	06:37:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	06:37:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	07:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	07:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	07:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	07:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	07:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	07:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	09:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	09:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	09:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	09:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	09:27:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	09:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	10:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	10:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	10:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	10:53:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	10:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	10:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	14:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	14:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	14:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	14:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2010/10/29	14:46:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/29	14:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/10/29	16:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	16:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	16:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/29	16:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/29	16:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/29	16:34:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	16:34:22.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/10/29	16:34:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/10/29	16:34:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/10/29	16:34:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/29	16:34:30.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2010/10/29	16:34:32.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/10/29	17:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	17:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	17:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/29	17:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/29	18:11:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/29	18:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/10/29	18:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	18:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	19:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/10/29	19:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	03 00 00 00 00		
2010/10/29	19:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/10/29	19:00:20.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/10/29	19:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/29	19:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/10/29	19:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	19:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	19:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/29	19:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/10/29	19:47:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/10/29	19:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2010/10/29	19:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	19:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	20:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/10/29	20:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00		
2010/10/29	20:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/10/29	20:00:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/10/29	20:02:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/10/29	20:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/29	20:03:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2010/10/29	20:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/10/29	21:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/10/29	21:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/10/29	21:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/10/29	21:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		

2010/10/29	21:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	21:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/29	22:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/29	22:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/29	22:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/29	22:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/29	23:00:01.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/29	23:01:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/30	00:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	00:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	00:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/30	00:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/30	00:24:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/30	00:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/30	01:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	01:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	01:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/30	01:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/30	01:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/30	01:59:30.0	XRT_Custom_418_OG [0x1a2]					
2010/10/30	02:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/30	03:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	03:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	03:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/30	03:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/30	03:36:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/30	03:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/30	04:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	04:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	04:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/30	04:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/10/30	04:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/10/30	05:14:00.0	XRT_Custom_418_OG [0x1a2]					
2010/10/30	05:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/10/30	05:57:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	05:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	05:58:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/10/30	05:58:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/10/30	05:58:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/10/30	05:58:20.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/10/30	06:00:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/10/30	06:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/10/30	06:07:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/10/30	06:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/10/30	06:08:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/10/30	06:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/10/30	06:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/10/30	06:08:20.0	XRT_ARS_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/10/30	06:10:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/10/30	06:10:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/10/30	06:11:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	

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2010/10/30	06:11:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/30	06:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/30	06:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/30	06:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/30	06:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/30	06:51:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/30	06:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/30	08:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/30	08:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/30	08:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/30	08:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/30	08:28:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/30	08:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/30	09:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/30	09:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/10/30	09:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/10/30	09:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/10/30	10:02:30.0	XRT_Custom_418_OG [0x1a2]							
2010/10/30	10:03:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/10/30	11:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/10/30	11:39:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				