

XRT Timeline to be uploaded on 2010/11/06

Period: 2010/11/06 10:08:00 - 2010/11/10 09:36:00

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Normal mode

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XOB #1836: CME watch - Ti/poly - AEC 1 - 1024 FOV - 2x2 - with PFB buffer												
Term	Pointing (x, y)							Comment				
11/06 10:32:36 - 11/06 17:50:00	Track (-780.3, -383.6) ^{Ⓜ 11/06 10:18:00}	# OP start + 10min: track new AR										
11/06 18:38:06 - 11/07 05:59:54	Track (-743.5, -387.3) ^{Ⓜ 11/06 18:23:30}	# track new AR										
11/07 07:08:36 - 11/07 16:49:30	Track (-682.3, -392.3) ^{Ⓜ 11/07 06:10:00}	# track new AR										
PROG= 15 1-time(s)												
└─ Subr= 1 10-time(s) 4.0sec												
└─ Seqn= 49 1-time(s) 105.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	1024x1024 (1024, 1024)	Q=98	1	0	25.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	1024x1024 (1024, 1024)	Q=98	1	1	25.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	1024x1024 (1024, 1024)	Q=98	1	2	25.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	1024x1024 (1024, 1024)	Q=98	1	3	25.0sec	
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	1024x1024 (1024, 1024)	Q=90	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)							Comment				
11/06 18:16:30 - 11/06 18:23:24	Fixed (0.0, 0.0)	synoptic, shifted 13.5 min										
11/07 06:03:00 - 11/07 06:09:54	Fixed (0.0, 0.0)	synoptic										
11/07 18:03:00 - 11/07 18:09:54	Fixed (0.0, 0.0)	synoptic										
11/08 06:06:00 - 11/08 06:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min										
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 8 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)							Comment				
11/06 10:32:36 - 11/06 17:50:00	Track (-780.3, -383.6) ^{Ⓜ 11/06 10:18:00}	# OP start + 10min: track new AR										
11/06 18:38:06 - 11/07 05:59:54	Track (-743.5, -387.3) ^{Ⓜ 11/06 18:23:30}	# track new AR										
11/07 07:08:36 - 11/07 16:49:30	Track (-682.3, -392.3) ^{Ⓜ 11/07 06:10:00}	# track new AR										
PROG= 16 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Seqn= 60 1-time(s) 2.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Subr= 3 30-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Seqn= 88 1-time(s) 2.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

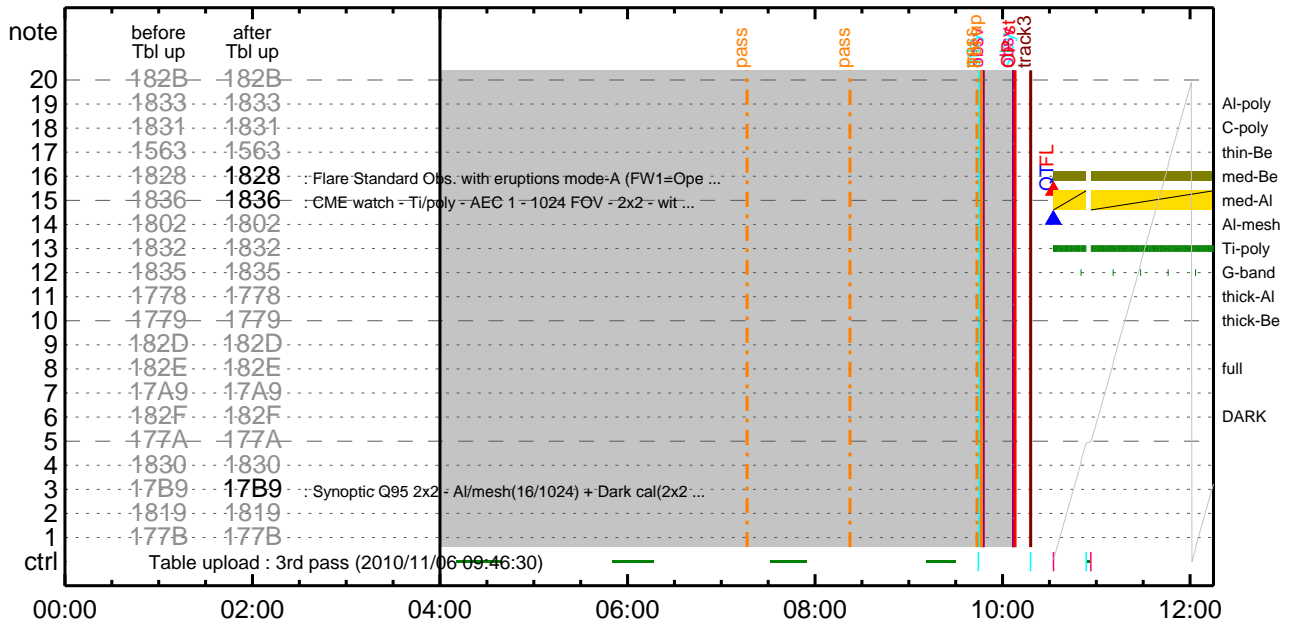
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Flare Detection

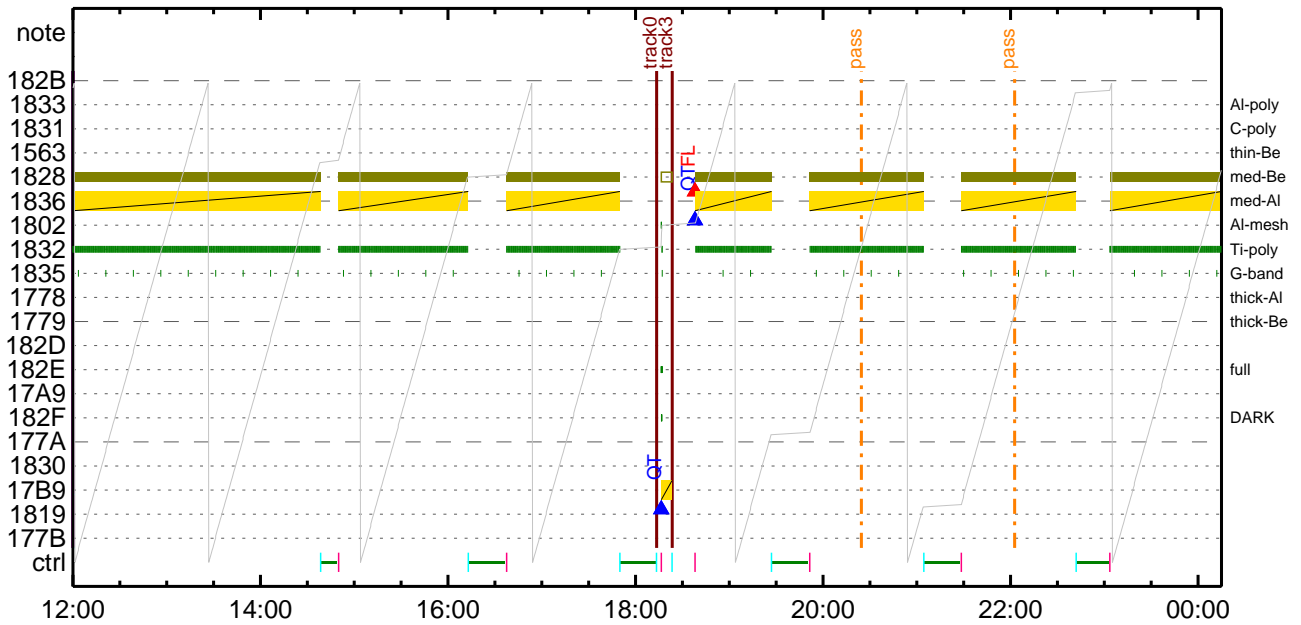
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FLD Patrol												
Term	Pointing (x, y)							Comment				
11/06 10:18:16 - 11/06 18:13:46	Track (-780.3, -383.6)	^{® 11/06 10:18:00}						# OP start + 10min: track new AR				
11/06 18:23:46 - 11/07 06:00:16	Track (-743.5, -387.3)	^{® 11/06 18:23:30}						# track new AR				
11/07 06:54:16 - 11/07 18:00:16	Track (-682.3, -392.3)	^{® 11/07 06:10:00}						# track new AR				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

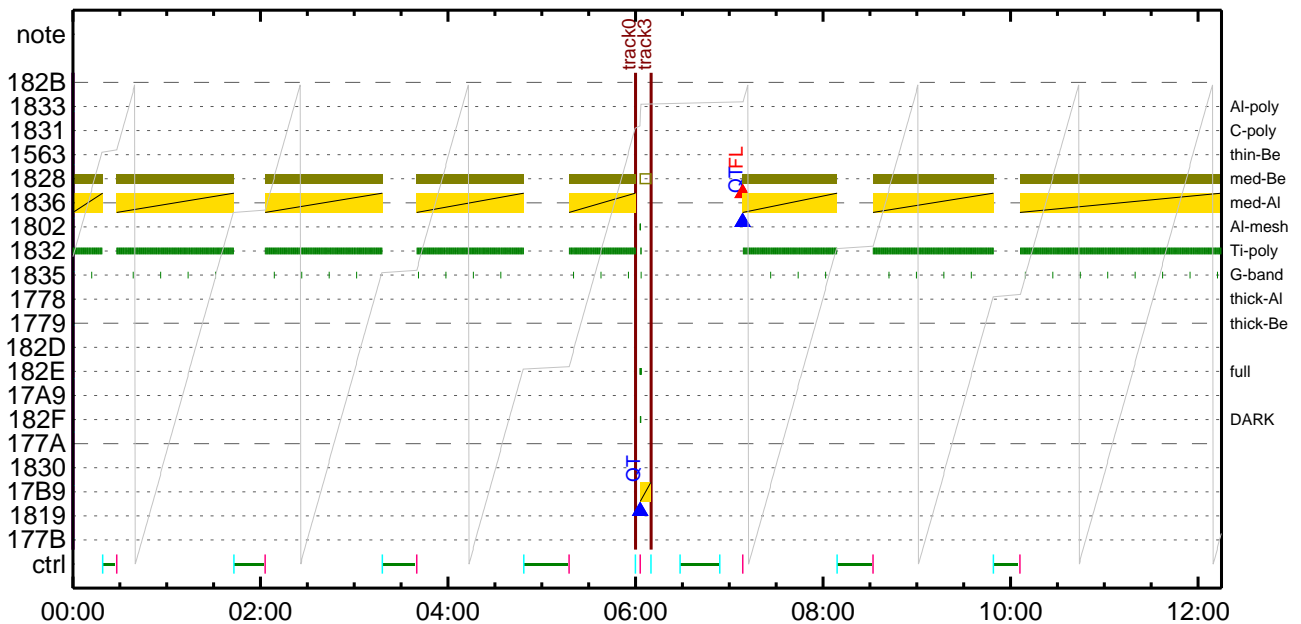
CMDI #0590 2010/11/06



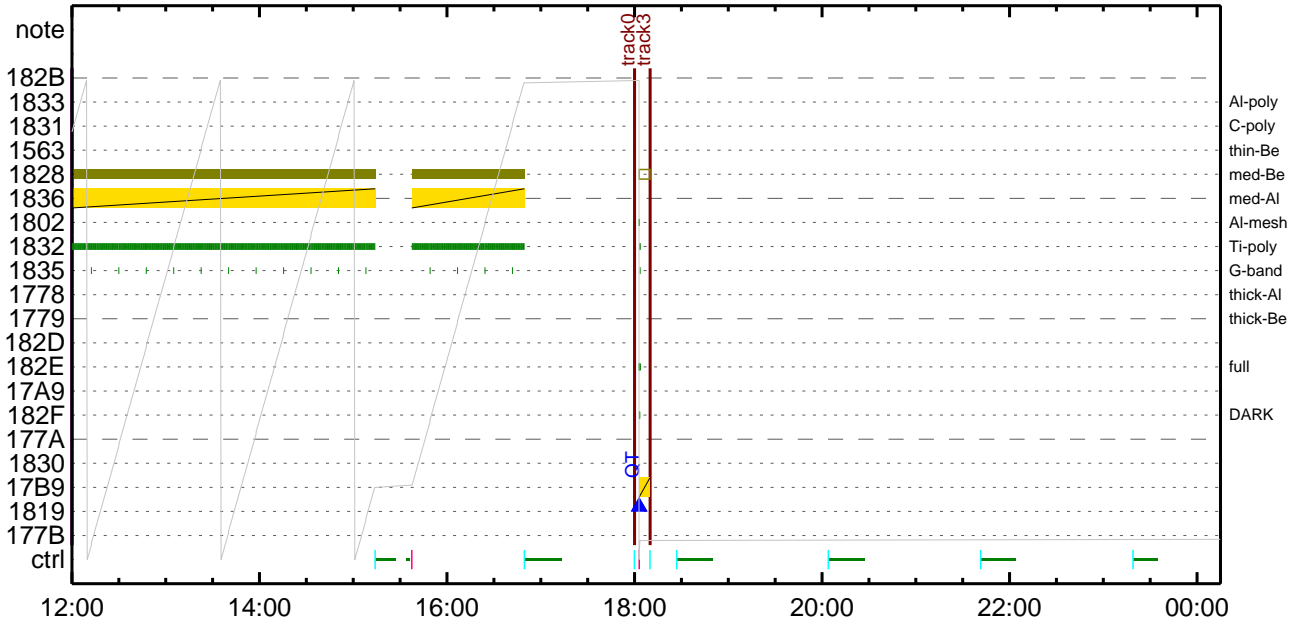
CMDI #0590 2010/11/06



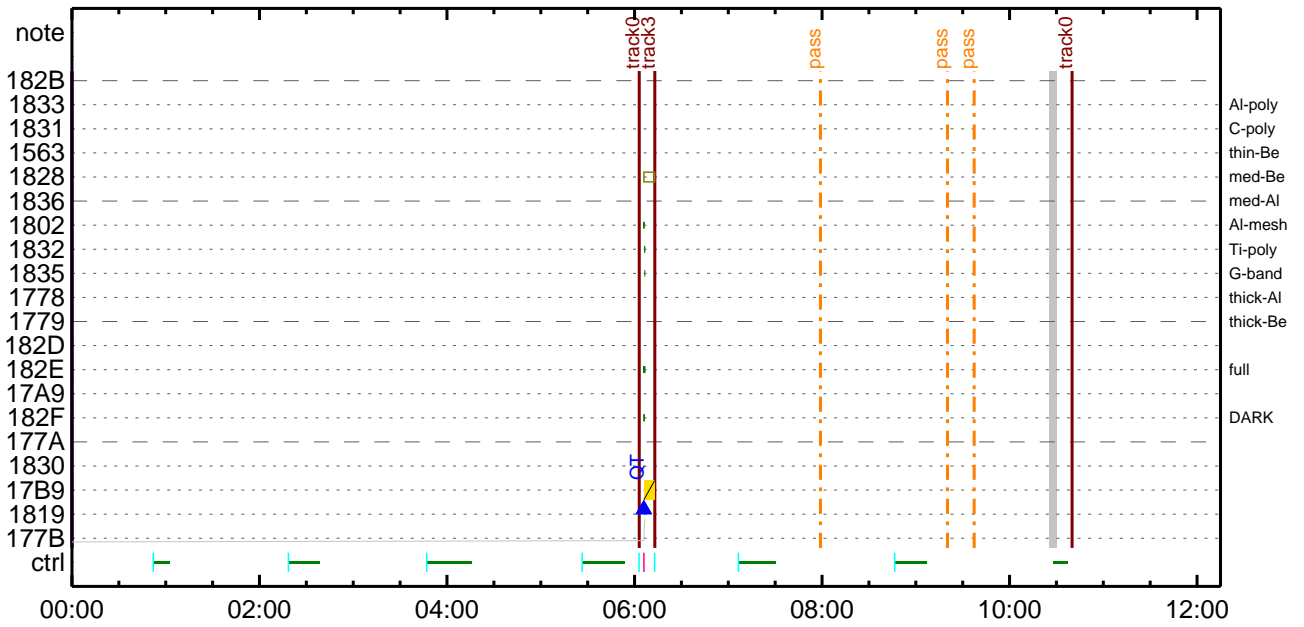
CMDI #0590 2010/11/07



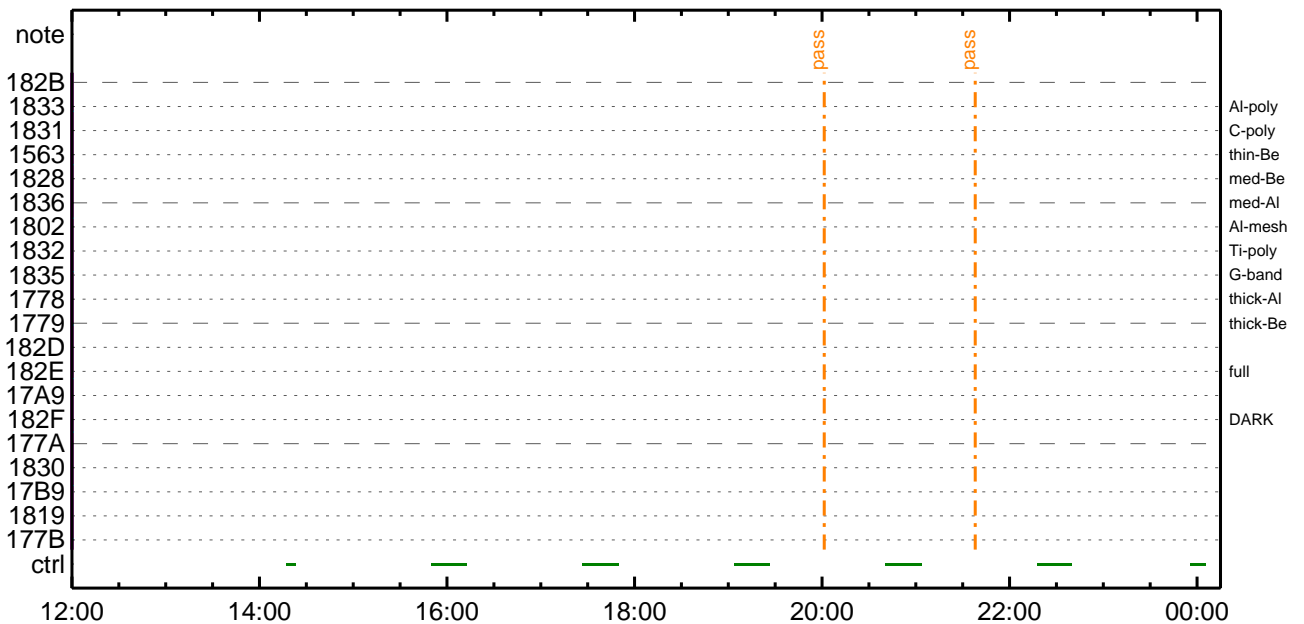
CMDI #0590 2010/11/07



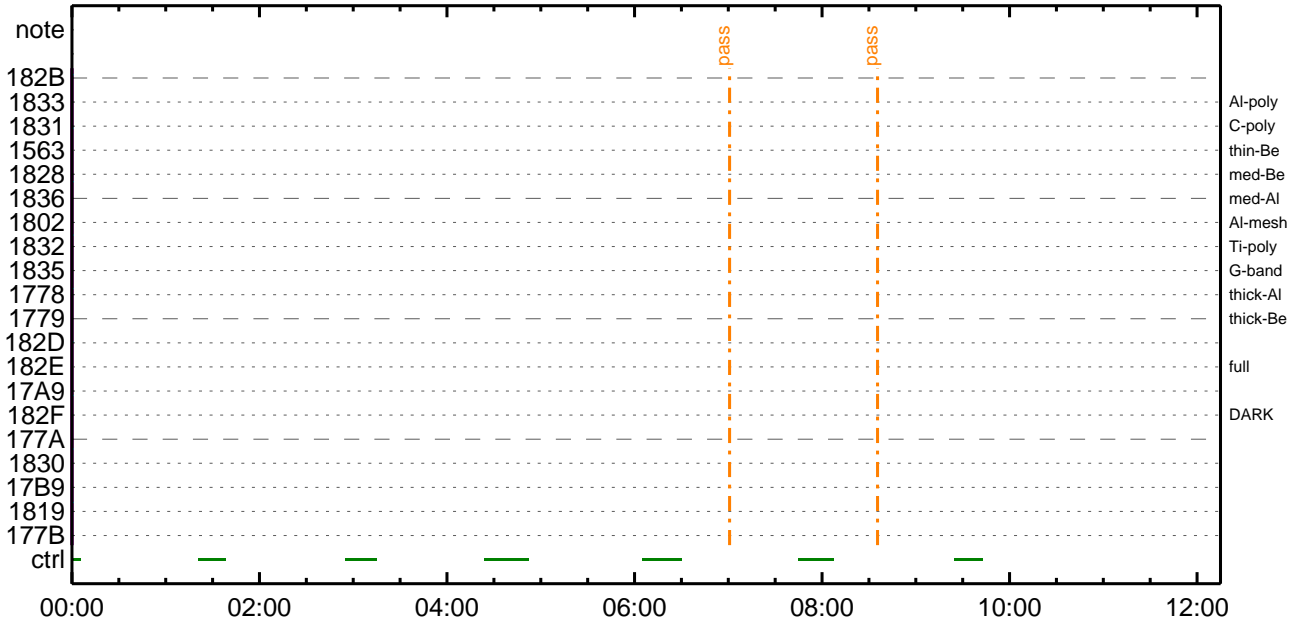
CMDI #0590 2010/11/08



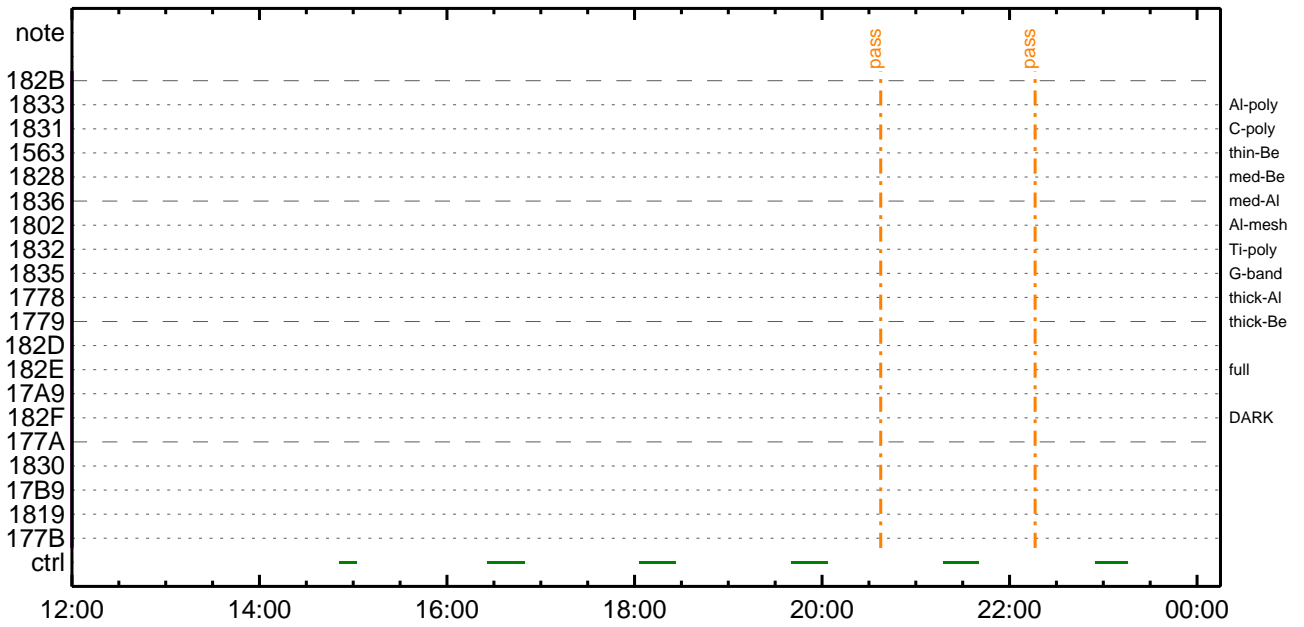
CMDI #0590 2010/11/08



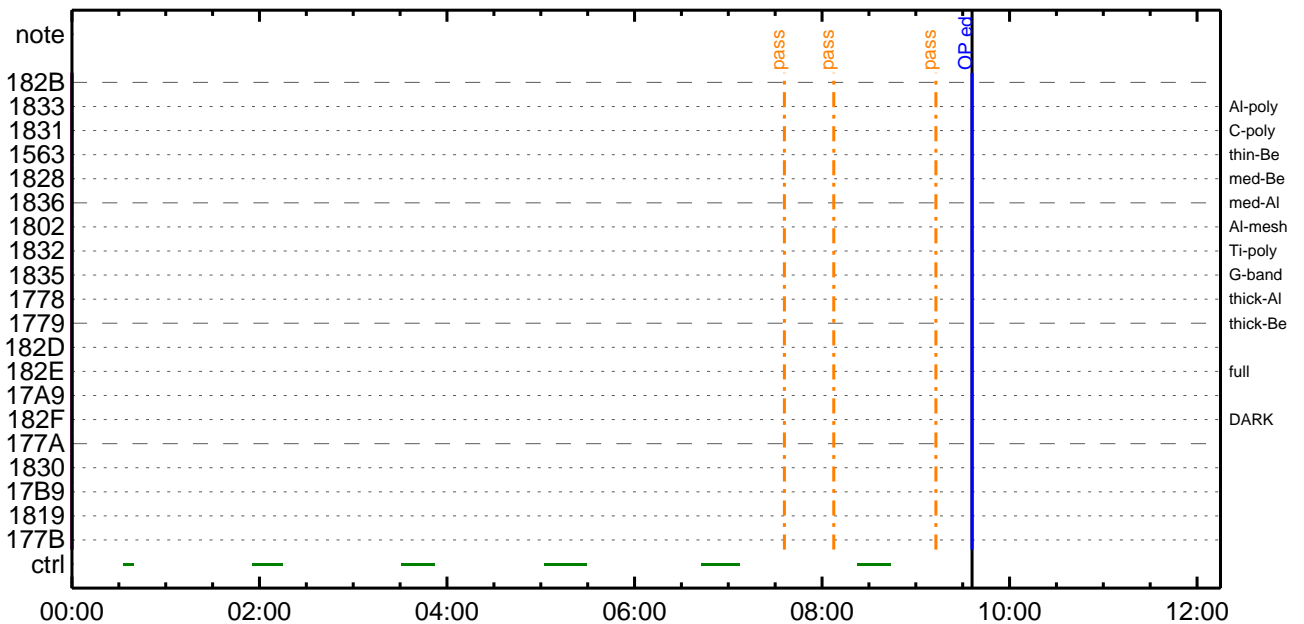
CMDI #0590 2010/11/09



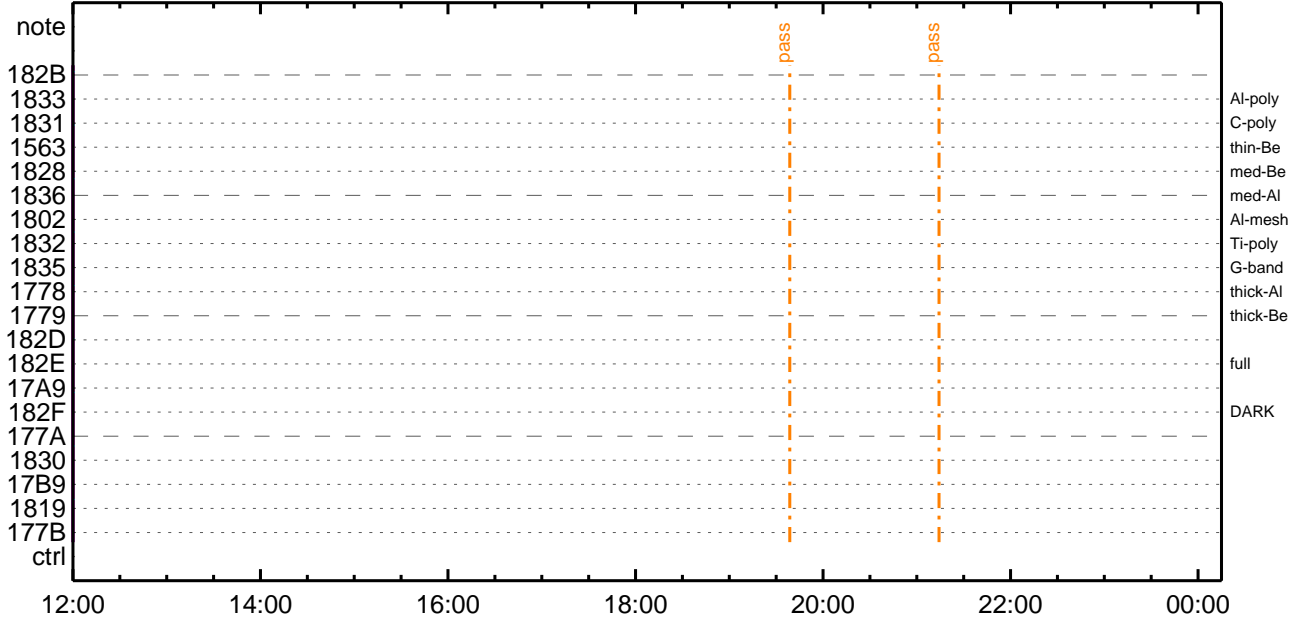
CMDI #0590 2010/11/09



CMDI #0590 2010/11/10



CMDI #0590 2010/11/10




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0096 C.          0300; SET0EDUMP0I00iYNY1000|0300;E
0097 C.
0098 C. TIY3YBY0Y0000ADi (UT)
0099 +. TI 2010-11-06 10:03:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2010-11-06 10:03:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2010-11-06 10:03:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2010-11-06 10:07:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. 0E2%0IAE%iIN0IYA¥$YA¥-1aIU
0116 C.          00[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          00[HK1_TI_CMD_NUM]          EQ          4
0118 C.          00[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          00[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TI0I0EYAY0YX
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          00[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          00[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          00[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          00[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          00[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          00[HK1_PKT_FORM_NO]          EQ          7
0136 C.          00[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          00[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YAY0YX%YA I»003IC$
0142 C.          00[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBL0I0E1000e2IOK0003IC$
0145 C.
0146 C. DHU¥â;¥¥E;E¥¥,¥i;¥¥E;E00Iâ01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          00[HK1_PKT_FORM_NO]          EQ          2
0150 C.          00[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2010-11-06 10:07:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2010-11-06 10:07:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2010-11-06 10:07:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]        EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-11-06 10:07:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]        EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0AIAI00Y0E0A000eDCBC0x2e *****
0192 C. (%â0IY0YAYEYBYEYAY0Y0E0E%000A»U010e)
0193 C. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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(a) Spacecraft Operation Procedure (real-commands)

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main-693 2010-11-06 12:56:00 114 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÈò¿òÀò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÇò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG_____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 80 80 10 10)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 10 10)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 . C. ----- Success Verify ? OK / NG _____
0055 C.
0056 C.
0057 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0058 C.
0059 +. DC 07-F0 MDP_XRT_MODE_OBSV
0060 BC (c2)
0061 +. TI 2010-11-06 10:07:02.0
0062 DC 07-F0 MDP_XRT_MODE_OBSV
0063 BC (c2)
0064 . C. ----- Success Verify ? OK / NG _____
0065 C.
0066 C. ***** XRT END *****
0067 . C. *****
0068 C. SOT table upload
0069 C. *****
0070 . C. < Stop FG table >
0071 +. DC 07-F0 MDP_FG_CTRL_MANU
0072 BC (51)
0073 . C. -----
0074 C. MDP_FG_CTRL_MODE = MANU [ ]
0075 C. -----
0076 C.
0077 . C. <Upload FG Observation Table>
0078 . S. RAM ram-265:MDP_OBS_F
0079 ( )
0080 C.
0081 . C. < Dump RAMID=MDP_OBS_F >
0082 +. DC 07-F0 MDP_DUMP_FGTBL
0083 BC (82 07 00 00 00 38 b8)
0084 C. -----
0085 C. MDP_OBS_F verify = OK/NG [ ]
0086 C. -----
0087 C.
0088 C. *****
0089 C. SOT TI command set
0090 C. *****
0091 C. Execute, after the success of TBL upload.
0092 +. TI 2010-11-06 10:07:18.0
0093 DC 07-F0 MDP_SOT_MODE_OBSV
0094 BC (40)
0095 . C. -----
```


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*** OP Sequence for XRT ***

2010/11/06	10:17:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	10:17:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/06	10:18:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/11/06	10:18:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/06	10:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/06	10:18:20.0	XRT_ARS_DIS_433_OG [0x1b1]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/06	10:32:30.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/06	10:32:32.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2010/11/06	10:32:34.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/06	10:32:36.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/06	10:53:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	10:53:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/06	10:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/06	10:55:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/06	10:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/06	10:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/06	14:38:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	14:38:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/06	14:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/06	14:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/06	14:49:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/06	14:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/06	16:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	16:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/06	16:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/06	16:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/06	16:36:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/06	16:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/06	17:50:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	17:50:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/06	17:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/06	17:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/06	18:13:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	18:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/06	18:13:30.5	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/11/06	18:13:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/11/06	18:13:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/11/06	18:16:26.0	XRT_ARS_DIS_447_OG [0x1bf]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/06	18:16:28.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/06	18:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/06	18:23:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/06	18:23:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/06	18:23:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/11/06	18:23:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/06	18:23:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/06	18:23:50.0	XRT_ARS_DIS_433_OG [0x1b1]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/06	18:38:00.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2010/11/06	18:38:02.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2010/11/06	18:38:04.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/06	18:38:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/06	19:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/06	19:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/06	19:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/06	19:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/06	19:50:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/06	19:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/06	21:04:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/06	21:04:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/06	21:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/06	21:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/06	21:27:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/06	21:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/06	22:42:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/06	22:42:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/06	22:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/06	22:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/06	23:02:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/06	23:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	00:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	00:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	00:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	00:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	00:27:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	00:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	01:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	01:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	01:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	01:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	02:02:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	02:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	03:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	03:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	03:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	03:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	03:39:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	03:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	04:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	04:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	04:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	04:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	05:16:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	05:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/11/07	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/11/07	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/07	06:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				

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2010/11/07	06:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/07	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/07	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	06:09:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/07	06:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	06:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	06:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	06:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	06:53:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	06:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/07	06:54:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/07	06:54:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/07	06:54:20.0	XRT_ARS_DIS_433_OG [0x1b1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/07	07:08:30.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	07:08:32.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2010/11/07	07:08:34.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/07	07:08:36.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	08:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	08:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	08:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	08:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	08:31:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	08:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	09:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	09:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	09:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	09:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	10:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	10:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	15:14:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	15:14:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	15:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	15:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	15:36:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/07	15:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	16:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	16:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	16:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	16:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/07	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/07	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/07	18:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/07	18:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/07	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				

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2010/11/07	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/07	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	18:09:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/07	18:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	18:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	18:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	18:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	20:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	20:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	20:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	20:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	21:41:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	21:41:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	21:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	21:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/07	23:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/07	23:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/07	23:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/07	23:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/08	00:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	00:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/08	00:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/08	00:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/08	02:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	02:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/08	02:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/08	02:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/08	03:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	03:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/08	03:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/08	03:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/08	05:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	05:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/08	05:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/08	05:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/08	06:02:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	06:02:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/08	06:03:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/08	06:03:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/08	06:03:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/08	06:05:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/08	06:05:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/08	06:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/08	06:12:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/08	06:12:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/08	06:13:00.0	AOCS_OrE-point_Start_1_OG [0x097]							

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2010/11/08	07:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	03	00	00	00	00
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/11/08	07:06:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2010/11/08	07:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/11/08	07:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/11/08	08:46:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/11/08	08:46:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2010/11/08	08:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2010/11/08	08:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2010/11/08	10:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00