XRT Timeline to be uploaded on 2010/11/10

Period: 2010/11/10 09:36:00 - 2010/11/13 10:19:00

* * * * * **Normal mode** * * * * *

XOB #182B: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 60sec-cad

Term	Pointing (x, y)	Comment
11/10 10:00:36 - 11/10 14:59:54	Track (-177.9, -404.5) @ 11/10 09:46:00	# OP start + 10min, track AR 11121, with HOP 174 (Sac Peak) over 15 - 19 UT.

PROG= 20 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 95 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec

Seqn= 18 45-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

XOB #1837: AR Standard-B(Morphology) with PFB, FW1=Open, Al/Mesh, 384x384 at 1064 1048, 40sec-cad

Term	Pointing (x, y)	Comment
11/10 15:14:36 - 11/10 18:40:00	Track (-177.9, -404.5) @ 11/10 09:46:00	# OP start + 10min, track AR 11121, with HOP 174 (Sac Peak) over 15 - 19 UT.

PROG= 09 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 95 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec

Seqn= 78 45-time(s) 24.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
11/10 19:08:00 - 11/10 19:14:54	Fixed (0.0, 0.0)	synoptic, shifted manually.
11/11 06:19:30 - 11/11 06:26:24	Fixed (0.0, 0.0)	synoptic, shifted 16.5 min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1830: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad

Term	Pointing (x, y)	Comment
11/11 02:00:36 - 11/11 05:40:00	Track (-41.6, -404.4) @ 11/11 01:46:00	* Resume tracking AR 11121.
11/11 06:41:06 - 11/11 10:41:54	Track (-1.5, -404.2) @ 11/11 06:26:30	# Cont.

PROG= 04 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 95 4-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Subr= 1 1-time(s) 2.0sec													
Seqn= 3 35-time(s) 80.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
11/10 10:00:36 - 11/10 14:59:54	Track (-177.9, -404.5) @ 11/10 09:46:00	# OP start + 10min, track AR 11121, with HOP 174 (Sac Peak) over 15 - 19 UT.
11/10 15:14:36 - 11/10 18:40:00	Track (-177.9, -404.5) @ 11/10 09:46:00	# OP start + 10min, track AR 11121, with HOP 174 (Sac Peak) over 15 - 19 UT.
11/11 02:00:36 - 11/11 05:40:00	Track (-41.6, -404.4) @ 11/11 01:46:00	* Resume tracking AR 11121.
11/11 06:41:06 - 11/11 10:41:54	Track (-1.5, -404.2) @ 11/11 06:26:30	# Cont.

PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 60 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

* * * * * **Active Region Search** * * * * *

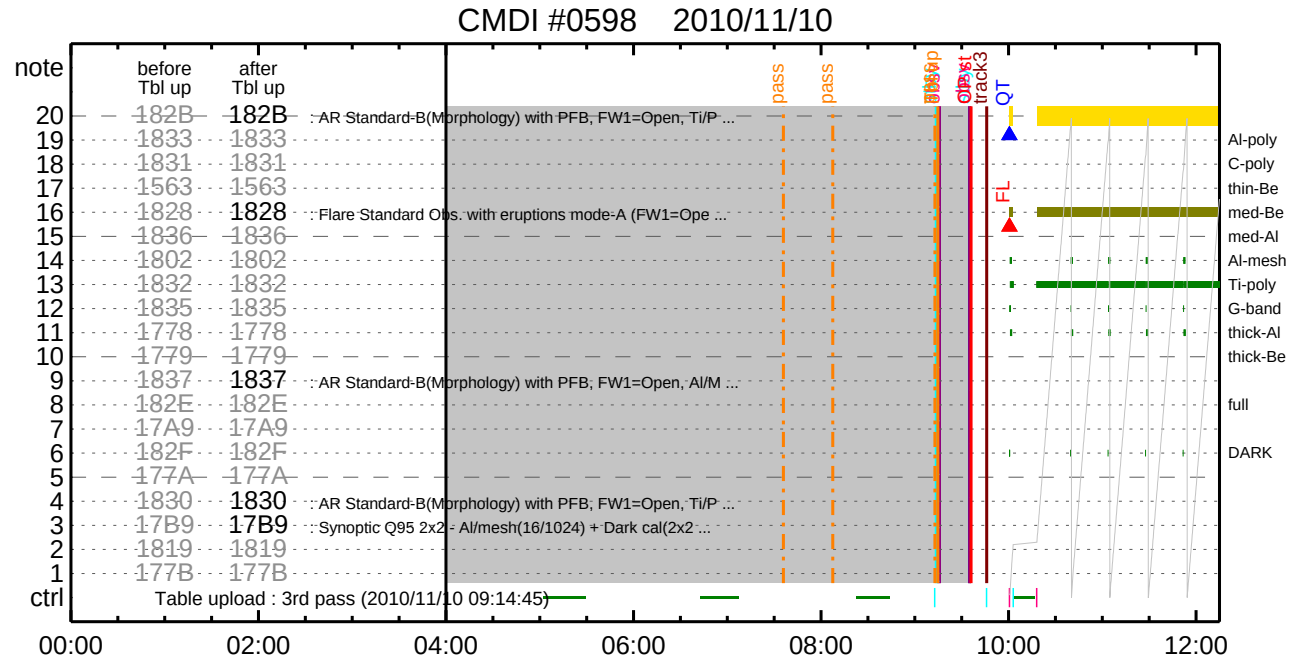
NOT USED

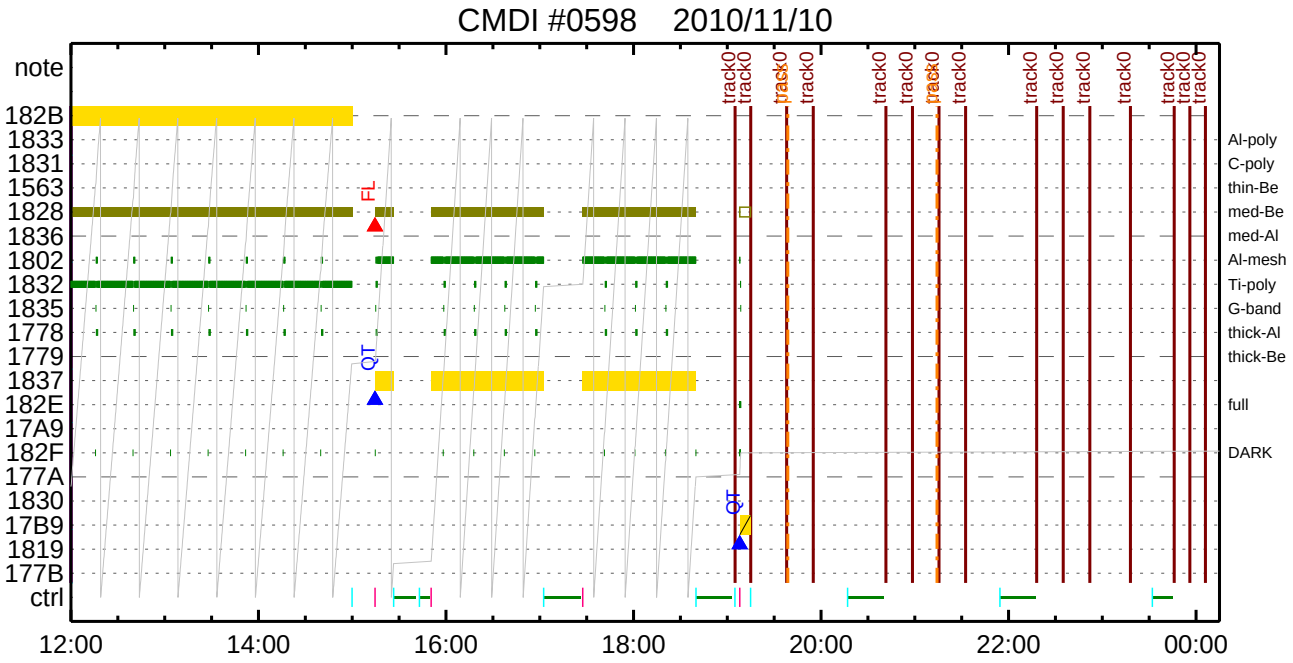
* * * * * **Flare Detection** * * * * *

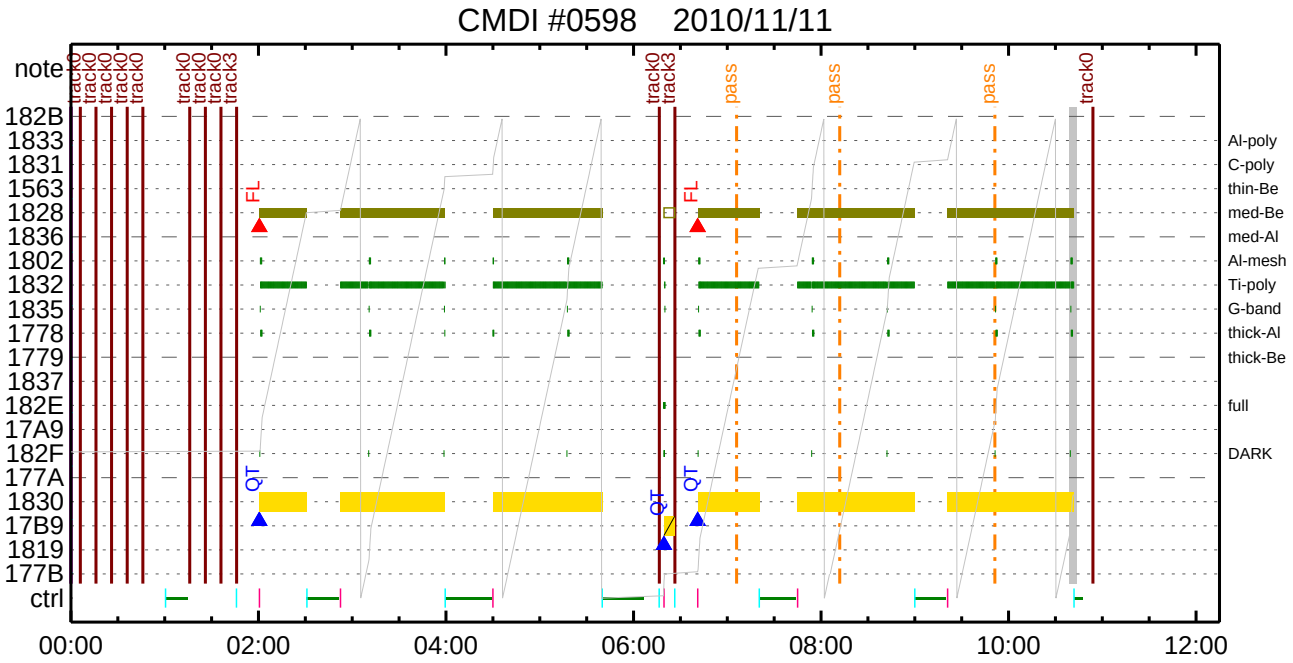
FLD Patrol

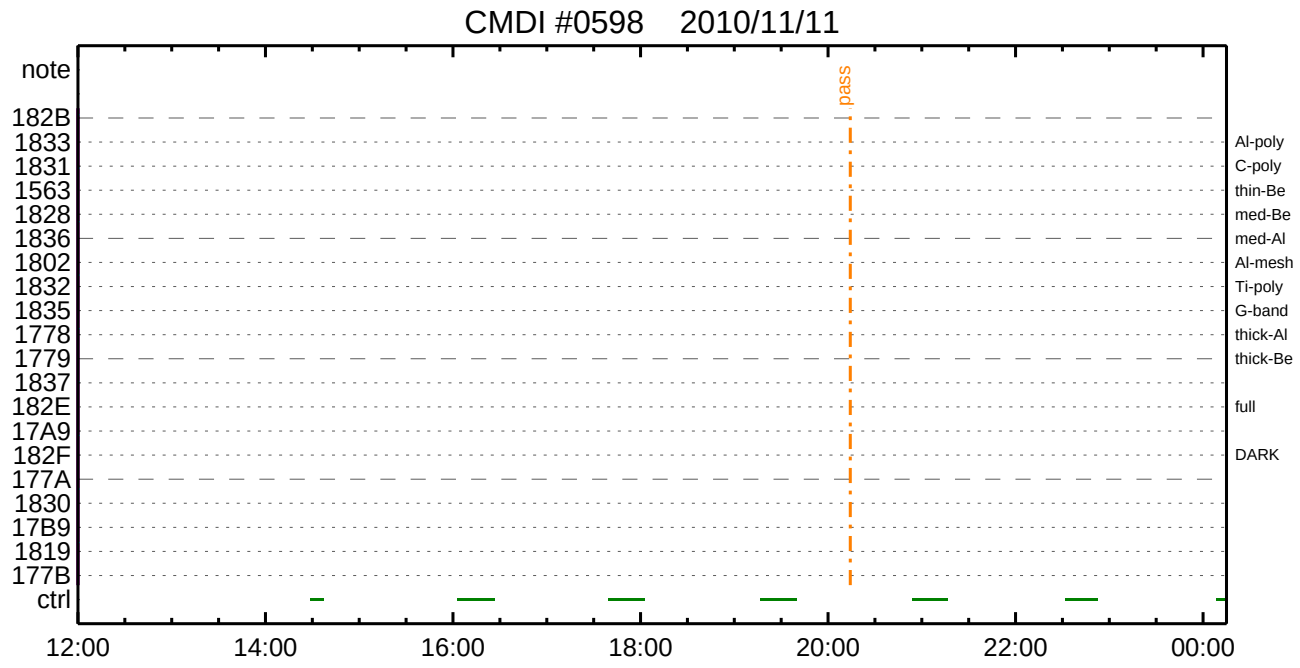
Term	Pointing (x, y)	Comment
11/10 09:46:16 - 11/10 19:05:16	Track (-177.9, -404.5) @ 11/10 09:46:00	# OP start + 10min, track AR 11121, with HOP 174 (Sac Peak) over 15 - 19 UT.
11/11 01:46:16 - 11/11 06:16:46	Track (-41.6, -404.4) @ 11/11 01:46:00	* Resume tracking AR 11121.
11/11 06:26:46 - 11/13 10:19:00	Track (-1.5, -404.2) @ 11/11 06:26:30	# Cont.

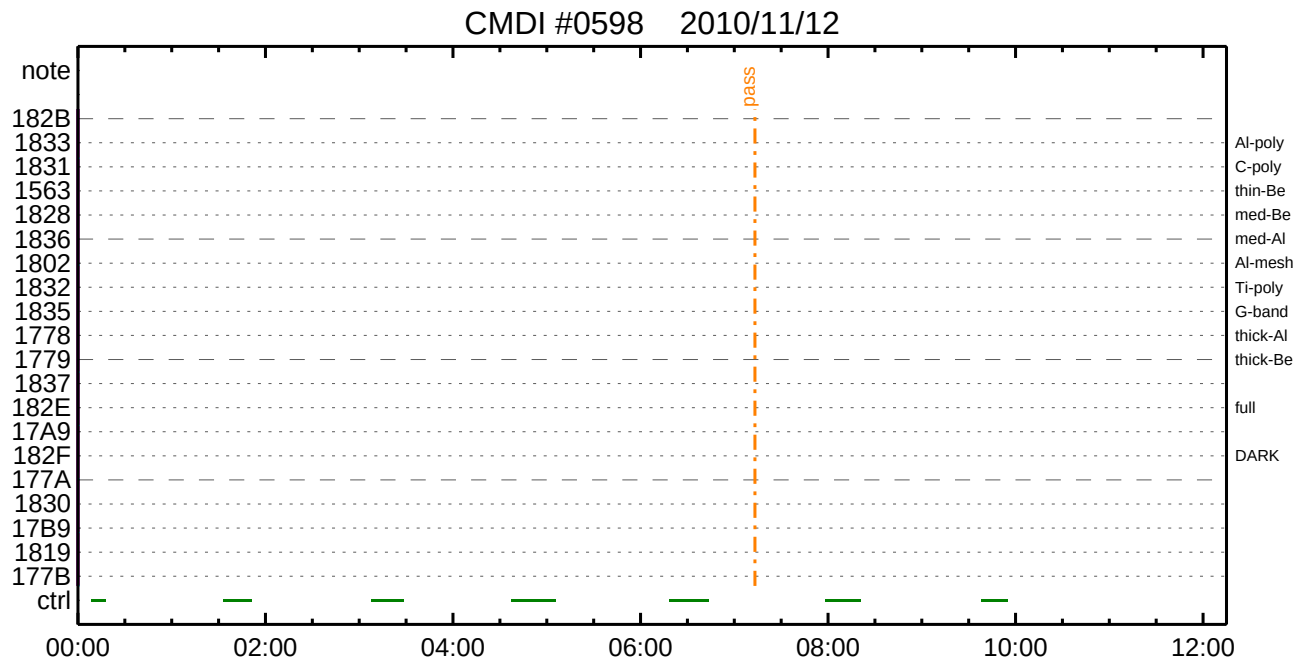
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

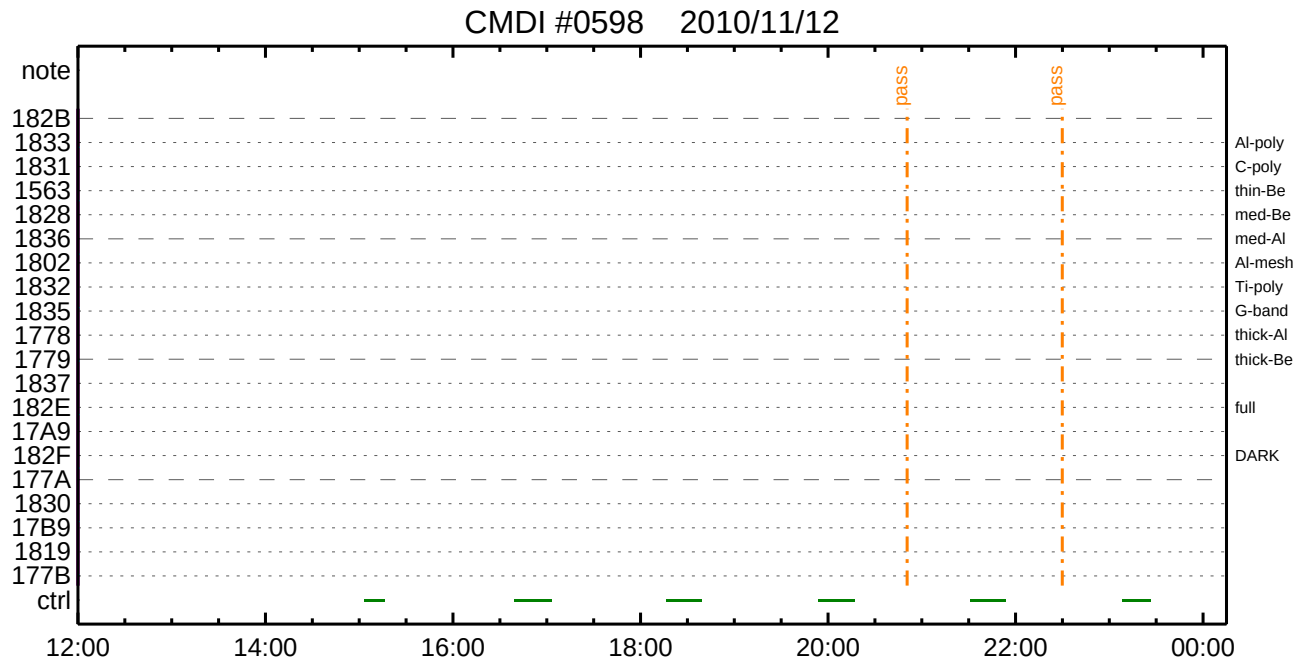

CMDI #0598 2010/11/10
note
before Tbl up
after Tbl up
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
: Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
: AR Standard-B(Morphology) with PFB, FW1=Open, Al/M ...
: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
Table upload : 3rd pass (2010/11/10 09:14:45)
pass
pass
track3
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

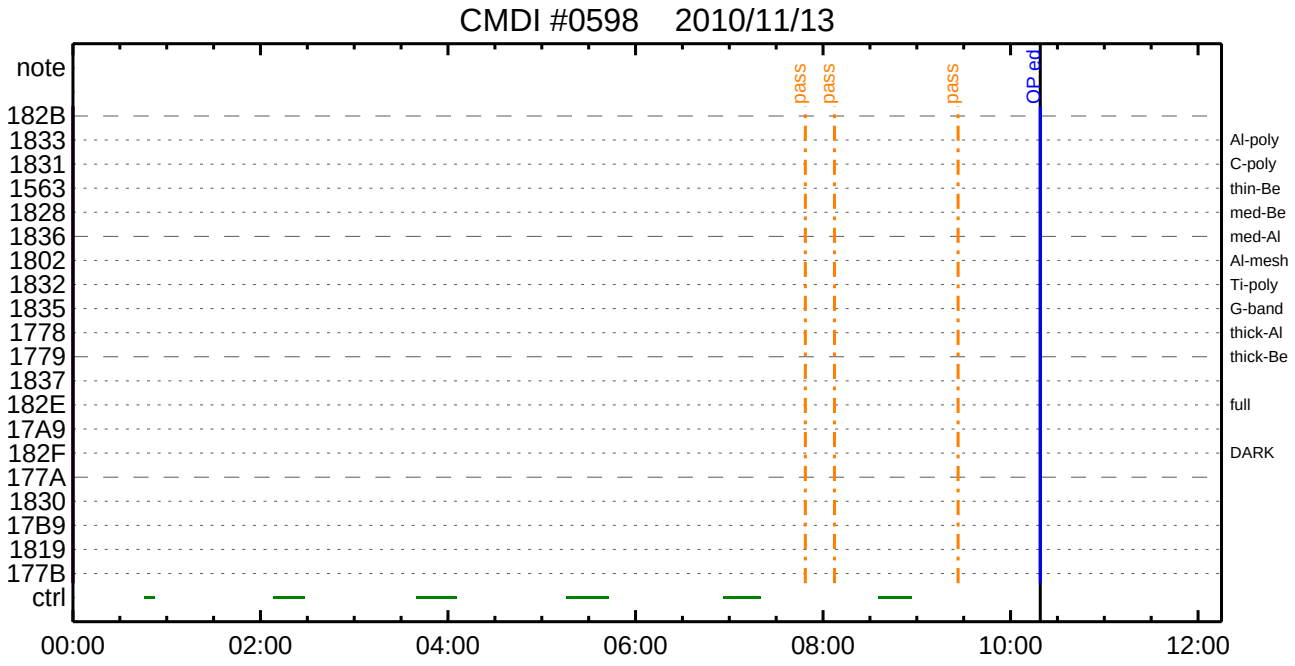

CMDI #0598 2010/11/10
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
track0
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

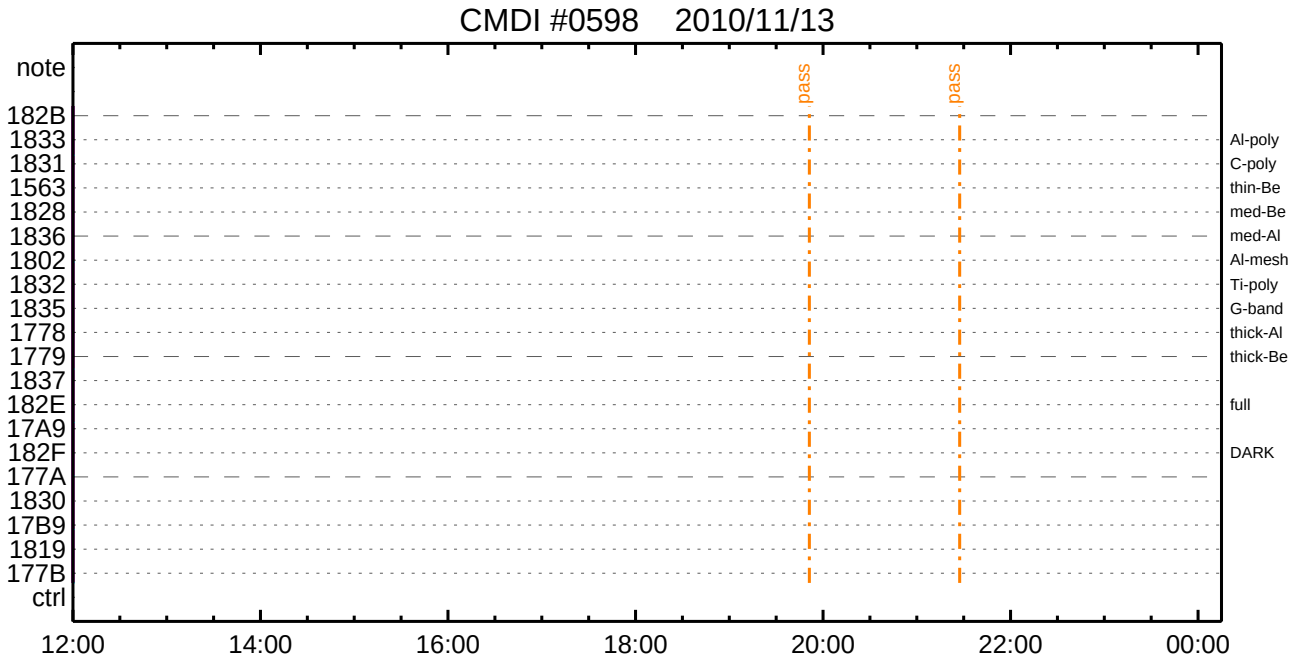

CMDI #0598 2010/11/11
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
track0
track3
pass
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0598 2010/11/11
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0598 2010/11/12
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0598 2010/11/12
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0598 2010/11/13
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0598 2010/11/13
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-698 2010-11-10 14:15:57 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-698:OP
0104     ()
0105   . S. OG    og-698:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2010-11-10 09:31:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2010-11-10 09:31:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2010-11-10 09:31:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

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0194     C.
0195   +. TI 2010-11-10 09:35:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2010-11-10 09:35:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2010-11-10 09:35:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257   +. TI 2010-11-10 09:35:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. ********** LOS **********
0278     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-699 2010-11-10 14:15:57 82 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017    . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034    . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048    . C. Verify EIS in MANUAL mode
0049    . C. Estimated OBSTBL upload time is 46s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053    . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055   +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060   +. TI 2010-11-10 09:35:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0069     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0070    . S. DC-BC dcbc-402:DCBC
0071     (MDP_known_event)
0072     C.
0073     C.
0074    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0075    . S. DC-BC dcbc-153:DCBC
0076     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077     C.
0078     C.
0079    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0080     C.
0081    . C. *********** LOS ***********
0082     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-700 2010-11-10 14:15:57 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +  DC 07-F0 MDP_XRT_MODE_STBY
0104     BC       (c3)
0105   . C. ----------- Success Verify ?              OK / NG____
0106     C.
0107     C. XRT Obs. Table Upload
0108   . S. RAM   ram-291:MDP_OBS_X
0109     ()
0110     C.
0111  +. DC 07-F0 MDP_DUMP_XRTTBL
0112     BC       (84 07 00 00 00 3a d4)
0113   . C. ----------- Comparison Check ?                        OK / ERR ____
0114     C.
0115     C.
0116  +. DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 01 b1 b1 04 04)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 02 b1 b1 08 08)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 03 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 04 b1 b1 06 06)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 05 85 83 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 06 85 83 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 07 80 80 20 20)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 08 80 80 20 08)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 09 80 80 08 20)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 0f 80 80 06 06)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 10 80 80 08 08)
0138   . C. ----------- Success Verify ?            OK / NG ____
0139     C.
0140     C.
0141   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0142     C.
0143  +. DC 07-F0 MDP_XRT_MODE_OBSV
0144     BC       (c2)
0145  +. TI 2010-11-10 09:35:02.0
0146     DC 07-F0 MDP_XRT_MODE_OBSV
0147     BC       (c2)
0148   . C. ----------- Success Verify ?            OK / NG ____
0149     C.
0150     C. ************ XRT END ***********
0151     C.
0152   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154   . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159   . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165   . C. *********** LOS ***********
0166     C.
```

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*** OP Sequence for XRT ***

2010/11/10  09:45:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  09:45:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/10  09:46:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/11/10  09:46:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/10  09:46:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/10  09:46:20.5  XRT_ARS_DIS_433_OG [0x1b1]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/10  10:00:30.5  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  10:00:32.5  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2010/11/10  10:00:34.5  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/10  10:00:36.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  10:03:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  10:03:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  10:03:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  10:06:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  10:17:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/10  10:18:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  14:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  14:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/10  15:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/10  15:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/10  15:00:20.0  XRT_ARS_DIS_433_OG [0x1b1]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/10  15:14:30.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  15:14:32.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2010/11/10  15:14:34.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/10  15:14:36.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  15:26:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  15:26:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  15:26:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  15:29:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  15:43:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  15:43:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  15:43:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  15:46:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  15:49:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/10  15:50:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  17:02:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  17:02:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  17:02:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  17:05:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  17:26:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/10  17:27:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  18:40:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  18:40:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  18:40:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  18:43:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  19:04:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  19:04:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
```

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2010/11/10  19:05:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/11/10  19:05:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/10  19:05:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/10  19:07:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/10  19:07:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/11/10  19:08:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/10  19:14:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  19:14:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  19:15:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 01 58 51 26
2010/11/10  19:38:00.5  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 01 58 3f 74
2010/11/10  19:55:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 01 58 2d c2
2010/11/10  20:17:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  20:17:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  20:17:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  20:20:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  20:41:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 01 58 1c 10
2010/11/10  20:58:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 01 58 0a 5e
2010/11/10  21:15:30.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 01 58 f8 ad
2010/11/10  21:32:30.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 01 58 e6 fa
2010/11/10  21:54:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  21:54:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  21:54:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  21:57:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  22:18:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 01 58 d5 48
2010/11/10  22:35:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 01 58 c3 96
2010/11/10  22:52:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 01 58 b1 e4
2010/11/10  23:18:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 14 72 51 58
2010/11/10  23:32:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/10  23:32:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/10  23:32:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/10  23:35:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/10  23:46:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 1a c8 41 a1
2010/11/10  23:56:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 1b be 31 3f
2010/11/11  00:06:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 1c 49 20 9b
2010/11/11  00:16:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 1c 9b 0f a5
2010/11/11  00:26:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 1c ac fe 4e
2010/11/11  00:36:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 1c 9b f2 4e
2010/11/11  00:46:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 1c 49 e1 59
2010/11/11  01:00:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  01:00:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  01:00:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  01:03:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  01:16:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 00 1b be d0 b5
2010/11/11  01:26:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 00 1a c8 c0 5b
2010/11/11  01:36:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 00 15 ca af 9e
2010/11/11  01:45:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  01:45:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
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                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/11  01:46:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/11/11  01:46:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/11  01:46:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/11  01:46:20.0  XRT_ARS_DIS_433_OG [0x1b1]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/11  02:00:30.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  02:00:32.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2010/11/11  02:00:34.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/11  02:00:36.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  02:31:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  02:31:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  02:31:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  02:34:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  02:51:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/11  02:52:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  03:59:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  03:59:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  03:59:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  04:02:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  04:29:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/11  04:30:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  05:40:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  05:40:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  05:40:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  05:43:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  06:16:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  06:16:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/11  06:16:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/11/11  06:16:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/11  06:16:48.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/11  06:19:26.0  XRT_ARS_DIS_447_OG [0x1bf]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/11  06:19:28.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/11/11  06:19:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  06:26:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  06:26:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/11  06:26:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/11/11  06:26:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/11  06:26:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/11  06:26:50.0  XRT_ARS_DIS_433_OG [0x1b1]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/11  06:41:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  06:41:02.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2010/11/11  06:41:04.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/11  06:41:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  07:20:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  07:20:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  07:20:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  07:23:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  07:44:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/11  07:45:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  09:00:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  09:00:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  09:00:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/11  09:03:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/11  09:20:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/11  09:21:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/11  10:41:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/11  10:41:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/11  10:54:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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