

XRT Timeline to be uploaded on 2010/11/13

Period: 2010/11/13 10:19:00 - 2010/11/18 10:08:00

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Normal mode

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XOB #1830: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad												
Term	Pointing (x, y)						Comment					
11/13 10:37:04 - 11/13 18:53:00	Track (226.8, -426.9) @ 11/13 10:29:00						# OP start + 10min, track AR 11123 with HOP174 over 15 - 19 UT.					
11/13 19:30:00 - 11/14 06:29:24	Track (299.2, -425.1) @ 11/13 19:27:00						# Cont.					
11/14 06:42:30 - 11/14 14:59:54	Track (386.5, -422.4) @ 11/14 06:39:30						# Cont.					
11/14 15:03:00 - 11/14 18:59:54	Track (185.0, 159.2) @ 11/14 15:00:00						# HOP 174 over 15 - 19 UT at AR11124					
11/14 19:13:00 - 11/15 06:01:54	Track (478.8, -418.9) @ 11/14 19:10:00						# Track AR11123.					
11/15 06:15:00 - 11/15 18:59:54	Track (554.7, -415.5) @ 11/15 06:12:00						# Cont, HOP174 over 15 - 19 UT.					
11/15 19:13:00 - 11/16 06:05:24	Track (636.1, -411.1) @ 11/15 19:10:00						# Cont.					
PROG= 04 1-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 3 35-time(s) 80.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)						Comment					
11/13 19:20:00 - 11/13 19:26:54	Fixed (0.0, 0.0)						synoptic, shifted manually.					
11/14 06:32:30 - 11/14 06:39:24	Fixed (0.0, 0.0)						synoptic, shifted 29.5 min					
11/14 19:03:00 - 11/14 19:09:54	Fixed (0.0, 0.0)						synoptic, shifted manually.					
11/15 06:05:00 - 11/15 06:11:54	Fixed (0.0, 0.0)						synoptic, shifted 2.0 min					
11/15 19:03:00 - 11/15 19:09:54	Fixed (0.0, 0.0)						synoptic shifted manually.					
11/16 06:08:30 - 11/16 06:15:24	Fixed (0.0, 0.0)						synoptic, shifted 5.5 min					

PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)						Comment					
11/13 10:37:04 - 11/13 18:53:00	Track (226.8, -426.9) @ 11/13 10:29:00						# OP start + 10min, track AR 11123 with HOP174 over 15 - 19 UT.					
11/13 19:30:00 - 11/14 06:29:24	Track (299.2, -425.1) @ 11/13 19:27:00						# Cont.					
11/14 06:42:30 - 11/14 14:59:54	Track (386.5, -422.4) @ 11/14 06:39:30						# Cont.					
11/14 15:03:00 - 11/14 18:59:54	Track (185.0, 159.2) @ 11/14 15:00:00						# HOP 174 over 15 - 19 UT at AR11124					
11/14 19:13:00 - 11/15 06:01:54	Track (478.8, -418.9) @ 11/14 19:10:00						# Track AR11123.					
11/15 06:15:00 - 11/15 18:59:54	Track (554.7, -415.5) @ 11/15 06:12:00						# Cont, HOP174 over 15 - 19 UT.					
11/15 19:13:00 - 11/16 06:05:24	Track (636.1, -411.1) @ 11/15 19:10:00						# Cont.					

PROG= 16 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												

└─ Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 4 24-time(s) 600.0sec													
└─ Seqn= 89 1-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

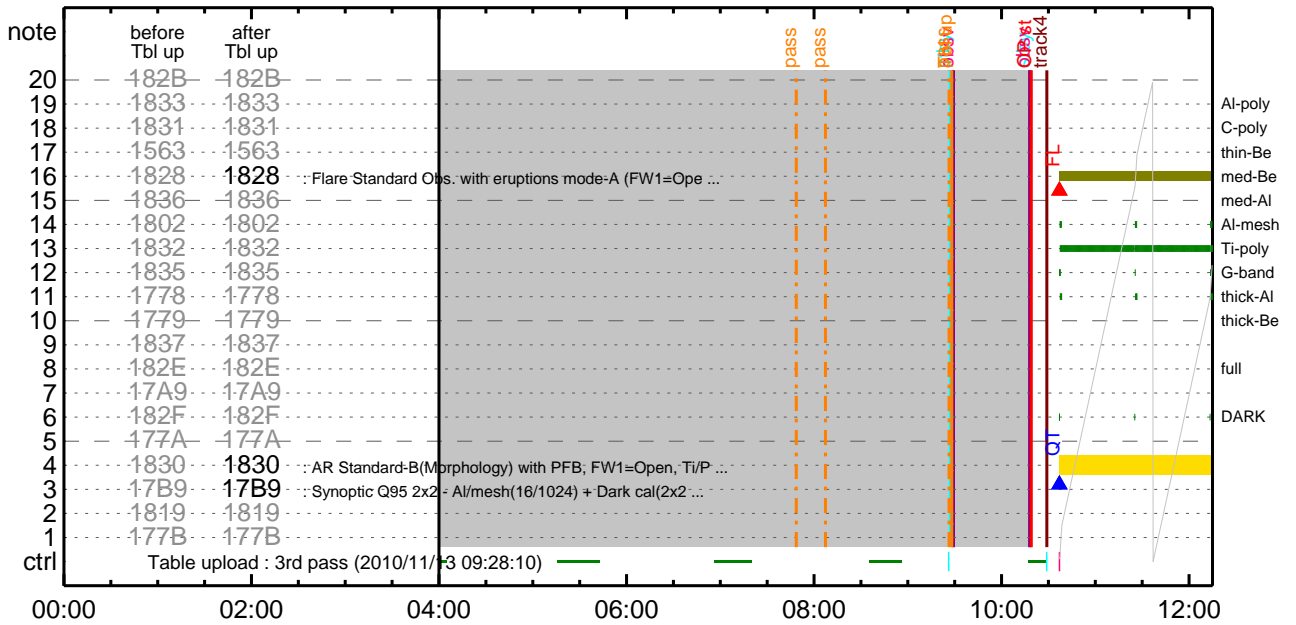
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Flare Detection

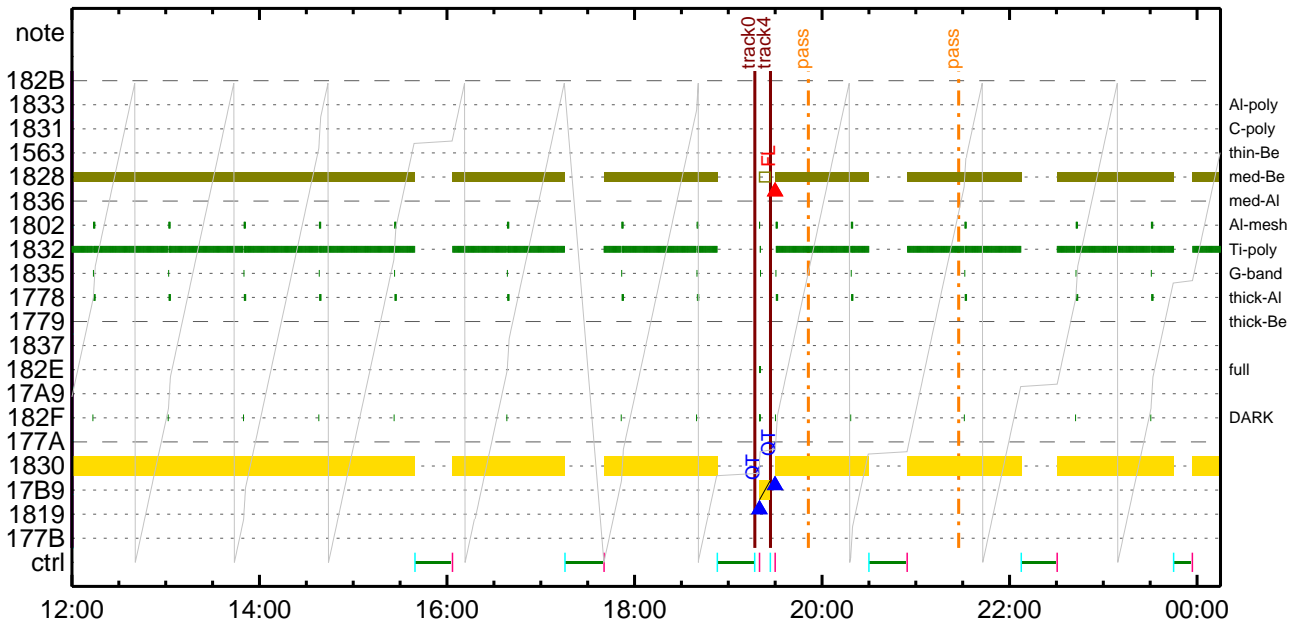
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FLD Patrol												
Term	Pointing (x, y)						Comment					
11/13 10:34:20 - 11/13 19:17:16	Track (226.8,	-426.9)	@ 11/13 10:29:00	# OP start + 10min, track AR 11123 with HOP174 over 15 - 19 UT.							
11/13 19:27:16 - 11/14 06:29:46	Track (299.2,	-425.1)	@ 11/13 19:27:00	# Cont.							
11/14 06:39:46 - 11/14 19:00:16	Track (386.5,	-422.4)	@ 11/14 06:39:30	# Cont.							
11/14 19:10:16 - 11/15 06:02:16	Track (478.8,	-418.9)	@ 11/14 19:10:00	# Track AR11123.							
11/15 06:12:16 - 11/15 19:00:16	Track (554.7,	-415.5)	@ 11/15 06:12:00	# Cont, HOP174 over 15 - 19 UT.							
11/15 19:10:16 - 11/16 06:05:46	Track (636.1,	-411.1)	@ 11/15 19:10:00	# Cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

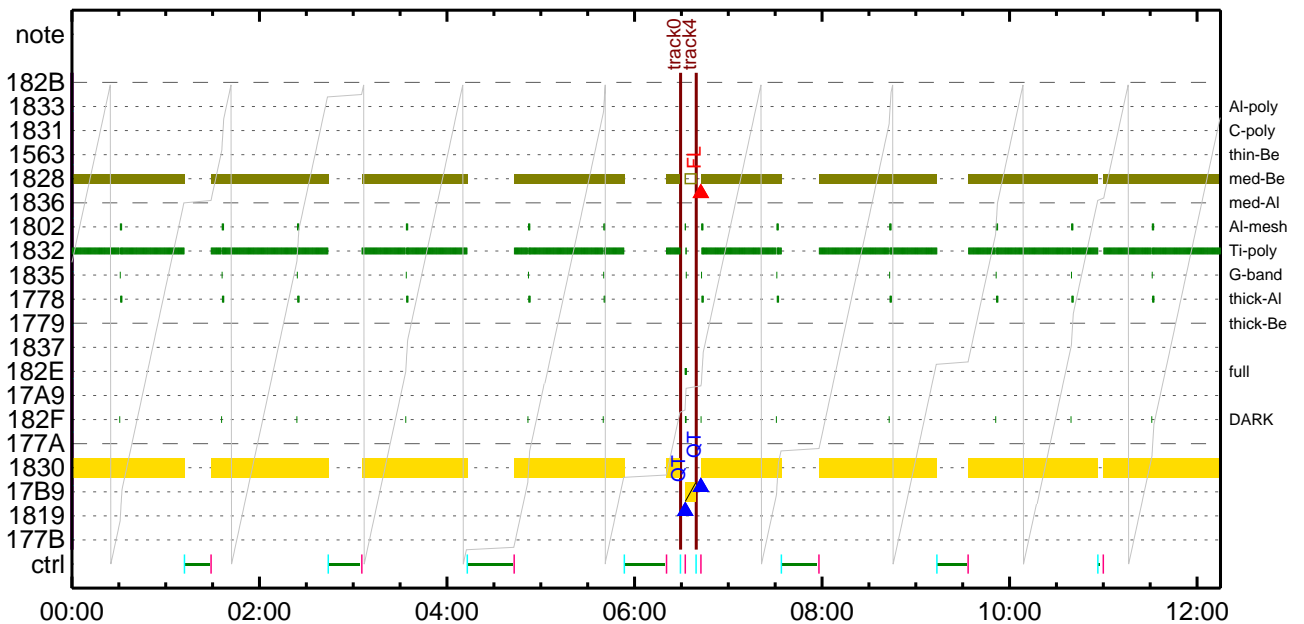
CMDI #0607 2010/11/13



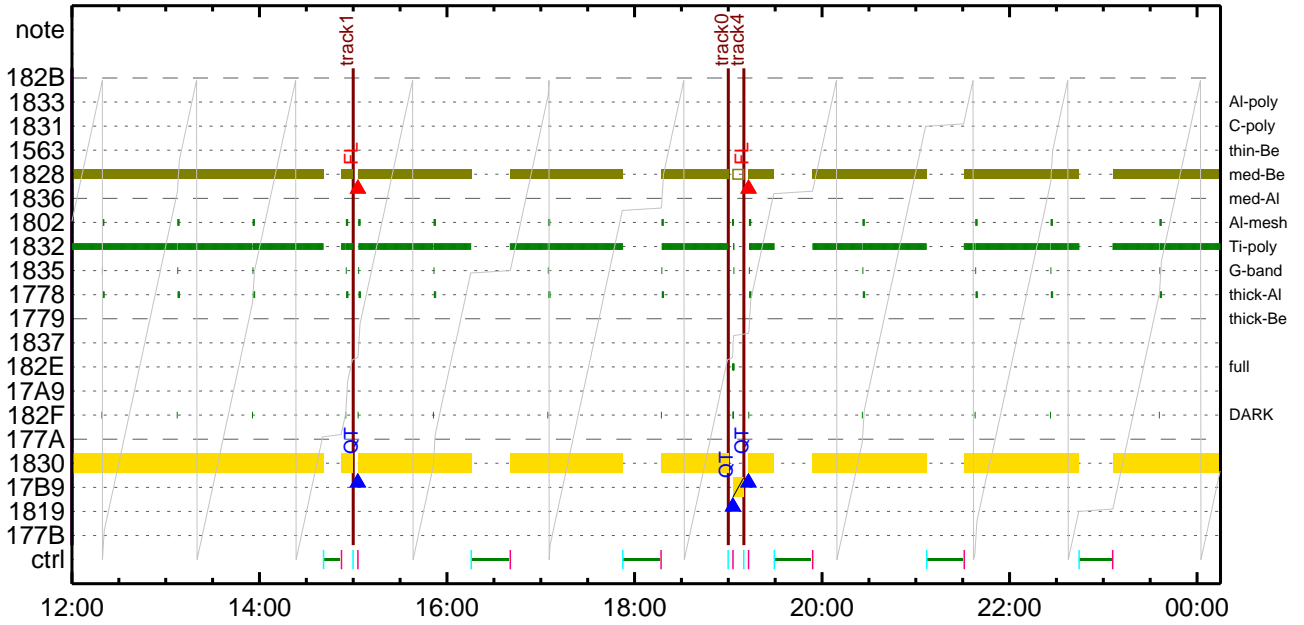
CMDI #0607 2010/11/13



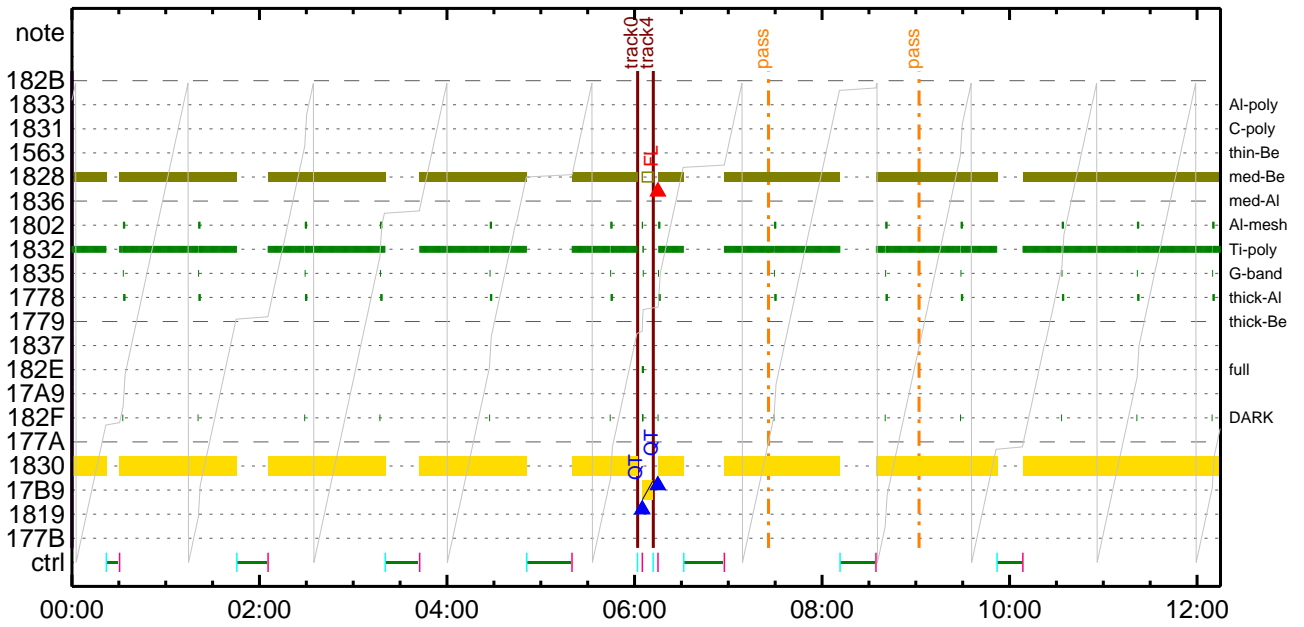
CMDI #0607 2010/11/14



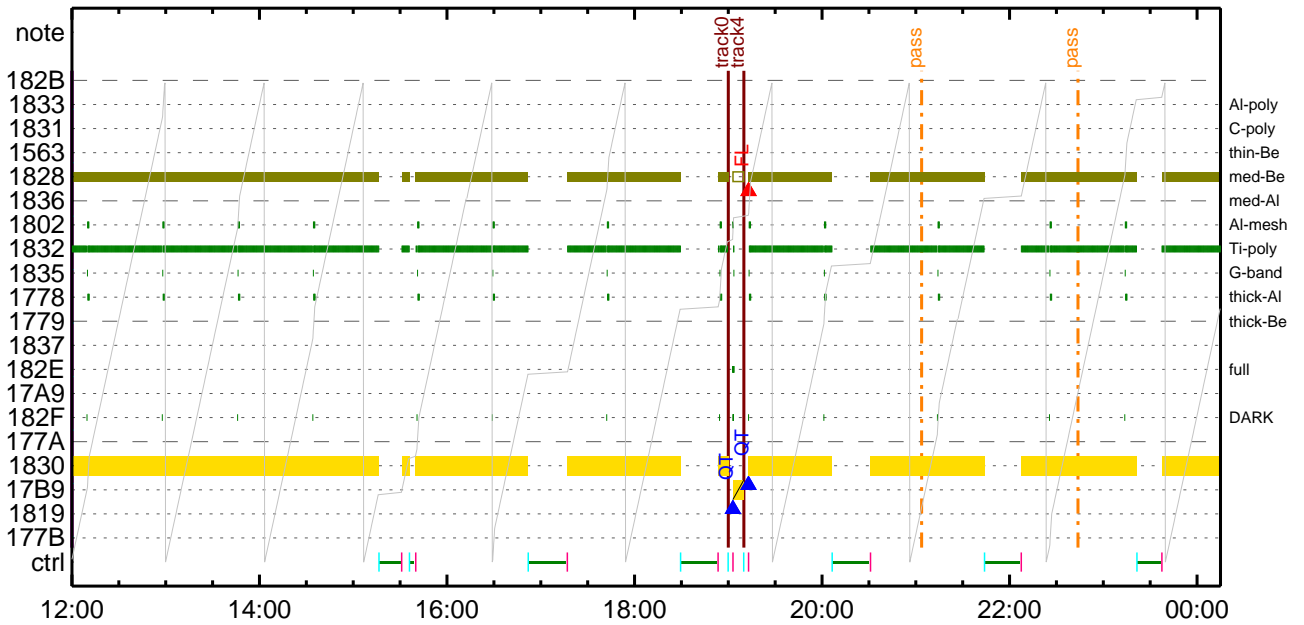
CMDI #0607 2010/11/14



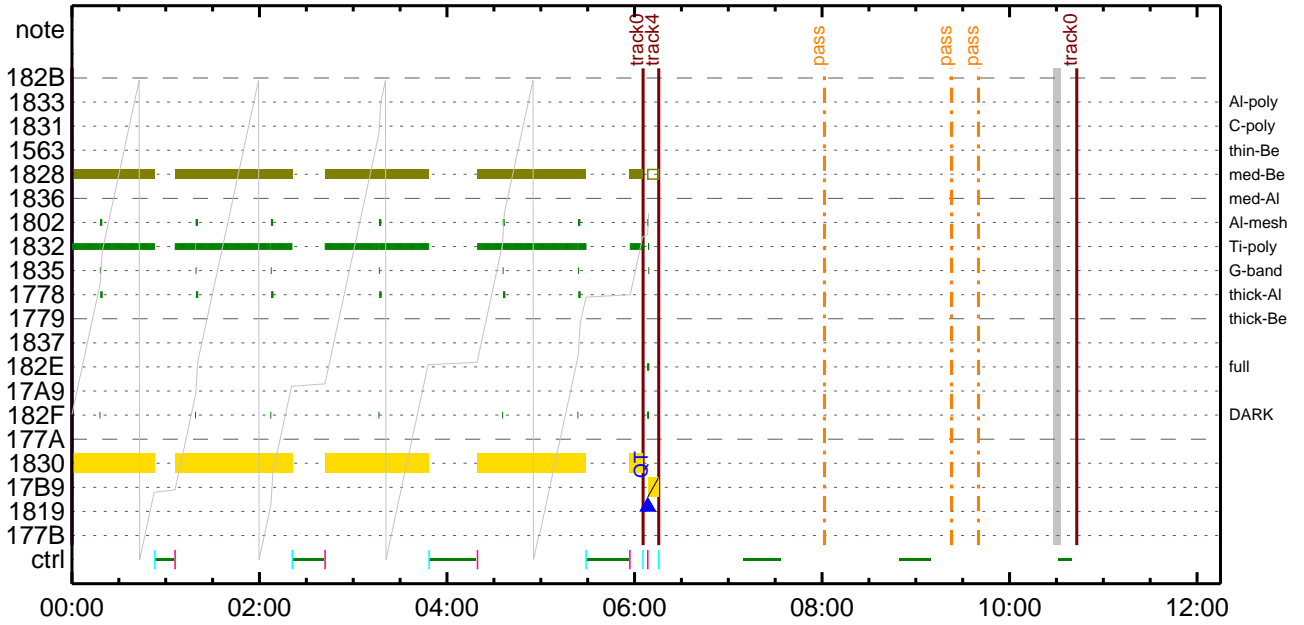
CMDI #0607 2010/11/15



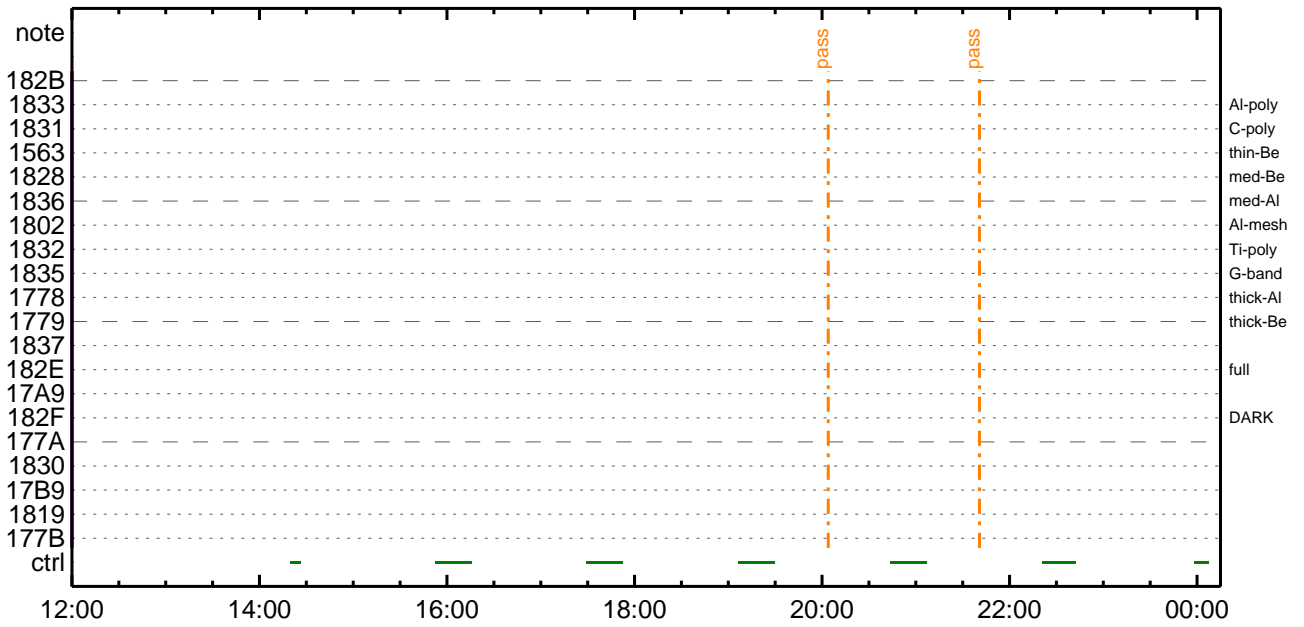
CMDI #0607 2010/11/15



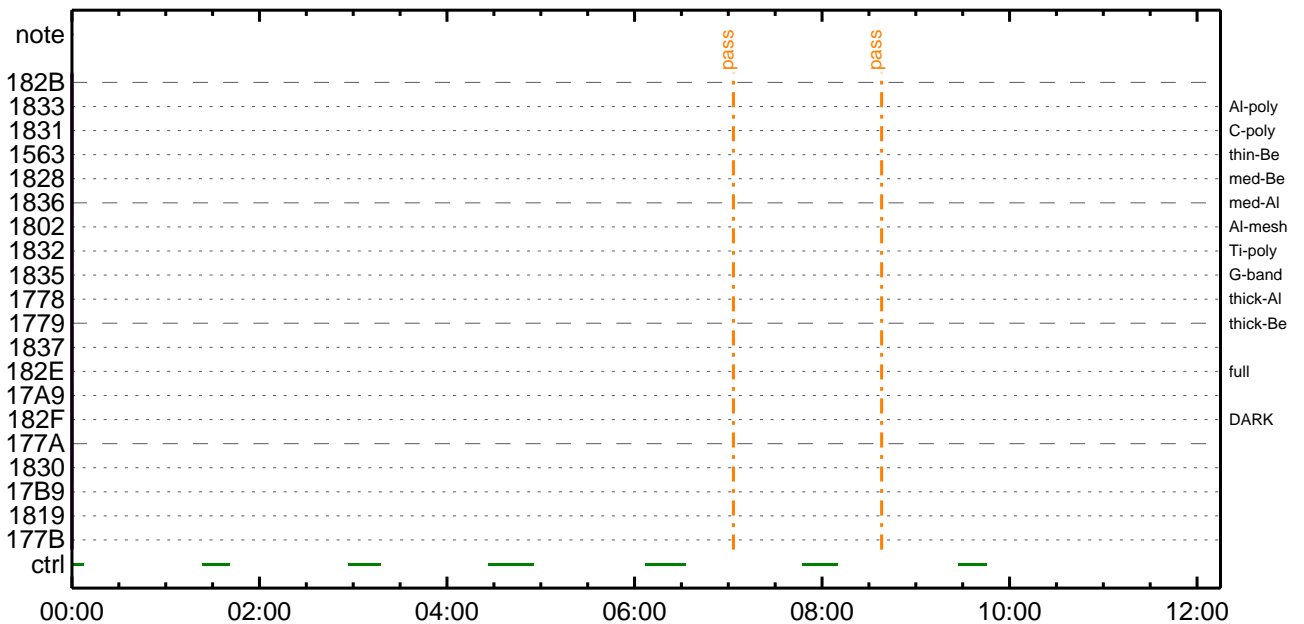
CMDI #0607 2010/11/16



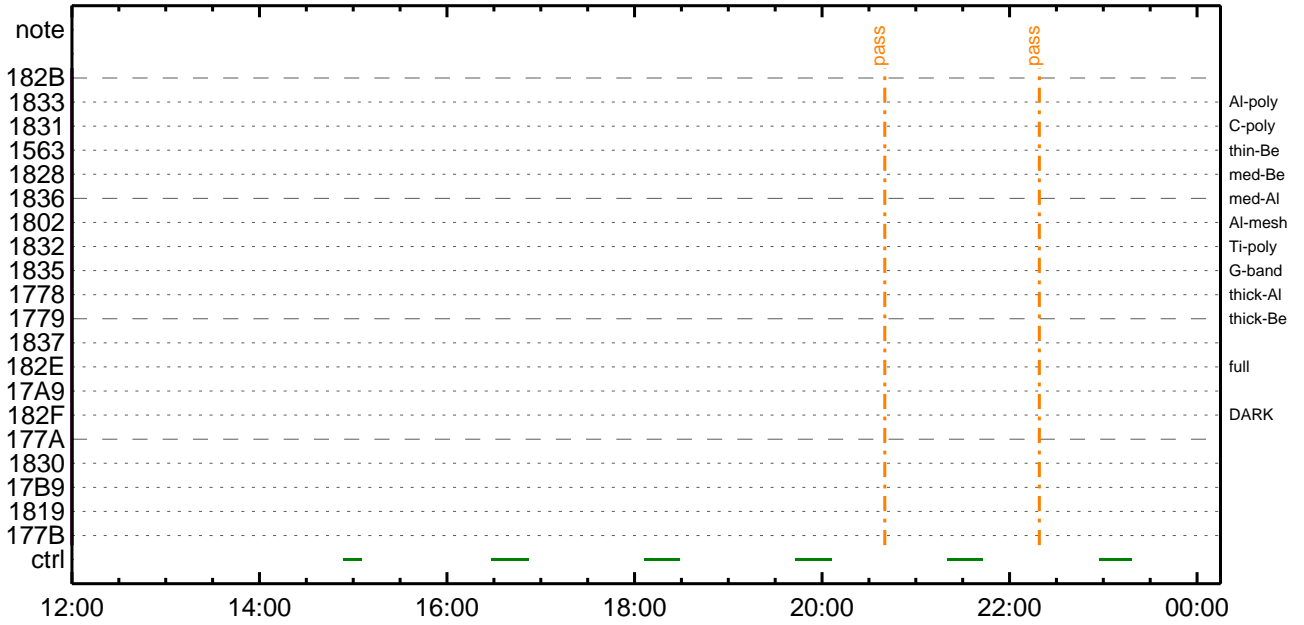
CMDI #0607 2010/11/16



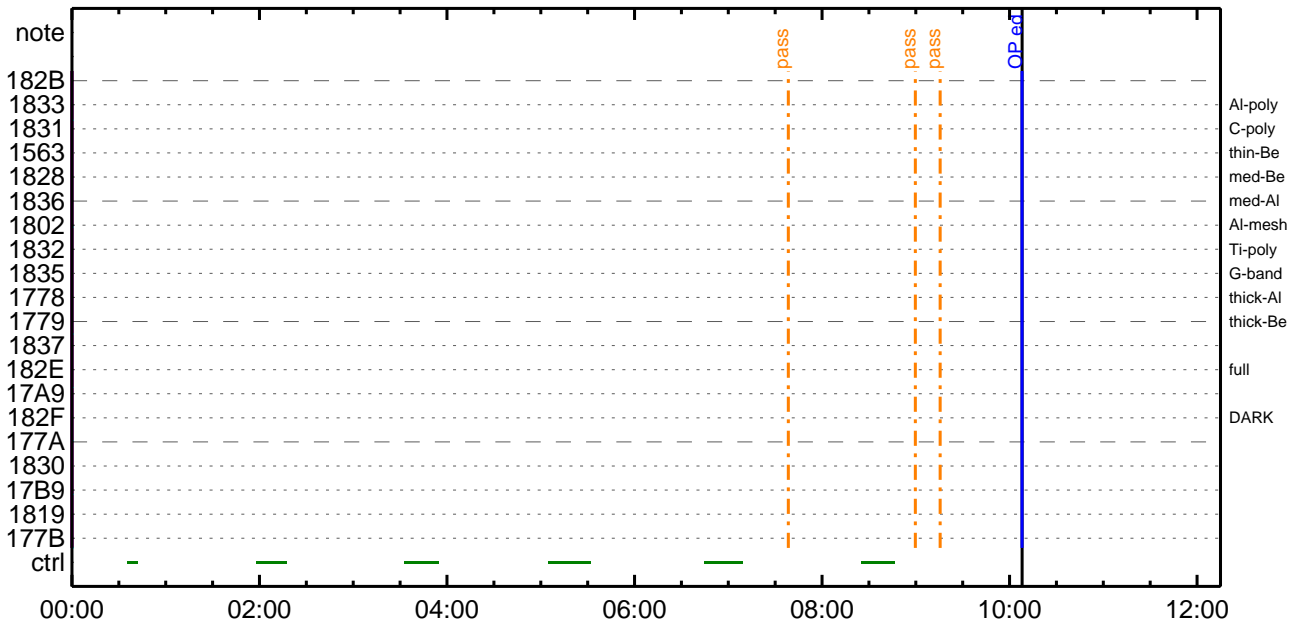
CMDI #0607 2010/11/17



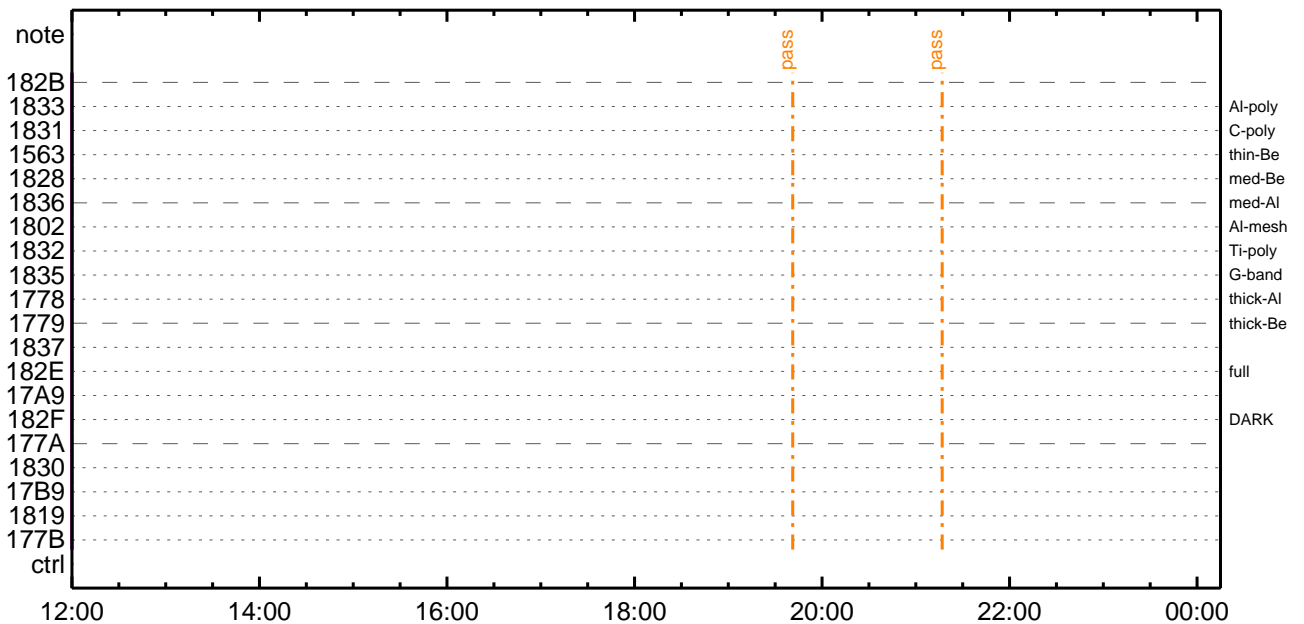
CMDI #0607 2010/11/17



CMDI #0607 2010/11/18



CMDI #0607 2010/11/18



(a) Spacecraft Operation Procedure (real-commands)

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main-705 2010-11-13 13:30:55 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YBYÓYÉÁ+çⓄ
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççãÑ•µ°Æ»Í×ÁÇçíYçYÁY×Yí;¼YÉ;ÈÈ%µ•ííÉ;ÈBÈ¼°ÇÖñ•çç¼í¹ççí;çÀⓄ, ùñ¹ãÈãÑçÁ+çⓄ•ãÈããçãÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç° °ÆÀ, í×ÈYñãLOSPçãí»p`Òãð¹íí, ñ•; çÉÖÍ×ãÈXÁÓONãí¹ÒãÈãíãÈããçãÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY-¾ôÃÖñ-°ÁÀêñ•çç;ç°Ê²¼ççí°ÆÀ, ¼Ê%ççð¼Á¹Òççç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç° RESTART;ÈPT1;Èñ•çççç¼¹ççí;ç°Ê²¼ççí¼Á¹Òççç°;çDCBC-150çççÈççç;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÛØ;ÈÁ•Á°²óÈò;È, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Áã»ßçççç;ç°Ê²¼ççð¼Á¹Òççç;ç
0055 C. YçYÓYÉYÈÁÛØãÁ•Á°²óÈòç-¼ççç¼¹ççí°í»çççççççççççç;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç° RESTART;ÈPT2;Èñ•çççç¼¹ççí;ç°Ê²¼ççí¼Á¹Òççç°;çDCBC-151çççÈççç;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÛØ;ÈÁ•Á°²óÈò;È, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áã»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-705:OP
0104 ( )
0105 S. OG og-705:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y³Y²E²òðÁDî¿(UT)
0183 +. TI 2010-11-13 10:14:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-11-13 10:14:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-11-13 10:14:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```
0194 C.
0195 +. TI 2010-11-13 10:18:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼õîÄë%îíñõîîŷÄŷ§ŷÄŷ¹àîÜ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. Tîîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»õ³îÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLõîîîÈ¹çªè²îOKõ³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èõõîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-11-13 10:18:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-11-13 10:18:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2010-11-13 10:18:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîõî»õ¼ŷõÈÄõ¹õèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèõÈ¼õõ¼Ä»Ûõ¹õè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñõîÈ¹õ¹õèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ðãLOSŷÄŷŷŷÄŷ¹¼Ä»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-706 2010-11-13 13:30:55 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Ä»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÄ•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¿°ÇÖ••¿¼í¹ç¿Í;çÄ®, ù¿¹¿È¿µ¿ÇÁ+¿®••¿È¿¿¿³¿È;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 16s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2010-11-13 10:18:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁî¿î»ö¼Y¿ÈÄ¿¹¿ÈDCBC•x²è *****
0069 C. (¼á°íY¿ÁYÈY¿YÈYáYçYÈ¿¼¿¿¼Ä»Û¿¹¿È)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YD¥¹.İ Daily±¿İÑ¿È`¿¹¿ÈDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁY$YÁY~¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-707 2010-11-13 13:30:55 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãâ•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈÈµ•íÉ;ËÈÈ¼°ÇÖã•çç¼í¹ççí;çÀ®, ùã¹ãèããçÁ+ç®ã•ãèããççÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ËYçãâLOSããççã»p´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÁÖONãí¹ÖãÈçãèããççÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖã-°ÁÁêã•ççç; ç°È²¼ççí°ÆÀ, ¼êççççç¼Á¹Öã¹ççç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•ççççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öã»ã°; çDCBC-150çççÈççç;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÁÚÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Áá»ßã•ççç, á; ç°È²¼ççç¼Á¹Öã¹ççç;ç
0055 C. YçYÓYÉYÉÁÚÁÖãÁ•Á°²óÈðã-¼áççç¼í¹ççí°ÆÀ¹Öã»ã°ãèããçççÁÖã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•ççççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öã»ã°; çDCBC-151çççÈççç;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÁÚÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 10 80 80 08 08)
0138 . C. ----- Success Verify ? OK / NG ____
0139 C.
0140 C.
0141 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0142 C.
0143 +. DC 07-F0 MDP_XRT_MODE_OBSV
0144 BC (c2)
0145 +. TI 2010-11-13 10:18:02.0
0146 DC 07-F0 MDP_XRT_MODE_OBSV
0147 BC (c2)
0148 . C. ----- Success Verify ? OK / NG ____
0149 C.
0150 C. ***** XRT END *****
0151 C.
0152 . C. ***** MDP (uãîâî»ö¼ýñÊÐá¹ñèDCBC•x²è *****
0153 C. (%ã°îÿÓÿÁÿËÿÏÿÛÿßÿàÿäÿèÿêÿìÿðÿôÿðÿÀÿÛÿàè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñÉ'Øñ¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÁÿÏÿÛÿßÿàÿäÿèÿêÿìÿðÿôÿðÿÀÿÛÿà»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.
```

Nov 13, 10 13:31

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*** OP Sequence for XRT ***

2010/11/13	10:28:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	10:28:56.0	XRT_STANDBY_417_OG [0x1a1]							
		XRT_STBY	2	07-F8	03 01				
2010/11/13	10:28:58.0	XRT_DIAGNOSTIC_439_OG [0x1b7]							
		XRT_DIAGNOSTIC	2	07-F8	03 07				
2010/11/13	10:29:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/11/13	10:29:00.5	XRT_Custom_435_OG [0x1b3]							
		XRT_FW1_RESET	4	07-F8	51 40 00 00				
2010/11/13	10:34:00.5	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/13	10:34:20.5	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/13	10:34:22.5	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/13	10:34:24.5	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/13	10:36:58.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/13	10:37:00.5	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/11/13	10:37:02.5	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/13	10:37:04.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/13	15:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	15:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/13	15:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/13	15:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/13	16:02:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/13	16:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/13	17:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	17:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/13	17:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/13	17:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/13	17:39:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/13	17:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/13	18:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	18:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/13	18:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/13	18:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/13	19:16:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	19:16:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/13	19:17:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/13	19:17:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/13	19:17:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/13	19:19:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/13	19:19:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/13	19:20:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/13	19:26:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/13	19:26:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/13	19:27:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/11/13	19:27:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/13	19:27:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/13	19:27:20.5	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/13	19:29:54.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/13	19:29:56.5	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/11/13	19:29:58.5	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/13	19:30:00.5	XRT_CTRL_AUTO_406_OG [0x196]							

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2010/11/13	20:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/13	20:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/13	20:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/13	20:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/13	20:53:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/13	20:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/13	22:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/13	22:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/13	22:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/13	22:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/13	22:29:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/13	22:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/13	23:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/13	23:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/13	23:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/13	23:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/13	23:56:00.0	XRT_Custom_418_OG [0x1a2]				
2010/11/13	23:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/14	01:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/14	01:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/14	01:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/14	01:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/14	01:28:00.0	XRT_Custom_418_OG [0x1a2]				
2010/11/14	01:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/14	02:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/14	02:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/14	02:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/14	02:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/14	03:04:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/14	03:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/14	04:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/14	04:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/14	04:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/14	04:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/14	04:42:00.0	XRT_Custom_418_OG [0x1a2]				
2010/11/14	04:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/14	05:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/14	05:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/14	05:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/14	05:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/14	06:19:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/14	06:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/14	06:29:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/14	06:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/14	06:29:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2010/11/14	06:29:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/11/14	06:29:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/11/14	06:32:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/14	06:32:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/14	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]				

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2010/11/14	06:39:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	06:39:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/11/14	06:39:30.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00	00
2010/11/14	06:39:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/14	06:39:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/14	06:39:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/14	06:42:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	06:42:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2010/11/14	06:42:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/14	06:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	07:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	07:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	07:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	07:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	07:57:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0				
2010/11/14	07:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	09:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	09:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	09:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	09:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	09:32:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0				
2010/11/14	09:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	10:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	10:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	10:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	10:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	11:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	14:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	14:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	14:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	14:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	14:51:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0				
2010/11/14	14:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	14:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/11/14	15:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2010/11/14	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/14	15:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/14	15:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/14	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	15:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2010/11/14	15:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/14	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	16:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	16:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	16:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	16:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	16:39:30.0	XRT_Custom_418_OG [0x1a2]								

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2010/11/14	16:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	17:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	17:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	17:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	17:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	18:16:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/14	18:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	18:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	18:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/14	19:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/14	19:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/14	19:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/14	19:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/14	19:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/14	19:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	19:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	19:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/14	19:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/11/14	19:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/14	19:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/14	19:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/14	19:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	19:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/11/14	19:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/14	19:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	19:53:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/14	19:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	21:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	21:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	21:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	21:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	21:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/14	21:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/14	22:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/14	22:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/14	22:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/14	22:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/14	23:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/14	23:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	00:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	00:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	00:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	00:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	00:29:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	00:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2010/11/15	01:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	01:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	01:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	01:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	02:04:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	02:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	03:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	03:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	03:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	03:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	03:41:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	03:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	04:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	04:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	04:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	04:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	05:19:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	05:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	06:01:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	06:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/15	06:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/15	06:02:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/15	06:02:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/15	06:04:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/15	06:04:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/15	06:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	06:11:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	06:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/15	06:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/11/15	06:12:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/15	06:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/15	06:12:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/15	06:14:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	06:14:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/11/15	06:14:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/15	06:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	06:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	06:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	06:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	06:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	06:56:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	06:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	08:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	08:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	08:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	08:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	08:33:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	08:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	09:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2010/11/15	09:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	09:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	09:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	10:07:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	10:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	15:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	15:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	15:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	15:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	15:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	15:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	15:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	15:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	15:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	15:39:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	15:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	15:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	16:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	16:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	16:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	16:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	17:16:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	17:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	18:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	18:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	18:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	18:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	18:52:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	18:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	18:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	18:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/15	19:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/15	19:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/15	19:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/15	19:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/15	19:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/15	19:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	19:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	19:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/15	19:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/11/15	19:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/15	19:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/15	19:10:20.5	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/15	19:12:54.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	19:12:56.5	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/11/15	19:12:58.5	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/15	19:13:00.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	20:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	20:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2010/11/15	20:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	20:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	20:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	20:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	21:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	21:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	21:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	21:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	22:06:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	22:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/15	23:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/15	23:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/15	23:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/15	23:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/15	23:36:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/15	23:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	00:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	00:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/16	00:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/16	00:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/16	01:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/16	01:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	02:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	02:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/16	02:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/16	02:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/16	02:41:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/16	02:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	03:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	03:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/16	03:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/16	03:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/16	04:18:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/16	04:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	05:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	05:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/16	05:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/16	05:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/16	05:56:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/16	05:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	06:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/16	06:05:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/16	06:05:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/16	06:05:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/16	06:08:26.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/16	06:08:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/16	06:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/16	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/16	06:15:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				

2010/11/16 10:43:00.0 AOCs_OrE-point_Start_2_OG [0x098]
AOCU_NM 5 02-76 00 00 00 00 00