XRT Timeline to be uploaded on 2010/11/16

Period: 2010/11/16 10:33:00 - 2010/11/20 09:33:00

* * * * * **Normal mode** * * * * *

XOB #1833: Full Sun - Ti/Poly - 4x4 - AEC1 - 4 min cadence

Term	Pointing (x, y)	Comment
11/16 10:46:04 - 11/16 14:19:30	Fixed (936.0, 0.0)	# OP start + 10min HOP130 1/15
11/16 14:27:00 - 11/16 14:59:54	Fixed (-815.0, -471.0)	15/15
11/16 15:03:05 - 11/16 20:00:00	Track (-124.0, -15.8) @ 11/16 15:00:00	# HOP179 and XRT bakeout for 12hours

PROG= 19 Inf.-time(s)
Subr= 1 1-time(s) 6.0sec
Seqn= 47 1-time(s) 240.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	63ms	Obs	4x4	2048x2048 (1024, 1024)	Q=95	1	0	2.0sec

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
11/17 08:02:30 - 11/17 14:59:54	Track (-124.0, -15.8) @ 11/16 15:00:00	# HOP179 and XRT bakeout for 12hours

PROG= 17 Inf.-time(s)
Subr= 1 1-time(s) 600.0sec
Seqn= 37 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #182B: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 30sec-cad

Term	Pointing (x, y)	Comment
11/17 15:06:30 - 11/17 18:49:54	Track (-185.7, -557.5) @ 11/17 15:00:00	# Track AR11126
11/17 20:07:00 - 11/17 23:59:54	Track (847.5, -392.8) @ 11/17 19:40:00	# Track AR11123
11/18 00:03:00 - 11/18 05:59:54	Fixed (850.0, -400.0)	# Fixed pointing at SE limb

PROG= 20 Inf.-time(s)
Subr= 2 1-time(s) 2.0sec
Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 95 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec
Seqn= 18 45-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
11/17 18:53:00 - 11/17 18:59:54	Fixed (0.0, 0.0)	Synoptic shifted manualy.
11/18 06:03:00 - 11/18 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 03 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
11/17 19:03:00 - 11/17 19:09:54	Fixed (-528.4, -528.4)	XRT Quadrant pointing 1/4

PROG= 11 1-time(s)
Subr= 1 1-time(s) 12.0sec

Seqn= 38 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long

Term	Pointing (x, y)	Comment
11/17 19:13:00 - 11/17 19:19:54	Fixed (528.4, -528.4)	2/2

PROG= 10 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 36 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
11/17 19:23:00 - 11/17 19:29:54	Fixed (528.4, 528.4)	3/4

PROG= 05 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 39 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
11/17 19:33:00 - 11/17 19:39:54	Fixed (-528.4, 528.4)	4/4

PROG= 01 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 40 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
11/16 15:03:05 - 11/16 20:00:00	Track (-124.0, -15.8) @ 11/16 15:00:00	# HOP179 and XRT bakeout for 12hours
11/17 15:06:30 - 11/17 18:49:54	Track (-185.7, -557.5) @ 11/17 15:00:00	# Track AR11126
11/17 20:07:00 - 11/17 23:59:54	Track (847.5, -392.8) @ 11/17 19:40:00	# Track AR11123
11/18 00:03:00 - 11/18 05:59:54	Fixed (850.0, -400.0)	# Fixed pointing at SE limb

PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													

Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

Active Region Search

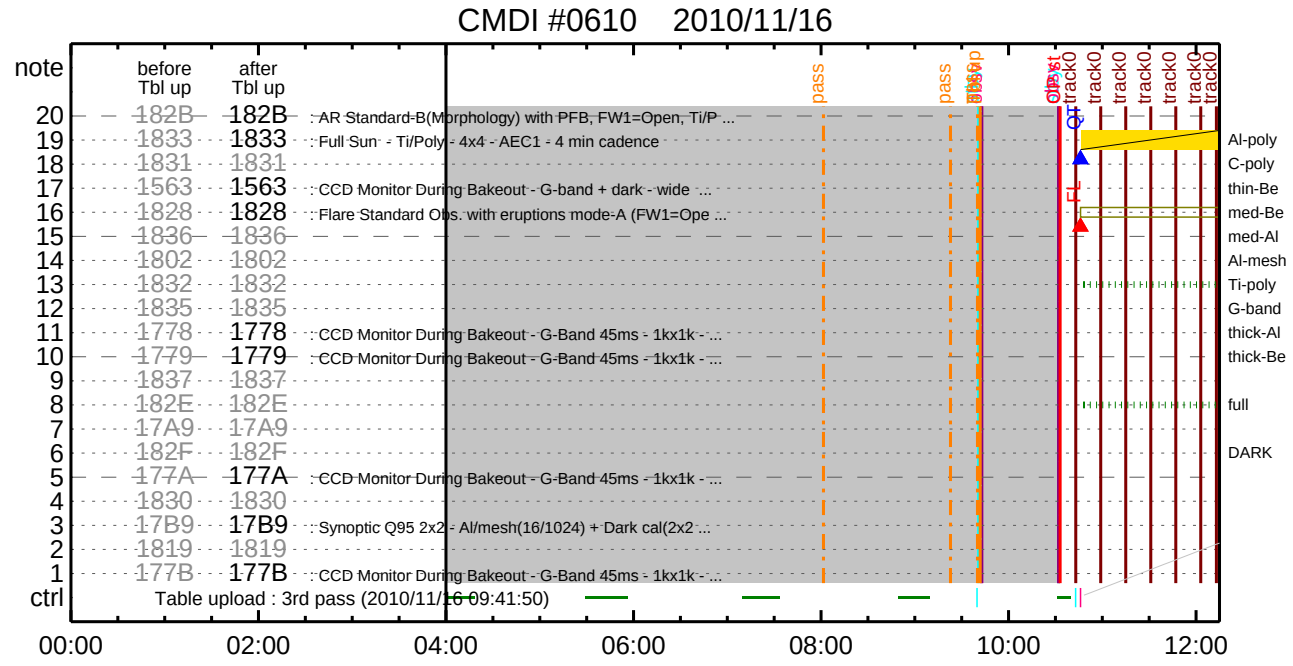
NOT USED

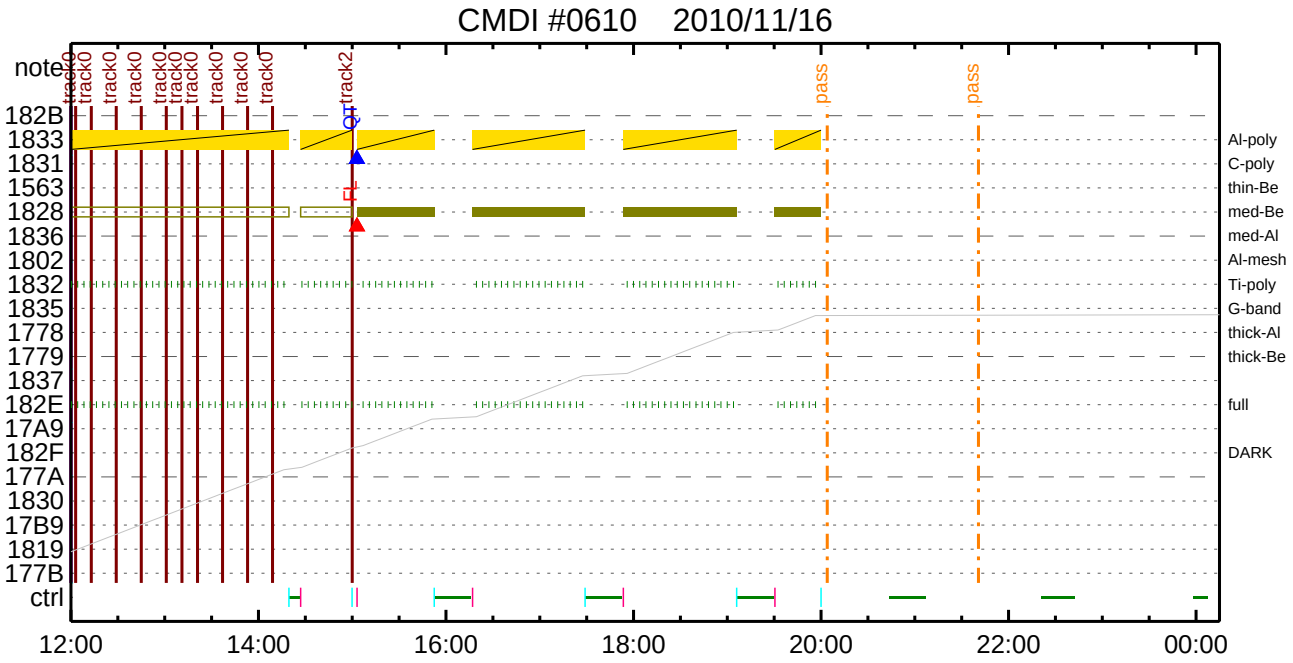
Flare Detection

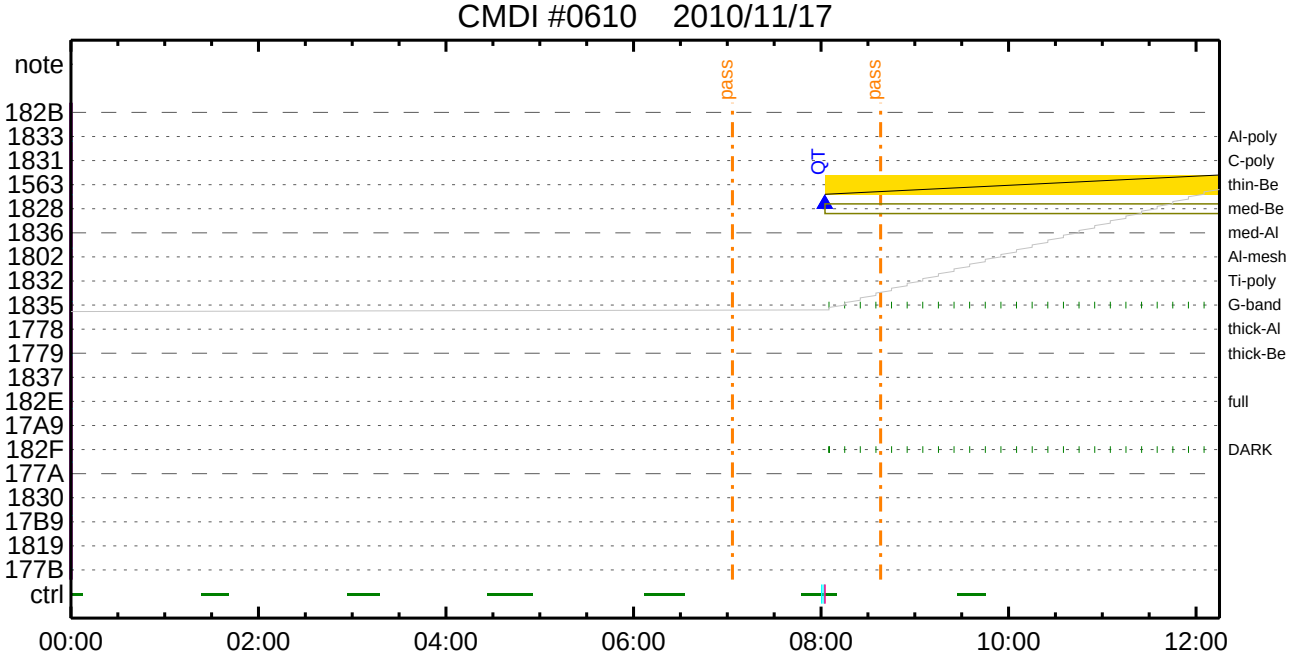
FLD Patrol

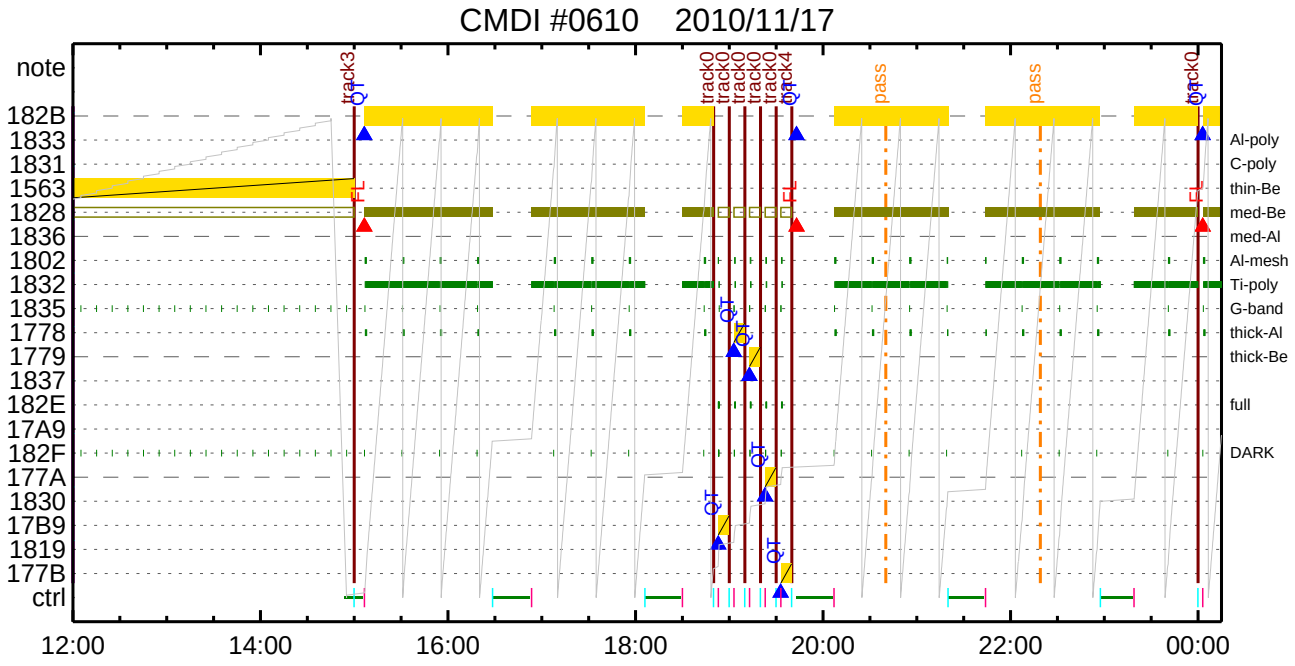
Term	Pointing (x, y)	Comment
11/16 15:00:16 - 11/17 08:02:26	Track (-124.0, -15.8) @ 11/16 15:00:00	# HOP179 and XRT bakeout for 12hours
11/17 15:00:16 - 11/17 18:50:16	Track (-185.7, -557.5) @ 11/17 15:00:00	# Track AR11126
11/17 19:40:21 - 11/18 06:00:16	Track (847.5, -392.8) @ 11/17 19:40:00	# Track AR11123

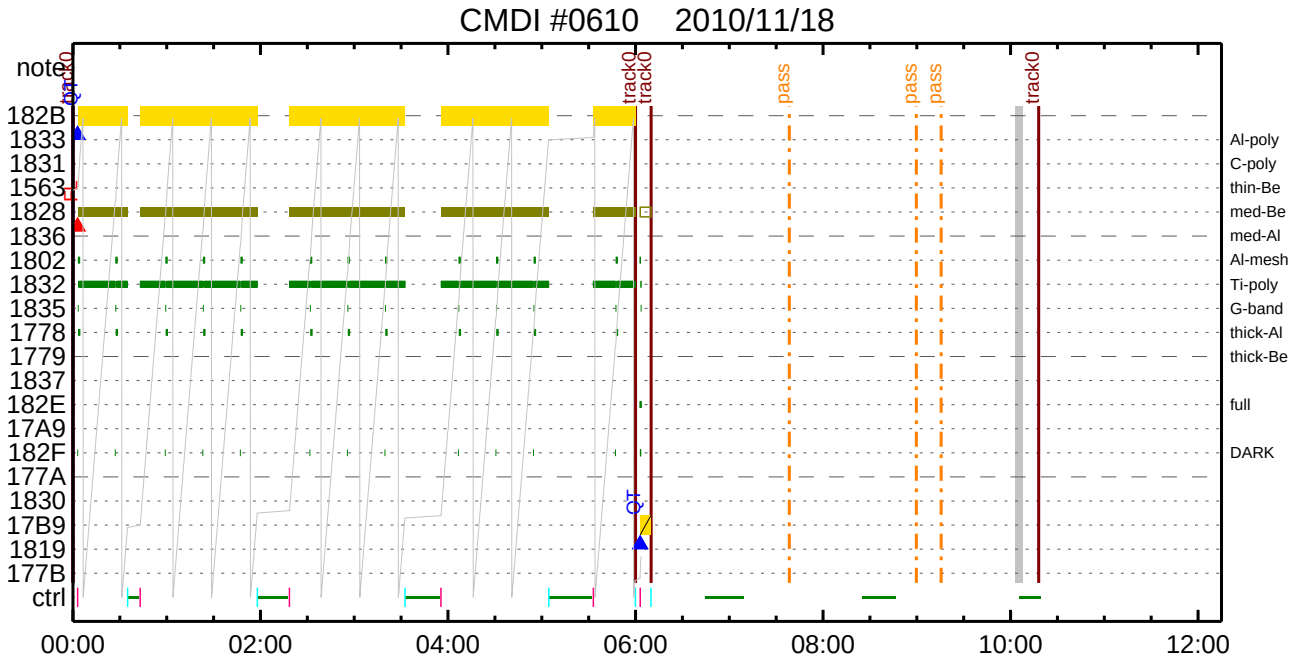
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0610 2010/11/16
note
before Tbl up
after Tbl up
20 182B 182B : AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
19 1833 1833 : Full Sun - Ti/Poly - 4x4 - AEC1 - 4 min cadence
18 1831 1831
17 1563 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
16 1828 1828 : Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
15 1836 1836
14 1802 1802
13 1832 1832
12 1835 1835
11 1778 1778 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
10 1779 1779 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
9 1837 1837
8 182E 182E
7 17A9 17A9
6 182F 182F
5 177A 177A : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
4 1830 1830
3 17B9 17B9 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
2 1819 1819
1 177B 177B : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
ctrl Table upload : 3rd pass (2010/11/16 09:41:50)
pass
pass
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0610 2010/11/16
note
track0
track2
QT
FL
pass
pass
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0610 2010/11/17
note
pass
pass
QT
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0610 2010/11/17
note
182B
1833
1831
1563
1828
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
track3
track0
track4
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0610 2010/11/18
note
182B
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1802
1832
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1778
1779
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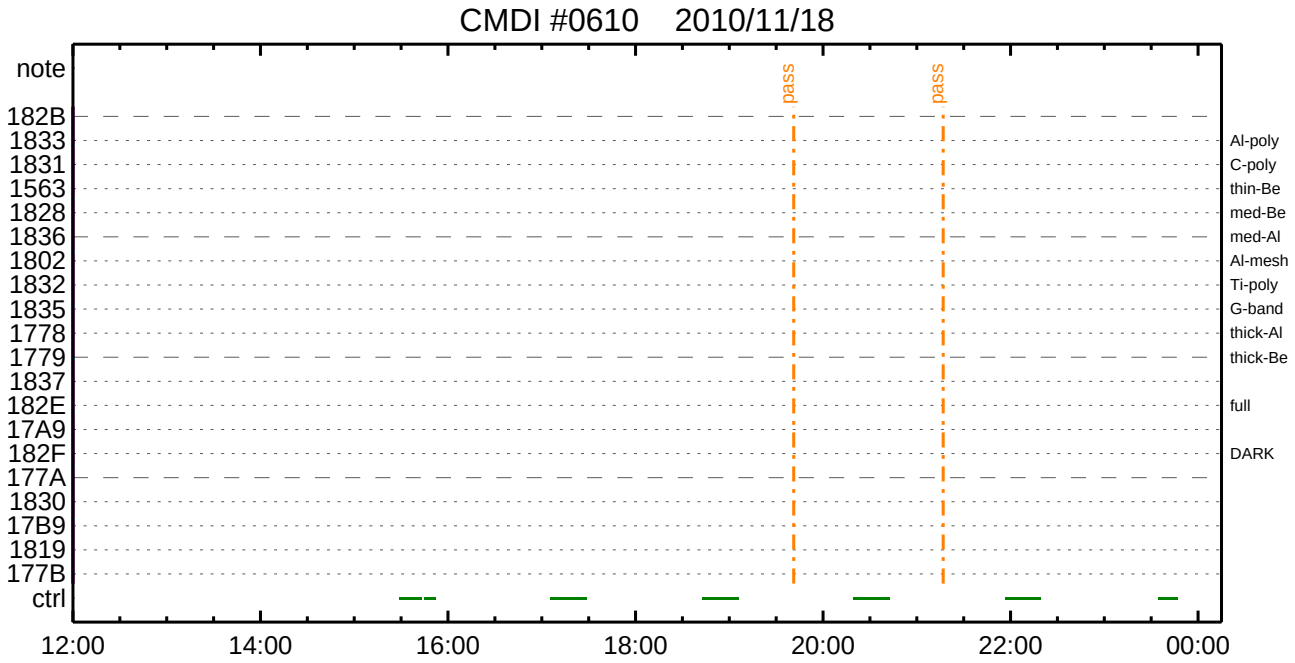
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
track0
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0610 2010/11/18
note
182B
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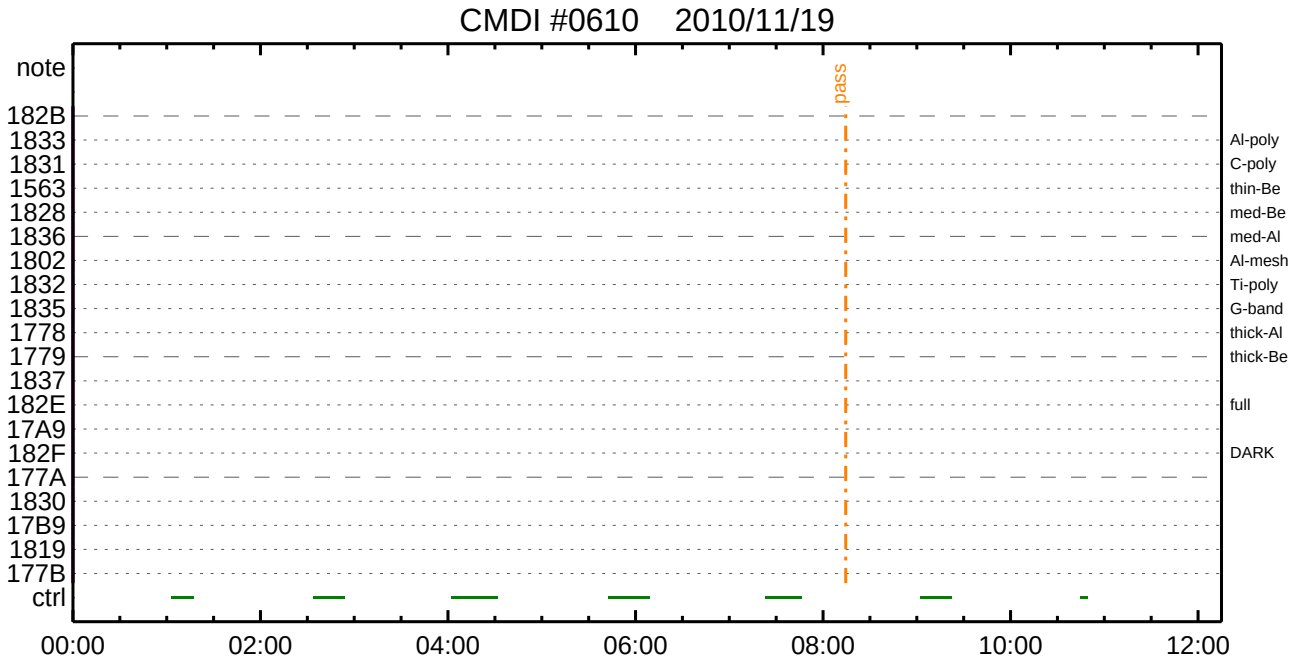
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0610 2010/11/19
note
182B
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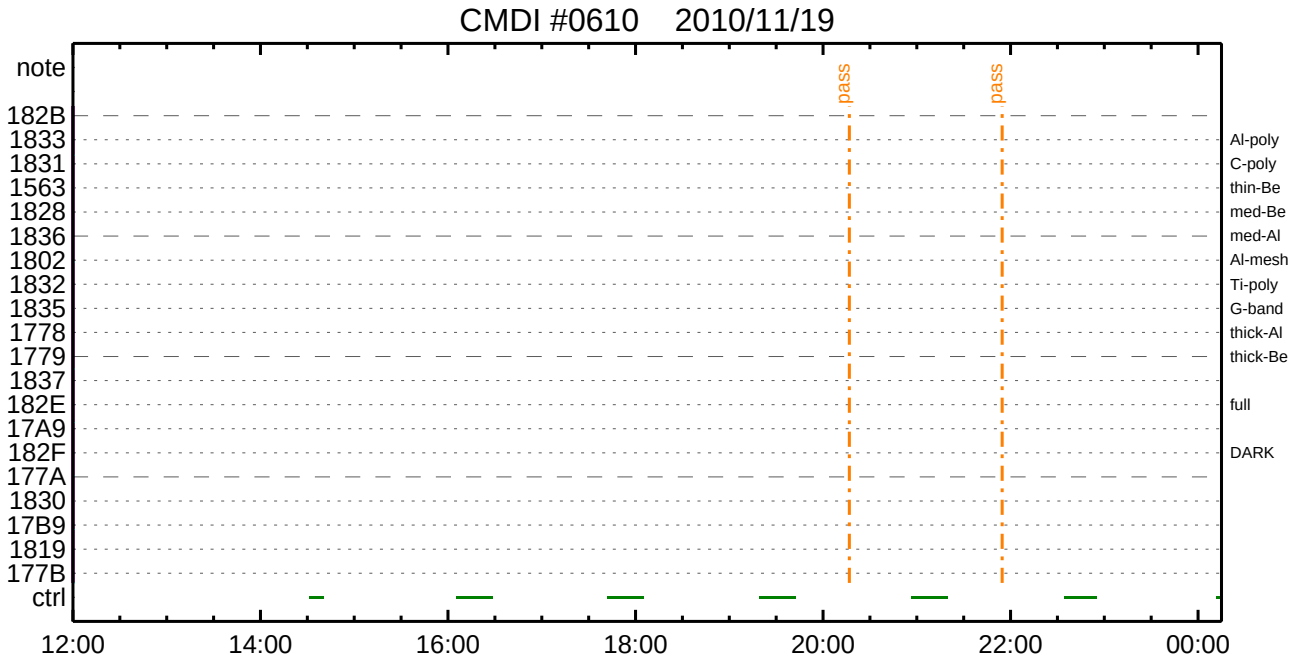
1836
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0610 2010/11/19
note
182B
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1831
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1828
1836
1802
1832
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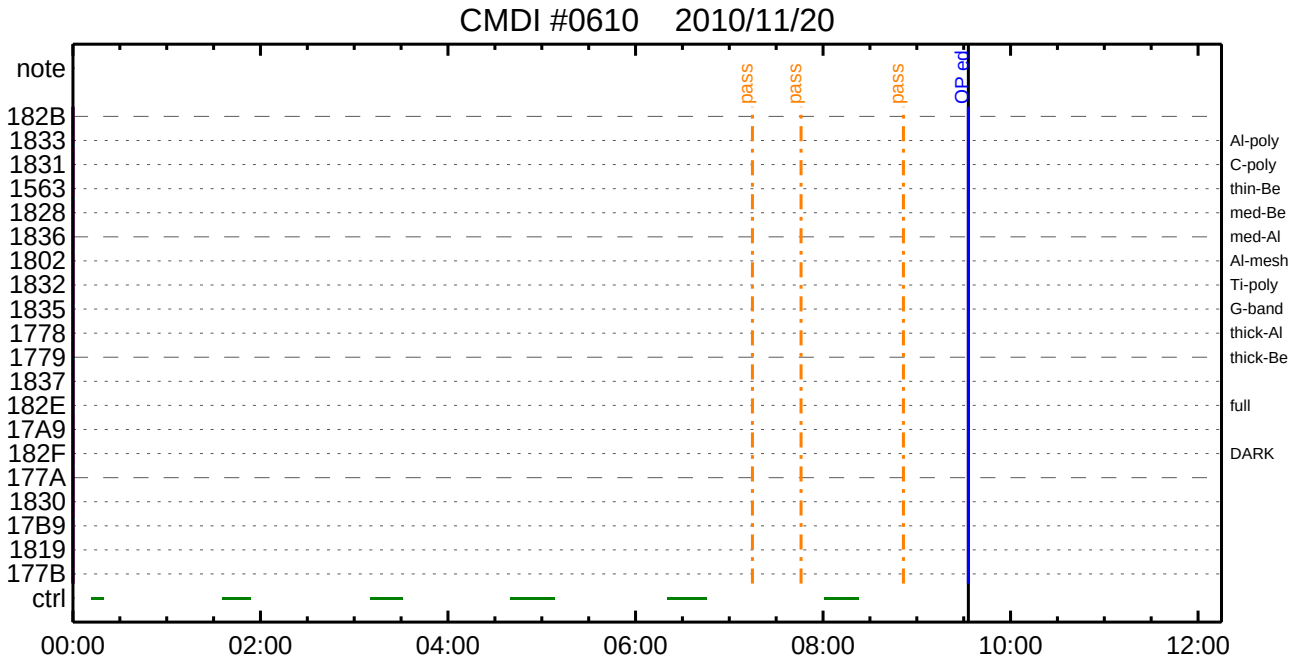
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0610 2010/11/20
note
182B
1833
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1828
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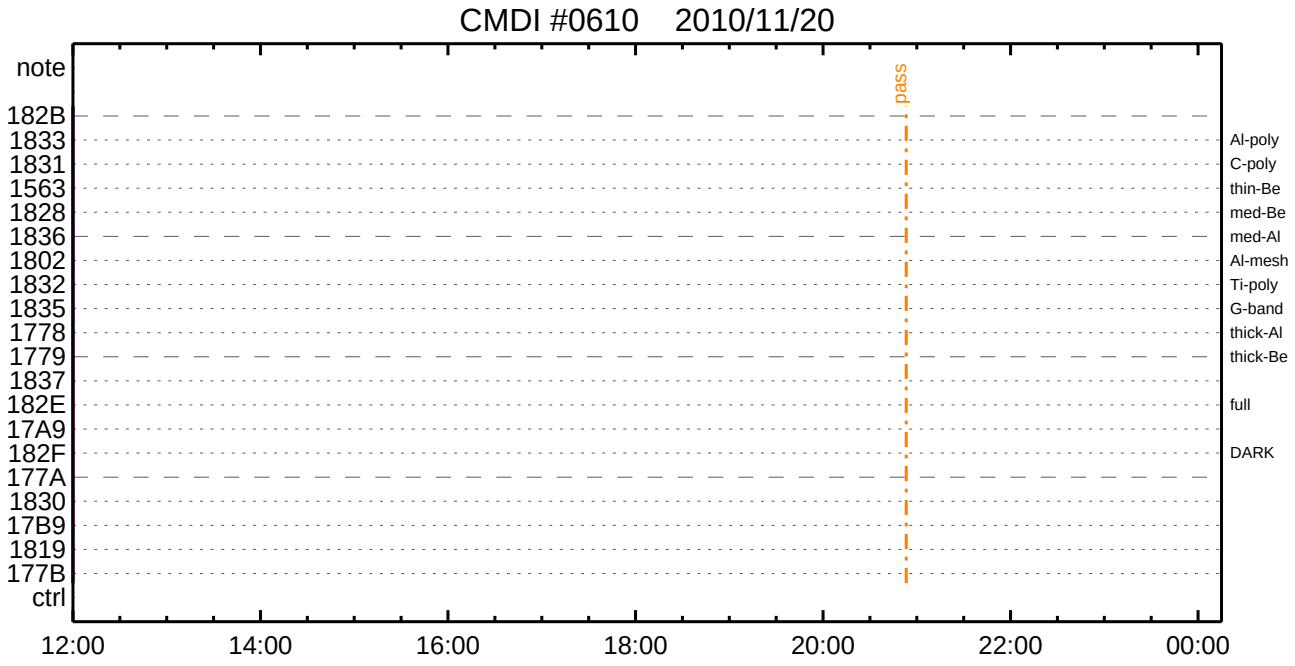
1802
1832
1835
1778
1779
1837
182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0610 2010/11/20
note
182B
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1779
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182E
17A9
182F
177A
1830
17B9
1819
177B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-710 2010-11-16 12:08:55 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-710:OP
0104     ()
0105   . S. OG    og-710:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2010-11-16 10:28:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2010-11-16 10:28:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2010-11-16 10:28:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2010-11-16 10:32:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2010-11-16 10:32:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2010-11-16 10:32:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2010-11-16 10:32:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2010-11-16 10:32:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-711 2010-11-16 12:08:55 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047  +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049  +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 4737269.7 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055  +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 43s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064  +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069  +. TI 2010-11-16 10:32:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-712 2010-11-16 12:08:55 236 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ********************************
0099     C.  SOT table upload
0100     C. ********************************
0101   . C. < Stop FG table >
0102  +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload FG Observation Table>
0109   . S. RAM   ram-268:MDP_OBS_F
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_F >
0113  +. DC 07-F0 MDP_DUMP_FGTBL
0114     BC       (82 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_F verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119   . C. < Upload DPL table >
0120     C.
0121     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0122     C.
0123   . S. RAM   ram-271:MDP_DPL
0124     ()
0125     C.
0126   . C. < Dump RAMID=MDP_DPL >
0127  +. DC 07-F0 MDP_DUMP_FGTBL
0128     BC       (82 07 00 38 b8 00 40)
0129     C. ------------------------------------
0130     C.  MDP_DPL verify          = OK    [ ]
0131     C. ------------------------------------
0132     C.
0133     C. STS_CHK¤òON¤Ë¤¹¤ë
0134     C.
0135   . C. < Update MDP DSC PAR1 >
0136  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137     BC       (4c)
0138     C.  MDP_CMD_CODE          = F04C0700[ ]
0139     C.  MDP_CMD_CNT        (count-up 1) [ ]
0140     C. ------------------------------------
0141     C.
0142   . C.
0143     C. ********************************
0144     C.  SOT TI command set
0145     C. ********************************
0146     C. Execute, after the success of TBL upload.
0147  +. TI 2010-11-16 10:32:18.0
0148     DC 07-F0 MDP_SOT_MODE_OBSV
0149     BC       (40)
0150   . C. ------------------------------------
0151     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0152     C. ------------------------------------
0153     C.
0154     C.
0155     C. ************ XRT START ************
0156     C.
0157  +. DC 07-F0 MDP_XRT_CTRL_MANU
0158     BC       (c1)
0159  +. DC 07-F8 XRT_STBY
0160     BC       (03 01)
0161  +. DC 07-F8 XRT_OPERATE
0162     BC       (03 02)
0163  +  DC 07-F0 MDP_XRT_MODE_STBY
0164     BC       (c3)
0165   . C. ----------- Success Verify ?             OK / NG____
0166     C.
0167     C. XRT Obs. Table Upload
0168   . S. RAM   ram-291:MDP_OBS_X
0169     ()
0170     C.
0171  +. DC 07-F0 MDP_DUMP_XRTTBL
0172     BC       (84 07 00 00 00 3a d4)
0173   . C. ----------- Comparison Check ?                      OK / ERR ____
0174     C.
0175     C.
0176  +. DC 07-F0 MDP_XRT_ROI_SET
0177     BC       (cd 01 b1 b1 04 04)
0178  +  DC 07-F0 MDP_XRT_ROI_SET
0179     BC       (cd 02 b1 b1 08 08)
0180  +  DC 07-F0 MDP_XRT_ROI_SET
0181     BC       (cd 03 b1 b1 08 08)
0182  +  DC 07-F0 MDP_XRT_ROI_SET
0183     BC       (cd 04 b1 b1 06 06)
0184  +  DC 07-F0 MDP_XRT_ROI_SET
0185     BC       (cd 05 85 83 06 06)
0186  +  DC 07-F0 MDP_XRT_ROI_SET
0187     BC       (cd 06 80 80 20 20)
0188  +  DC 07-F0 MDP_XRT_ROI_SET
0189     BC       (cd 07 80 80 20 04)
0190  +  DC 07-F0 MDP_XRT_ROI_SET
0191     BC       (cd 08 85 83 06 06)
0192  +  DC 07-F0 MDP_XRT_ROI_SET
0193     BC       (cd 09 80 80 20 08)
```

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0194  +  DC 07-F0 MDP_XRT_ROI_SET
0195     BC       (cd 0a 80 80 08 20)
0196  +  DC 07-F0 MDP_XRT_ROI_SET
0197     BC       (cd 0b c0 c0 10 10)
0198  +  DC 07-F0 MDP_XRT_ROI_SET
0199     BC       (cd 0c 40 c0 10 10)
0200  +  DC 07-F0 MDP_XRT_ROI_SET
0201     BC       (cd 0d 40 40 10 10)
0202  +  DC 07-F0 MDP_XRT_ROI_SET
0203     BC       (cd 0e c0 40 10 10)
0204  +  DC 07-F0 MDP_XRT_ROI_SET
0205     BC       (cd 0f 80 80 06 06)
0206  +  DC 07-F0 MDP_XRT_ROI_SET
0207     BC       (cd 10 80 80 08 08)
0208   . C. ---------- Success Verify ?           OK / NG ____
0209     C.
0210     C.
0211   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0212     C.
0213  +. DC 07-F0 MDP_XRT_MODE_OBSV
0214     BC       (c2)
0215  +. TI 2010-11-16 10:32:02.0
0216     DC 07-F0 MDP_XRT_MODE_OBSV
0217     BC       (c2)
0218   . C. ---------- Success Verify ?           OK / NG ____
0219     C.
0220     C. ************ XRT END ***********
0221     C.
0222   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0223     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0224   . S. DC-BC dcbc-402:DCBC
0225     (MDP_known_event)
0226     C.
0227     C.
0228   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0229   . S. DC-BC dcbc-153:DCBC
0230     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0231     C.
0232     C.
0233   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0234     C.
0235   . C. *********** LOS ***********
0236     C.
```

```
*** OP Sequence for XRT ***

2010/11/16  10:42:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  10:42:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/16  10:43:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 ac cd
2010/11/16  10:43:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/16  10:43:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/16  10:45:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  10:45:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/16  10:46:00.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2010/11/16  10:46:02.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/16  10:46:04.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  10:59:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 d6 67
2010/11/16  11:15:00.5  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/11/16  11:31:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 29 99
2010/11/16  11:47:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 00 00 53 33
2010/11/16  12:03:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 d6 36 b7 8e
2010/11/16  12:13:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 b4 b5 db 75
2010/11/16  12:29:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 ac 5b 00 00
2010/11/16  12:45:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 b4 b5 24 8b
2010/11/16  13:01:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 d6 36 48 72
2010/11/16  13:11:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 29 ca b7 8e
2010/11/16  13:21:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 4b 4b db 75
2010/11/16  13:37:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 53 a5 00 00
2010/11/16  13:53:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 4b 4b 24 8b
2010/11/16  14:09:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 29 db 48 72
2010/11/16  14:19:30.5  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  14:19:32.5  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  14:19:34.5  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/16  14:22:44.5  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/16  14:26:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/16  14:27:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  14:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  14:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/16  15:00:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 02 01 58 00 fd
2010/11/16  15:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/16  15:00:18.0  XRT_FLRCTRL_ENA_445_OG [0x1bd]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/16  15:02:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  15:02:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/16  15:03:00.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2010/11/16  15:03:02.5  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/16  15:03:05.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  15:52:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  15:52:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  15:52:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/16  15:55:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/16  16:16:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/16  16:17:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  17:29:00.0  XRT_CTRL_MANU_408_OG [0x198]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  17:29:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  17:29:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/16  17:32:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/16  17:52:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/16  17:53:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  19:06:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  19:06:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/16  19:06:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/16  19:09:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/16  19:29:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/16  19:30:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/16  20:00:00.0  XRT_CTRL_MANU_448_OG [0x1c0]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/16  20:00:30.0  XRT_TCIB_XRT_S_HTR_A_ENA_449_OG [0x1c1]
                                        TCIB_XRT_S_HTR_A_ENA 0 04-BC
2010/11/16  22:00:30.0  XRT_Custom_446_OG [0x1be]
2010/11/17  00:00:30.0  XRT_Custom_446_OG [0x1be]
2010/11/17  02:00:30.0  XRT_Custom_446_OG [0x1be]
2010/11/17  04:00:30.0  XRT_Custom_404_OG [0x194]
2010/11/17  04:00:40.0  XRT_Custom_446_OG [0x1be]
2010/11/17  06:00:40.0  XRT_Custom_405_OG [0x195]
2010/11/17  08:00:18.0  XRT_Custom_404_OG [0x194]
2010/11/17  08:00:28.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  08:00:30.0  XRT_TCIB_XRT_S_HTR_A_DIS_407_OG [0x197]
                                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2010/11/17  08:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  08:02:02.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  08:02:22.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/11/17  08:02:24.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  08:02:26.0  XRT_FLD_DIS_423_OG [0x1a7]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  08:02:28.0  XRT_FLRCTRL_DIS_437_OG [0x1b5]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  08:02:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  14:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  14:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/17  15:00:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/11/17  15:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/17  15:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/17  15:00:20.0  XRT_ARS_DIS_416_OG [0x1a0]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  15:06:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  15:06:26.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2010/11/17  15:06:28.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/17  15:06:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  16:28:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  16:28:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  16:28:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/17  16:31:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/17  16:52:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/17  16:53:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  18:06:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  18:06:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  18:06:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/17  18:09:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/17  18:29:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/17  18:30:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  18:49:54.0  XRT_CTRL_MANU_400_OG [0x190]
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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  18:49:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  18:50:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2010/11/17  18:50:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  18:50:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  18:52:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  18:52:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/11/17  18:53:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  18:59:54.0  XRT_CTRL_MANU_438_OG [0x1b6]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  19:00:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                    AOCU_NM              5 02-76 00 2e f9 2e f9
2010/11/17  19:02:32.0  XRT_FOCUS_POSITION_440_OG [0x1b8]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  19:02:52.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/11/17  19:02:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  19:02:56.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  19:02:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  19:03:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  19:09:54.0  XRT_CTRL_MANU_438_OG [0x1b6]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  19:10:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                    AOCU_NM              5 02-76 00 2e f9 d1 07
2010/11/17  19:12:32.0  XRT_FOCUS_POSITION_440_OG [0x1b8]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  19:12:52.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2010/11/17  19:12:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  19:12:56.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  19:12:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  19:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  19:19:54.0  XRT_CTRL_MANU_438_OG [0x1b6]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  19:20:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2010/11/17  19:22:32.0  XRT_FOCUS_POSITION_440_OG [0x1b8]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  19:22:52.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2010/11/17  19:22:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  19:22:56.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  19:22:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  19:23:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  19:29:54.0  XRT_CTRL_MANU_438_OG [0x1b6]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  19:30:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2010/11/17  19:32:32.0  XRT_FOCUS_POSITION_440_OG [0x1b8]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/17  19:32:52.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2010/11/17  19:32:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/17  19:32:56.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/17  19:32:58.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  19:33:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  19:39:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  19:39:56.0  XRT_ROI_A_425_OG [0x1a9]
                                    MDP_XRT_ROI_SET      6 07-F0 cd 05 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 06 80 80 20 20
                                    MDP_XRT_ROI_SET      6 07-F0 cd 08 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 09 80 80 20 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0a 80 80 08 20
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2010/11/17  19:40:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                    AOCU_NM              5 02-76 04 00 00 00 00
2010/11/17  19:40:01.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
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2010/11/17  19:40:21.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/17  19:40:23.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/17  19:40:25.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/17  19:42:59.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  19:43:01.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2010/11/17  19:43:03.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/17  20:06:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/17  20:07:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  21:20:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  21:20:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  21:20:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/17  21:23:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/17  21:43:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/17  21:44:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  22:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  22:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/17  22:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/17  23:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/17  23:18:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/17  23:19:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/17  23:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/17  23:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/11/18  00:00:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 00 23 8d b4 73
2010/11/18  00:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/11/18  00:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/11/18  00:00:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/18  00:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/18  00:02:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2010/11/18  00:02:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/11/18  00:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  00:35:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  00:35:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/18  00:35:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/18  00:38:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/18  00:42:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/18  00:43:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  01:58:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  01:58:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/18  01:58:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/18  02:01:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/18  02:17:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/18  02:18:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  03:32:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  03:32:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/18  03:32:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/18  03:35:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/18  03:54:30.0  XRT_Custom_418_OG [0x1a2]
2010/11/18  03:55:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  05:04:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  05:04:32.0  XRT_FLD_RESET_412_OG [0x19c]
```

```
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/11/18  05:04:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/11/18  05:07:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/11/18  05:32:00.0  XRT_Custom_418_OG [0x1a2]
2010/11/18  05:33:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  05:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/11/18  06:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2010/11/18  06:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/11/18  06:00:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/11/18  06:02:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/11/18  06:02:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/11/18  06:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/11/18  06:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/11/18  06:10:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                    AOCU_NM              5 02-76 00 23 8d b4 73
2010/11/18  10:18:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```