

# XRT Timeline to be uploaded on 2010/11/20

Period: 2010/11/20 09:33:00 - 2010/11/25 09:22:00

\* \* \* \* \*

Normal mode

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XOB #1838: HOP 176 - Ti/poly - 1x1 - 384x384 - 1min cadence - AEC 1 - Q95 +context													
Term	Pointing (x, y)		Comment										
11/20 09:58:36 - 11/20 11:59:54	Track ( 271.5, -553.4)	<sup>Ⓜ 11/20 09:43:00</sup>	# OP start + 10min, HOP 176 (9-12 UT)										
11/21 09:03:00 - 11/21 11:59:54	Track ( 429.1, -549.0)	<sup>Ⓜ 11/21 09:00:00</sup>	HOP 176 (9-12 UT)										
11/22 09:03:00 - 11/22 11:59:54	Track ( 571.1, -543.6)	<sup>Ⓜ 11/22 09:00:00</sup>	HOP 176 (9-12 UT)										
11/23 09:03:00 - 11/23 09:58:00	Track ( 685.5, -537.6)	<sup>Ⓜ 11/23 09:00:00</sup>	HOP 176 (9-12 UT)										
<b>PROG= 08 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 25 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
┌ Subr= 2 3-time(s) 2.0sec													
└─ Seqn= 30 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Seqn= 10 20-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad													
Term	Pointing (x, y)		Comment										
11/20 12:03:00 - 11/20 17:55:24	Track ( -403.1, 348.5)	<sup>Ⓜ 11/20 12:00:00</sup>	AR11127 obs										
11/20 18:08:30 - 11/21 05:59:54	Track ( -356.4, 348.1)	<sup>Ⓜ 11/20 18:05:30</sup>	AR11127 obs.										
11/21 06:13:00 - 11/21 08:38:30	Track ( -260.6, 347.8)	<sup>Ⓜ 11/21 06:10:00</sup>	AR 11127 Obs										
11/21 12:03:00 - 11/21 17:59:24	Track ( -212.9, 347.8)	<sup>Ⓜ 11/21 12:00:00</sup>	AR11127 obs.										
11/21 18:12:30 - 11/22 05:28:54	Track ( -161.9, 348.0)	<sup>Ⓜ 11/21 18:09:30</sup>	AR11127 obs.										
11/22 05:42:00 - 11/22 08:59:54	Track ( -65.4, 348.5)	<sup>Ⓜ 11/22 05:39:00</sup>	AR11127 obs.										
11/22 12:03:00 - 11/22 15:59:54	Fixed ( 5.3, 349.4)		AR obs (12-16 UT; O abundances)										
11/22 16:03:00 - 11/22 17:55:00	Track ( 22.3, 349.3)	<sup>Ⓜ 11/22 16:00:00</sup>	AR 11127 obs.										
11/22 18:31:30 - 11/23 06:04:24	Track ( 43.2, 349.5)	<sup>Ⓜ 11/22 18:28:30</sup>	AR11127 obs.										
11/23 06:17:30 - 11/23 08:59:54	Track ( 142.4, 350.8)	<sup>Ⓜ 11/23 06:14:30</sup>	AR11127 obs.										
<b>PROG= 07 Inf.-time(s)</b>													
┌ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
┌ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 52 35-time(s) 120.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)		Comment									
11/20 17:58:30 - 11/20 18:05:24	Fixed ( 0.0, 0.0)		synoptic, shifted -4.5 min									
11/21 06:03:00 - 11/21 06:09:54	Fixed ( 0.0, 0.0)		synoptic									
11/21 18:02:30 - 11/21 18:09:24	Fixed ( 0.0, 0.0)		synoptic, shifted -0.5 min									
11/22 05:32:00 - 11/22 05:38:54	Fixed ( 0.0, 0.0)		synoptic, shifted -31.0 min									
11/22 18:21:30 - 11/22 18:28:24	Fixed ( 0.0, 0.0)		synoptic, shifted 18.5 min									
11/23 06:07:30 - 11/23 06:14:24	Fixed ( 0.0, 0.0)		synoptic, shifted 4.5 min									

PROG= 03 1-time(s)													
┌ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)		Comment									
11/20 09:58:36 - 11/20 11:59:54	Track ( 271.5, -553.4)	<sup>Ⓜ</sup> 11/20 09:43:00	# OP start + 10min, HOP 176 (9-12 UT)									
11/20 12:03:00 - 11/20 17:55:24	Track ( -403.1, 348.5)	<sup>Ⓜ</sup> 11/20 12:00:00	AR11127 obs									
11/20 18:08:30 - 11/21 05:59:54	Track ( -356.4, 348.1)	<sup>Ⓜ</sup> 11/20 18:05:30	AR11127 obs.									
11/21 06:13:00 - 11/21 08:38:30	Track ( -260.6, 347.8)	<sup>Ⓜ</sup> 11/21 06:10:00	AR 11127 Obs									
11/21 09:03:00 - 11/21 11:59:54	Track ( 429.1, -549.0)	<sup>Ⓜ</sup> 11/21 09:00:00	HOP 176 (9-12 UT)									
11/21 12:03:00 - 11/21 17:59:24	Track ( -212.9, 347.8)	<sup>Ⓜ</sup> 11/21 12:00:00	AR11127 obs.									
11/21 18:12:30 - 11/22 05:28:54	Track ( -161.9, 348.0)	<sup>Ⓜ</sup> 11/21 18:09:30	AR11127 obs.									
11/22 05:42:00 - 11/22 08:59:54	Track ( -65.4, 348.5)	<sup>Ⓜ</sup> 11/22 05:39:00	AR11127 obs.									
11/22 09:03:00 - 11/22 11:59:54	Track ( 571.1, -543.6)	<sup>Ⓜ</sup> 11/22 09:00:00	HOP 176 (9-12 UT)									
11/22 12:03:00 - 11/22 15:59:54	Fixed ( 5.3, 349.4)		AR obs (12-16 UT; O abundances)									
11/22 16:03:00 - 11/22 17:55:00	Track ( 22.3, 349.3)	<sup>Ⓜ</sup> 11/22 16:00:00	AR 11127 obs.									
11/22 18:31:30 - 11/23 06:04:24	Track ( 43.2, 349.5)	<sup>Ⓜ</sup> 11/22 18:28:30	AR11127 obs.									
11/23 06:17:30 - 11/23 08:59:54	Track ( 142.4, 350.8)	<sup>Ⓜ</sup> 11/23 06:14:30	AR11127 obs.									
11/23 09:03:00 - 11/23 09:58:00	Track ( 685.5, -537.6)	<sup>Ⓜ</sup> 11/23 09:00:00	HOP 176 (9-12 UT)									
<b>PROG= 16 1-time(s)</b>												
Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

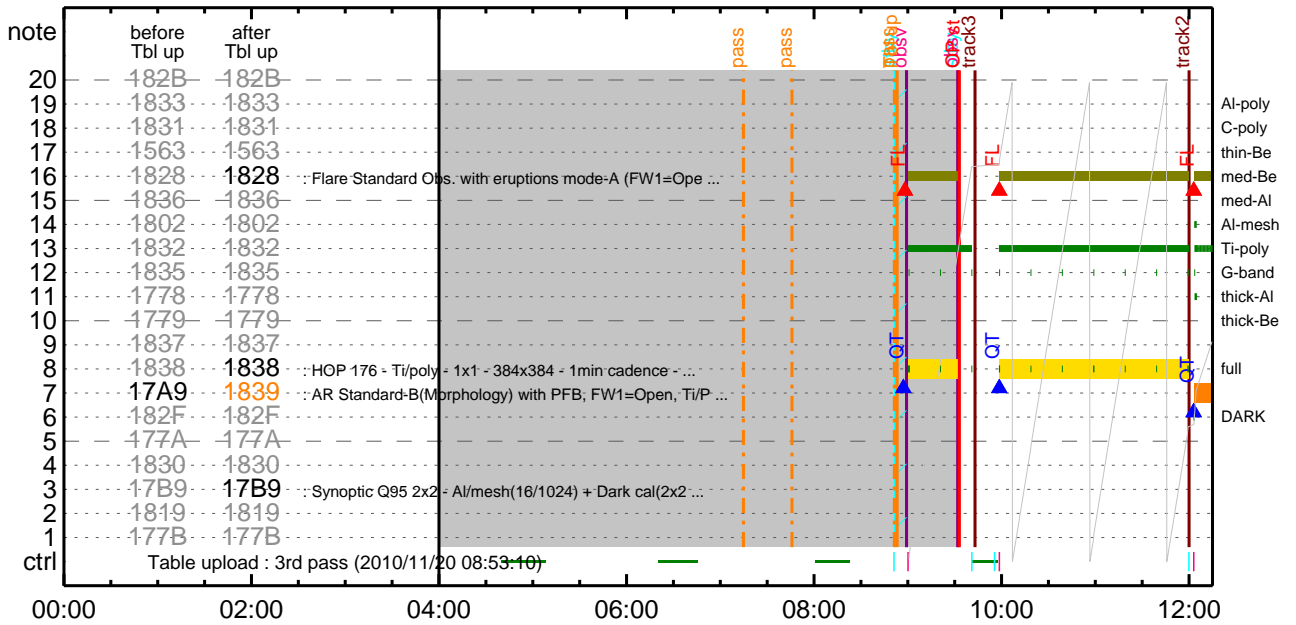
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### Flare Detection

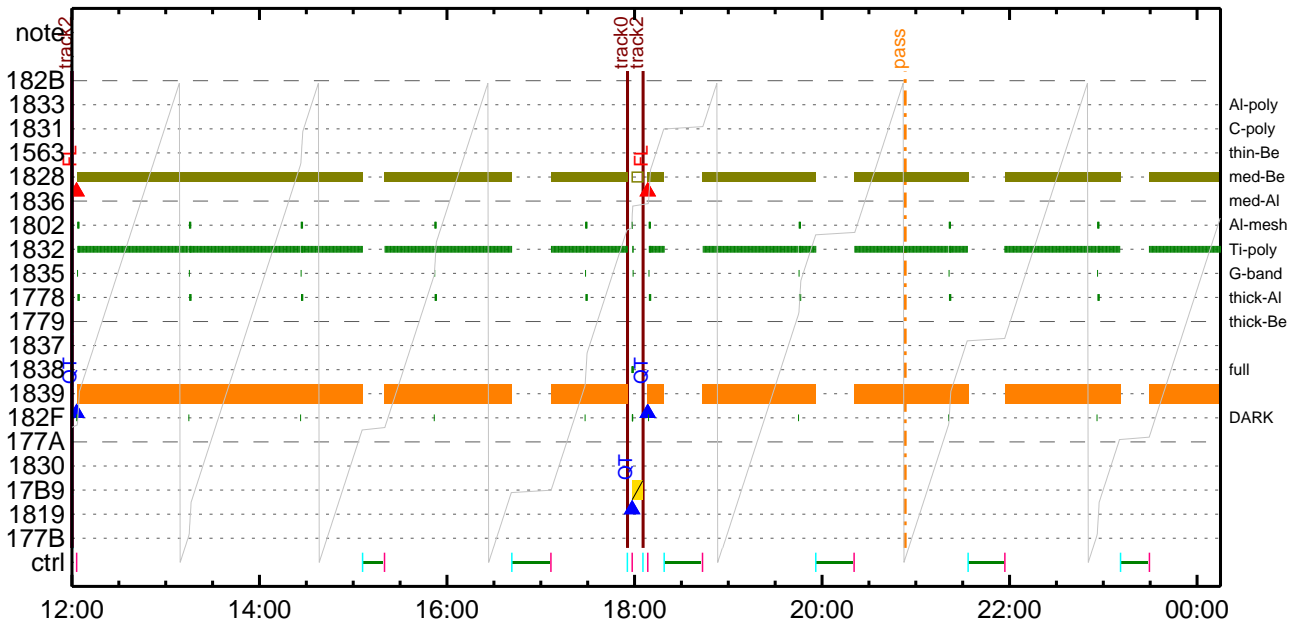
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FLD Patrol										
Term	Pointing (x, y)						Comment			
11/20 18:05:46 - 11/21 06:00:16	Track (	-356.4,	348.1)	<sup>@ 11/20 18:05:30</sup>	AR1127	obs.				
11/21 06:10:16 - 11/21 17:59:46	Track (	-260.6,	347.8)	<sup>@ 11/21 06:10:00</sup>	AR 11127	Obs				
11/21 18:09:46 - 11/22 05:29:16	Track (	-161.9,	348.0)	<sup>@ 11/21 18:09:30</sup>	AR11127	obs.				
11/22 05:39:16 - 11/22 18:18:46	Track (	-65.4,	348.5)	<sup>@ 11/22 05:39:00</sup>	AR11127	obs.				
11/22 18:28:46 - 11/23 06:04:46	Track (	43.2,	349.5)	<sup>@ 11/22 18:28:30</sup>	AR11127	obs.				
11/23 06:14:46 - 11/25 09:22:00	Track (	142.4,	350.8)	<sup>@ 11/23 06:14:30</sup>	AR11127	obs.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

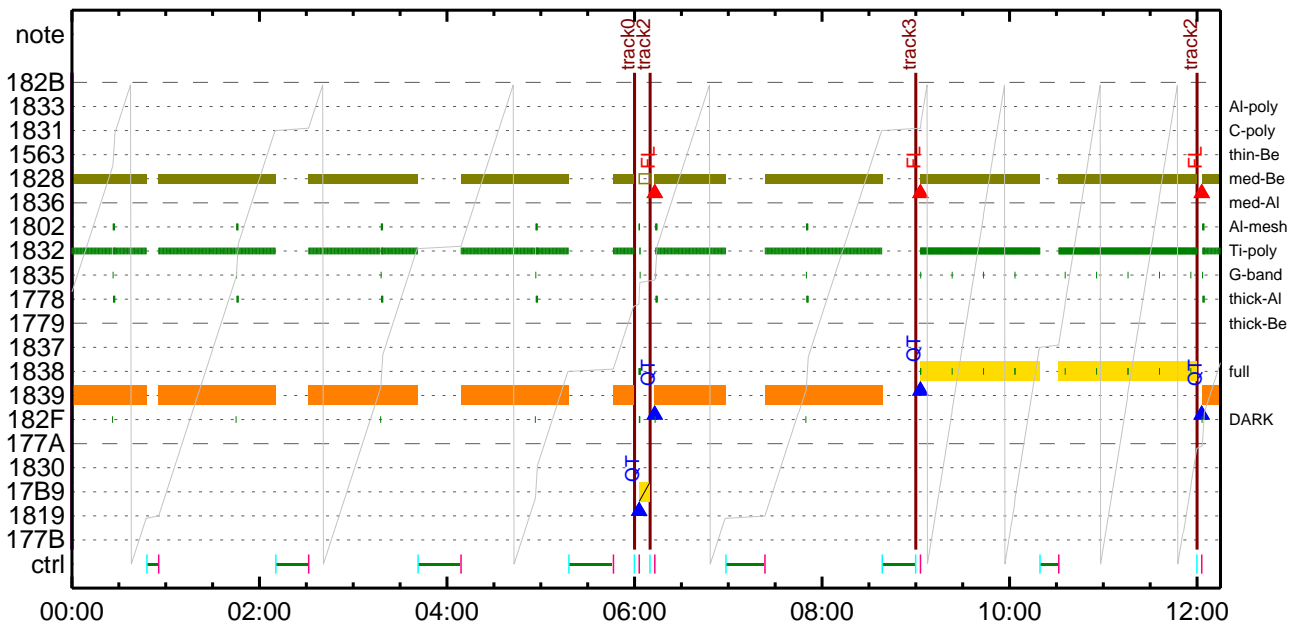
### CMDI #0621 2010/11/20



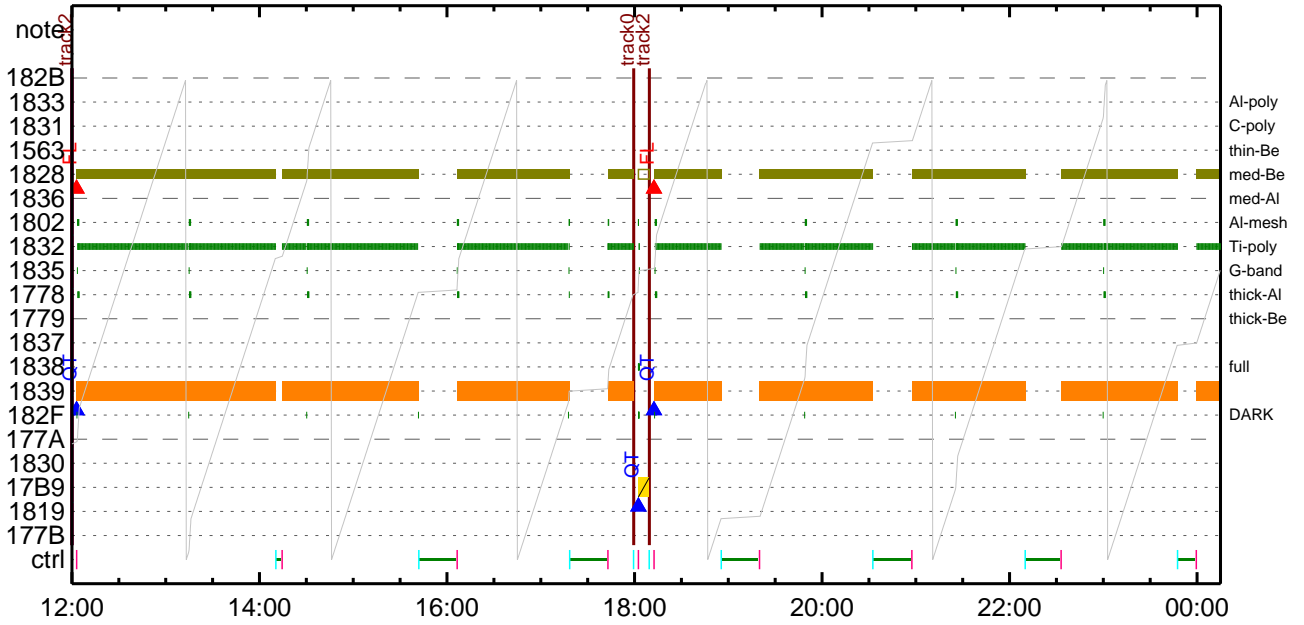
### CMDI #0621 2010/11/20



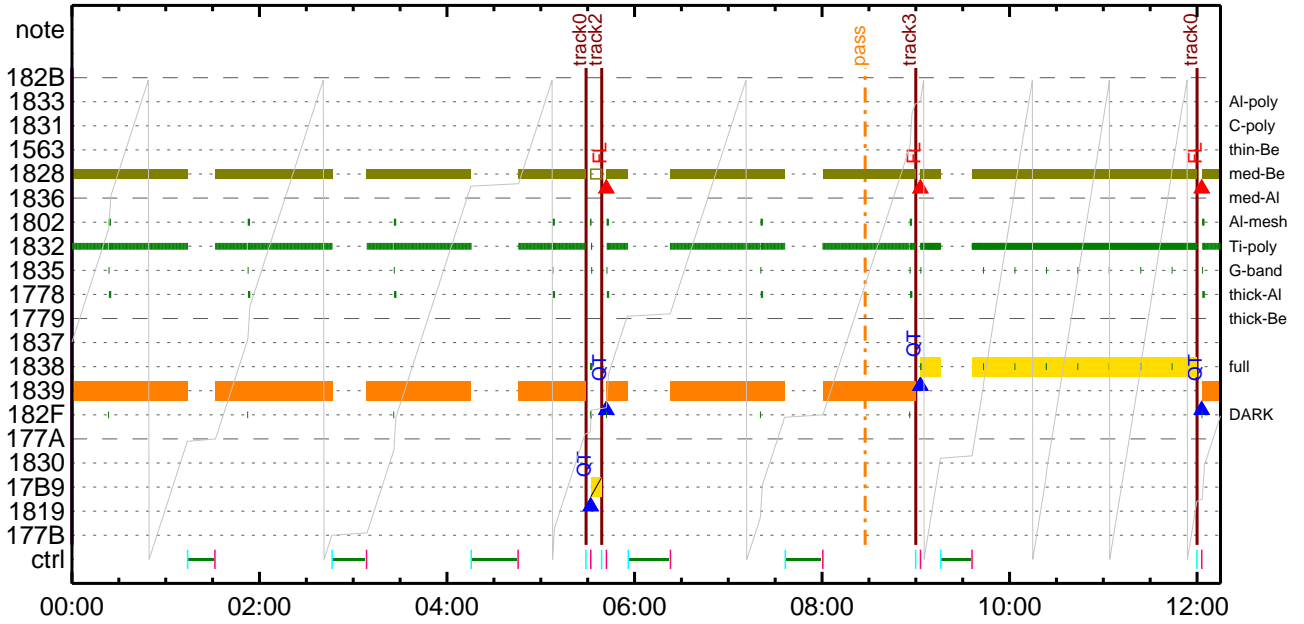
### CMDI #0621 2010/11/21



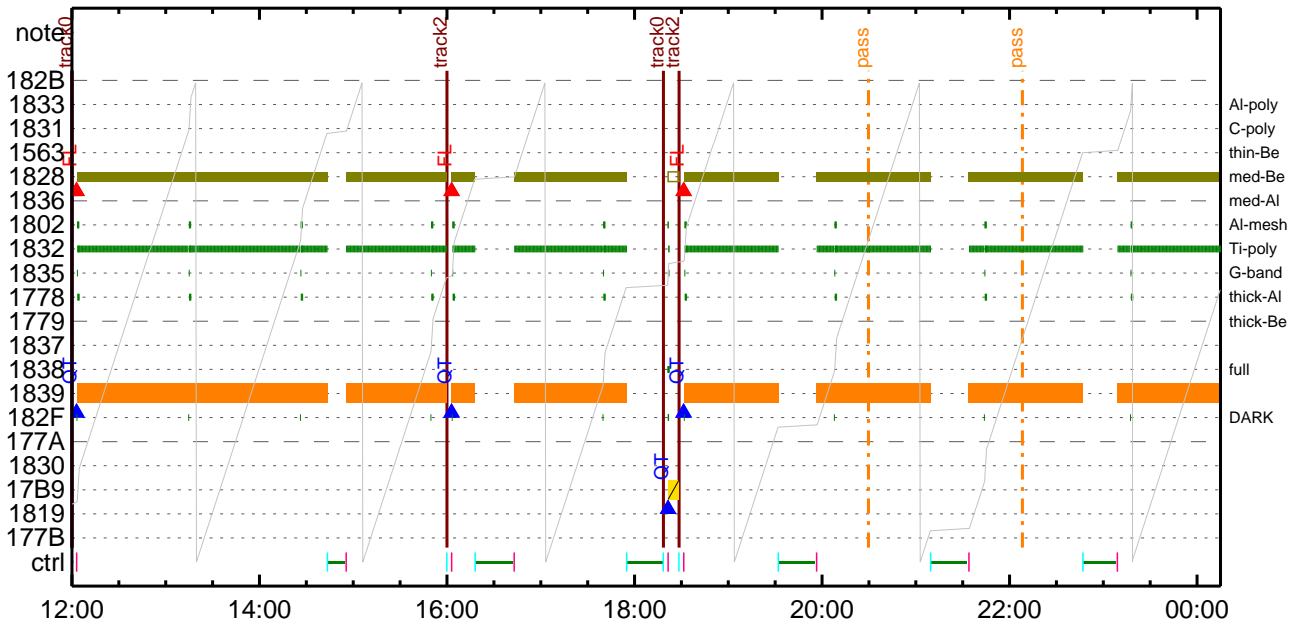
CMDI #0621 2010/11/21



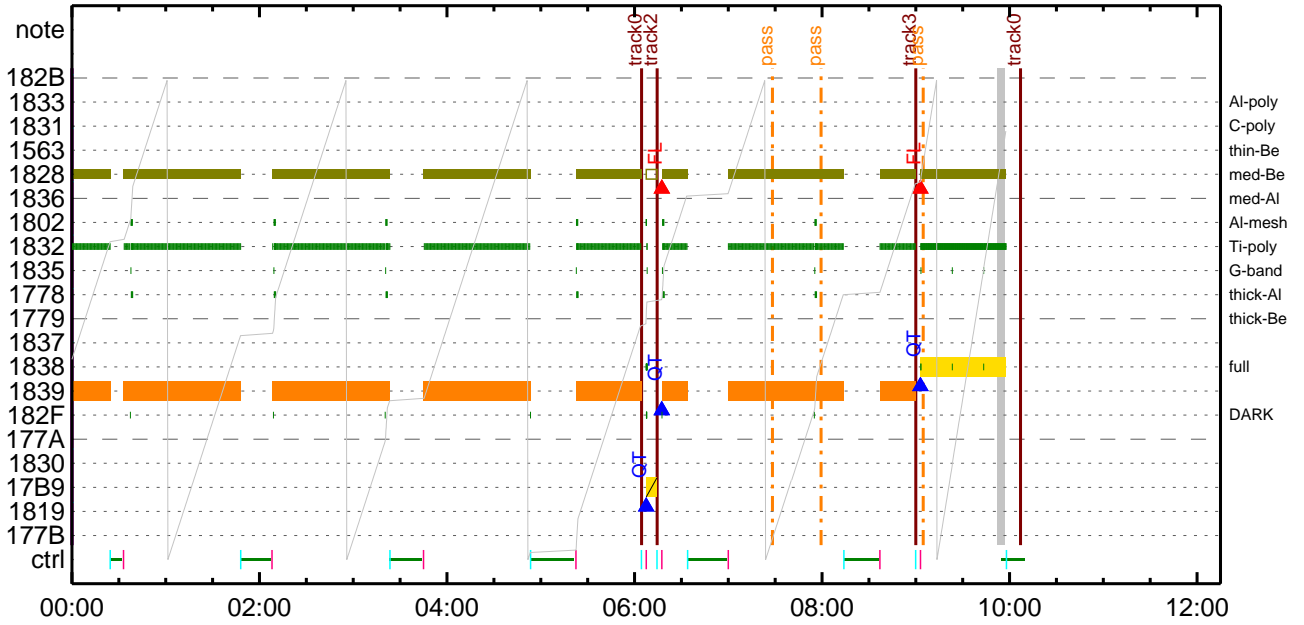
CMDI #0621 2010/11/22



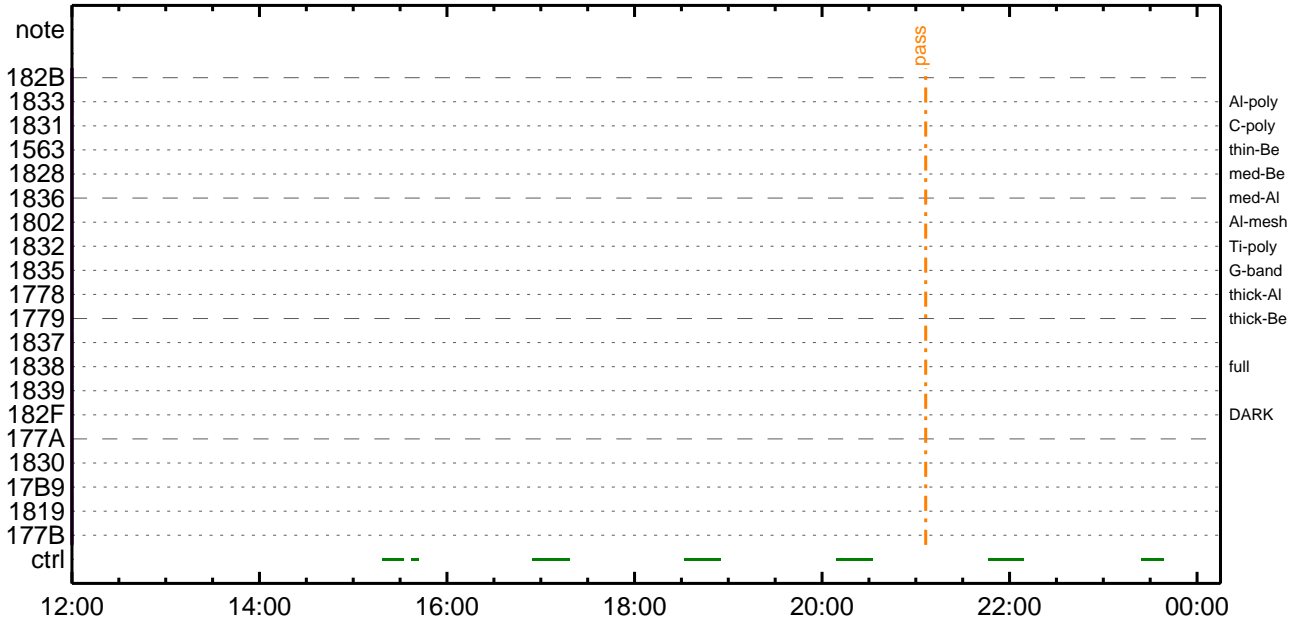
CMDI #0621 2010/11/22



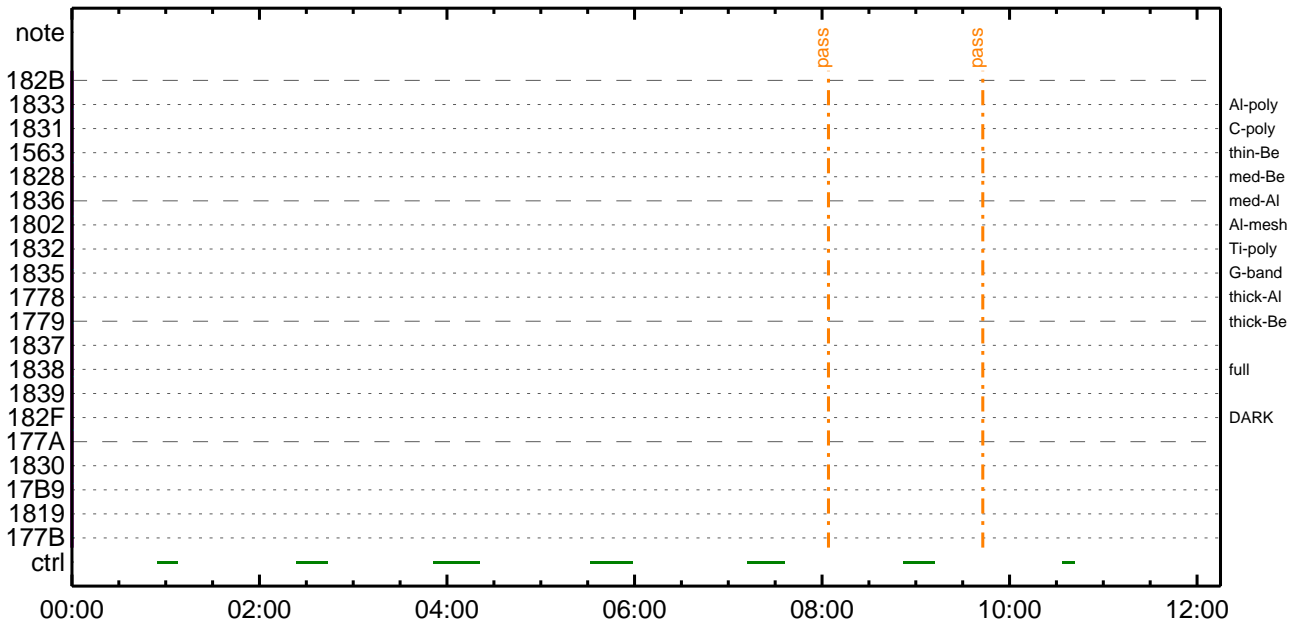
CMDI #0621 2010/11/23



CMDI #0621 2010/11/23



CMDI #0621 2010/11/24





(a) Spacecraft Operation Procedure (real-commands)

```
main-720 2010-11-20 14:01:32 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY´¼Á»Ü;ä
0005 C.
0006 C. YÁY$;¼Y³YBÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÀò•μº®×Í×ÁÇ¿ÍY¿YÁY×YÍ;¼YÉ;ÈÈè¾μ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹¿òí;¿À®, ùò¹òèòòò¿Á+¿®ò•òÈòòò³òÈ;£
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYöY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-720:OP
0020 ()
0021 . S. OG og-720:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍÊ°èYÁYöY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.     ¿¿[HK1_DMP_TOP_ADDR_1]           EQ    40
0029 C.     ¿¿[HK1_DMP_TOP_ADDR_0]           EQ    0
0030 C.     ¿¿[HK1_DMP_BLOCK_NUM]           EQ   127
0031 C.     ¿¿[HK1_DMP_REPEAT_NUM]          EQ    0
0032 C.     ¿¿[HK1_DMA_DMP_PIM]             EQ   DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.     ¿¿[HK1_PKT_FORM_NO]              EQ    7
0036 C.     ¿¿[HK1_PKT_GEN_TIME]             EQ   0.25 s
0037 C.     ¿¿[HK1_S_TLM_BIT_RATE]          EQ   32k
0038 C.     ¿¿[HK1_X_TLM_BIT_RATE]          EQ    4M
0039 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   EXEC
0040 . C. YÁYöY×¾ªî»òð³ÍÇ§
0041 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   NON
0042 . C. RAM ID=NMOGòí¾È¹¿•è²ÍOKòð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.     ¿¿[HK1_DMP_TOP_ADDR_1]           EQ    41
0048 C.     ¿¿[HK1_DMP_TOP_ADDR_0]           EQ    0
0049 C.     ¿¿[HK1_DMP_BLOCK_NUM]           EQ   127
0050 C.     ¿¿[HK1_DMP_REPEAT_NUM]          EQ    0
0051 C.     ¿¿[HK1_DMA_DMP_PIM]             EQ   DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.     ¿¿[HK1_PKT_FORM_NO]              EQ    7
0055 C.     ¿¿[HK1_PKT_GEN_TIME]             EQ   0.25 s
0056 C.     ¿¿[HK1_S_TLM_BIT_RATE]          EQ   32k
0057 C.     ¿¿[HK1_X_TLM_BIT_RATE]          EQ    4M
0058 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   EXEC
0059 . C. YÁYöY×¾ªî»òð³ÍÇ§
0060 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   NON
0061 . C. RAM ID=NMOGòí¾È¹¿•è²ÍOKòð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.     ¿¿[HK1_DMP_TOP_ADDR_1]           EQ    42
0067 C.     ¿¿[HK1_DMP_TOP_ADDR_0]           EQ    0
0068 C.     ¿¿[HK1_DMP_BLOCK_NUM]           EQ    65
0069 C.     ¿¿[HK1_DMP_REPEAT_NUM]          EQ    0
0070 C.     ¿¿[HK1_DMA_DMP_PIM]             EQ   DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.     ¿¿[HK1_PKT_FORM_NO]              EQ    7
0074 C.     ¿¿[HK1_PKT_GEN_TIME]             EQ   0.25 s
0075 C.     ¿¿[HK1_S_TLM_BIT_RATE]          EQ   32k
0076 C.     ¿¿[HK1_X_TLM_BIT_RATE]          EQ    4M
0077 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   EXEC
0078 . C. YÁYöY×¾ªî»òð³ÍÇ§
0079 C.     ¿¿[HK1_DMP_CHK_FLG]             EQ   NON
0080 . C. RAM ID=NMOG, RAM ID=OPòí¾È¹¿•è²ÍOKòð³ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¾Á´¶ÀºòÈÈ-òºÁ+¿® (¼áμ-YÁYöY×¾ªî»òð³ÍÇ§òðÁÓÃæò¿ªº°ò¼¼¼í¹¿ò¿òá) *****
0083 C. DHU¿á;¼YÉ;È¾Y¾;Yí;¼YÉ;ÈòðÍáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.     ¿¿[HK1_PKT_FORM_NO]              EQ    2
0087 C.     ¿¿[HK1_PKT_GEN_TIME]             EQ   0.5S
0088 C.     ¿¿[HK1_S_TLM_BIT_RATE]          EQ   32k
0089 C.     ¿¿[HK1_X_TLM_BIT_RATE]          EQ    4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADò-Á+¿®NGòí¾È¹¿ç;¿òÈ²¼òí¾TI-CMDÁ+¿®òí¾Á¹Ôò•òÈòòò³òÈ;£
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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F8 XRT_STBY
0104 BC (03 01)
0105 +. DC 07-F8 XRT_OPERATE
0106 BC (03 02)
0107 + DC 07-F0 MDP_XRT_MODE_STBY
0108 BC (c3)
0109 . C. ----- Success Verify ? OK / NG____
0110 C.
0111 C. XRT Obs. Table Upload
0112 . S. RAM ram-291:MDP_OBS_X
0113 ( )
0114 C.
0115 +. DC 07-F0 MDP_DUMP_XRTTBL
0116 BC (84 07 00 00 00 3a d4)
0117 . C. ----- Comparison Check ? OK / ERR ____
0118 C.
0119 C.
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 01 b1 b1 04 04)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 02 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 03 b1 b1 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 04 b1 b1 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 05 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 06 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 07 85 83 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 08 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 09 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F8 XRT_FOCUS_POSITION
0143 BC (22 fe 97 00)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 +. DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 08)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 10)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0164 BC (c0)
0165 +. TI 2010-11-20 09:32:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 . C. *****
0172 C. SOT table upload
0173 C. *****
0174 . C. < Stop FG table >
0175 +. DC 07-F0 MDP_FG_CTRL_MANU
0176 BC (51)
0177 . C. -----
0178 C. MDP_FG_CTRL_MODE = MANU [ ]
0179 C. -----
0180 C.
0181 . C. <Upload FG Observation Table>
0182 . S. RAM ram-261:MDP_OBS_F
0183 ( )
0184 C.
0185 . C. < Dump RAMID=MDP_OBS_F >
0186 +. DC 07-F0 MDP_DUMP_FGTBL
0187 BC (82 07 00 00 00 38 b8)
0188 C. -----
0189 C. MDP_OBS_F verify = OK/NG [ ]
0190 C. -----
0191 C.
0192 . C. < Resume FG table (auto mode) >
0193 +. DC 07-F0 MDP_FG_CTRL_AUTO

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\*\*\* OP Sequence for XRT \*\*\*

2010/11/20	09:41:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	09:41:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/20	09:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/20	09:43:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2010/11/20	09:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/20	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	09:55:32.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/20	09:55:52.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/20	09:55:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/20	09:55:56.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/20	09:58:30.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/20	09:58:32.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2010/11/20	09:58:34.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/11/20	09:58:36.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/20	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/20	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2010/11/20	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/20	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/20	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/20	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/20	12:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2010/11/20	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/11/20	12:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/20	15:06:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	15:06:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/20	15:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/20	15:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/20	15:19:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/20	15:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/20	16:41:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	16:41:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/20	16:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/20	16:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/20	17:05:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/20	17:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/20	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/11/20	17:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/11/20	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/11/20	17:55:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/11/20	17:58:26.0	XRT_ARS_DIS_447_OG [0x1bf]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/20	17:58:28.0	XRT_QT_PROG_SET_410_OG [0x19a]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/11/20	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/20	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/20	18:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/20	18:05:30.0	AOCS_ORe-point_Start_2_OG [0x098]					

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2010/11/20	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00	00	00	00
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/20	18:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/20	18:05:50.0	XRT_ARS_DIS_420_OG [0x1a4]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/20	18:08:24.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/20	18:08:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]		2	07-F0	c4	07			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/11/20	18:08:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	10			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/20	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/20	18:19:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/20	18:19:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/20	18:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/20	18:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/20	18:42:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/20	18:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/20	19:56:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/20	19:56:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/20	19:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/20	19:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/20	20:19:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/20	20:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/20	21:33:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/20	21:33:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/20	21:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/20	21:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/20	21:56:00.0	XRT_Custom_418_OG [0x1a2]								
2010/11/20	21:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/20	23:11:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/20	23:11:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/20	23:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/20	23:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/20	23:28:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/20	23:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	00:48:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	00:48:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	00:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	00:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	00:54:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/21	00:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	02:10:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	02:10:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	02:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	02:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	02:30:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/21	02:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	03:41:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	03:41:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	03:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	03:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	04:08:00.0	XRT_Custom_418_OG [0x1a2]								
2010/11/21	04:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	05:18:00.0	XRT_CTRL_MANU_408_OG [0x198]								



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2010/11/21	05:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	05:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	05:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	05:45:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	05:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/21	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/21	06:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/21	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/21	06:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/21	06:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/21	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/21	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/21	06:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
			AOCU_NM	5	02-76	02 00 00 00 00				
2010/11/21	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/21	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/21	06:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/21	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	06:12:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2010/11/21	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/21	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	06:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	06:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	06:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	07:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	07:22:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	07:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	08:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	08:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	08:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	08:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/21	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/21	09:00:00.5	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00				
			AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/21	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/21	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/21	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/21	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	09:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/11/21	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/21	09:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/21	10:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/21	10:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/21	10:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/21	10:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2010/11/21	10:30:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/21	10:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/21	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/11/21	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/21	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/21	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/21	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	12:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/11/21	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/21	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	14:10:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	14:10:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	14:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	14:13:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/21	14:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	14:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	15:42:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	15:42:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	15:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	15:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	16:05:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/21	16:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	17:18:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	17:18:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	17:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	17:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	17:42:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/21	17:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	17:59:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	17:59:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/21	17:59:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/11/21	17:59:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/11/21	17:59:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/11/21	18:02:26.0	XRT_ARS_DIS_447_OG [0x1bf]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/21	18:02:28.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/21	18:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	18:09:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	18:09:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/21	18:09:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/11/21	18:09:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/21	18:09:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/21	18:09:50.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/21	18:12:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	18:12:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/11/21	18:12:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/21	18:12:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	18:55:30.0	XRT_CTRL_MANU_408_OG [0x198]			

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2010/11/21	18:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	18:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	18:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	19:19:00.0	XRT_Custom_418_OG [0x1a2]				
2010/11/21	19:20:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	20:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	20:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	20:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	20:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	20:56:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/21	20:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	22:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	22:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	22:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	22:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	22:32:00.0	XRT_Custom_418_OG [0x1a2]				
2010/11/21	22:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/21	23:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/21	23:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/21	23:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/21	23:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/21	23:58:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/21	23:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	01:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	01:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	01:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	01:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	01:30:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/22	01:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	02:46:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	02:46:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	02:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	02:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	03:07:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/22	03:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	04:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	04:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	04:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	04:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	04:44:30.0	XRT_Custom_418_OG [0x1a2]				
2010/11/22	04:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	05:28:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	05:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/22	05:29:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2010/11/22	05:29:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/11/22	05:29:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/11/22	05:31:56.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/22	05:31:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/22	05:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]				

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2010/11/22	05:38:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	05:39:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/22	05:39:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2010/11/22	05:39:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/22	05:39:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/22	05:41:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/22	05:41:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	05:41:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/11/22	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/22	05:56:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	05:56:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	05:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	05:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	06:22:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	06:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/11/22	07:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0
2010/11/22	07:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	07:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	07:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	07:59:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	08:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0
2010/11/22	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	09:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/22	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	AOCU_NM	5	02-76	03 00 00 00 00
2010/11/22	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/22	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/22	09:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/22	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	09:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/11/22	09:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/22	09:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	09:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	09:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	09:35:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	09:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_418_OG [0x1a2]			
2010/11/22	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0
2010/11/22	12:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/22	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	AOCU_NM	5	02-76	00 e0 ee ff 86
2010/11/22	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/22	12:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/22	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/22	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	14:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2010/11/22	14:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	14:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/22	14:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/22	14:54:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/22	14:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/22	15:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	15:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/11/22	16:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00	00
2010/11/22	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/22	16:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/22	16:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/22	16:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	16:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/11/22	16:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/22	16:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/22	16:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	16:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	16:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/22	16:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/22	16:42:00.0	XRT_Custom_418_OG [0x1a2]								
2010/11/22	16:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/22	17:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	17:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	17:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/22	17:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/22	18:18:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	18:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/11/22	18:18:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2010/11/22	18:18:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/22	18:18:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/22	18:21:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/22	18:21:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2010/11/22	18:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/22	18:28:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	18:28:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/11/22	18:28:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00	00
2010/11/22	18:28:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/22	18:28:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/22	18:28:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/22	18:31:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	18:31:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/11/22	18:31:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/22	18:31:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/22	19:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/22	19:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/22	19:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/22	19:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2010/11/22	19:55:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/22	19:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	21:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	21:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	21:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	21:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	21:33:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/22	21:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/22	22:47:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/22	22:47:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/22	22:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/22	22:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/22	23:08:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/22	23:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	00:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	00:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	00:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	00:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	00:32:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/23	00:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	01:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	01:48:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	01:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	01:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	02:07:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/23	02:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	03:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	03:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	03:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	03:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	03:44:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/23	03:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	04:53:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	04:53:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	04:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	04:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	05:21:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/23	05:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	06:04:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	06:04:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/11/23	06:04:30.0	AOCs_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/11/23	06:04:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/11/23	06:04:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/11/23	06:07:26.0	XRT_ARS_DIS_447_OG [0x1bf]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/23	06:07:28.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/23	06:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	06:14:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	06:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/23	06:14:30.0	AOCs_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/11/23	06:14:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

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2010/11/23	06:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/23	06:14:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/23	06:17:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	06:17:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2010/11/23	06:17:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/11/23	06:17:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	06:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	06:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	06:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/23	06:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/23	06:59:00.0	XRT_Custom_418_OG [0x1a2]						
2010/11/23	07:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	08:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	08:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	08:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/23	08:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/23	08:36:00.0	XRT_Custom_418_OG [0x1a2]						
2010/11/23	08:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2010/11/23	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00	
2010/11/23	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/23	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/23	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/23	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	09:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08	
2010/11/23	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/11/23	09:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	09:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	10:07:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	