

# XRT Timeline to be uploaded on 2010/11/23

Period: 2010/11/23 09:57:00 - 2010/11/27 10:02:00

\* \* \* \* \*

**Normal mode**

\* \* \* \* \*

<b>XOB #183A: QS, HOP 176, Ti/poly, 1x1, 384x384, 1min cadence, AEC0, Q95</b>												
Term	Pointing (x, y)		Comment									
11/23 10:10:30 - 11/23 11:59:54	Fixed (	0.0, 0.0)	# OP start + 10min : HOP 176									
11/24 09:13:00 - 11/24 11:59:54	Fixed (	0.0, 0.0)	HOP176 DC fixed pointing									
11/25 09:03:00 - 11/25 09:30:00	Fixed (	0.0, 0.0)	HOP176 cont.									
<b>PROG= 18 Inf.-time(s)</b>												
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 25	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	3-time(s)	2.0sec										
└─ Seqn= 57	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 54	20-time(s)	60.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad</b>												
Term	Pointing (x, y)		Comment									
11/23 12:04:30 - 11/23 17:59:54	Track (	190.3, 351.6)	AR11127 obs.									
11/23 18:13:00 - 11/24 06:08:24	Track (	241.1, 352.5)	# AR11127 cont.									
11/24 06:21:30 - 11/24 08:52:00	Track (	338.4, 354.4)	# AR11127 cont.									
11/24 18:10:00 - 11/25 05:41:54	Track (	428.8, 356.6)	# AR11127									
11/25 05:55:00 - 11/25 08:59:54	Track (	513.3, 358.8)	# AR11127 cont.									
<b>PROG= 07 Inf.-time(s)</b>												
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 19	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95	4-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 52	35-time(s)	120.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)</b>												
Term	Pointing (x, y)		Comment									
11/23 18:03:00 - 11/23 18:09:54	Fixed (	0.0, 0.0)	synoptic									
11/24 06:11:30 - 11/24 06:18:24	Fixed (	0.0, 0.0)	synoptic, shifted 8.5 min									
11/24 18:00:00 - 11/24 18:06:54	Fixed (	0.0, 0.0)	synoptic, shifted -3.0 min									
11/25 05:45:00 - 11/25 05:51:54	Fixed (	0.0, 0.0)	synoptic, shifted -18.0 min									
<b>PROG= 03 1-time(s)</b>												
Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 7	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

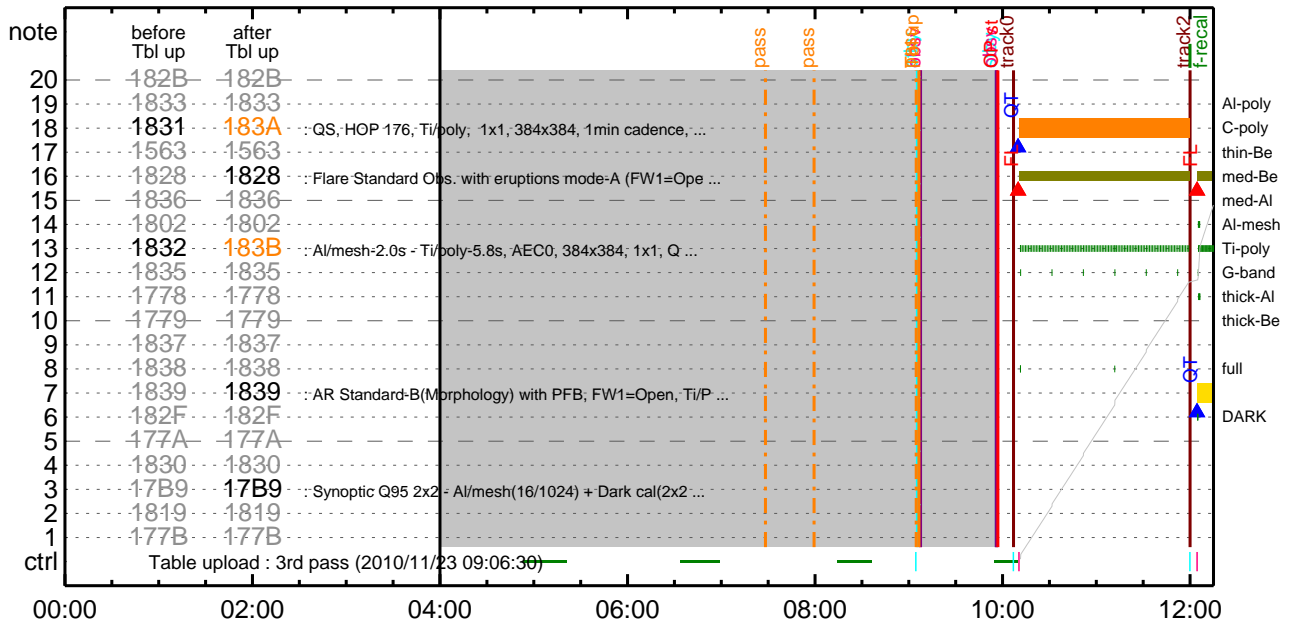
<b>XOB #183B: Al/mesh-2.0s - Ti/poly-5.8s, AEC0, 384x384, 1x1, Q95</b>												
Term	Pointing (x, y)		Comment									
11/24 12:03:00 - 11/24 17:31:30	Track (	-147.9, -516.0)	TOO HOP163 CH study									
<b>PROG= 13 Inf.-time(s)</b>												
Subr= 1	1-time(s)	75.0sec										
└─ Seqn= 57	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec



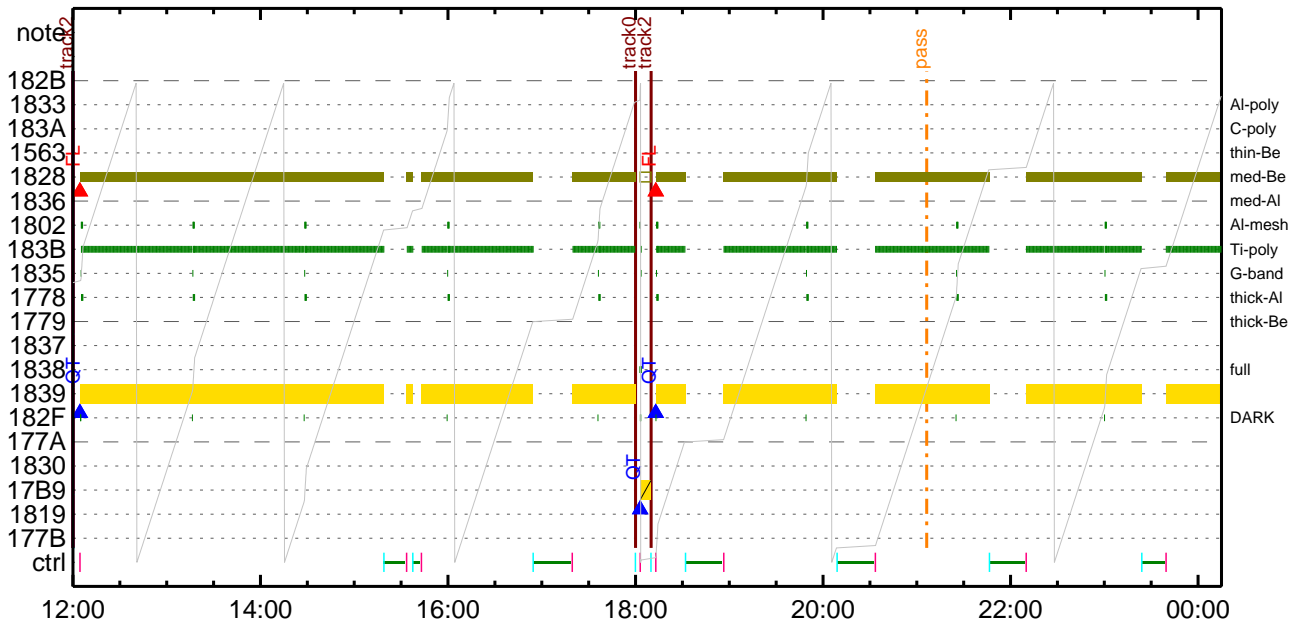
11/23 10:07:16 - 11/23 18:00:16 Fixed ( 0.0, 0.0) # OP start + 10min : HOP 176  
 11/23 18:10:16 - 11/24 06:08:46 Track ( 241.1, 352.5) @ 11/23 18:10:00 # AR11127 cont.  
 11/24 06:18:46 - 11/24 17:57:16 Track ( 338.4, 354.4) @ 11/24 06:18:30 # AR11127 cont.  
 11/24 18:07:16 - 11/25 05:42:16 Track ( 428.8, 356.6) @ 11/24 18:07:00 # AR11127  
 11/25 05:52:16 - 11/27 10:02:00 Track ( 513.3, 358.8) @ 11/25 05:52:00 # AR11127 cont.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

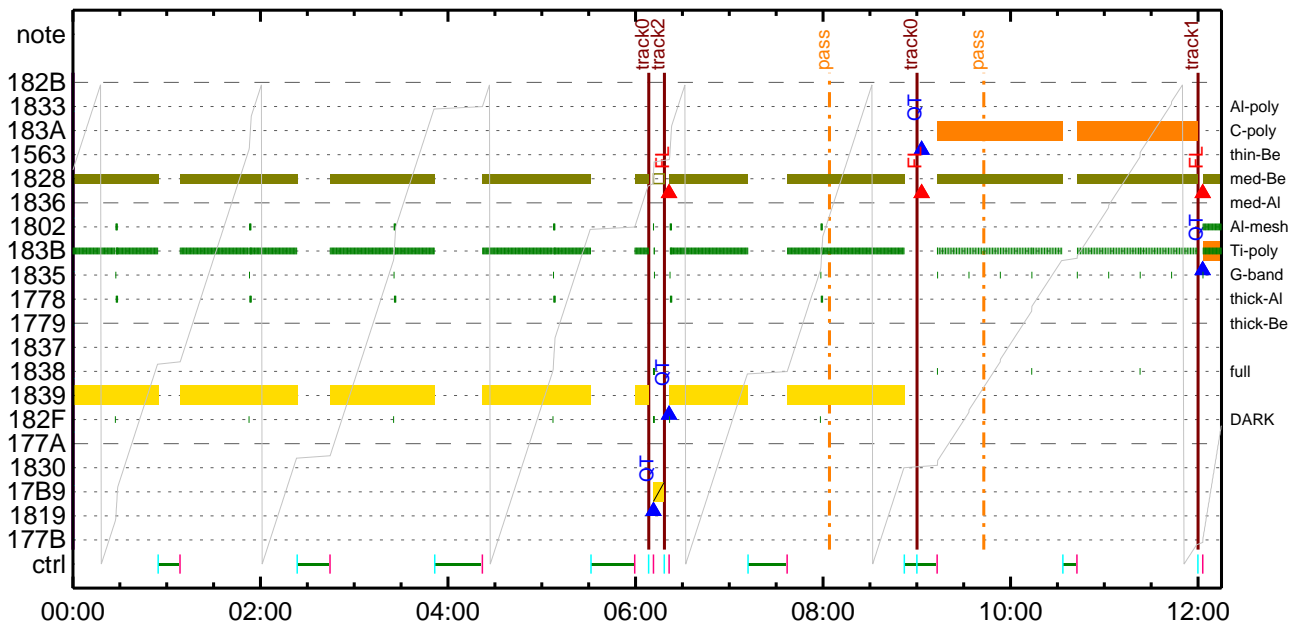
### CMDI #0625 2010/11/23



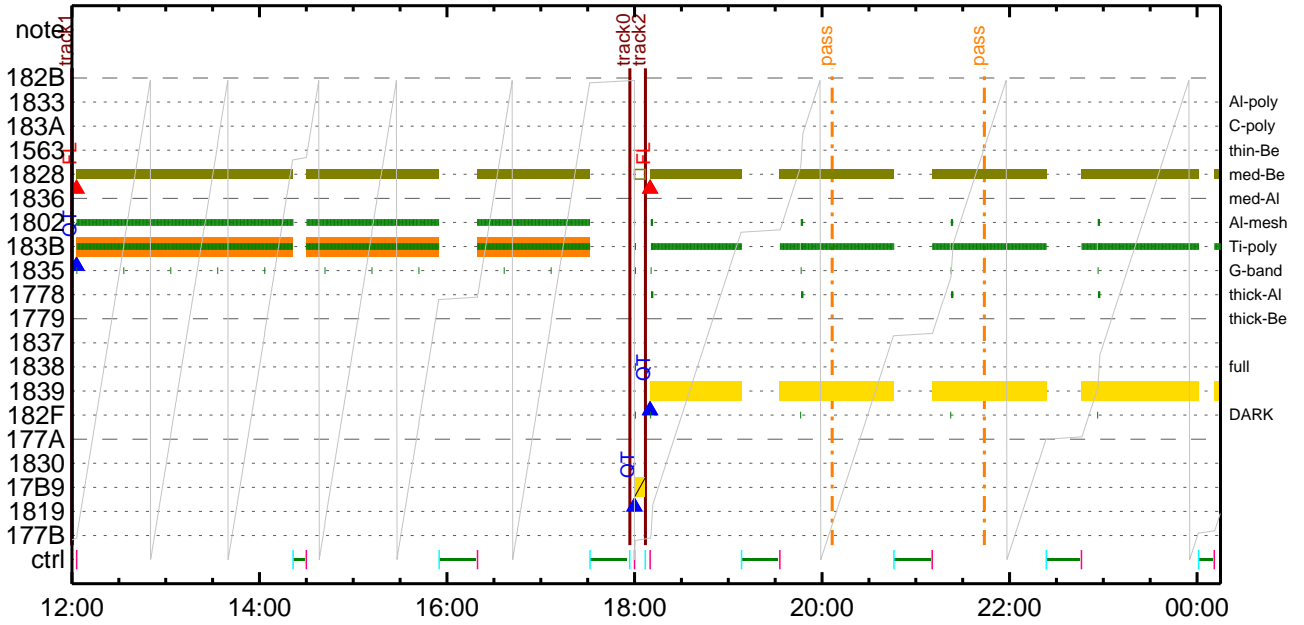
### CMDI #0625 2010/11/23



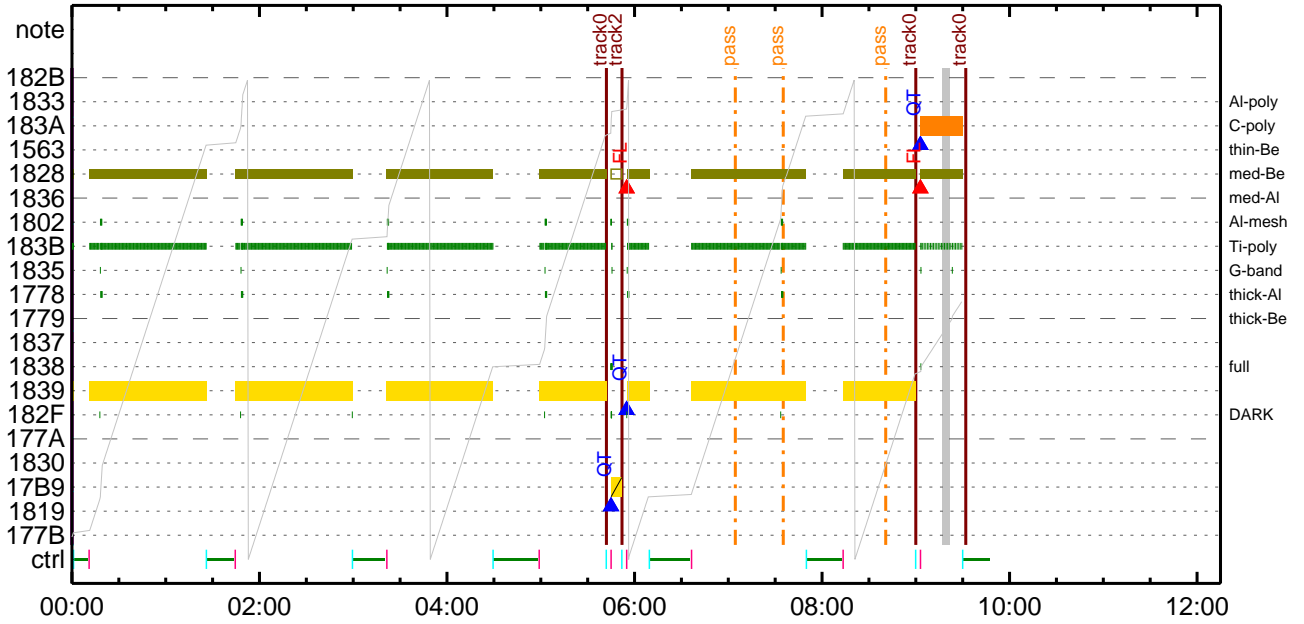
### CMDI #0625 2010/11/24



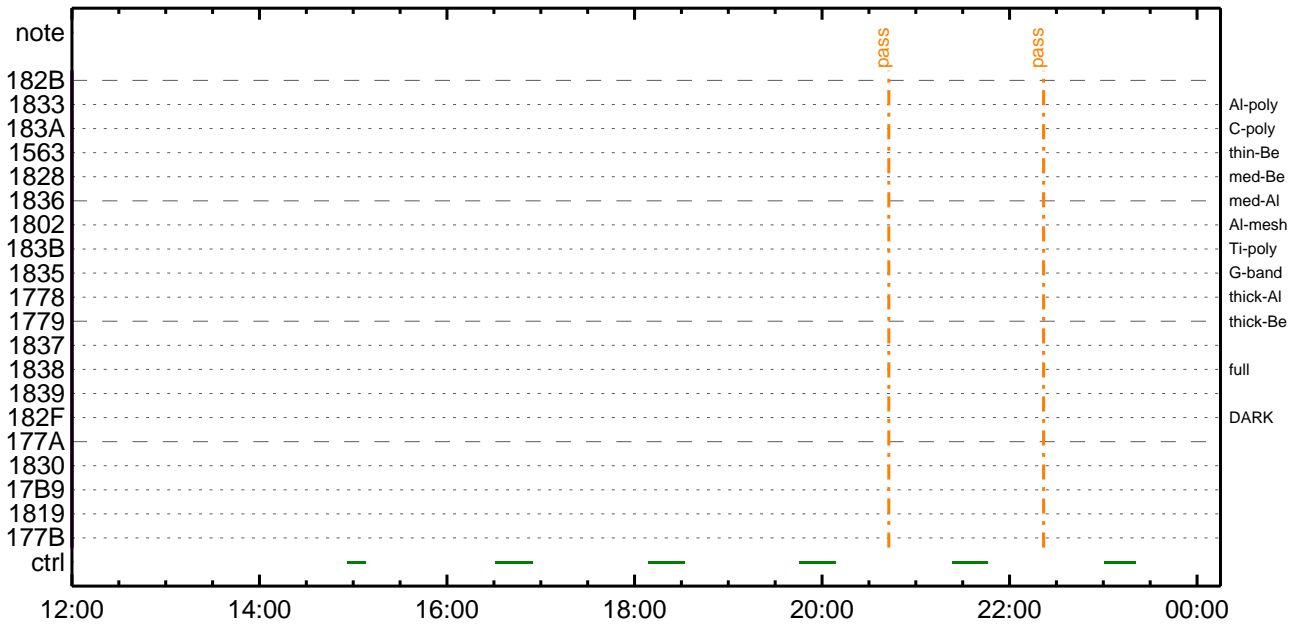
CMDI #0625 2010/11/24



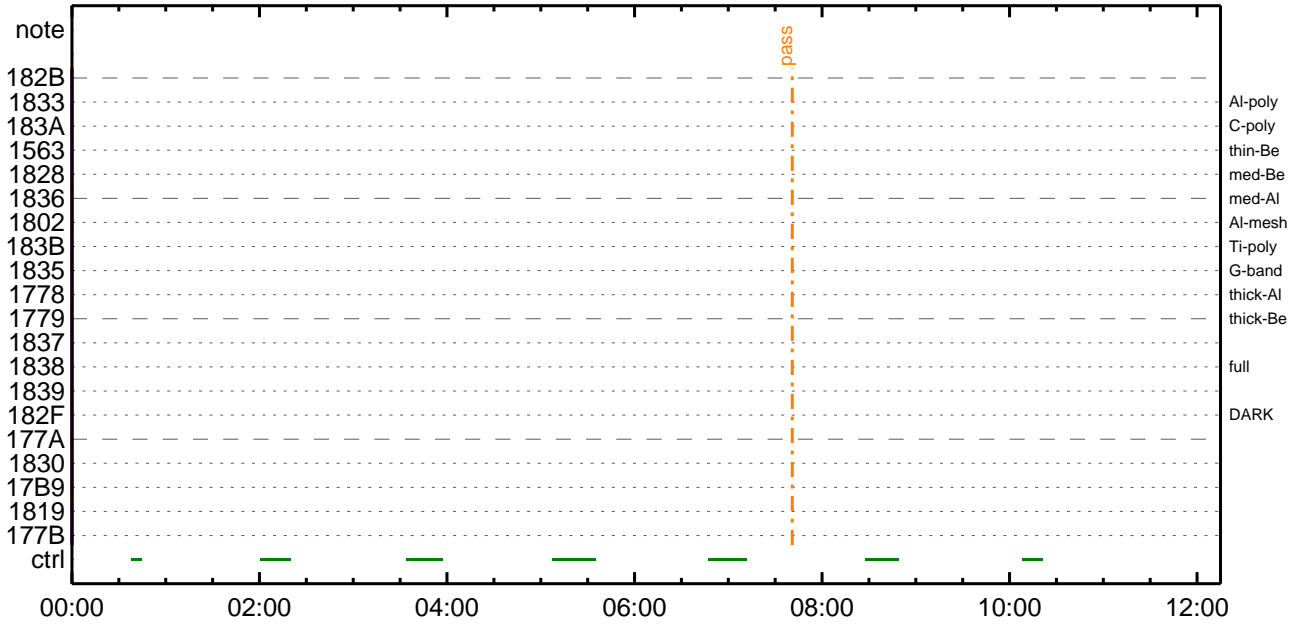
CMDI #0625 2010/11/25



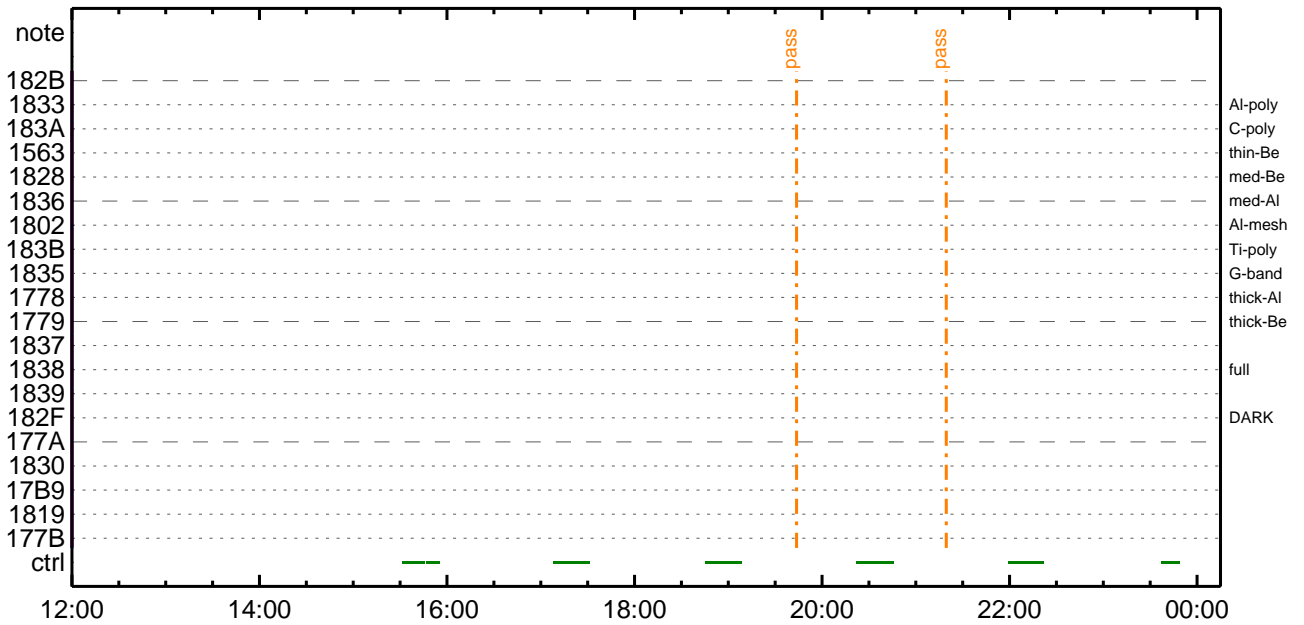
CMDI #0625 2010/11/25



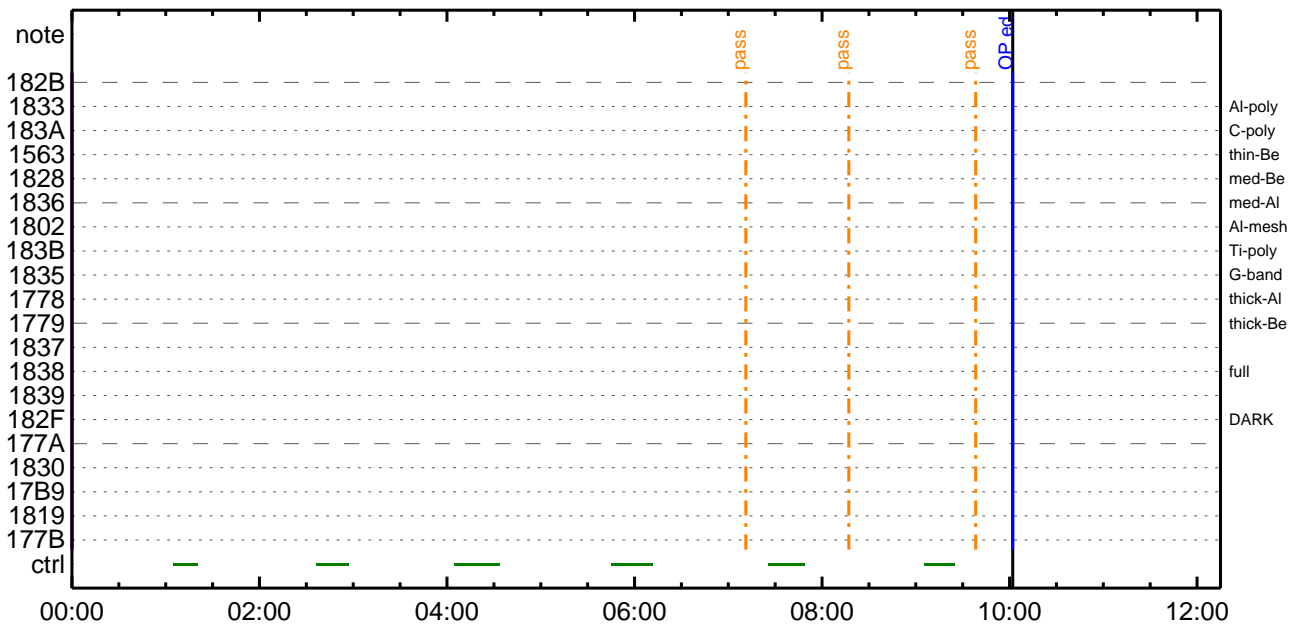
CMDI #0625 2010/11/26



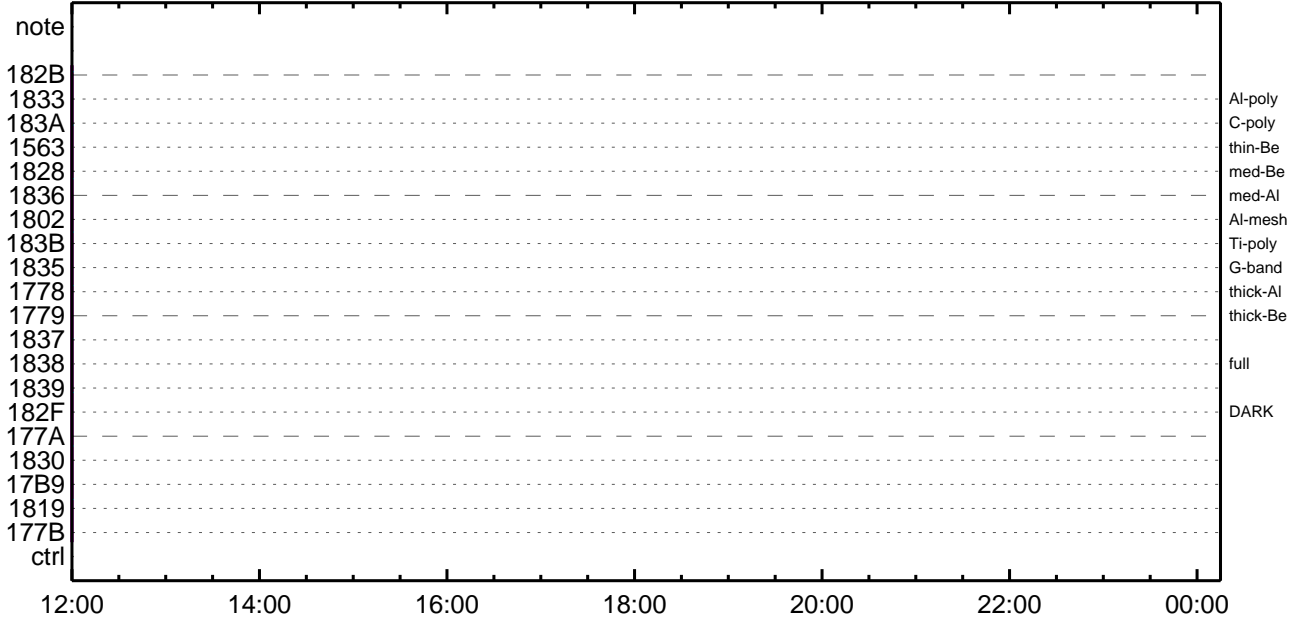
CMDI #0625 2010/11/26



CMDI #0625 2010/11/27



CMDI #0625 2010/11/27







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-724:OP
0104 ( )
0105 S. OG og-724:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓæòÇ¼ª°"òè¼i¹çòçðâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°iYNY¹²ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½E²òðÁDî¿(UT)
0183 +. TI 2010-11-23 09:52:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-11-23 09:52:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-11-23 09:52:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```







```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 . C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C.
0143 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0144 C.
0145 +. DC 07-F0 MDP_XRT_MODE_OBSV
0146 BC (c2)
0147 +. TI 2010-11-23 09:56:02.0
0148 DC 07-F0 MDP_XRT_MODE_OBSV
0149 BC (c2)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C. ***** XRT END *****
0153 . C. *****
0154 C. SOT table upload
0155 C. *****
0156 . C. < Stop SP table >
0157 +. DC 07-F0 MDP_SP_CTRL_MANU
0158 BC (61)
0159 C. -----
0160 C. MDP_SP_CTRL_MODE = MANU [ ]
0161 C. -----
0162 C.
0163 . C. <Upload SP Observation Table>
0164 . S. RAM ram-281:MDP_OBS_S
0165 ( )
0166 C.
0167 . C. < Dump RAMID=MDP_OBS_S >
0168 +. DC 07-F0 MDP_DUMP_SPTBL
0169 BC (83 07 00 00 00 38 b8)
0170 C. -----
0171 C. MDP_OBS_S verify = OK/NG [ ]
0172 C. -----
0173 C.
0174 . C. < Upload DPL table >
0175 +. DC 07-F0 MDP_FG_CTRL_MANU
0176 BC (51)
0177 . C. -----
0178 C. MDP_FG_CTRL_MODE = MANU [ ]
0179 C. -----
0180 C.
0181 C. ¥çŸÃŸ×Ÿí;¼ŸÉ°îÁ°αĚSTS_CHKαðOFFαĚα¹αĚ
0182 C.
0183 . S. RAM ram-271:MDP_DPL
0184 ( )
0185 C.
0186 . C. < Dump RAMID=MDP_DPL >
0187 +. DC 07-F0 MDP_DUMP_FGTBL
0188 BC (82 07 00 38 b8 00 40)
0189 C. -----
0190 C. MDP_DPL verify = OK [ ]
0191 C. -----
0192 C.
0193 C. STS_CHKαðONαĚα¹αĚ

```

```
0194 C.
0195 . C. < Update MDP DSC PAR1 >
0196 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0197 BC (4c)
0198 C. MDP_CMD_CODE = F04C0700[ ]
0199 C. MDP_CMD_CNT (count-up 1) [ ]
0200 C. -----
0201 C.
0202 . C.
0203 C. *****
0204 C. SOT TI command set
0205 C. *****
0206 C. Execute, after the success of TBL upload.
0207 +. TI 2010-11-23 09:56:18.0
0208 DC 07-F0 MDP_SOT_MODE_OBSV
0209 BC (40)
0210 . C. -----
0211 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0212 C. -----
0213 C.
0214 C.
0215 . C. ***** MDP `úÃîñî»ò¼ÝñÊÂðñ¹ñèDCBC•x²è *****
0216 C. (¼Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ¼ñ¼Á»Ûñ¹ñè)
0217 . S. DC-BC dcbc-402:DCBC
0218 (MDP_known_event)
0219 C.
0220 C.
0221 . C. ***** ÝDÝ¹•Ï Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0222 . S. DC-BC dcbc-153:DCBC
0223 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0224 C.
0225 C.
0226 . C. ¡ãLOSÝÁÝ§ÝÃÝ¬¼Á»Ûñ¹ñè
0227 C.
0228 . C. ***** LOS *****
0229 C.
```

Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 1/6

\*\*\* OP Sequence for XRT \*\*\*

2010/11/23	10:06:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	10:06:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/23	10:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/11/23	10:07:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/23	10:07:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/23	10:07:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/23	10:09:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/23	10:09:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	10:09:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2010/11/23	10:09:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/11/23	10:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	11:59:56.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2010/11/23	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2010/11/23	12:03:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/23	12:04:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/23	12:04:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/23	12:04:20.0	XRT_ARS_DIS_407_OG [0x197]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/23	12:04:24.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	12:04:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2010/11/23	12:04:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/11/23	12:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	15:19:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	15:19:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	15:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/23	15:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/23	15:32:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/23	15:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	15:37:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	15:37:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	15:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/23	15:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/23	15:42:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/23	15:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	16:54:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	16:54:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/23	16:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/23	16:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/23	17:18:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/23	17:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/23	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/23	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/11/23	18:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/11/23	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/11/23	18:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/11/23	18:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/23	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/11/23	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					

Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 2/6

2010/11/23	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/23	18:10:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00
2010/11/23	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/23	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/23	18:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/23	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	18:12:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/11/23	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/23	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	18:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	18:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	18:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	18:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	18:55:30.5	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/23	18:56:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	20:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	20:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	20:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	20:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	20:32:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/23	20:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	21:46:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	21:46:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	21:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	21:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	22:09:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/23	22:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/23	23:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/23	23:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/23	23:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/23	23:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/23	23:38:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/23	23:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/24	00:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/24	00:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/24	00:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/24	00:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/24	01:07:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/24	01:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/24	02:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/24	02:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/24	02:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/24	02:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/24	02:43:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2010/11/24	02:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/24	03:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/24	03:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/24	03:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8



Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 3/6

2010/11/24	03:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/24	04:21:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/24	04:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	05:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	05:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/24	05:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/24	05:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/24	05:58:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/24	05:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	06:08:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	06:08:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/24	06:08:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/24	06:08:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/24	06:08:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/24	06:11:26.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/24	06:11:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/24	06:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	06:18:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	06:18:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/24	06:18:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/11/24	06:18:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/24	06:18:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/24	06:18:50.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/24	06:21:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/24	06:21:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2010/11/24	06:21:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/24	06:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	07:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	07:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/24	07:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/24	07:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/24	07:36:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/24	07:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	08:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	08:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/24	08:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/24	08:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/24	09:00:03.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/24	09:00:05.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/24	09:00:09.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/24	09:00:25.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/24	09:00:27.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/24	09:00:29.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/24	09:03:03.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/24	09:03:05.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2010/11/24	09:03:07.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/24	09:12:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/24	09:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/24	10:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							

Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 4/6

2010/11/24	10:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	10:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/24	10:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/24	10:41:30.0	XRT_Custom_418_OG [0x1a2]						
2010/11/24	10:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/24	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/24	12:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00		
2010/11/24	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/24	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/24	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/24	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	12:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2010/11/24	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/11/24	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/24	14:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	14:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	14:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/24	14:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/24	14:29:00.0	XRT_Custom_418_OG [0x1a2]						
2010/11/24	14:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/24	15:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	15:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	15:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/24	15:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/24	16:18:30.0	XRT_Custom_418_OG [0x1a2]						
2010/11/24	16:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/24	17:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	17:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	17:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/11/24	17:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/11/24	17:56:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	17:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/11/24	17:57:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2010/11/24	17:57:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/11/24	17:57:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/11/24	17:59:56.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/24	17:59:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/11/24	18:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/11/24	18:06:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/11/24	18:06:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/11/24	18:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00		
2010/11/24	18:07:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/11/24	18:07:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/11/24	18:07:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/11/24	18:09:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/11/24	18:09:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2010/11/24	18:09:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]						

Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 5/6

2010/11/24	18:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/24	19:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/24	19:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/24	19:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/24	19:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/24	19:32:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/24	19:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/24	20:46:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/24	20:46:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/24	20:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/24	20:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/24	21:09:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/24	21:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/24	22:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/24	22:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/24	22:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/24	22:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/24	22:45:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/24	22:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	00:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	00:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	00:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	00:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	00:10:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	00:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	01:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	01:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	01:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	01:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	01:43:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	01:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	02:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	02:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	02:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	03:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	03:20:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	03:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	04:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	04:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	04:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	04:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	04:58:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	04:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	05:41:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	05:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/11/25	05:42:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00	00
2010/11/25	05:42:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/11/25	05:42:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/11/25	05:44:56.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/11/25	05:44:58.0	XRT_QT_PROG_SET_410_OG [0x19a]					

Nov 23, 10 12:55

## XRT\_OGLIST\_0625.chk

Page 6/6

2010/11/25	05:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	05:51:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	05:51:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/11/25	05:52:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/11/25	05:52:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/11/25	05:52:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/11/25	05:52:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/11/25	05:54:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	05:54:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2010/11/25	05:54:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2010/11/25	05:55:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	06:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	06:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	06:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	06:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	06:35:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	06:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	07:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	07:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	07:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/25	07:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/25	08:12:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/25	08:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/11/25	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/11/25	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/11/25	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/11/25	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/11/25	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/25	09:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2010/11/25	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2010/11/25	09:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/25	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/25	09:32:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	