

# XRT Timeline to be uploaded on 2010/11/27

Period: 2010/11/27 10:02:00 - 2010/12/02 10:38:00

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## Normal mode

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<b>XOB #183A: QS, HOP 176, Ti/poly, 1x1, 384x384, 1min cadence, AEC0, Q95</b>													
Term	Pointing (x, y)						Comment						
11/27 10:15:00 - 11/27 11:59:54	Fixed ( -11.0, -15.0)						# OP start + 10min, HOP176 DC fixed						
11/28 09:03:00 - 11/28 11:59:54	Fixed ( -11.0, -15.0)						HOP 176 DC fixed pointing						
11/29 09:03:00 - 11/29 11:59:54	Fixed ( -11.0, -15.0)						HOP 176 DC fixed pointing						
<b>PROG= 18 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 25 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 3-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 54 20-time(s) 60.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad</b>													
Term	Pointing (x, y)						Comment						
11/27 12:03:00 - 11/27 17:44:30	Track ( 806.9, 370.2) @ 11/27 12:00:00						AR 11127 tracking						
11/27 18:21:00 - 11/28 01:59:54	Track ( 828.6, 371.5) @ 11/27 18:18:00						AR 11127 tracking						
11/28 02:03:00 - 11/28 05:55:24	Fixed ( 851.0, 373.0)						AR 11127 at west limb fixed						
11/28 06:08:30 - 11/28 08:59:54	Fixed ( 851.0, 373.0)						AR 11127 at west limb fixed						
11/28 12:03:00 - 11/28 17:57:54	Fixed ( 851.0, 373.0)						AR 11127 at west limb fixed						
<b>PROG= 07 Inf.-time(s)</b>													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 52 35-time(s) 120.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)</b>													
Term	Pointing (x, y)						Comment						
11/27 18:11:00 - 11/27 18:17:54	Fixed ( 0.0, 0.0)						synoptic, shifted 8.0 min						
11/28 05:58:30 - 11/28 06:05:24	Fixed ( 0.0, 0.0)						synoptic, shifted -4.5 min						
11/28 18:01:00 - 11/28 18:07:54	Fixed ( 0.0, 0.0)						synoptic, shifted -2.0 min						
11/29 06:03:00 - 11/29 06:09:54	Fixed ( 0.0, 0.0)						synoptic						
11/29 18:03:30 - 11/29 18:10:24	Fixed ( 0.0, 0.0)						synoptic, shifted 0.5 min						
11/30 05:34:30 - 11/30 05:41:24	Fixed ( 0.0, 0.0)						synoptic, shifted -28.5 min						
<b>PROG= 03 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT</b>												
Term	Pointing (x, y)						Comment					
11/28 18:23:00 - 11/28 20:09:54	Fixed ( 0.0, 945.0)						Co-alignment N limb					
<b>PROG= 11 1-time(s)</b>												
└─ Subr= 1 1-time(s) 360.0sec												

Seqn= 21	24-time(s)	300.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT**

Term	Pointing (x, y)	Comment
11/28 20:25:00 - 11/28 22:09:54	Fixed ( -945.0, 0.0)	Co-alignment E limb

**PROG= 06 1-time(s)**

Subr= 1	1-time(s)	360.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #183C: smallAR, Al/mesh, Ti/poly, G-band, 1x1, FOV 256x256, 36sec cad, with PFB(384x384)**

Term	Pointing (x, y)	Comment
11/28 22:13:00 - 11/29 05:59:54	Track ( -370.8, -282.1) <sup>@ 11/28 22:10:00</sup>	AR 11128 tracking
11/29 06:13:00 - 11/29 08:41:00	Track ( -303.7, -282.0) <sup>@ 11/29 06:10:00</sup>	AR 11128 tracking
11/29 12:03:00 - 11/29 18:00:24	Track ( -253.6, -281.9) <sup>@ 11/29 12:00:00</sup>	AR 11128 tracking

**PROG= 20 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 46	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	256x256 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	256x256 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 50	120-time(s)	2.0sec										
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	256x256 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	256x256 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1827: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 120s cad**

Term	Pointing (x, y)	Comment
11/29 18:15:00 - 11/30 05:31:24	Track ( -199.6, -281.7) <sup>@ 11/29 18:10:30</sup>	AR 11128 tracking
11/30 05:44:30 - 11/30 09:12:00	Track ( -97.3, -281.0) <sup>@ 11/30 05:41:30</sup>	AR 11128 tracking

**PROG= 01 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 20	4-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 15	20-time(s)	2.0sec										
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
11/27 10:15:00 - 11/27 11:59:54	Fixed ( -11.0, -15.0)	# OP start + 10min, HOP176 DC fixed
11/27 12:03:00 - 11/27 17:44:30	Track ( 806.9, 370.2) <sup>@ 11/27 12:00:00</sup>	AR 11127 tracking
11/27 18:21:00 - 11/28 01:59:54	Track ( 828.6, 371.5) <sup>@ 11/27 18:18:00</sup>	AR 11127 tracking
11/28 02:03:00 - 11/28 05:55:24	Fixed ( 851.0, 373.0)	AR 11127 at west limb fixed
11/28 06:08:30 - 11/28 08:59:54	Fixed ( 851.0, 373.0)	AR 11127 at west limb fixed
11/28 09:03:00 - 11/28 11:59:54	Fixed ( -11.0, -15.0)	HOP 176 DC fixed pointing
11/28 12:03:00 - 11/28 17:57:54	Fixed ( 851.0, 373.0)	AR 11127 at west limb fixed
11/28 22:13:00 - 11/29 05:59:54	Track ( -370.8, -282.1) <sup>@ 11/28 22:10:00</sup>	AR 11128 tracking
11/29 06:13:00 - 11/29 08:41:00	Track ( -303.7, -282.0) <sup>@ 11/29 06:10:00</sup>	AR 11128 tracking
11/29 09:03:00 - 11/29 11:59:54	Fixed ( -11.0, -15.0)	HOP 176 DC fixed pointing
11/29 12:03:00 - 11/29 18:00:24	Track ( -253.6, -281.9) <sup>@ 11/29 12:00:00</sup>	AR 11128 tracking
11/29 18:15:00 - 11/30 05:31:24	Track ( -199.6, -281.7) <sup>@ 11/29 18:10:30</sup>	AR 11128 tracking
11/30 05:44:30 - 11/30 09:12:00	Track ( -97.3, -281.0) <sup>@ 11/30 05:41:30</sup>	AR 11128 tracking

PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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### Active Region Search

\* \* \* \* \*

NOT USED

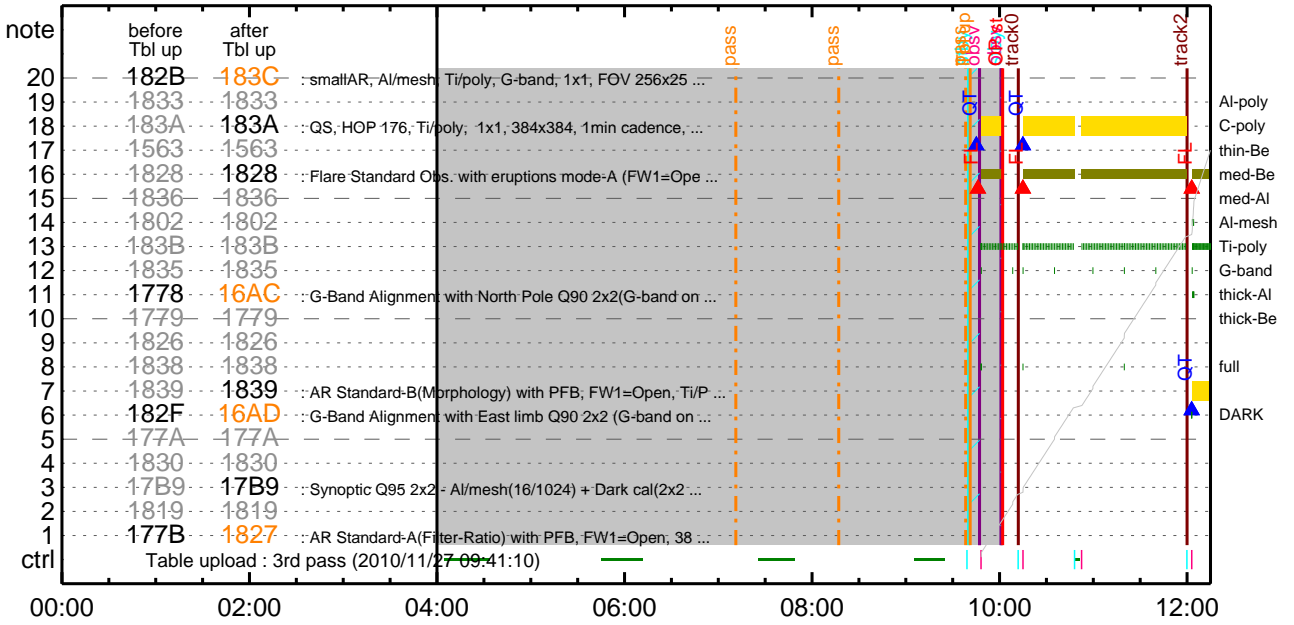
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### Flare Detection

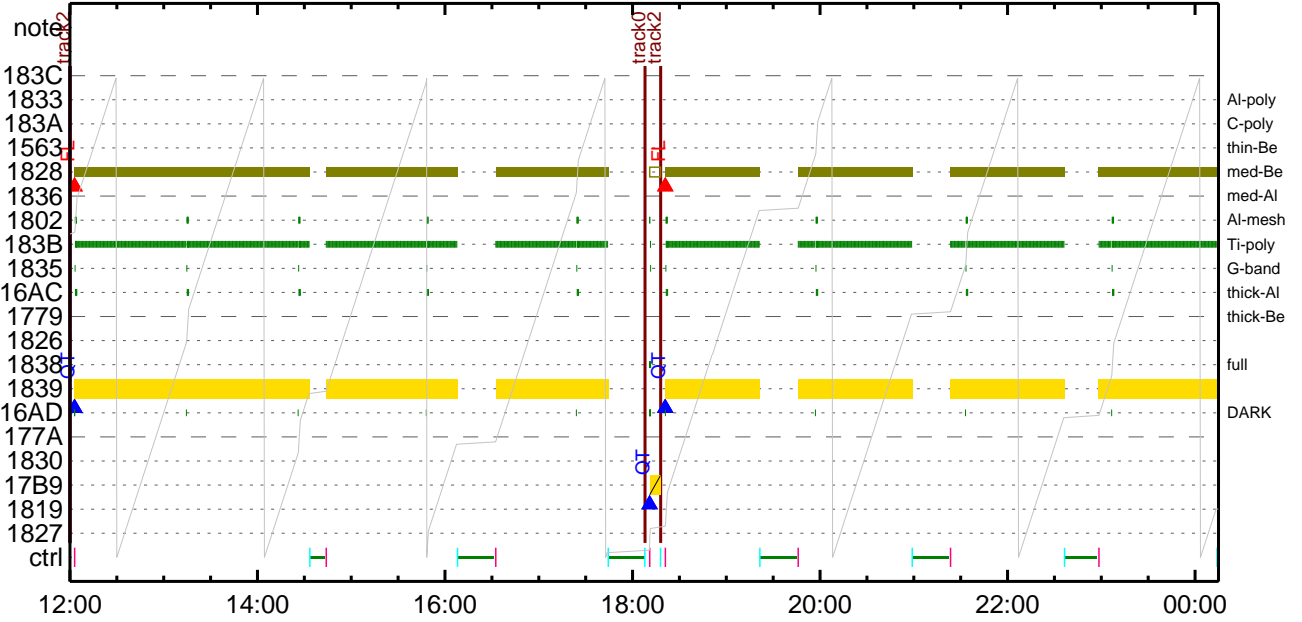
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FLD Patrol												
Term	Pointing (x, y)								Comment			
11/27 18:18:16 - 11/28 05:55:46	Track (	828.6,	371.5)	@ 11/27 18:18:00	AR 11127	tracking						
11/28 06:05:46 - 11/28 17:58:16	Fixed (	851.0,	373.0)		AR 11127	at west limb fixed						
11/28 22:10:16 - 11/29 06:00:16	Track (	-370.8,	-282.1)	@ 11/28 22:10:00	AR 11128	tracking						
11/29 06:10:16 - 11/29 18:00:46	Track (	-303.7,	-282.0)	@ 11/29 06:10:00	AR 11128	tracking						
11/29 18:14:46 - 11/30 05:31:46	Track (	-199.6,	-281.7)	@ 11/29 18:10:30	AR 11128	tracking						
11/30 05:41:46 - 12/02 10:38:00	Track (	-97.3,	-281.0)	@ 11/30 05:41:30	AR 11128	tracking						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

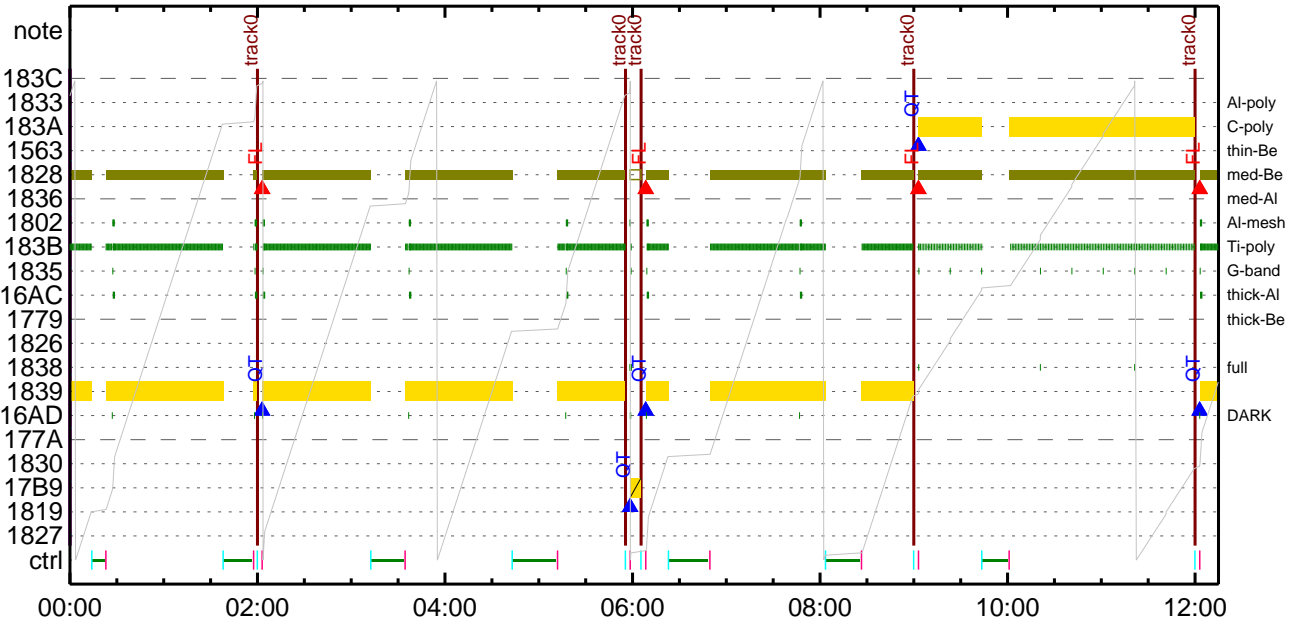
### CMDI #0635 2010/11/27



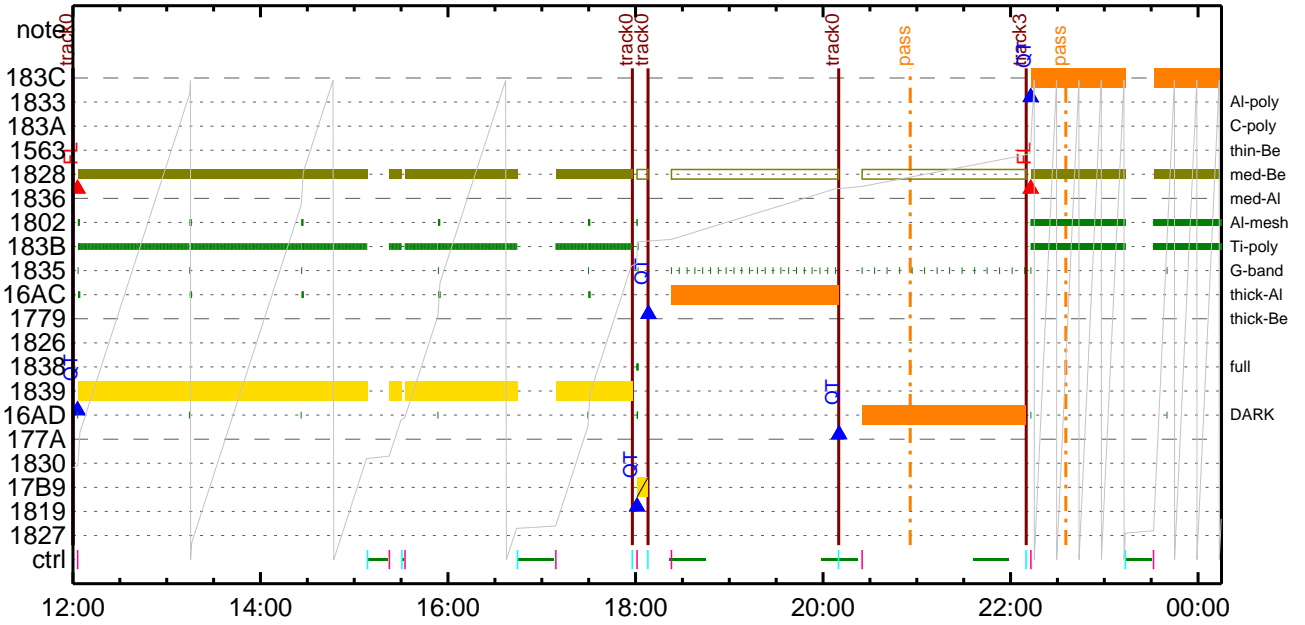
### CMDI #0635 2010/11/27



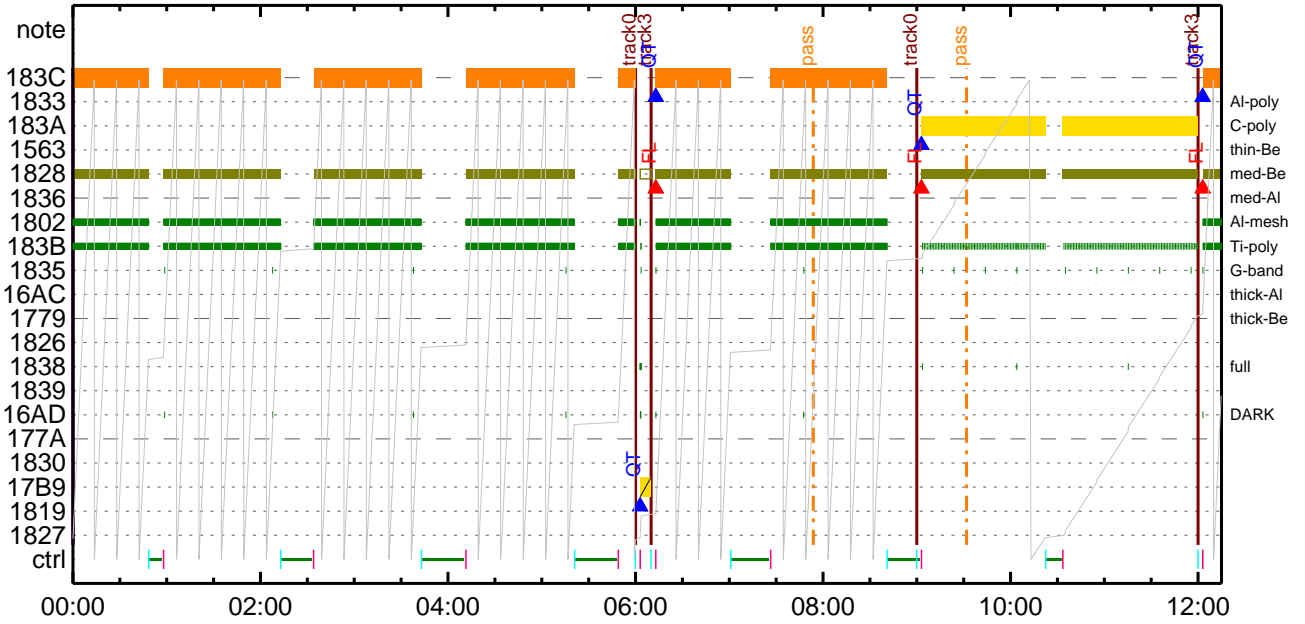
### CMDI #0635 2010/11/28



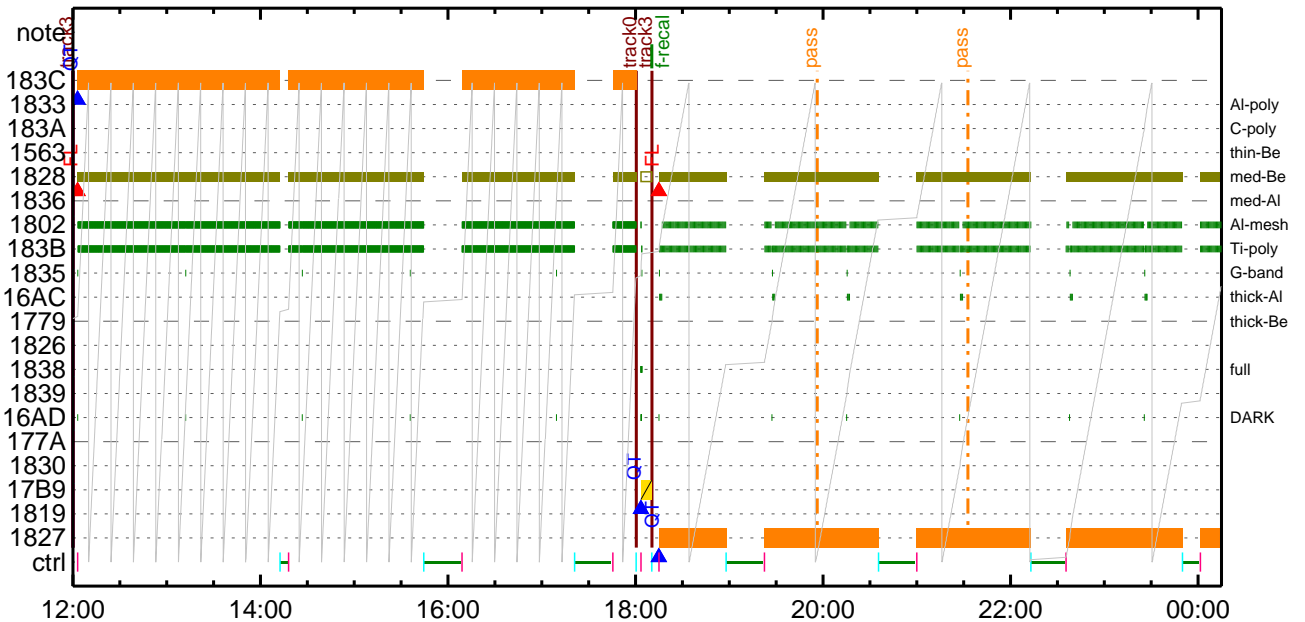
CMDI #0635 2010/11/28



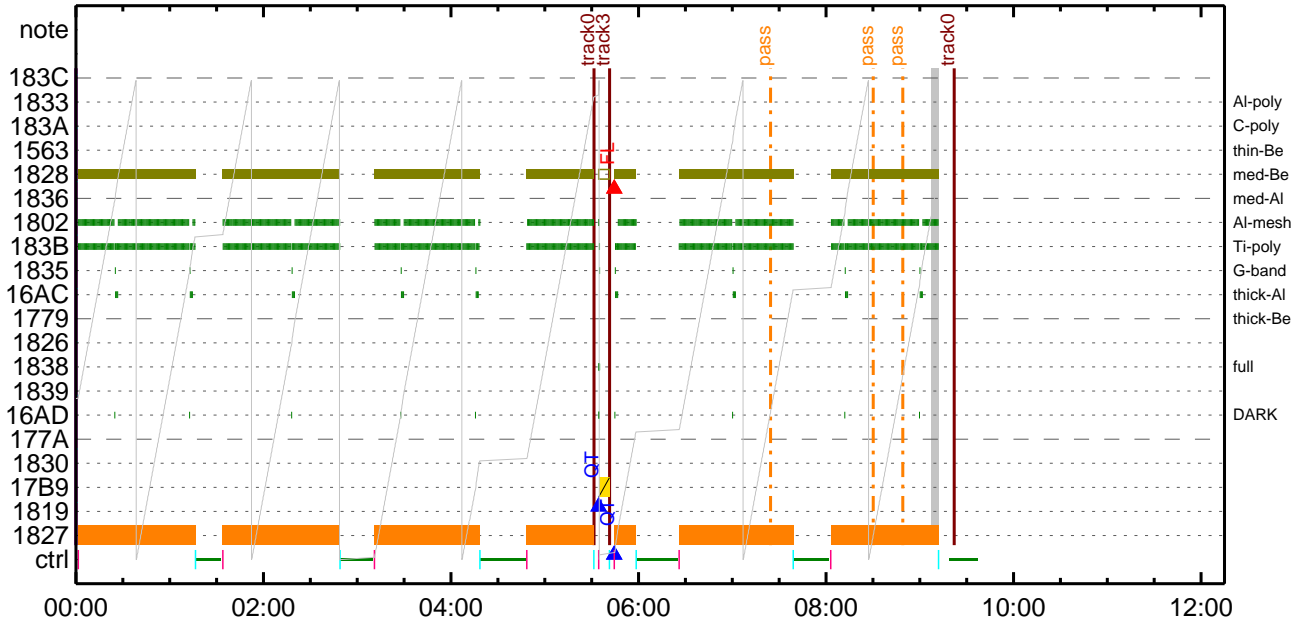
CMDI #0635 2010/11/29



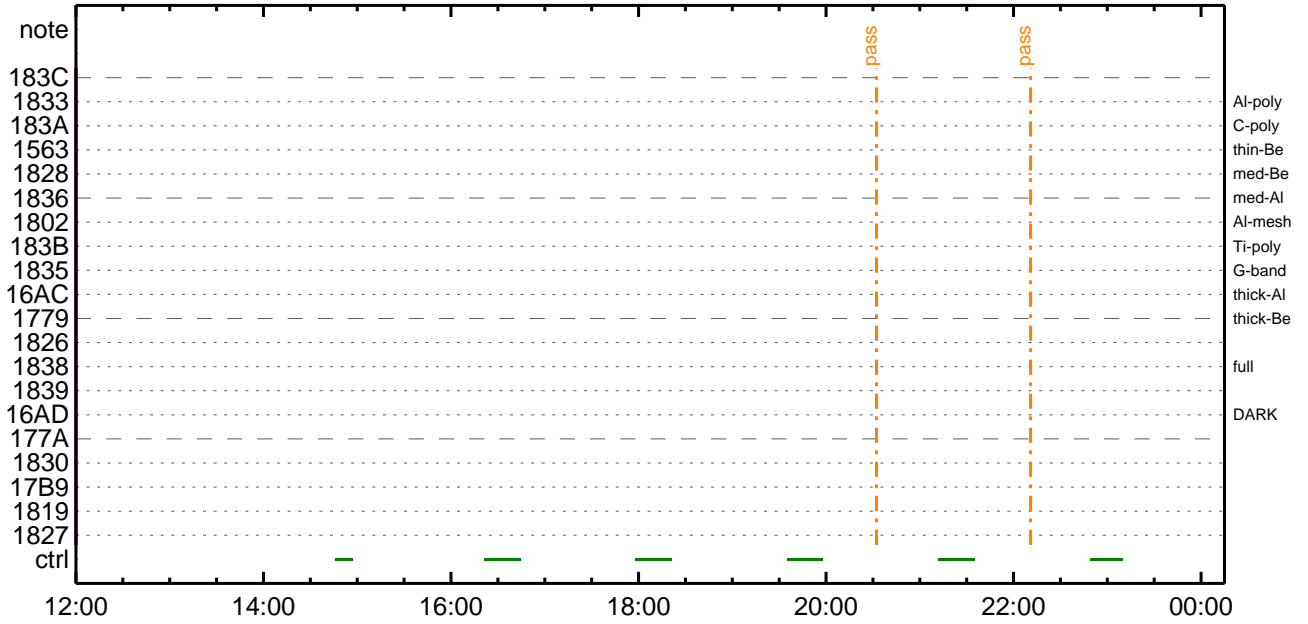
CMDI #0635 2010/11/29



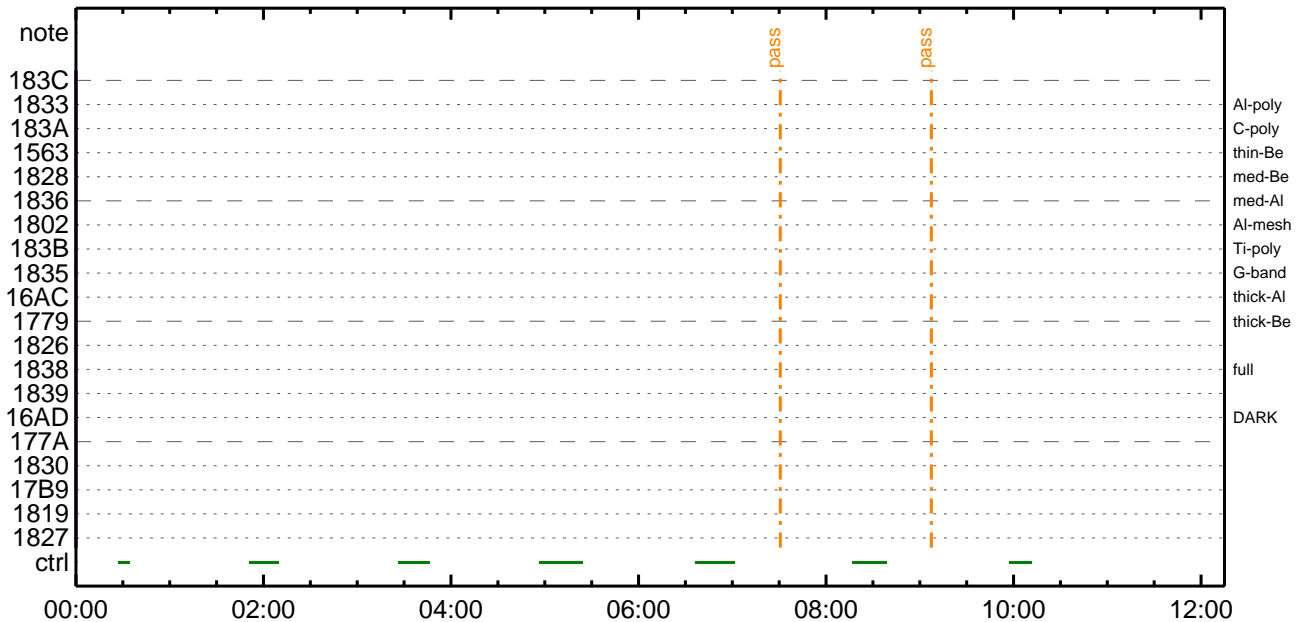
### CMDI #0635 2010/11/30



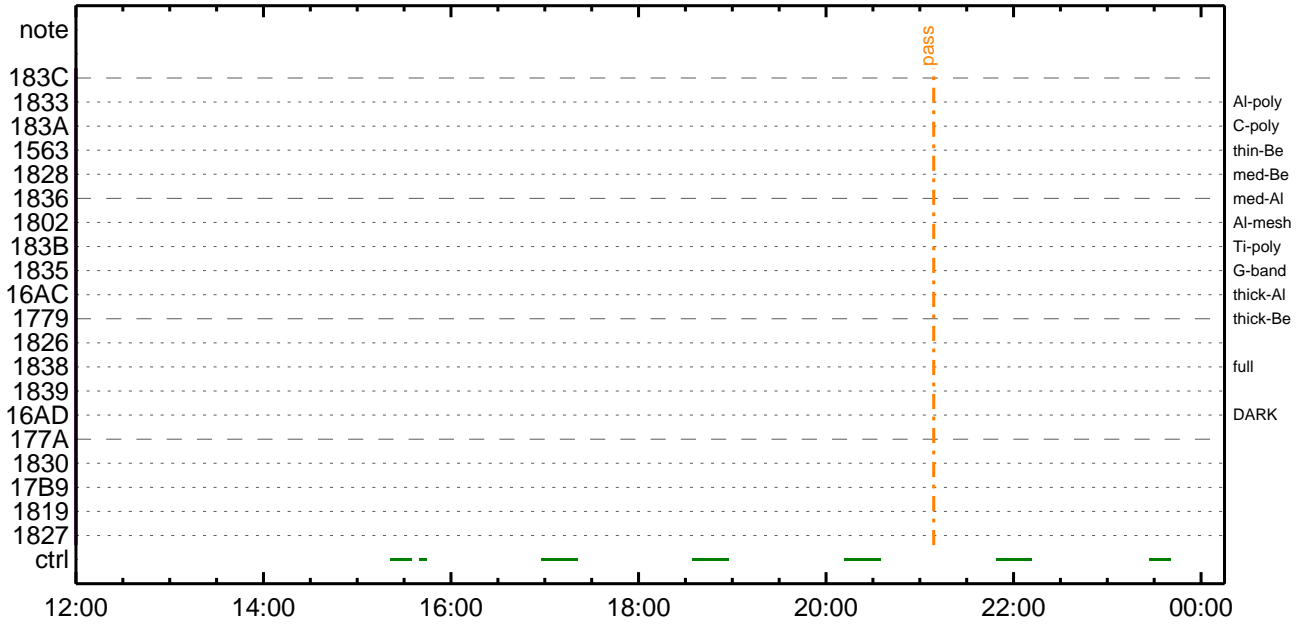
### CMDI #0635 2010/11/30



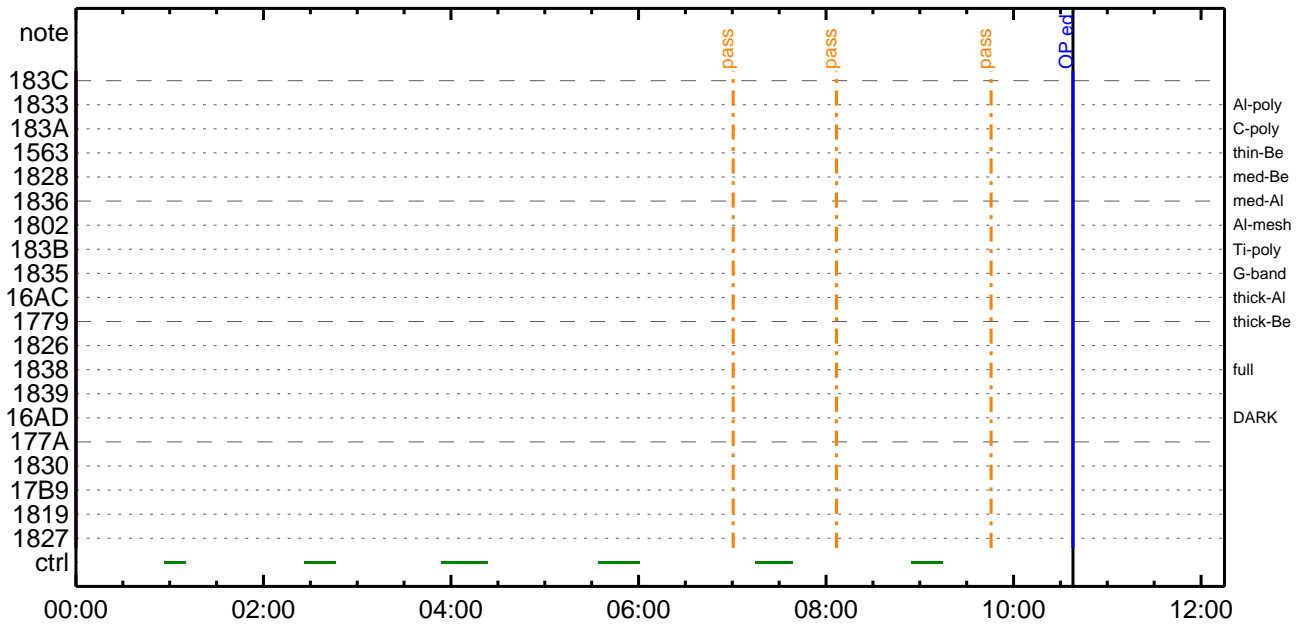
### CMDI #0635 2010/12/01



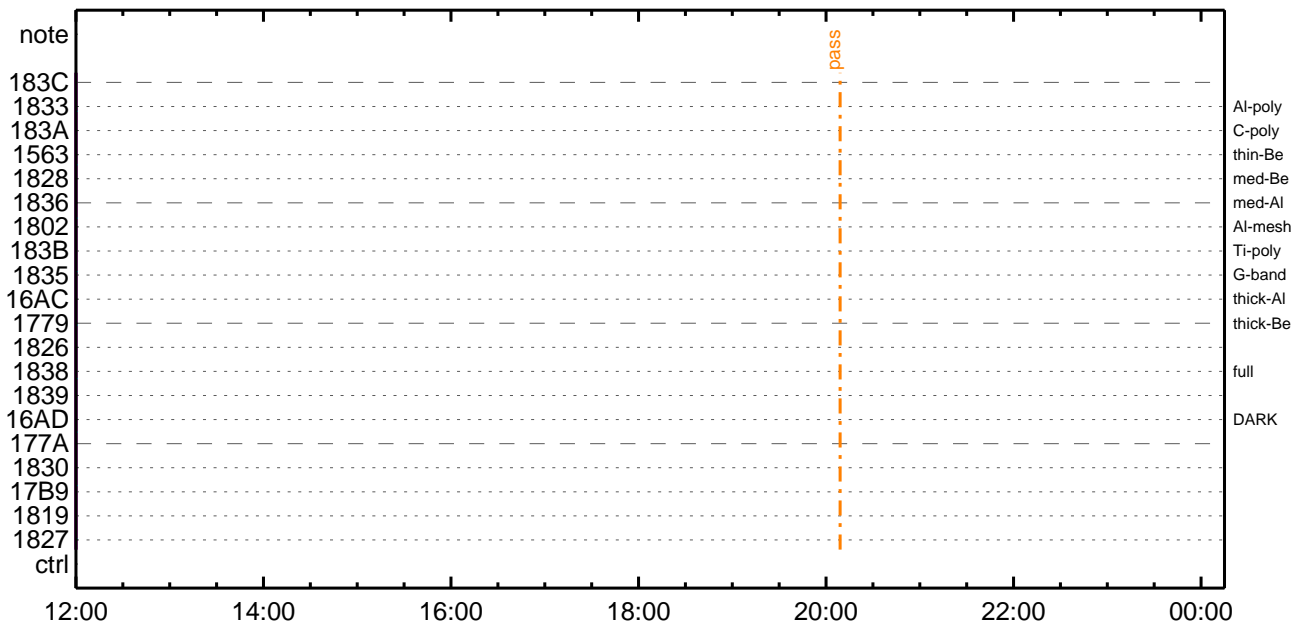
CMDI #0635 2010/12/01



CMDI #0635 2010/12/02



CMDI #0635 2010/12/02



(a) Spacecraft Operation Procedure (real-commands)

```
main-733 2010-11-27 11:58:11 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ä
0005 C.
0006 C. YÁY$;¼Y³YFÖYÉÁ÷¸®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëò¸ðÅð·μ°Æ»Í×ÁÇòÁYçYÁY×Yí;¼YÉ;ËËè½μ·íÊ;ËòÈ¼°ÇÖò·ò¸¼l¹çòÍ;çÀ®, ùò¹òèòðòçÁ÷¸®ò·òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-733:OP
0020 ( )
0021 . S. OG og-733:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÎ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRES_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×¼ªª Î»òð³ ÎÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòÁÎ¾Ê¹ç·è² ÎOKòð³ ÎÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRES_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×¼ªª Î»òð³ ÎÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòÁÎ¾Ê¹ç·è² ÎOKòð³ ÎÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRES_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×¼ªª Î»òð³ ÎÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòÁÎ¾Ê¹ç·è² ÎOKòð³ ÎÇ$
0081 C.
0082 . C. ***** òÈ²¼òÍ¾Ä´¶¼òÈÈ-òÒÁ÷¸® (¼åμ-YÁYÖY×¼è½çòðÄóÃæòç¼ªª ò¼¼¼¼¹¹çòçòâ) *****
0083 C. DHUYâ;¼YÉ;Ë¾Y¾;Yí;¼YÉ;ËòðÍá²¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á÷¸®NGòÁÎ¾¹¹ç;ç°È²¼òÍTI-CMDÁ÷¸®ò¼¼Ä¹Òò·òÈòòò³òÈ;f
```



```
0096 C. 0p00z;çSET0EDUMP0İÆ±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 . C. TIY³YF¥ÖYÉ0dÄDİ¿(UT)
0099 +. TI 2010-11-27 09:57:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2010-11-27 09:57:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2010-11-27 09:57:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2010-11-27 10:01:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. °E²¼0İÄê%îİÑ0İYÁY§YÄY-¹àİÜ
0116 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C. çç[HK1_TI_CMD_NUM] EQ 4
0118 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0119 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0129 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0131 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0132 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. çç[HK1_PKT_FORM_NO] EQ 7
0136 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0138 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0139 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. YÄYÖY×¼¹İ»0d³İÇ§
0142 C. çç[HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼,¥i;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. çç[HK1_PKT_FORM_NO] EQ 2
0150 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0151 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0152 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2010-11-27 10:01:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2010-11-27 10:01:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2010-11-27 10:01:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-11-27 10:01:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 'üÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (¼0İYÖYÄYÈYB¥ÈYÁYçYè0È¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 23s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2010-11-27 10:01:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÃîï»ö¼ÝðËÂÐª¹ëDCBC•x²è *****
0153 C. (¼ª°îÝÓÝÄÝËÝÞÝËÝÀÝ¼ÝËªË¼ª¼Ã»Ûª¹ë)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝÐÝ¹•Ï Daily±¿ÏÑªË´Øª¹ëDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÝÁÝ§ÝÄÝ¹¼Ã»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-735 2010-11-27 11:58:11 153 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αΑβ•μ°Ε»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈ%μ•íÉ;ÈÈ¼°ÇÔα•α¿¼í¹çαÍ;çÀ®, ùα¹αÈαβαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2010-11-27 10:01:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2010-11-27 10:01:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 + DC 07-F0 MDP_XRT_MODE_STBY
0067 BC (c3)
0068 . C. ----- Success Verify ? OK / NG_____
0069 C.
0070 C. XRT Obs. Table Upload
0071 . S. RAM ram-291:MDP_OBS_X
0072 ( )
0073 C.
0074 +. DC 07-F0 MDP_DUMP_XRTTBL
0075 BC (84 07 00 00 00 3a d4)
0076 . C. ----- Comparison Check ? OK / ERR _____
0077 C.
0078 C.
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 01 b1 b1 04 04)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 02 b1 b1 08 08)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 03 b1 b1 08 08)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 04 b1 b1 06 06)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 05 85 83 06 06)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 06 80 80 20 20)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 07 80 80 08 08)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 08 85 83 06 06)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```

0096 BC (cd 09 80 80 20 08)  
0097 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0098 BC (cd 0a 80 80 08 20)  
0099 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0100 BC (cd 0b 80 60 20 18)  
0101 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0102 BC (cd 0c a0 80 18 20)  
0103 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0104 BC (cd 0d 85 83 04 04)  
0105 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0106 BC (cd 0f 80 80 06 06)  
0107 + DC 07-F0 MDP\_XRT\_ROI\_SET  
0108 BC (cd 10 80 80 08 08)  
0109 + DC 07-F8 XRT\_FOCUS\_POSITION  
0110 BC (22 fe 97 00)  
0111 + DC 07-F0 MDP\_XRT\_FLD\_ENA  
0112 BC (d8)  
0113 + DC 07-F0 MDP\_XRT\_FLRCTRL\_ENA  
0114 BC (c8)  
0115 + DC 07-F0 MDP\_XRT\_ARS\_DIS  
0116 BC (d5)  
0117 + DC 07-F0 MDP\_XRT\_FLD\_RESET  
0118 BC (da)  
0119 + DC 07-F0 MDP\_XRT\_QT\_PROG\_SET  
0120 BC (c4 12)  
0121 + DC 07-F0 MDP\_XRT\_FL\_PROG\_SET  
0122 BC (c5 10)  
0123 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0124 C.  
0125 C.  
0126 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0127 C.  
0128 + DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0129 BC (c2)  
0130 + DC 07-F0 MDP\_XRT\_CTRL\_AUTO  
0131 BC (c0)  
0132 + TI 2010-11-27 10:01:02.0  
0133 DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0134 BC (c2)  
0135 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0136 C.  
0137 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0138 C.  
0139 . C. \*\*\*\*\* MDP `uAÎnÎ»ô%YnEÂDn¹nèDCBC•x²è \*\*\*\*\*  
0140 C. (%â°iYÔYÁYÈYÏYËYáYçYènE%¼n¼A»Ûn¹nè)  
0141 . S. DC-BC dcbc-402:DCBC  
0142 (MDP\_known\_event)  
0143 C.  
0144 C.  
0145 . C. \*\*\*\*\* YDÿ¹.Ï Daily±;îÑnÈ´Øn¹nèDCBC•x²è \*\*\*\*\*  
0146 . S. DC-BC dcbc-153:DCBC  
0147 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0148 C.  
0149 C.  
0150 . C. ;ãLOSÿÁÿSÿYÿ¼A»Û;ã  
0151 C.  
0152 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0153 C.

\*\*\* OP Sequence for XRT \*\*\*

```

2010/11/27 10:11:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 10:11:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2010/11/27 10:12:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 01 58 00 fd
2010/11/27 10:12:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2010/11/27 10:12:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2010/11/27 10:12:20.0 XRT_ARS_DIS_420_OG [0x1a4]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2010/11/27 10:14:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 10:14:56.0 XRT_QT_PROG_SET_425_OG [0x1a9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2010/11/27 10:14:58.0 XRT_FL_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2010/11/27 10:15:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2010/11/27 10:48:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 10:48:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 10:48:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2010/11/27 10:51:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2010/11/27 10:51:30.0 XRT_Custom_418_OG [0x1a2]
2010/11/27 10:52:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2010/11/27 11:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 11:59:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2010/11/27 12:00:00.5 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 02 00 00 00 00
2010/11/27 12:00:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2010/11/27 12:00:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2010/11/27 12:00:20.0 XRT_ARS_DIS_420_OG [0x1a4]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2010/11/27 12:02:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 12:02:56.0 XRT_QT_PROG_SET_423_OG [0x1a7]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 07
2010/11/27 12:02:58.0 XRT_FL_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2010/11/27 12:03:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2010/11/27 14:33:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 14:33:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 14:33:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2010/11/27 14:36:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2010/11/27 14:43:00.0 XRT_Custom_418_OG [0x1a2]
2010/11/27 14:44:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2010/11/27 16:08:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 16:08:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 16:08:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2010/11/27 16:11:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2010/11/27 16:31:30.0 XRT_Custom_418_OG [0x1a2]
2010/11/27 16:32:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2010/11/27 17:44:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 17:44:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2010/11/27 17:44:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2010/11/27 17:47:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2010/11/27 18:07:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2010/11/27 18:07:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2010/11/27 18:08:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2010/11/27 18:08:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2010/11/27 18:08:18.0 XRT_FLRCTRL_DIS_428_OG [0x1ac]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9

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2010/11/27	18:10:56.0	XRT_ARS_DIS_447_OG [0x1bf]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/27	18:10:58.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/11/27	18:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/27	18:17:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/27	18:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/27	18:18:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/11/27	18:18:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/27	18:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/27	18:18:20.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/11/27	18:20:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/27	18:20:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/11/27	18:20:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/11/27	18:21:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/27	19:21:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/27	19:21:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/27	19:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/27	19:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/27	19:45:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/27	19:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/27	20:59:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/27	20:59:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/27	20:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/27	21:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/27	21:22:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/27	21:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/27	22:36:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/27	22:36:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/27	22:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/27	22:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/27	22:57:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/27	22:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/28	00:14:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/28	00:14:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/28	00:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/28	00:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/28	00:22:00.0	XRT_Custom_418_OG [0x1a2]			
2010/11/28	00:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/28	01:38:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/28	01:38:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/11/28	01:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/11/28	01:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/11/28	01:56:30.0	XRT_Custom_418_OG [0x1a2]			
2010/11/28	01:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/11/28	01:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/11/28	01:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/11/28	02:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 de da b4 5b
2010/11/28	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/11/28	02:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/11/28	02:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5



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2010/11/28	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	02:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2010/11/28	02:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2010/11/28	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	03:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	03:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	03:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/28	03:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/28	03:33:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/28	03:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	04:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	04:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	04:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/28	04:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/28	05:11:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/28	05:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	05:55:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	05:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2010/11/28	05:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00
2010/11/28	05:55:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/11/28	05:55:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/11/28	05:58:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/11/28	05:58:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2010/11/28	05:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	06:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/11/28	06:05:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	de da b4 5b
2010/11/28	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/11/28	06:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/11/28	06:05:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/11/28	06:08:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	06:08:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2010/11/28	06:08:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2010/11/28	06:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	06:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	06:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	06:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/28	06:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/28	06:48:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/28	06:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	08:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	08:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/28	08:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/28	08:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/28	08:25:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/28	08:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/28	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/28	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/11/28	09:00:00.5	AOCS_Ore-point_Start_1_OG [0x097]					

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2010/11/28	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	01	58	00	fd
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/28	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/28	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	09:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]		2	07-F0	c4	12			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2010/11/28	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	10			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/28	09:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	09:43:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	09:43:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	09:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/28	09:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/28	10:00:00.0	XRT_Custom_418_OG [0x1a2]								
2010/11/28	10:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/11/28	12:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]		5	02-76	00	de	da	b4	5b
			AOCU_NM	5	02-76	00	de	da	b4	5b
2010/11/28	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/28	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/28	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	12:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]		2	07-F0	c4	07			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/11/28	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	10			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/11/28	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	15:08:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	15:08:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	15:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/28	15:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/28	15:21:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/28	15:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	15:30:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	15:30:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	15:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/28	15:31:30.0	XRT_Custom_418_OG [0x1a2]								
2010/11/28	15:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	15:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/28	16:44:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	16:44:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	16:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/28	16:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/28	17:08:00.0	XRT_Custom_418_OG [0x1a2]								
2010/11/28	17:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	17:57:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	17:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		4	07-F8	22	ff	aa	00	
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/11/28	17:58:00.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	00	00	00	00	00
			AOCU_NM	5	02-76	00	00	00	00	00
2010/11/28	17:58:16.0	XRT_FLD_DIS_402_OG [0x192]		1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/28	17:58:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]		1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/28	18:00:56.0	XRT_ARS_DIS_447_OG [0x1bf]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	18:00:58.0	XRT_QT_PROG_SET_410_OG [0x19a]		2	07-F0	c4	03			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			

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2010/11/28	18:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	18:07:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	18:07:56.0	XRT_FOCUS_POSITION_438_OG [0x1b6]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/28	18:08:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2010/11/28	18:08:16.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2010/11/28	18:08:18.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/28	18:08:20.0	XRT_FLRCTRL_DIS_442_OG [0x1ba]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/28	18:08:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	18:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	20:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	20:09:56.0	XRT_FOCUS_POSITION_438_OG [0x1b6]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/28	20:10:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2010/11/28	20:10:16.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/11/28	20:10:18.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/28	20:10:20.0	XRT_FLRCTRL_DIS_442_OG [0x1ba]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/28	20:10:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	20:25:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	22:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	22:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/28	22:10:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/28	22:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/28	22:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/28	22:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/28	22:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	22:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/11/28	22:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/28	22:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/28	23:13:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/28	23:13:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/28	23:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/28	23:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/28	23:30:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/28	23:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	00:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	00:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	00:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	00:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	00:57:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	00:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	02:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	02:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	02:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	02:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	02:33:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	02:34:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	03:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	03:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	03:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2010/11/29	03:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	04:10:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	04:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	05:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	05:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	05:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	05:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	05:48:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	05:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/29	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/29	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/29	06:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/29	06:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/29	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/29	06:10:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/29	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/29	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/29	06:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	06:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/11/29	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/29	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	07:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	07:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	07:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	07:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	07:25:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	07:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	08:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	08:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	08:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	08:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	08:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/29	09:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 01 58 00 fd				
2010/11/29	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/29	09:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/29	09:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	09:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2010/11/29	09:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/29	09:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	10:22:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2010/11/29	10:22:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	10:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	10:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	10:32:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	10:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/29	12:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/29	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/29	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/29	12:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	12:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2010/11/29	12:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/11/29	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	14:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	14:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	14:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	14:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	14:17:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	14:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	15:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	15:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	15:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	15:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	16:08:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	16:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	17:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	17:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/11/29	17:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/11/29	17:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/11/29	17:44:30.0	XRT_Custom_418_OG [0x1a2]							
2010/11/29	17:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	18:00:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	18:00:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/11/29	18:00:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/11/29	18:00:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/11/29	18:00:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/11/29	18:03:26.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	18:03:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/11/29	18:03:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/11/29	18:10:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/11/29	18:10:26.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2010/11/29	18:10:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/11/29	18:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/11/29	18:14:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/11/29	18:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/11/29	18:14:50.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/11/29	18:14:54.0	XRT_FLD_RESET_412_OG [0x19c]							

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2010/11/29	18:14:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2010/11/29	18:14:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2010/11/29	18:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/29	18:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/29	18:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/29	18:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/29	19:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/29	19:21:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/29	19:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/29	20:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/29	20:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/29	20:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/29	20:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/29	20:59:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/29	21:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/29	22:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/29	22:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/29	22:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/29	22:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/29	22:34:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/29	22:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/29	23:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/29	23:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/29	23:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/29	23:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/30	00:00:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/30	00:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/30	01:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/30	01:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/30	01:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/30	01:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/30	01:33:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/30	01:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/30	02:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/30	02:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/30	02:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/30	02:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/30	03:10:00.0	XRT_Custom_418_OG [0x1a2]					
2010/11/30	03:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/30	04:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/30	04:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/11/30	04:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/11/30	04:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/11/30	04:47:30.0	XRT_Custom_418_OG [0x1a2]					
2010/11/30	04:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/11/30	05:31:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/11/30	05:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/11/30	05:31:30.5	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2010/11/30	05:31:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/11/30	05:31:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					

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2010/11/30	05:34:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/11/30	05:34:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2010/11/30	05:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/11/30	05:41:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/11/30	05:41:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2010/11/30	05:41:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	03	00	00	00
2010/11/30	05:41:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/11/30	05:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/11/30	05:41:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/11/30	05:44:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/11/30	05:44:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2010/11/30	05:44:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2010/11/30	05:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/11/30	05:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/11/30	05:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/11/30	05:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/11/30	06:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/11/30	06:25:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/30	06:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/11/30	07:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/11/30	07:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/11/30	07:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/11/30	07:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/11/30	08:02:00.0	XRT_Custom_418_OG [0x1a2]							
2010/11/30	08:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/11/30	09:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/11/30	09:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00