

XRT Timeline to be uploaded on 2010/12/07

Period: 2010/12/07 10:27:00 - 2010/12/11 09:27:00

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Normal mode

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XOB #183D: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad													
Term	Pointing (x, y)	Comment											
12/07 10:44:00 - 12/07 18:02:54	Track (-313.9, -425.7) @ 12/07 10:37:00	# OP start + 10min EIS spectral atlas synoptic											
12/07 18:16:00 - 12/08 06:59:54	Track (-102.2, 500.5) @ 12/07 18:13:00	# Track AR11131; HOP102 from 19UT, for ~12hrs.											
12/08 07:13:00 - 12/08 11:00:00	Track (-2.5, 501.5) @ 12/08 07:10:00	# Continue to track AR11131 and XRT Bakeout Heater on											
12/09 07:58:00 - 12/09 17:59:54	Track (187.1, 503.4) @ 12/09 07:55:00	Continue to track AR 11131											
12/09 18:13:00 - 12/10 06:13:24	Track (263.3, 504.1) @ 12/09 18:10:00	# Track AR11131											
12/10 06:26:36 - 12/10 07:17:30	Track (351.1, 504.8) @ 12/10 06:23:30	# Continue to track AR11131											
PROG= 10 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 20 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 16 18-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term	Pointing (x, y)	Comment											
12/07 18:06:00 - 12/07 18:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min											
12/08 07:03:00 - 12/08 07:09:54	Fixed (0.0, 0.0)	synoptic											
12/09 18:03:00 - 12/09 18:09:54	Fixed (0.0, 0.0)	synoptic											
12/10 06:16:30 - 12/10 06:23:24	Fixed (0.0, 0.0)	synoptic, shifted 13.5 min											
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long													
Term	Pointing (x, y)	Comment											
12/09 07:18:05 - 12/09 07:24:54	Fixed (-528.4, -528.4)	# XRT Post-Bakeout Quadra Ptg. #1											
PROG= 15 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 38 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long												
Term	Pointing (x, y)	Comment										
12/09 07:28:00 - 12/09 07:34:54	Fixed (528.4, -528.4)	# XRT Post-Bakeout Quadra Ptg. #2										

PROG= 14 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 36 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long												
Term			Pointing (x, y)				Comment					
12/09 07:38:00 - 12/09 07:44:54			Fixed (528.4, 528.4)				# XRT Post-Bakeout Quadra Ptg. #3					

PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 39 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long												
Term			Pointing (x, y)				Comment					
12/09 07:48:00 - 12/09 07:54:54			Fixed (-528.4, 528.4)				# XRT Post-Bakeout Quadra Ptg. #4					

PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 40 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term			Pointing (x, y)				Comment					
12/07 10:44:00 - 12/07 18:02:54			Track (-313.9, -425.7) @ 12/07 10:37:00				# OP start + 10min EIS spectral atlas synoptic					
12/07 18:16:00 - 12/08 06:59:54			Track (-102.2, 500.5) @ 12/07 18:13:00				# Track AR11131; HOP102 from 19UT, for ~12hrs.					
12/08 07:13:00 - 12/08 11:00:00			Track (-2.5, 501.5) @ 12/08 07:10:00				# Continue to track AR11131 and XRT Bakeout Heater on					
12/09 07:58:00 - 12/09 17:59:54			Track (187.1, 503.4) @ 12/09 07:55:00				Continue to track AR 11131					
12/09 18:13:00 - 12/10 06:13:24			Track (263.3, 504.1) @ 12/09 18:10:00				# Track AR11131					
12/10 06:26:36 - 12/10 07:17:30			Track (351.1, 504.8) @ 12/10 06:23:30				# Continue to track AR11131					

PROG= 16 1-time(s)													
└─ Subr= 1 30-time(s) 20.0sec													
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 2.0sec													

	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

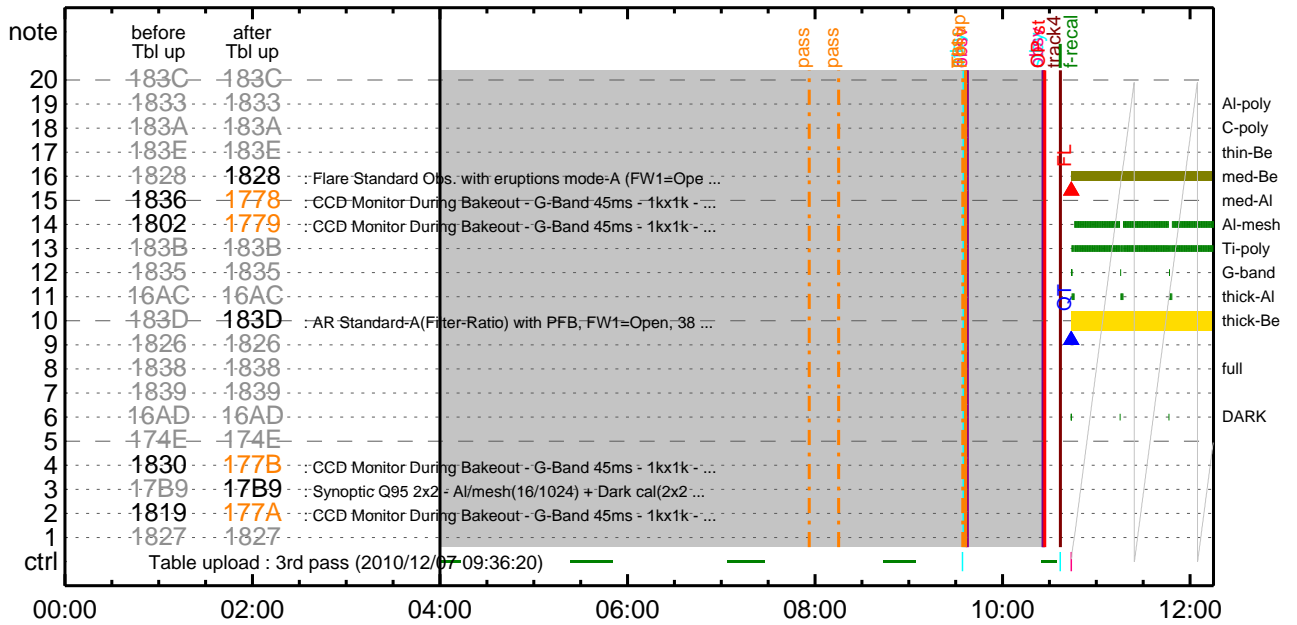
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Flare Detection

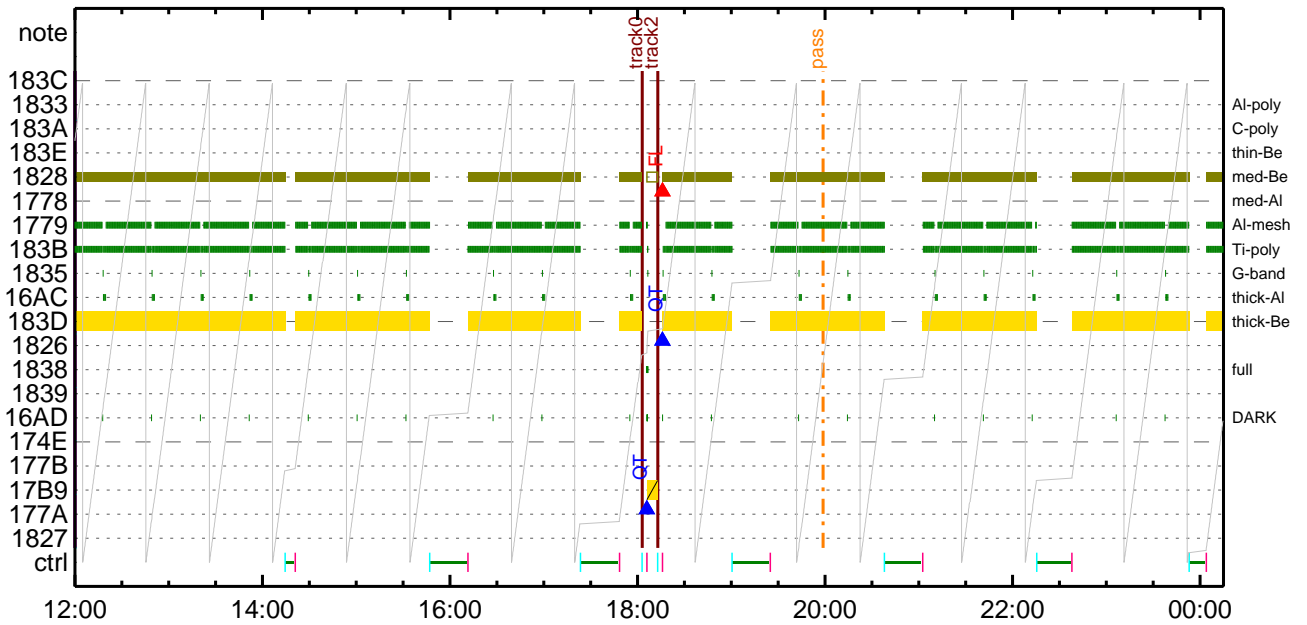
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FLD Patrol												
Term	Pointing (x, y)					Comment						
12/07 10:41:16 - 12/07 18:03:16	Track (-313.9,	-425.7)	[Ⓢ] 12/07 10:37:00	# OP start + 10min EIS spectral atlas synoptic							
12/07 18:13:16 - 12/08 07:00:16	Track (-102.2,	500.5)	[Ⓢ] 12/07 18:13:00	# Track AR11131; HOP102 from 19UT, for ~12hrs.							
12/08 07:10:16 - 12/09 07:17:59	Track (-2.5,	501.5)	[Ⓢ] 12/08 07:10:00	# Continue to track AR11131 and XRT Bakeout Heater on							
12/09 07:55:16 - 12/09 18:00:16	Track (187.1,	503.4)	[Ⓢ] 12/09 07:55:00	Continue to track AR 11131							
12/09 18:10:16 - 12/10 06:13:46	Track (263.3,	504.1)	[Ⓢ] 12/09 18:10:00	# Track AR11131							
12/10 06:23:51 - 12/11 09:27:00	Track (351.1,	504.8)	[Ⓢ] 12/10 06:23:30	# Continue to track AR11131							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

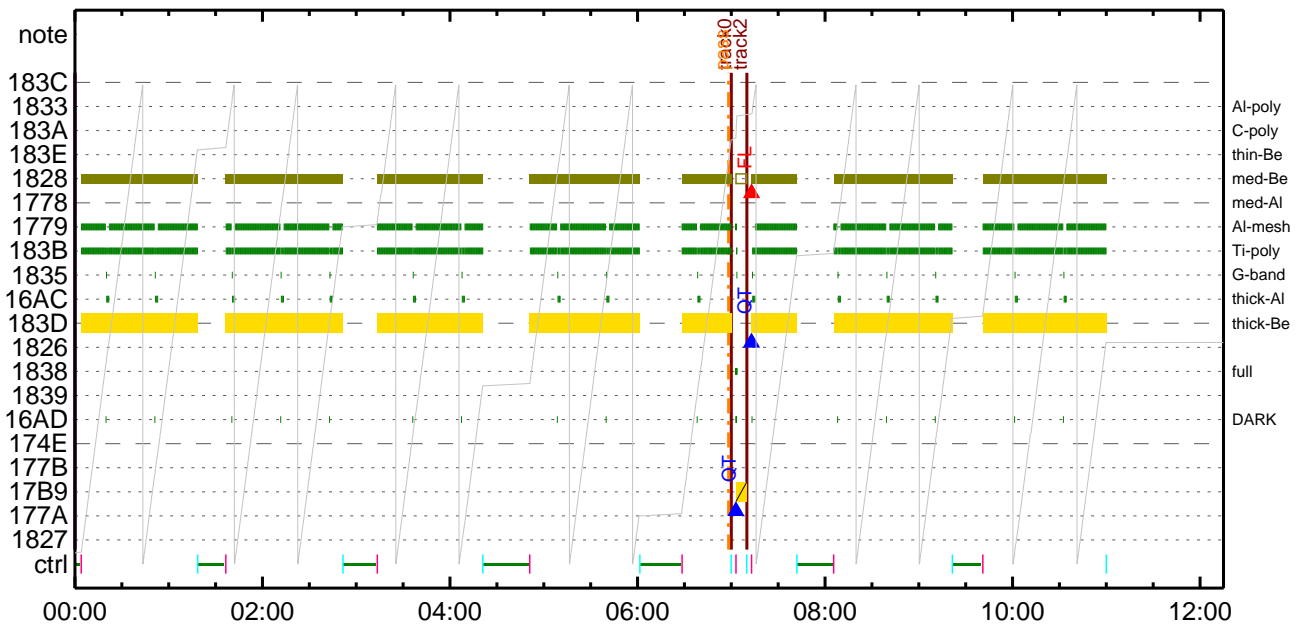
CMDI #0656 2010/12/07



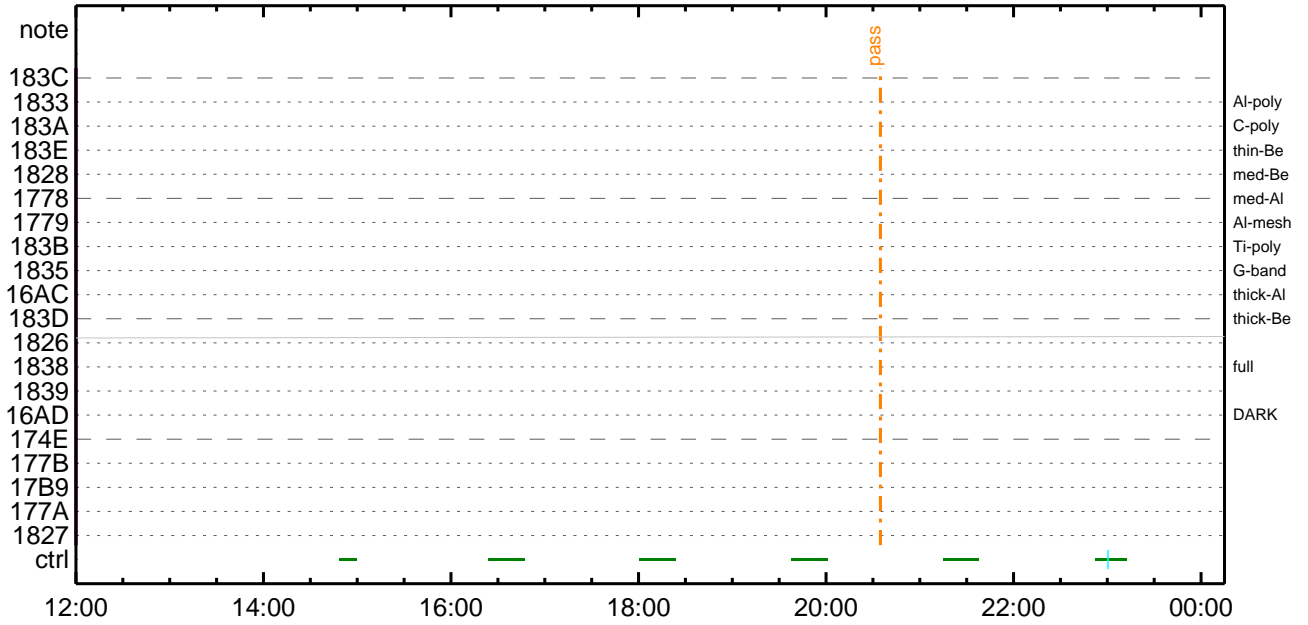
CMDI #0656 2010/12/07



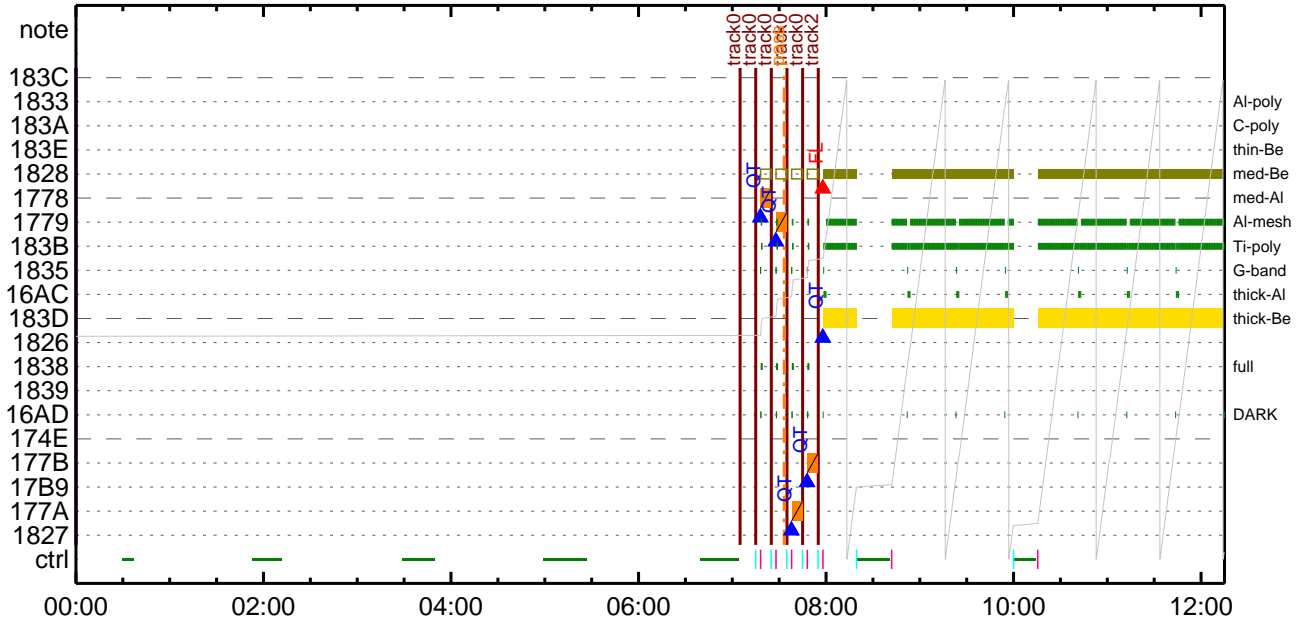
CMDI #0656 2010/12/08



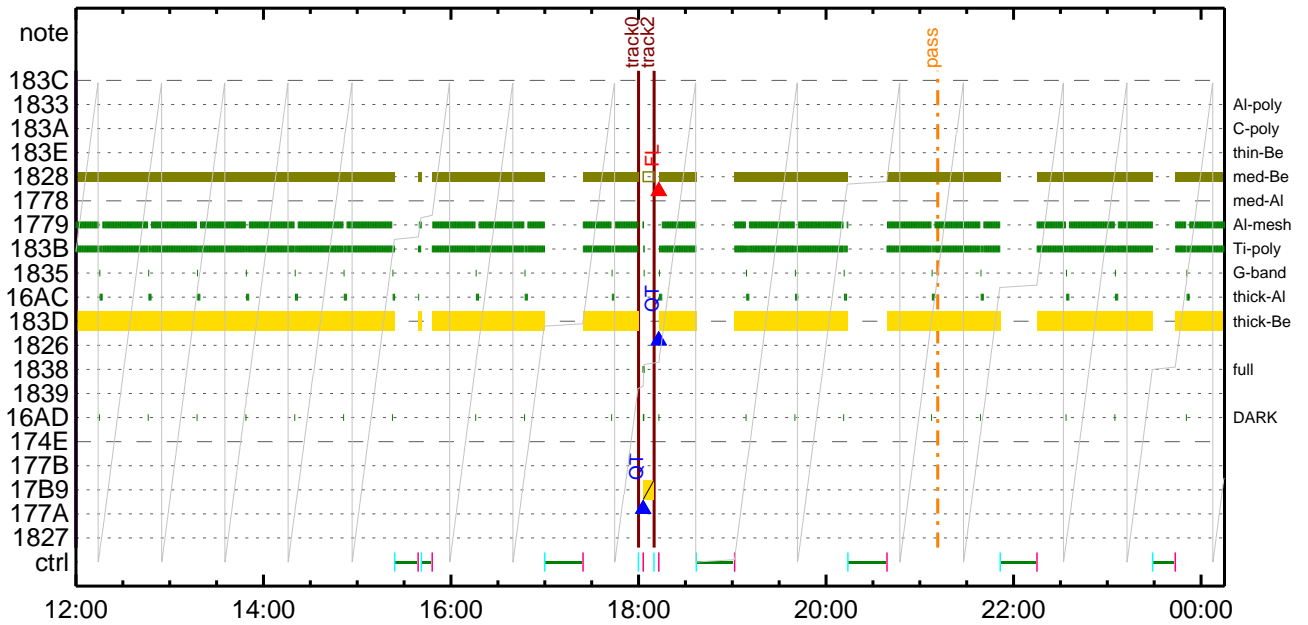
CMDI #0656 2010/12/08



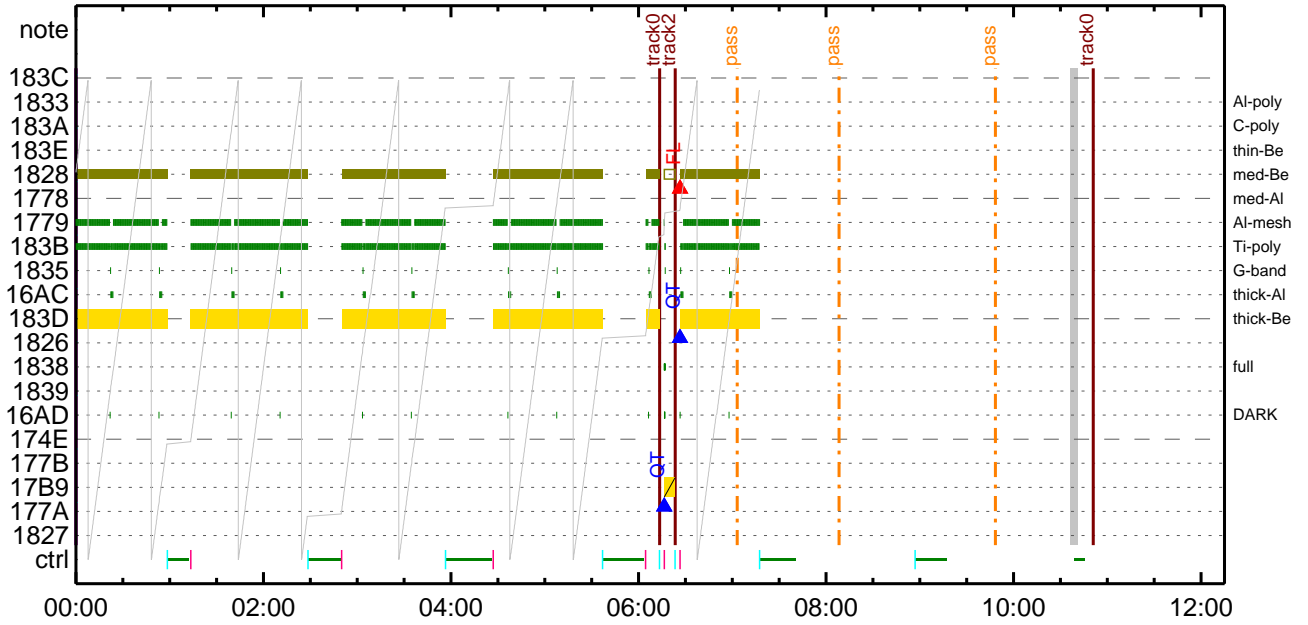
CMDI #0656 2010/12/09



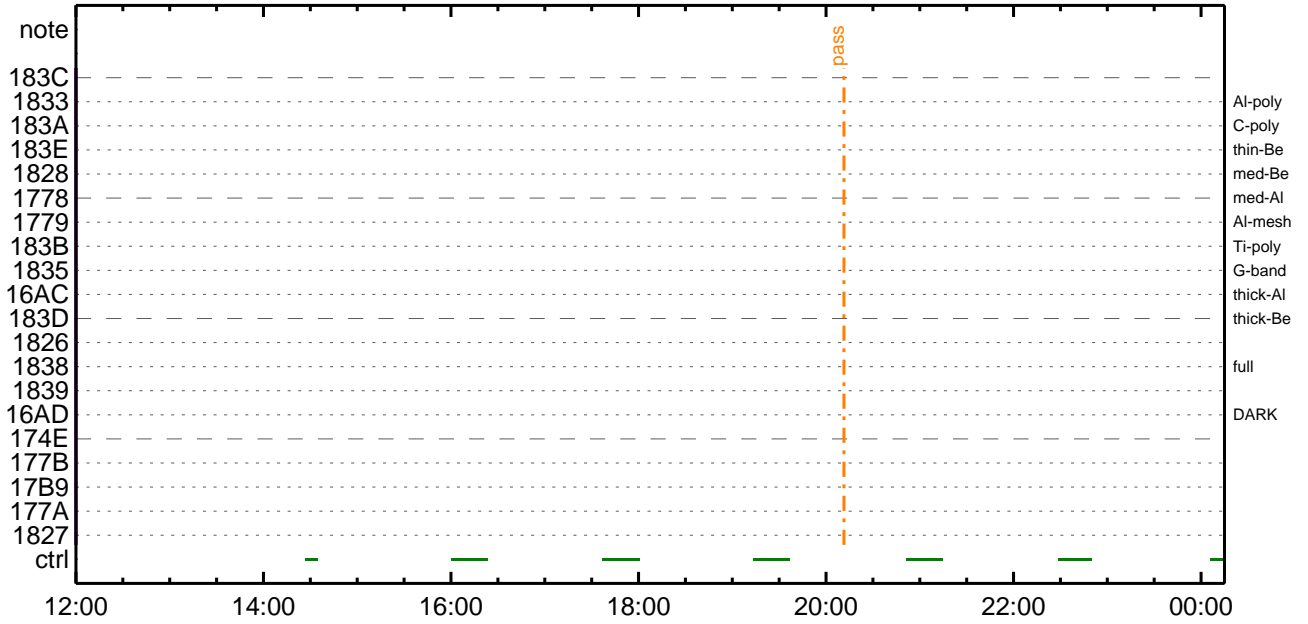
CMDI #0656 2010/12/09



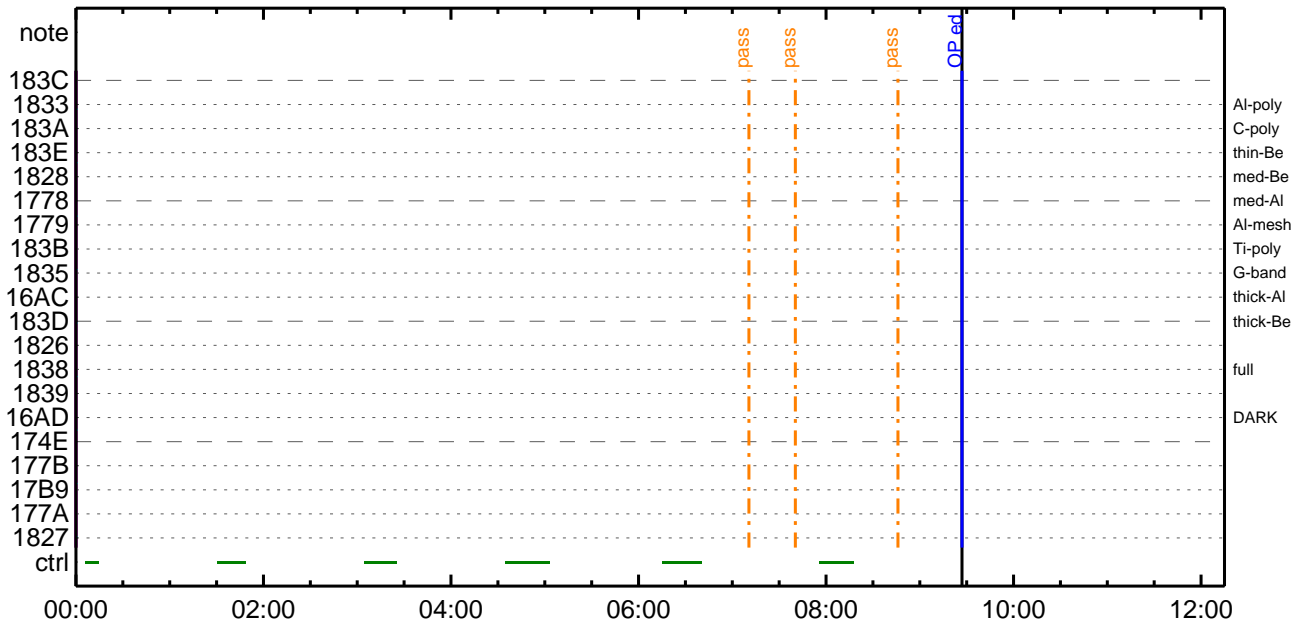
CMDI #0656 2010/12/10



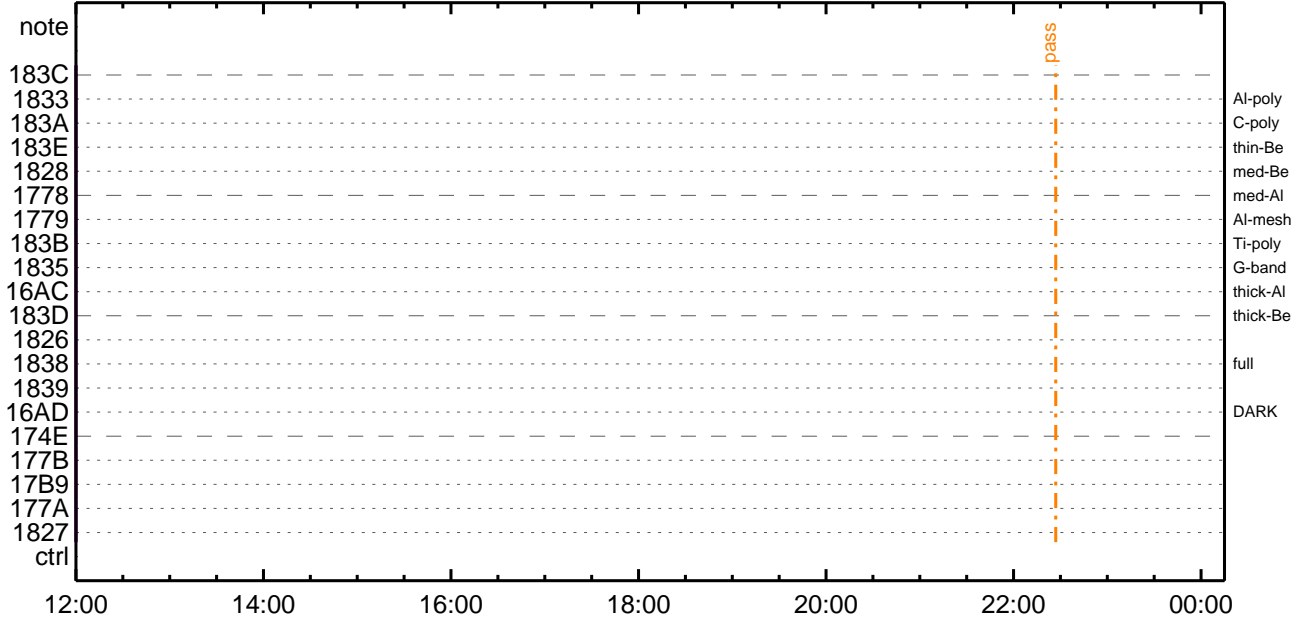
CMDI #0656 2010/12/10



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CMDI #0656 2010/12/11



(a) Spacecraft Operation Procedure (real-commands)

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main-754 2010-12-07 12:01:49 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•µ°Æ»Í×ÁÇòÍYÇYÁY×YÍ;¼YÉ;ÈÈÈ¼µ•ÍÉ;ÈòÈ¼°Çò•µ¿¼Ì¹ÇòÍ;ÇÀ®, ùò¹òÈòÈòÇÁ+¿®•òÈòòò³òÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@µ;ON
0016 C. *****
0017 C. Ç“ °ÆÀ, Í×ÈYòãLOSòÈòÇòÍ»P´Òòò¹ÍÍ, ñ•; ÇÉÒÍ×òÈXÁÒONòÍ¹ÒòÈòÍòÈòòò³òÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY~¾ÒÁÒò-òÁÁèò•µ¿;Ç; Ç°È²¼òÍ°ÆÀ, ¼È¼Çòò¼Á¹Òò¹òÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÌ¼í°ÆÀ,
0033 C. *****
0034 C. Ç“ RESTART;ÈPT1;Èò•µ¿ò¼¼Ì¹ÇòÍ; Ç°È²¼òÍ°ÆÀ¹Òò»ò°; ÇDCBC-150òØ¿Èòà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÈÁÛÁØ;ÈÁ•Á°²òÈò;È, áòÍ°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ñ-¼«Æ°Áá»ßò•µ¿, á; Ç°È²¼òò¼Á¹Òò¹òÈ; f
0055 C. YÇYÓYÉYÈÁÛÁØòÁÁ•Á°²òÈòò-¾áò¼¼Ì¹ÇòÍ°ÆÀ¹Òò»ò°; ÇDCBC-150òØ¿Èòà; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÌ¼í°ÆÀ,
0059 C. *****
0060 C. Ç“ RESTART;ÈPT2;Èò•µ¿ò¼¼Ì¹ÇòÍ; Ç°È²¼òÍ°ÆÀ¹Òò»ò°; ÇDCBC-151òØ¿Èòà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÈÁÛÁØ;ÈÁ•Á°²òÈò;È, áòÍ°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß;ÇXÁ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-754:OP
0104 ( )
0105 S. OG og-754:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÁDî¿(UT)
0183 +. TI 2010-12-07 10:22:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-12-07 10:22:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-12-07 10:22:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èµ¿µÄµ•µ°È»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ÈÈÈµ•ííÈ;ÈµÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ;¿À®, ùµ¹µÈµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¿ °ÄÄ, Í×ÈYµãLOSµµÇµÍ»p´Öµµ¹íí, µ•; ¿ÈÖÍ×µÈXÁÖONµÍ¹ÖµÈµíµÈµµµ³µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.             ¿¿[HK1_XPA_ON/OFF]                      EQ      ON
0025 C.             ¿¿[HK1_XPA_PWR_HI/LO]                  EQ      HI
0026 C.             ¿¿[HK1_XMOD_ON/OFF]                    EQ      ON
0027 C.             ¿¿[HK1_XMOD_QPSK/PM]                   EQ      QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY-¾ÖÁÖµ-°ÄÄÈµ•µ¿µÉ; ¿°È²¼µí°ÄÄ, ¼È¾Çµµµ¼Ä¹Öµ¹µÈ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÄÄ,
0033 C. *****
0034 C. ¿" RESTART;ÈPT1;Èµ•µ¿µ¼¼í¹ÇµÍ; ¿°È²¼µí°ÄÄ¹Öµ»µ°; ¿DCBC-150µµ¿¿Èµã;f
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC              (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.             ¿¿[HK1_REP_PT_1/2]                      EQ      PT1      (¼Ä¹Ö, ;¼Ú)
0043 C.             ¿¿[HK1_REP_STA/STP]                    EQ      START   (¼Ä¹Ö, ;¼Ú)
0044 C.             ¿¿[HK1_X_VC4_ON/OFF]                   EQ      ON       (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÈÄÜÁÖ;ÈÄ•Ä°²óÈò;È, áµí°ÄÄ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.             ¿¿[HK1_REP_PT_1/2]                      EQ      PT1      (¼Ä¹Ö, ;¼Ú)
0050 C.             ¿¿[HK1_REP_STA/STP]                    EQ      START   (¼Ä¹Ö, ;¼Ú)
0051 C.             ¿¿[HK1_X_VC4_ON/OFF]                   EQ      ON       (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, µ-¼«È°ÄÄ»ßµ•µ¿, á; ¿°È²¼µµ¼Ä¹Öµ¹µÈ;f
0055 C. YÇYÓYÉYÈÄÜÁÖµãÄ•Ä°²óÈòµ-¼áµµ¼¼í¹ÇµÍ´°í»µ¹µÈµµµÇÁÖµÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÄÄ,
0059 C. *****
0060 C. ¿" RESTART;ÈPT2;Èµ•µ¿µ¼¼í¹ÇµÍ; ¿°È²¼µí°ÄÄ¹Öµ»µ°; ¿DCBC-151µµ¿¿Èµã;f
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC              (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.             ¿¿[HK1_REP_PT_1/2]                      EQ      PT2      (¼Ä¹Ö, ;¼Ú)
0069 C.             ¿¿[HK1_REP_STA/STP]                    EQ      START   (¼Ä¹Ö, ;¼Ú)
0070 C.             ¿¿[HK1_X_VC4_ON/OFF]                   EQ      ON       (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÈÄÜÁÖ;ÈÄ•Ä°²óÈò;È, áµí°ÄÄ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.             ¿¿[HK1_REP_PT_1/2]                      EQ      PT2      (¼Ä¹Ö, ;¼Ú)
0076 C.             ¿¿[HK1_REP_STA/STP]                    EQ      START   (¼Ä¹Ö, ;¼Ú)
0077 C.             ¿¿[HK1_X_VC4_ON/OFF]                   EQ      ON       (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÄÄ, Äã»ß; ¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÄÄ, Äã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.             ¿¿[HK1_REP_STA/STP]                      EQ      STOP
0087 C.             ¿¿[HK1_S_VC4_ON/OFF]                    EQ      OFF
0088 C.             ¿¿[HK1_X_VC4_ON/OFF]                    EQ      OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.             ¿¿[HK1_XMOD_ON/OFF]                      EQ      OFF
0095 C.             ¿¿[HK1_XPA_ON/OFF]                       EQ      OFF

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2010-12-07 10:26:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F8 XRT_STBY
0136 BC (03 01)
0137 +. DC 07-F8 XRT_OPERATE
0138 BC (03 02)
0139 + DC 07-F0 MDP_XRT_MODE_STBY
0140 BC (c3)
0141 . C. ----- Success Verify ? OK / NG____
0142 C.
0143 C. XRT Obs. Table Upload
0144 . S. RAM ram-291:MDP_OBS_X
0145 ( )
0146 C.
0147 +. DC 07-F0 MDP_DUMP_XRTTBL
0148 BC (84 07 00 00 00 3a d4)
0149 . C. ----- Comparison Check ? OK / ERR ____
0150 C.
0151 C.
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 01 b1 b1 04 04)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 02 b1 b1 08 08)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 03 b1 b1 08 08)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 04 b1 b1 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 05 85 83 06 06)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 06 85 83 06 06)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 07 80 80 20 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 08 80 80 20 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 09 80 80 08 20)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0a c0 c0 10 10)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0b 40 c0 10 10)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0c 40 40 10 10)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0d c0 40 10 10)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 0f 80 80 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 10 80 80 08 08)
0182 . C. ----- Success Verify ? OK / NG ____
0183 C.
0184 C.
0185 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0186 C.
0187 +. DC 07-F0 MDP_XRT_MODE_OBSV
0188 BC (c2)
0189 +. TI 2010-12-07 10:26:02.0
0190 DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

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0194 C. ***** XRT END *****
0195 C.
0196 . C. ***** MDP 'úÃîî»ò¼ÝñÊÄð¹ñèDCBC•x²è *****
0197 C. (¼ã°îÏÓŸÄŸÈŸPŸEŸáŸçŸèñ¼ñ¼Ä»Ûñ¹ñè)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ŸDŸ¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLOSŸÁŸ§ŸÄŸ-¼Ä»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.

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*** OP Sequence for XRT ***

2010/12/07	10:36:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	10:36:56.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]					
		XRT_FOCUS_RECAL	2	07-F8	78	00	
2010/12/07	10:37:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04	00	00 00 00
2010/12/07	10:40:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/12/07	10:41:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/07	10:41:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/07	10:41:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/07	10:43:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/07	10:43:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2010/12/07	10:43:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/12/07	10:44:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/07	14:14:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	14:14:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/07	14:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/07	14:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/07	14:20:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/07	14:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/07	15:47:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	15:47:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/07	15:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/07	15:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/07	16:10:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/07	16:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/07	17:23:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	17:23:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/07	17:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/07	17:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/07	17:47:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/07	17:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/07	18:02:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	18:02:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2010/12/07	18:03:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00	00	00 00 00
2010/12/07	18:03:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/07	18:03:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/07	18:05:56.0	XRT_ARS_DIS_447_OG [0x1bf]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/07	18:05:58.0	XRT_QT_PROG_SET_410_OG [0x19a]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2010/12/07	18:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/07	18:12:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/07	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/12/07	18:13:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	02	00	00 00 00
2010/12/07	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/07	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/07	18:13:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/07	18:15:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/07	18:15:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2010/12/07	18:15:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/12/07	18:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2010/12/07	19:00:30.5	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/07	19:00:32.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/07	19:00:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/07	19:03:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/07	19:24:00.0	XRT_Custom_418_OG [0x1a2]			
2010/12/07	19:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/07	20:38:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/07	20:38:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/07	20:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/07	20:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/07	21:01:30.0	XRT_Custom_418_OG [0x1a2]			
2010/12/07	21:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/07	22:15:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/07	22:15:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/07	22:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/07	22:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/07	22:37:00.0	XRT_Custom_418_OG [0x1a2]			
2010/12/07	22:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/07	23:53:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/07	23:53:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/07	23:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/07	23:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/08	00:03:00.0	XRT_Custom_418_OG [0x1a2]			
2010/12/08	00:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/08	01:18:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/08	01:18:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/08	01:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/08	01:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/08	01:35:30.0	XRT_Custom_418_OG [0x1a2]			
2010/12/08	01:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/08	02:51:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/08	02:51:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/08	02:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/08	02:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/08	03:12:30.0	XRT_Custom_418_OG [0x1a2]			
2010/12/08	03:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/08	04:21:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/08	04:21:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/08	04:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/08	04:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/08	04:50:00.0	XRT_Custom_418_OG [0x1a2]			
2010/12/08	04:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/08	06:01:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/08	06:01:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/08	06:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/08	06:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/08	06:27:30.0	XRT_Custom_418_OG [0x1a2]			
2010/12/08	06:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/08	06:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/08	06:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/12/08	07:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			

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2010/12/08	07:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2010/12/08	07:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2010/12/08	07:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/12/08	07:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]		2	07-F0	c4	03			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	03			
2010/12/08	07:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/12/08	07:09:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/08	07:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2010/12/08	07:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]		5	02-76	02	00	00	00	00
		AOCU_NM		5	02-76	02	00	00	00	00
2010/12/08	07:10:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2010/12/08	07:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2010/12/08	07:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/12/08	07:12:54.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/12/08	07:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]		2	07-F0	c4	0a			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0a			
2010/12/08	07:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	10			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	10			
2010/12/08	07:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/12/08	07:42:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/08	07:42:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/12/08	07:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/12/08	07:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/12/08	08:04:30.0	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
2010/12/08	08:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/12/08	09:21:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/08	09:21:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2010/12/08	09:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2010/12/08	09:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2010/12/08	09:40:00.0	XRT_Custom_418_OG [0x1a2]		1	07-F0	c0				
2010/12/08	09:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/12/08	11:00:00.0	XRT_CTRL_MANU_414_OG [0x19e]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/08	11:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_415_OG [0x19f]		0	04-BC					
		TCIB_XRT_S_HTR_A_ENA		0	04-BC					
2010/12/08	13:00:30.0	XRT_Custom_416_OG [0x1a0]								
2010/12/08	15:00:30.0	XRT_Custom_416_OG [0x1a0]								
2010/12/08	17:00:30.0	XRT_Custom_416_OG [0x1a0]								
2010/12/08	19:00:30.0	XRT_Custom_417_OG [0x1a1]								
2010/12/08	19:00:40.0	XRT_Custom_416_OG [0x1a0]								
2010/12/08	21:00:40.0	XRT_Custom_427_OG [0x1ab]								
2010/12/08	23:00:18.0	XRT_Custom_417_OG [0x1a1]								
2010/12/08	23:00:28.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/08	23:00:30.0	XRT_TCIB_XRT_S_HTR_A_DIS_429_OG [0x1ad]		0	04-C0					
		TCIB_XRT_S_HTR_A_DIS		0	04-C0					
2010/12/09	07:05:00.0	AOCS_OrE-point_Start_2_OG [0x098]		5	02-76	00	00	00	00	00
		AOCU_NM		5	02-76	00	00	00	00	00
2010/12/09	07:14:59.0	XRT_CTRL_MANU_430_OG [0x1ae]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/09	07:15:00.0	AOCS_OrE-point_Start_4_OG [0x09a]		5	02-76	00	2e	f9	2e	f9
		AOCU_NM		5	02-76	00	2e	f9	2e	f9
2010/12/09	07:17:37.0	XRT_FOCUS_POSITION_431_OG [0x1af]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2010/12/09	07:17:57.0	XRT_QT_PROG_SET_432_OG [0x1b0]		2	07-F0	c4	0f			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0f			
2010/12/09	07:17:59.0	XRT_FLD_DIS_402_OG [0x192]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2010/12/09	07:18:01.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2010/12/09	07:18:03.0	XRT_ARS_DIS_447_OG [0x1bf]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2010/12/09	07:18:05.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2010/12/09	07:24:54.0	XRT_CTRL_MANU_430_OG [0x1ae]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2010/12/09	07:25:00.0	AOCS_OrE-point_Start_5_OG [0x09b]		5	02-76	00	2e	f9	d1	07
		AOCU_NM		5	02-76	00	2e	f9	d1	07
2010/12/09	07:27:32.0	XRT_FOCUS_POSITION_431_OG [0x1af]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2010/12/09	07:27:52.0	XRT_QT_PROG_SET_434_OG [0x1b2]								

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2010/12/09	07:27:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e	
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/09	07:27:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/09	07:27:58.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	07:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	07:34:54.0	XRT_CTRL_MANU_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	07:35:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	d1 07 d1 07	
2010/12/09	07:37:32.0	XRT_FOCUS_POSITION_431_OG [0x1af]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2010/12/09	07:37:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02	
2010/12/09	07:37:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/09	07:37:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/09	07:37:58.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	07:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	07:44:54.0	XRT_CTRL_MANU_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	07:45:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	d1 07 2e f9	
2010/12/09	07:47:32.0	XRT_FOCUS_POSITION_431_OG [0x1af]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2010/12/09	07:47:52.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2010/12/09	07:47:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/09	07:47:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/09	07:47:58.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	07:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	07:54:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	07:54:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2010/12/09	07:55:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	00 00 00 00	
2010/12/09	07:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/09	07:55:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/09	07:55:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	07:57:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	07:57:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2010/12/09	07:57:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/12/09	07:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	08:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	08:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	08:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	08:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	08:41:00.0	XRT_Custom_418_OG [0x1a2]						
2010/12/09	08:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	10:00:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	10:00:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	10:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	10:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	10:14:30.0	XRT_Custom_418_OG [0x1a2]						
2010/12/09	10:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	15:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	15:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	15:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	15:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	15:38:00.0	XRT_Custom_418_OG [0x1a2]						
2010/12/09	15:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						

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2010/12/09	15:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	15:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	15:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	15:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	15:47:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	15:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	17:00:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	17:00:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	17:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	17:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	17:23:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	17:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/12/09	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2010/12/09	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/09	18:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/09	18:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/12/09	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/12/09	18:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00		
2010/12/09	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/09	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/09	18:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/09	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	18:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2010/12/09	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/12/09	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	18:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	18:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	18:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	18:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	19:00:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	19:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	20:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	20:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	20:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	20:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	20:38:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	20:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	21:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	21:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/09	21:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	21:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	21:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	22:14:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	22:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/09	23:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

2010/12/09	23:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/09	23:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/09	23:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/09	23:42:30.0	XRT_Custom_418_OG [0x1a2]						
2010/12/09	23:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	00:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	00:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/10	00:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/10	01:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/10	01:12:30.0	XRT_Custom_418_OG [0x1a2]						
2010/12/10	01:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	02:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	02:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/10	02:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/10	02:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/10	02:49:00.0	XRT_Custom_418_OG [0x1a2]						
2010/12/10	02:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	03:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	03:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/10	03:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/10	03:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/10	04:26:00.0	XRT_Custom_418_OG [0x1a2]						
2010/12/10	04:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	05:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	05:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/10	05:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/10	05:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/10	06:03:30.0	XRT_Custom_418_OG [0x1a2]						
2010/12/10	06:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	06:13:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	06:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/12/10	06:13:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2010/12/10	06:13:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/10	06:13:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/10	06:16:26.0	XRT_ARS_DIS_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/10	06:16:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/12/10	06:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/10	06:23:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/10	06:23:26.0	XRT_ROI_A_445_OG [0x1bd]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08		
2010/12/10	06:23:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00		
2010/12/10	06:23:31.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/12/10	06:23:51.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/10	06:23:53.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/10	06:23:55.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/10	06:26:29.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/10	06:26:31.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2010/12/10	06:26:34.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		

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2010/12/10	06:26:36.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/10	07:17:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/10	07:17:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/10	07:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/10	07:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/10	08:57:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/10	08:57:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/10	08:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/10	09:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/10	10:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					