

XRT Timeline to be uploaded on 2010/12/11

Period: 2010/12/11 09:27:00 - 2010/12/16 09:16:00

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Normal mode

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XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad												
Term	Pointing (x, y)						Comment					
12/11 09:52:30 - 12/11 17:49:54	Track (601.0, -376.2) @ 12/11 09:37:00						# OP start + 10min; Track SW filament channel.					
12/11 18:03:00 - 12/12 05:59:54	Track (651.8, -376.0) @ 12/11 18:00:00						# Continue to track SW filament channel.					
12/12 06:13:00 - 12/12 10:59:54	Track (718.3, -375.9) @ 12/12 06:10:00						# Continue to track SW filament channel.					
12/12 14:33:00 - 12/12 17:53:54	Track (758.4, -376.0) @ 12/12 14:30:00						# Continue to track SW filament channel.					
12/12 18:12:00 - 12/13 06:26:54	Fixed (-900.0, 330.0)						# AR approaching E-limb.					
12/13 06:40:00 - 12/13 17:50:00	Track (743.4, 489.5) @ 12/13 06:37:00						# Track AR 11131.					
12/13 18:26:30 - 12/14 01:42:30	Track (782.8, 488.7) @ 12/13 18:23:30						# Continue to track AR 11131.					
12/14 02:03:00 - 12/14 02:59:54	Track (667.9, 279.6) @ 12/14 02:00:00						# HOP 173; Track AR11133.					
12/14 03:13:00 - 12/14 06:28:30	Track (806.1, 488.0) @ 12/14 03:10:00						# HOP173; Track AR 11131.					
12/14 07:03:00 - 12/14 08:08:30	Fixed (811.2, 502.8)						# Switch to fixed pointing near the W-limb.					
PROG= 07 Inf.-time(s)												
└ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┴ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 95 4-time(s) 2.0sec												
└─┬ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┴ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 52 35-time(s) 120.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec												
└─┴ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)						Comment					
12/11 17:53:00 - 12/11 17:59:54	Fixed (0.0, 0.0)						synoptic, shifted -10.0 min					
12/12 06:03:00 - 12/12 06:09:54	Fixed (0.0, 0.0)						synoptic					
12/12 17:57:00 - 12/12 18:08:54	Fixed (0.0, 0.0)						synoptic, shifted -6.0 min					
12/13 06:30:00 - 12/13 06:36:54	Fixed (0.0, 0.0)						synoptic, shifted 27.0 min					
12/13 18:16:30 - 12/13 18:23:24	Fixed (0.0, 0.0)						synoptic, shifted 13.5 min					
PROG= 03 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─┬ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┴ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┴ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 8 1-time(s) 4.0sec												
└─┬ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┴ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1826: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al (64s) - 2 loops												
Term	Pointing (x, y)						Comment					
12/12 11:03:00 - 12/12 14:29:54	Track (-9.5, -0.1) @ 12/12 11:00:00						# TOO QS Normal Map Study + EIS 3.5hr sensitivity run.					
PROG= 09 1-time(s)												
└ Subr= 1 2-time(s) 300.0sec												
└─ Seqn= 9 1-time(s) 2.0sec												
└─┬ Open/thick-Al Open/thick-Al close Safe Norm 64.0s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 12 1-time(s) 2.0sec												
└─┬ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┴ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 11 1-time(s) 2.0sec												
└─┬ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┴ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 14 1-time(s) 2.0sec												
└─┬ Open/Ti-poly Open/Ti-poly close Safe Dark 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17A9: Full-disk Full-Res AI/Mesh, Ti/Poly, Thick/AI - 2 loops												
Term	Pointing (x, y)			Comment								
12/14 03:03:00 - 12/14 03:09:54	Fixed (0.0, 0.0)			synoptic in support of HOP173								
PROG= 08 1-time(s)												
└─ Subr= 1 2-time(s) 300.0sec												
└─ Seqn= 13 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	22.6s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 12 1-time(s) 2.0sec												
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 14 1-time(s) 2.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)			Comment								
12/11 09:52:30 - 12/11 17:49:54	Track (601.0, -376.2) @ 12/11 09:37:00			# OP start + 10min; Track SW filament channel.								
12/11 18:03:00 - 12/12 05:59:54	Track (651.8, -376.0) @ 12/11 18:00:00			# Continue to track SW filament channel.								
12/12 06:13:00 - 12/12 10:59:54	Track (718.3, -375.9) @ 12/12 06:10:00			# Continue to track SW filament channel.								
12/12 14:33:00 - 12/12 17:53:54	Track (758.4, -376.0) @ 12/12 14:30:00			# Continue to track SW filament channel.								
12/12 18:12:00 - 12/13 06:26:54	Fixed (-900.0, 330.0)			# AR approaching E-limb.								
12/13 06:40:00 - 12/13 17:50:00	Track (743.4, 489.5) @ 12/13 06:37:00			# Track AR 11131.								
12/13 18:26:30 - 12/14 01:42:30	Track (782.8, 488.7) @ 12/13 18:23:30			# Continue to track AR 11131.								
12/14 02:03:00 - 12/14 02:59:54	Track (667.9, 279.6) @ 12/14 02:00:00			# HOP 173; Track AR11133.								
12/14 03:13:00 - 12/14 06:28:30	Track (806.1, 488.0) @ 12/14 03:10:00			# HOP173; Track AR 11131.								
12/14 07:03:00 - 12/14 08:08:30	Fixed (811.2, 502.8)			# Switch to fixed pointing near the W-limb.								
PROG= 16 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec												
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec												
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec												
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 30-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec												
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 4 24-time(s) 600.0sec												
└─ Seqn= 89 1-time(s) 2.0sec												

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

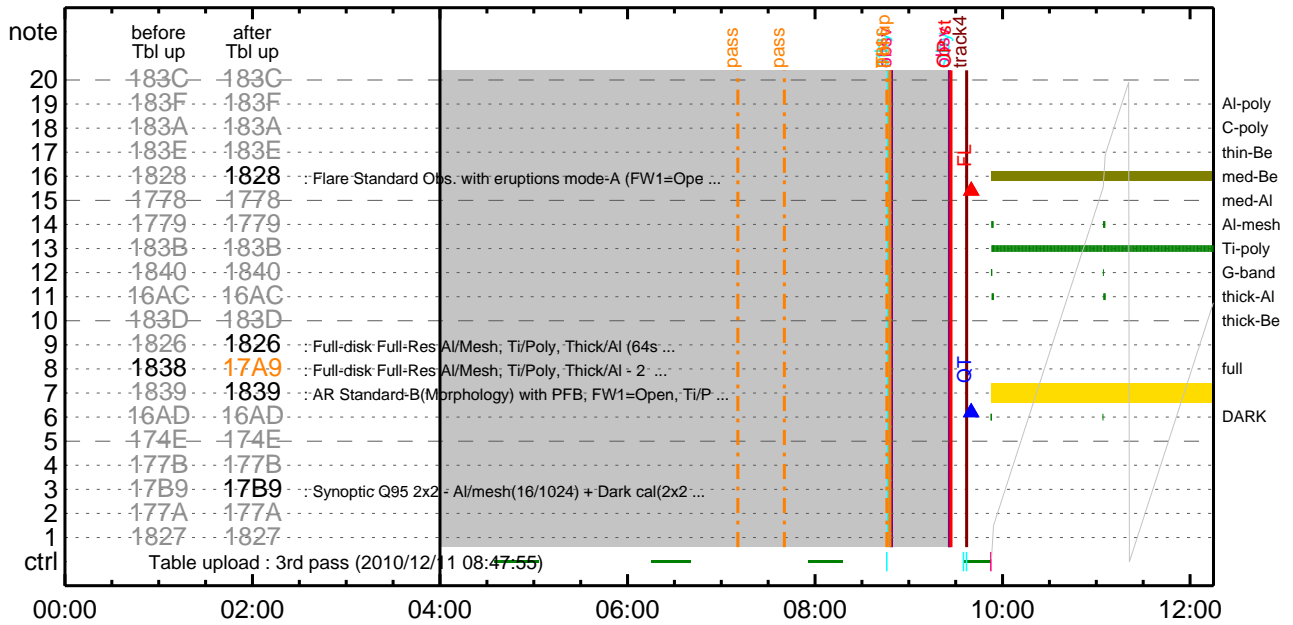
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Flare Detection

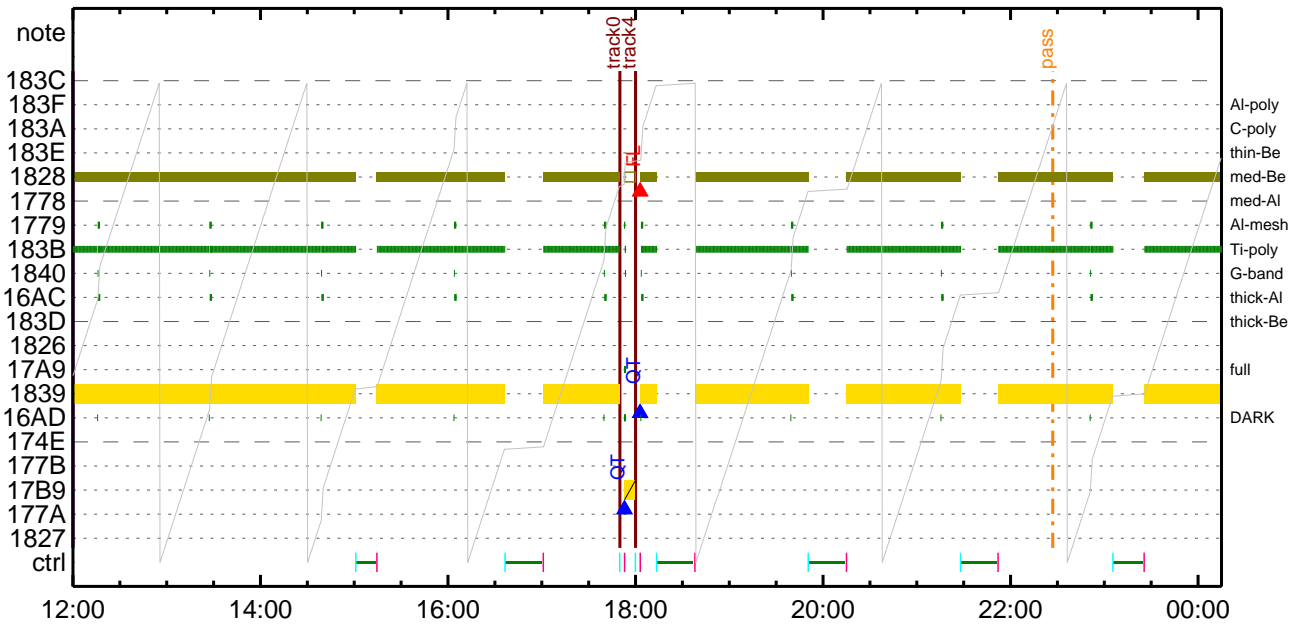
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FLD Patrol												
Term	Pointing (x, y)							Comment				
12/11 09:37:16 - 12/11 17:50:16	Track (601.0, -376.2)							@ 12/11 09:37:00	# OP start + 10min; Track SW filament channel.			
12/11 18:00:16 - 12/12 06:00:16	Track (651.8, -376.0)							@ 12/11 18:00:00	# Continue to track SW filament channel.			
12/12 06:10:16 - 12/12 11:00:16	Track (718.3, -375.9)							@ 12/12 06:10:00	# Continue to track SW filament channel.			
12/12 14:30:16 - 12/12 17:54:16	Track (758.4, -376.0)							@ 12/12 14:30:00	# Continue to track SW filament channel.			
12/12 18:09:16 - 12/13 06:27:16	Fixed (-900.0, 330.0)								# AR approaching E-limb.			
12/13 06:37:16 - 12/13 18:13:46	Track (743.4, 489.5)							@ 12/13 06:37:00	# Track AR 11131.			
12/13 18:23:46 - 12/14 03:00:16	Track (782.8, 488.7)							@ 12/13 18:23:30	# Continue to track AR 11131.			
12/14 03:10:16 - 12/16 09:16:00	Track (806.1, 488.0)							@ 12/14 03:10:00	# HOP173; Track AR 11131.			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

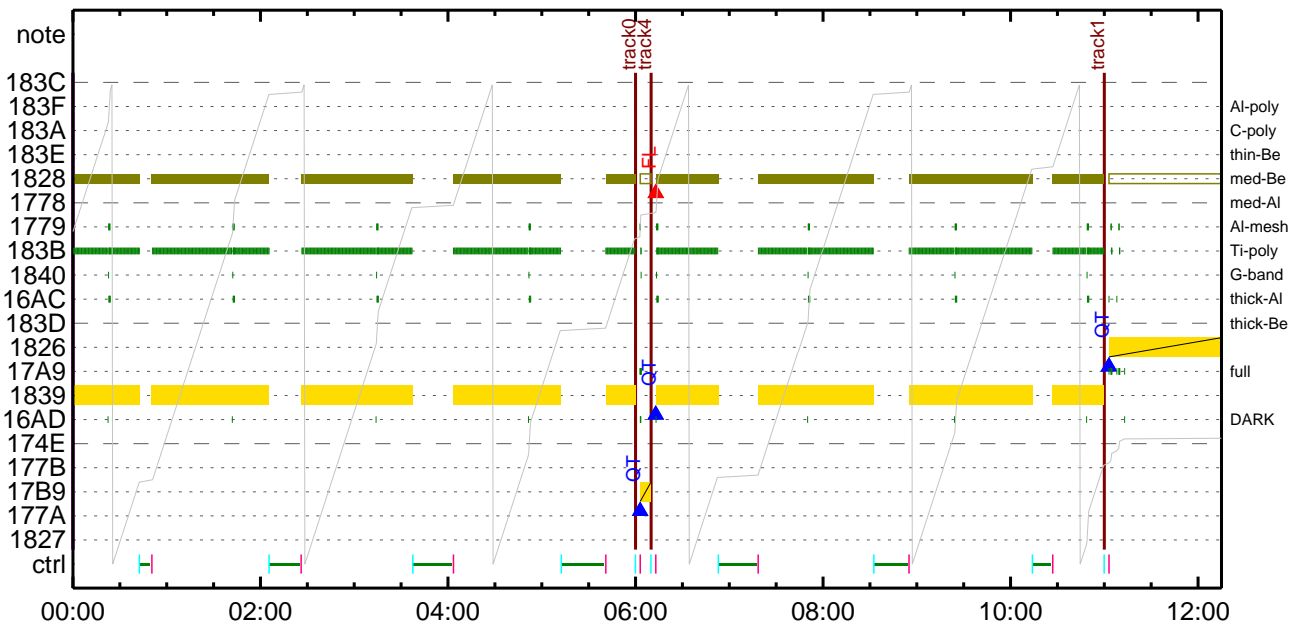
CMDI #0662 2010/12/11



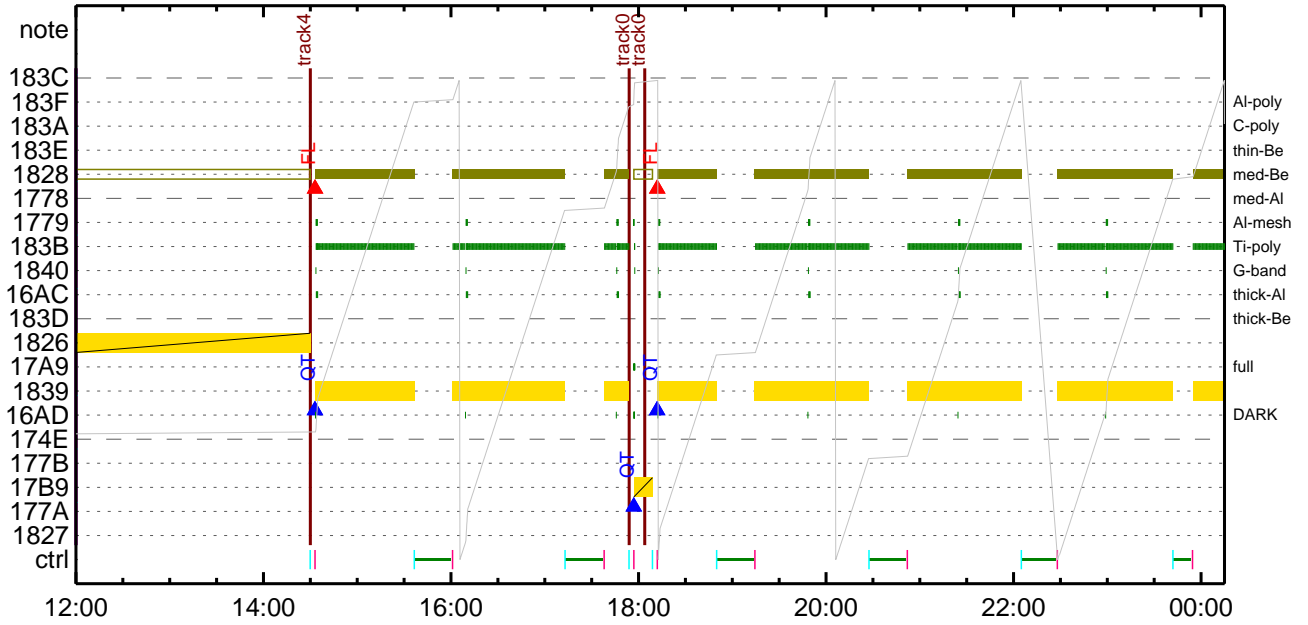
CMDI #0662 2010/12/11



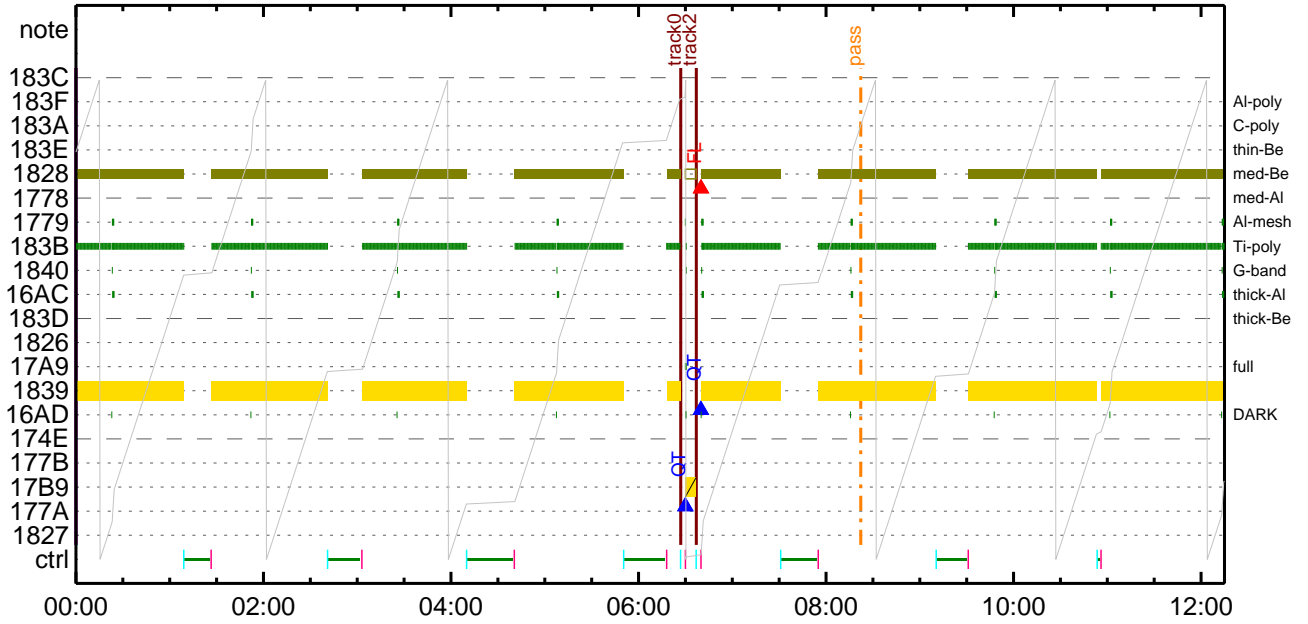
CMDI #0662 2010/12/12



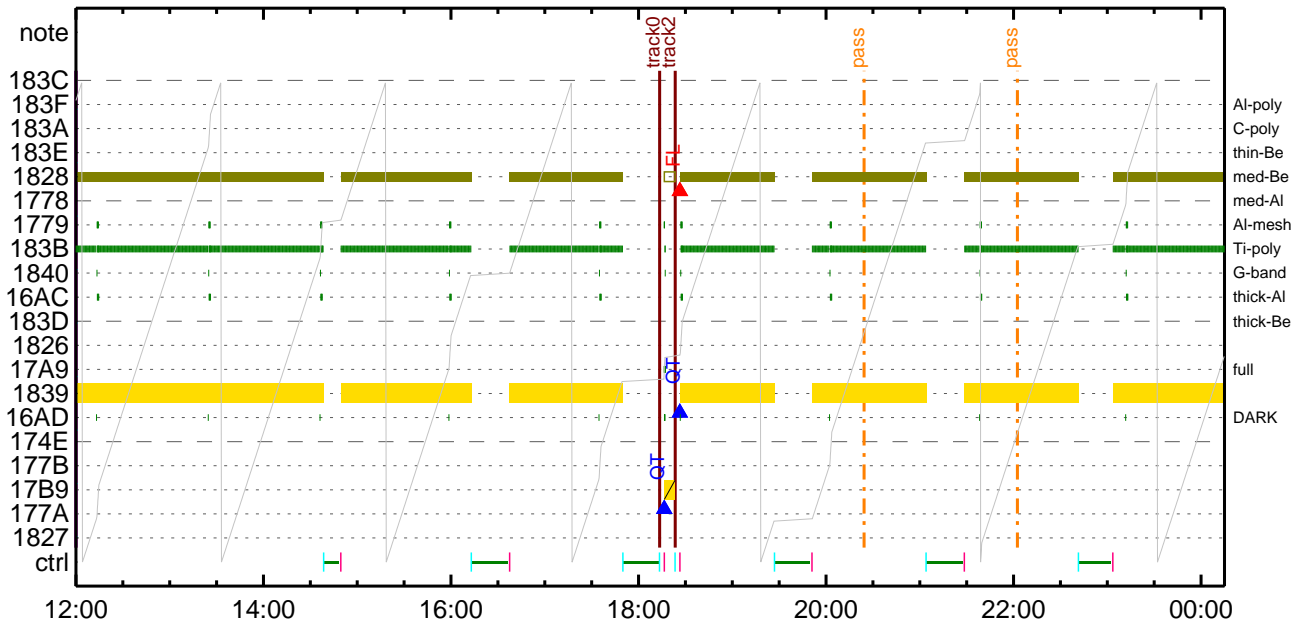
CMDI #0662 2010/12/12



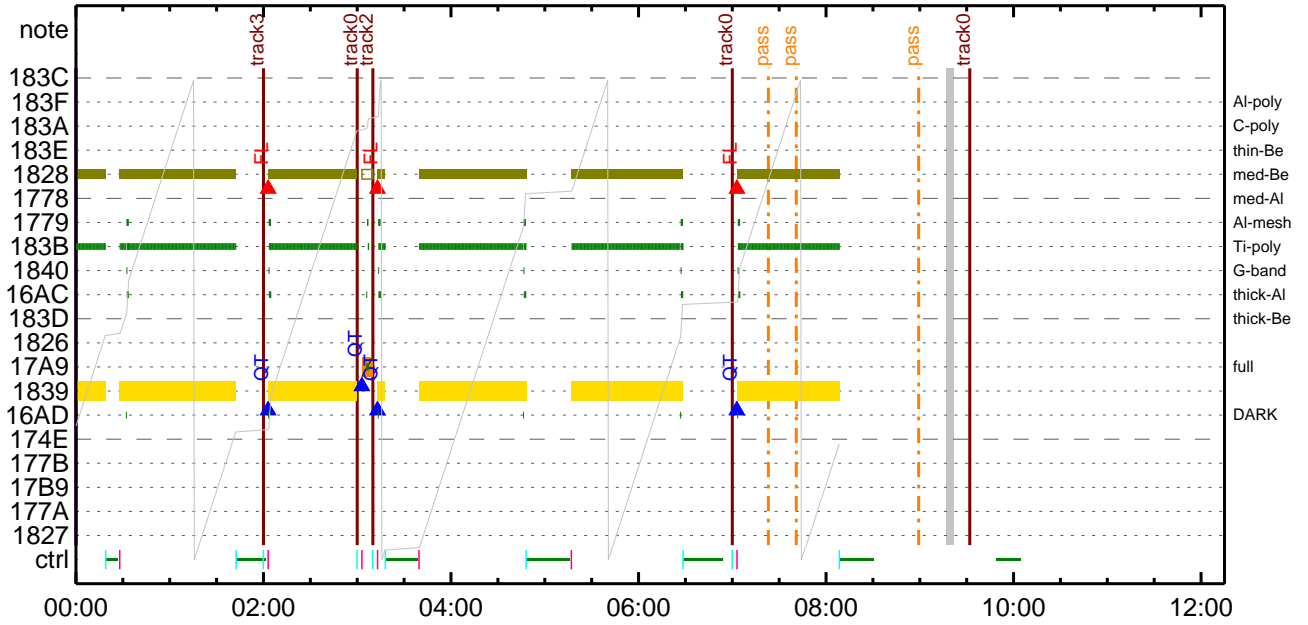
CMDI #0662 2010/12/13



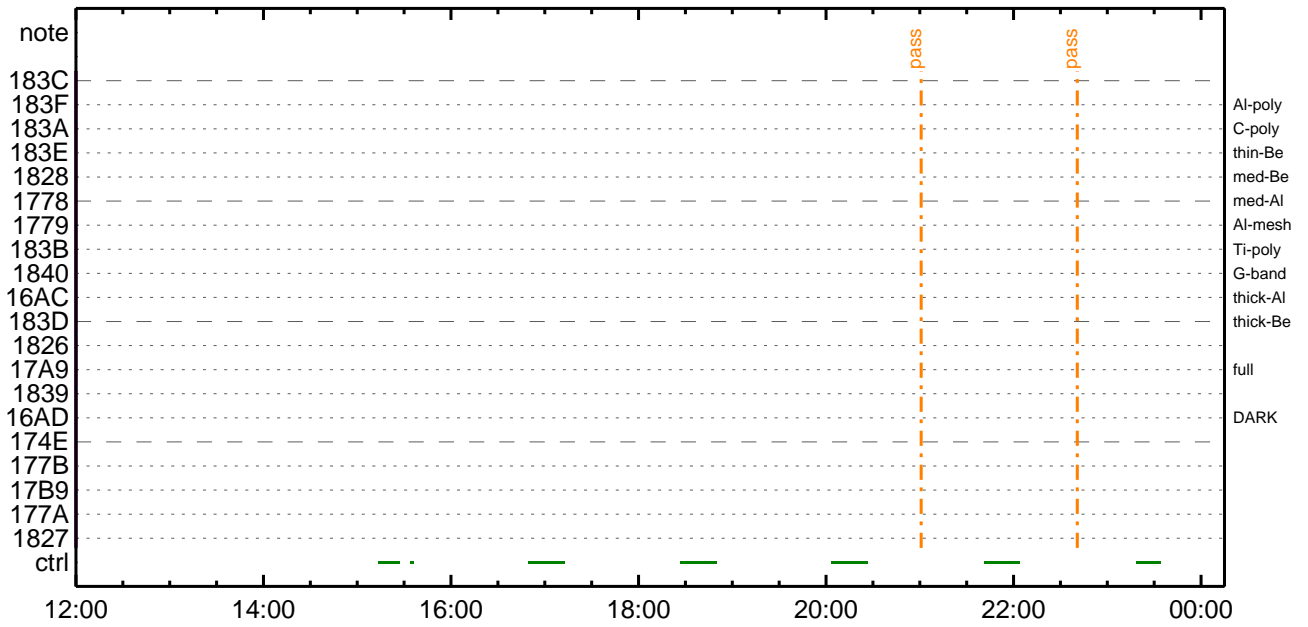
CMDI #0662 2010/12/13



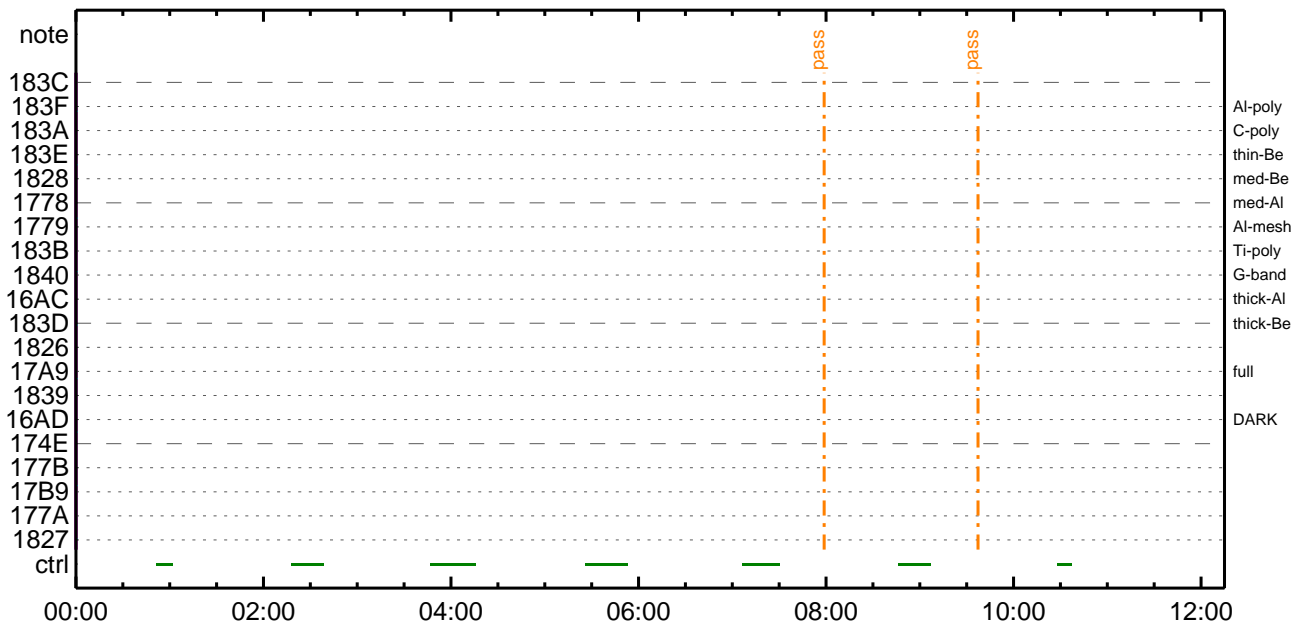
CMDI #0662 2010/12/14



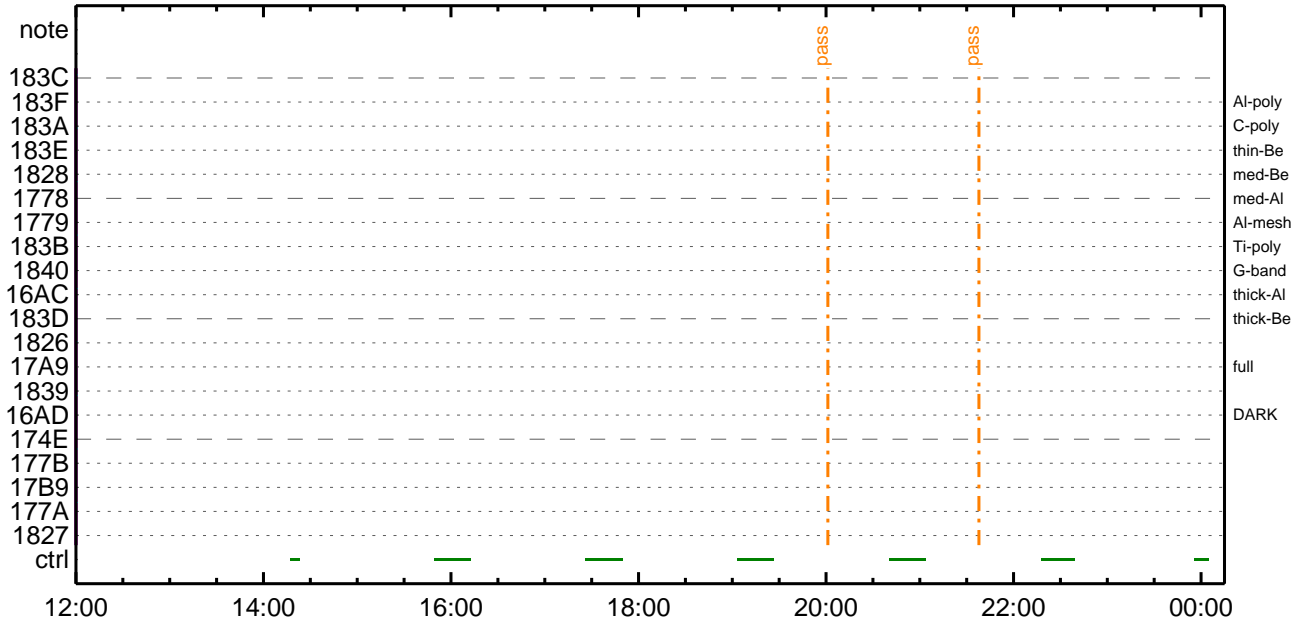
CMDI #0662 2010/12/14



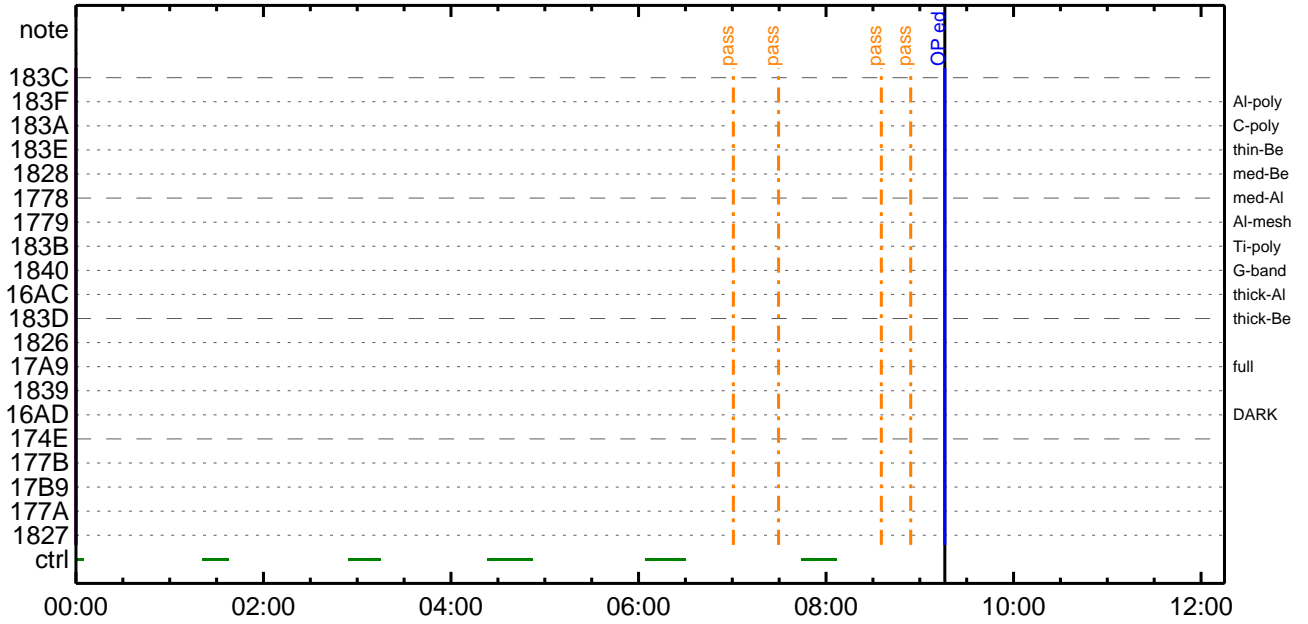
CMDI #0662 2010/12/15



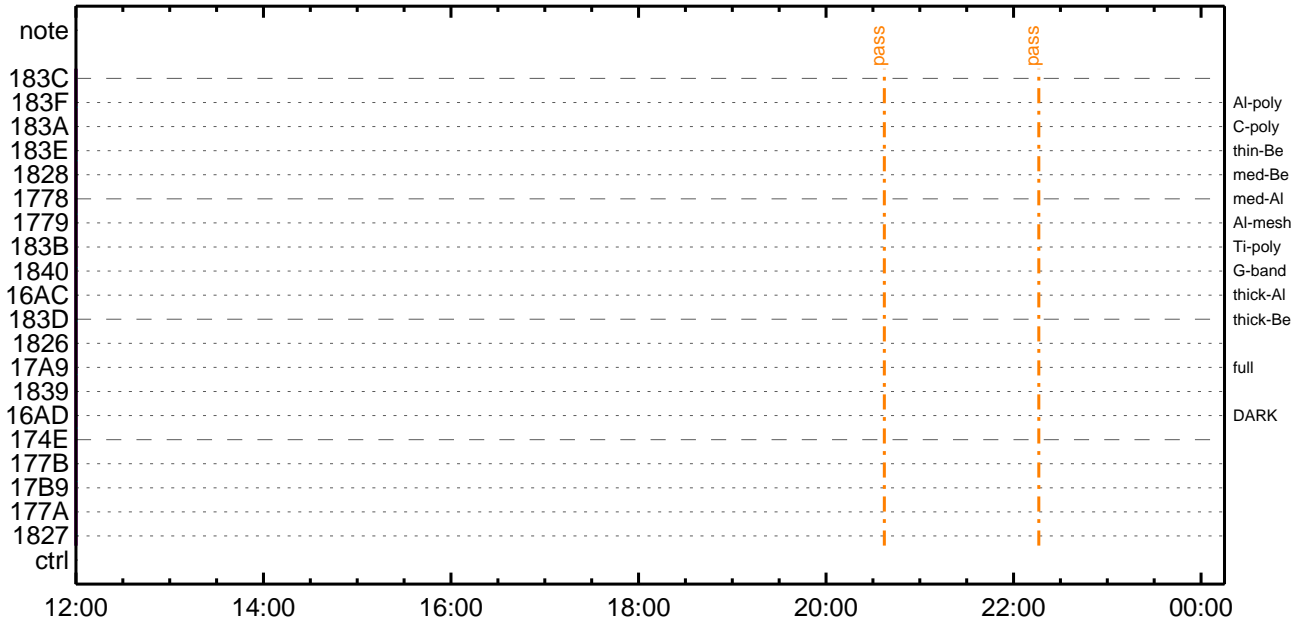
CMDI #0662 2010/12/15



CMDI #0662 2010/12/16



CMDI #0662 2010/12/16




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-761:OP
0104 ( )
0105 S. OG og-761:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹ç°²E¼î¹çòÇòâ) *****
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0182 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0183 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0184 +. TI 2010-12-11 09:22:00.0
0185 DC 01-B3 DHU_OP_STOP
0186 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0187 C.
0188 +. TI 2010-12-11 09:22:01.0
0189 DC 01-B4 DHU_OP_COPY
0190 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0191 C.
0192 +. TI 2010-12-11 09:22:01.0
0193 DC 01-B5 DHU_OPOG_COPY
0194 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-12-11 09:26:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °È²¼αîÄè%îíñαîîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                        EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2010-12-11 09:26:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2010-12-11 09:26:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2010-12-11 09:26:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-12-11 09:26:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼αα¼Ä»Ûα¹αè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷÐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F8 XRT_STBY
0104 BC (03 01)
0105 +. DC 07-F8 XRT_OPERATE
0106 BC (03 02)
0107 + DC 07-F0 MDP_XRT_MODE_STBY
0108 BC (c3)
0109 . C. ----- Success Verify ? OK / NG____
0110 C.
0111 C. XRT Obs. Table Upload
0112 . S. RAM ram-291:MDP_OBS_X
0113 ( )
0114 C.
0115 +. DC 07-F0 MDP_DUMP_XRTTBL
0116 BC (84 07 00 00 00 3a d4)
0117 . C. ----- Comparison Check ? OK / ERR ____
0118 C.
0119 C.
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 01 b1 b1 04 04)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 02 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 03 b1 b1 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 04 b1 b1 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 05 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 06 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 07 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 08 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 09 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2010-12-11 09:26:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C. ***** XRT END *****
0155 . C. *****
0156 C. SOT table upload
0157 C. *****
0158 . C. < Stop FG table >
0159 +. DC 07-F0 MDP_FG_CTRL_MANU
0160 BC (51)
0161 . C. -----
0162 C. MDP_FG_CTRL_MODE = MANU [ ]
0163 C. -----
0164 C.
0165 . C. <Upload FG Observation Table>
0166 . S. RAM ram-267:MDP_OBS_F
0167 ( )
0168 C.
0169 . C. < Dump RAMID=MDP_OBS_F >
0170 +. DC 07-F0 MDP_DUMP_FGTBL
0171 BC (82 07 00 00 00 38 b8)
0172 C. -----
0173 C. MDP_OBS_F verify = OK/NG [ ]
0174 C. -----
0175 C.
0176 C. *****
0177 C. SOT TI command set
0178 C. *****
0179 C. Execute, after the success of TBL upload.
0180 +. TI 2010-12-11 09:26:18.0
0181 DC 07-F0 MDP_SOT_MODE_OBSV
0182 BC (40)
0183 . C. -----
0184 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0185 C. -----
0186 C.
0187 C.
0188 . C. ***** MDP `uÃiãî»öÿoÊÄð¹æDCBC•x²è *****
0189 C. (%ã°iÿÓÿÄÿËÿPÿËÿáÿçÿèæE%¼ã¼Ä»Û¹æé)
0190 . S. DC-BC dcbc-402:DCBC
0191 (MDP_known_event)
0192 C.
0193 C.

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0194 . C. ***** ¥D¥!•İ Daily±;İÑòĚ'Ø¹òĚDCBC•x²è *****
0195 . S. DC-BC dcbc-153:DCBC
0196 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0197 C.
0198 C.
0199 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0200 C.
0201 . C. ***** LOS *****
0202 C.
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*** OP Sequence for XRT ***

2010/12/11	09:35:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	09:35:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	09:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/11	09:36:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	09:36:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2010/12/11	09:37:00.0	AOCS_Orе-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2010/12/11	09:37:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2010/12/11	09:37:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2010/12/11	09:37:20.0	XRT_ARS_DIS_420_OG [0x1a4]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/12/11	09:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/11	09:39:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	09:39:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07					
2010/12/11	09:39:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2010/12/11	09:52:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/11	15:01:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	15:01:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	15:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/11	15:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/11	15:13:30.0	XRT_Custom_418_OG [0x1a2]								
2010/12/11	15:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/11	16:36:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	16:36:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	16:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/11	16:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/11	17:00:00.0	XRT_Custom_418_OG [0x1a2]								
2010/12/11	17:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/11	17:49:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	17:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2010/12/11	17:50:00.0	AOCS_Orе-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2010/12/11	17:50:16.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2010/12/11	17:50:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2010/12/11	17:52:56.0	XRT_ARS_DIS_447_OG [0x1bf]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/12/11	17:52:58.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2010/12/11	17:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/11	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	17:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2010/12/11	18:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2010/12/11	18:00:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2010/12/11	18:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2010/12/11	18:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/12/11	18:02:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	18:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07					
2010/12/11	18:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2010/12/11	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/11	18:13:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/11	18:13:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/11	18:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								

2010/12/11	18:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/11	18:37:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/11	18:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/11	19:50:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/11	19:50:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/11	19:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/11	19:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/11	20:14:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/11	20:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/11	21:28:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/11	21:28:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/11	21:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/11	21:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/11	21:51:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/11	21:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/11	23:05:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/11	23:05:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/11	23:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/11	23:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/11	23:24:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/11	23:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/12	00:42:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/12	00:42:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/12	00:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/12	00:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/12	00:49:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	00:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/12	02:05:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/12	02:05:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/12	02:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/12	02:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/12	02:25:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	02:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/12	03:37:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/12	03:37:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/12	03:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/12	03:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/12	04:02:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	04:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/12	05:12:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/12	05:12:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/12	05:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/12	05:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/12	05:40:00.5	XRT_Custom_418_OG [0x1a2]					
2010/12/12	05:41:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/12	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/12	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/12/12	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2010/12/12	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/12/12	06:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

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2010/12/12	06:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/12	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2010/12/12	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2010/12/12	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04	00 00 00 00	
2010/12/12	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/12	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/12	06:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/12	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/12	06:12:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2010/12/12	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2010/12/12	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	06:53:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	06:53:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/12	06:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/12	06:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/12	07:17:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	07:18:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	08:32:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	08:32:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/12	08:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/12	08:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/12	08:54:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	08:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	10:14:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	10:14:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/12	10:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/12	10:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/12	10:26:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/12	10:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	10:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2010/12/12	11:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	01	00 00 00 00	
2010/12/12	11:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/12	11:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/12	11:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/12	11:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/12/12	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/12	14:29:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/12	14:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2010/12/12	14:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04	00 00 00 00	
2010/12/12	14:30:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/12	14:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/12	14:30:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/12	14:32:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/12	14:32:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2010/12/12	14:32:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	

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2010/12/12	14:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	15:36:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	15:36:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	15:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/12	15:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/12	16:00:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/12	16:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	17:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	17:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	17:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/12	17:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/12	17:37:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/12	17:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/12	17:54:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/12	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/12	17:54:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/12	17:56:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/12	17:56:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/12/12	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	18:04:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e2 a8 50 00				
2010/12/12	18:08:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	18:08:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/12/12	18:09:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/12	18:09:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/12	18:09:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/12	18:11:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	18:11:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2010/12/12	18:11:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/12/12	18:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	18:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	18:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	18:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/12	18:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/12	19:13:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/12	19:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	20:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	20:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	20:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/12	20:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/12	20:51:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/12	20:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/12	22:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/12	22:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/12	22:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/12	22:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/12	22:27:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/12	22:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2010/12/12	23:42:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/12	23:42:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/12	23:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/12	23:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/12	23:53:30.0	XRT_Custom_418_OG [0x1a2]								
2010/12/12	23:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	01:09:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	01:09:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/13	01:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/13	01:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/13	01:25:30.0	XRT_Custom_418_OG [0x1a2]								
2010/12/13	01:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	02:41:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	02:41:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/13	02:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/13	02:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/13	03:02:00.0	XRT_Custom_418_OG [0x1a2]								
2010/12/13	03:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	04:10:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	04:10:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/13	04:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/13	04:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/13	04:39:30.0	XRT_Custom_418_OG [0x1a2]								
2010/12/13	04:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	05:50:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	05:50:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/13	05:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2010/12/13	05:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2010/12/13	06:17:00.0	XRT_Custom_418_OG [0x1a2]								
2010/12/13	06:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	06:26:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	06:26:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2010/12/13	06:27:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2010/12/13	06:27:16.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2010/12/13	06:27:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2010/12/13	06:29:56.0	XRT_ARS_DIS_447_OG [0x1bf]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/12/13	06:29:58.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2010/12/13	06:30:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	06:36:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/12/13	06:36:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2010/12/13	06:37:00.0	AOCS_Ore-point_Start_5_OG [0x09b]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2010/12/13	06:37:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2010/12/13	06:37:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2010/12/13	06:37:20.0	XRT_ARS_DIS_420_OG [0x1a4]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2010/12/13	06:39:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2010/12/13	06:39:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07					
2010/12/13	06:39:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2010/12/13	06:40:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/12/13	07:31:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					

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2010/12/13	07:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	07:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	07:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	07:54:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	07:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	09:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	09:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	09:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	09:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	09:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	09:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	10:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	10:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	10:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	10:55:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	10:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	10:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	14:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	14:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	14:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	14:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	14:48:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	14:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	16:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	16:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	16:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	16:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	16:36:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	16:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	17:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	17:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	17:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	17:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	18:13:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	18:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/13	18:13:30.0	AOCs_Or-e-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/13	18:13:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/13	18:13:48.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/13	18:16:26.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/13	18:16:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/12/13	18:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	18:23:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	18:23:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/12/13	18:23:30.0	AOCs_Or-e-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/12/13	18:23:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/13	18:23:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/13	18:23:50.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/13	18:26:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	18:26:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				

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2010/12/13	18:26:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/12/13	18:26:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	19:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	19:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	19:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	19:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	19:50:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	19:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	21:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	21:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	21:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	21:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	21:27:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	21:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/13	22:41:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/13	22:41:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/13	22:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/13	22:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/13	23:02:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/13	23:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/14	00:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/14	00:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/14	00:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/14	00:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/14	00:27:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/14	00:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/14	01:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/14	01:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/14	01:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/14	01:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/14	01:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/14	01:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/12/14	02:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/12/14	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/14	02:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/14	02:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/14	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/14	02:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2010/12/14	02:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/12/14	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/14	02:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/14	02:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/12/14	03:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/12/14	03:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/14	03:00:18.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/14	03:02:56.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/14	03:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2010/12/14	03:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/14	03:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							

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2010/12/14	03:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	03:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/12/14	03:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2010/12/14	03:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/12/14	03:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/12/14	03:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/12/14	03:12:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	03:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/12/14	03:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/12/14	03:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/14	03:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	03:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	03:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/14	03:38:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/14	03:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_Custom_418_OG [0x1a2]			
2010/12/14	04:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/14	04:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	04:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	04:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/14	05:16:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/14	05:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_Custom_418_OG [0x1a2]			
2010/12/14	06:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/14	06:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	06:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	06:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/14	06:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/14	06:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	07:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/12/14	07:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 d3 4c b7 e8
2010/12/14	07:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/12/14	07:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/12/14	07:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/12/14	07:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	07:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2010/12/14	07:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/12/14	08:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/14	08:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/14	08:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/14	08:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/14	09:32:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00