XRT Timeline to be uploaded on 2010/12/14

Period: 2010/12/14 09:22:00 - 2010/12/18 10:44:00

* * * * * **Normal mode** * * * * *

XOB #17F7: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 4min-cad

Term	Pointing (x, y)	Comment
12/14 09:35:00 - 12/14 17:59:54	Track (754.5, -482.6) @ 12/14 09:32:00	# OP start + 10min filament eruption

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 6 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 86 10-time(s) 240.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
12/14 18:03:00 - 12/14 18:09:54	Fixed (0.0, 0.0)	synoptic
12/15 03:03:00 - 12/15 03:09:54	Fixed (0.0, 0.0)	# Synoptic
12/15 18:08:30 - 12/15 18:15:24	Fixed (0.0, 0.0)	# synoptic, shifted 5.5 min
12/16 03:18:00 - 12/16 03:24:54	Fixed (0.0, 0.0)	# Synoptic shift 15min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #182B: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 30sec-cad

Term	Pointing (x, y)	Comment
12/14 18:13:00 - 12/15 01:59:54	Fixed (810.7, 486.4)	# AR11131 obs

PROG= 06 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 95 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec

Seqn= 18 45-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

XOB #180E: HOP173 prominence Al/mesh, Ti/poly, G-band-DPCM 1x1-512x512-FOV -AEC2-5min cadence

Term	Pointing (x, y)	Comment
12/15 02:03:00 - 12/15 02:18:00	Fixed (-910.0, 250.0)	# HOP173 by fixed pointing
12/16 02:03:00 - 12/16 02:54:00	Fixed (-910.0, 250.0)	# HOP173 by fixed pointing

PROG= 11 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 57 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 1 12-time(s) 300.0sec

Seqn= 68 1-time(s) 2.0sec

Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 34 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad

Term	Pointing (x, y)	Comment
12/15 03:13:00 - 12/15 17:26:00	Track (-631.6, 346.1) @ 12/15 03:10:00	# HOP173 by tracking AR
12/15 18:18:30 - 12/16 01:21:00	Track (-530.1, 348.6) @ 12/15 18:15:30	# Track NE AR
12/16 03:28:00 - 12/16 07:44:30	Track (-463.1, 350.1) @ 12/16 03:25:00	# HOP173 by tracking

PROG= 07 Inf.-time(s)													
Subr= 2 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 95 4-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec													
Seqn= 52 35-time(s) 120.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
12/14 09:35:00 - 12/14 17:59:54	Track (754.5, -482.6) @ 12/14 09:32:00	# OP start + 10min filament eruption
12/14 18:13:00 - 12/15 01:59:54	Fixed (810.7, 486.4)	# AR11131 obs
12/15 02:03:00 - 12/15 02:18:00	Fixed (-910.0, 250.0)	# HOP173 by fixed pointing
12/15 03:13:00 - 12/15 17:26:00	Track (-631.6, 346.1) @ 12/15 03:10:00	# HOP173 by tracking AR
12/15 18:18:30 - 12/16 01:21:00	Track (-530.1, 348.6) @ 12/15 18:15:30	# Track NE AR
12/16 02:03:00 - 12/16 02:54:00	Fixed (-910.0, 250.0)	# HOP173 by fixed pointing
12/16 03:28:00 - 12/16 07:44:30	Track (-463.1, 350.1) @ 12/16 03:25:00	# HOP173 by tracking

PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

Active Region Search

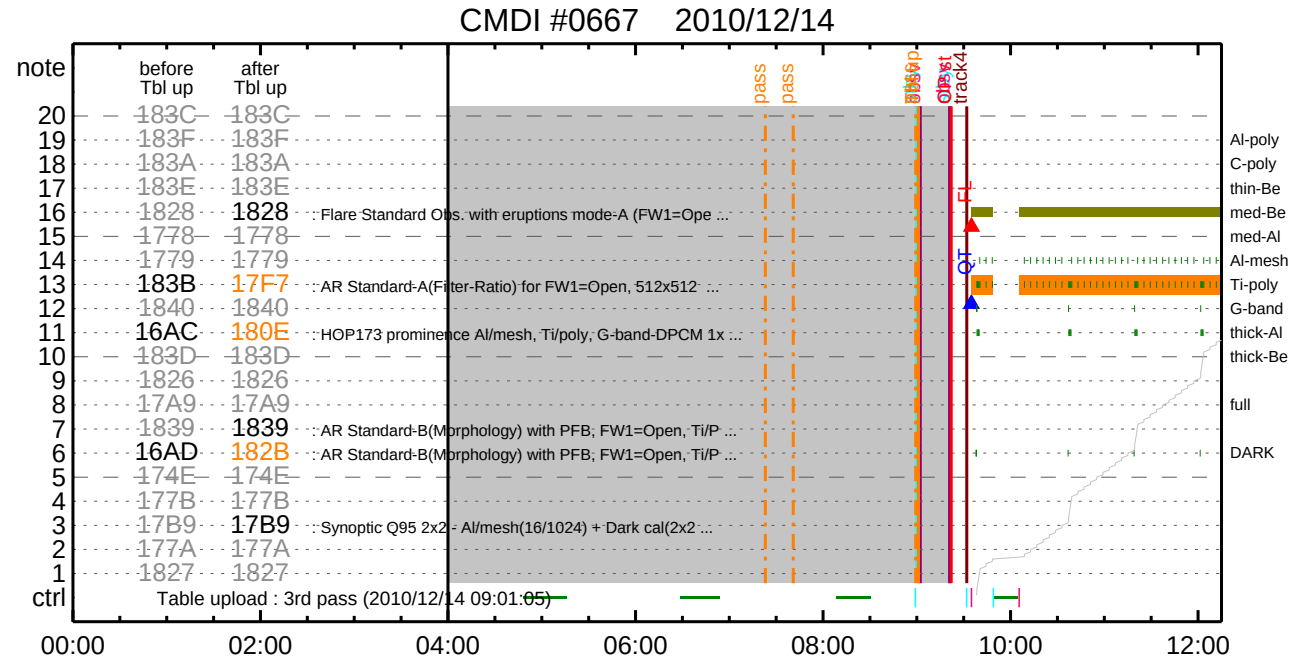
NOT USED

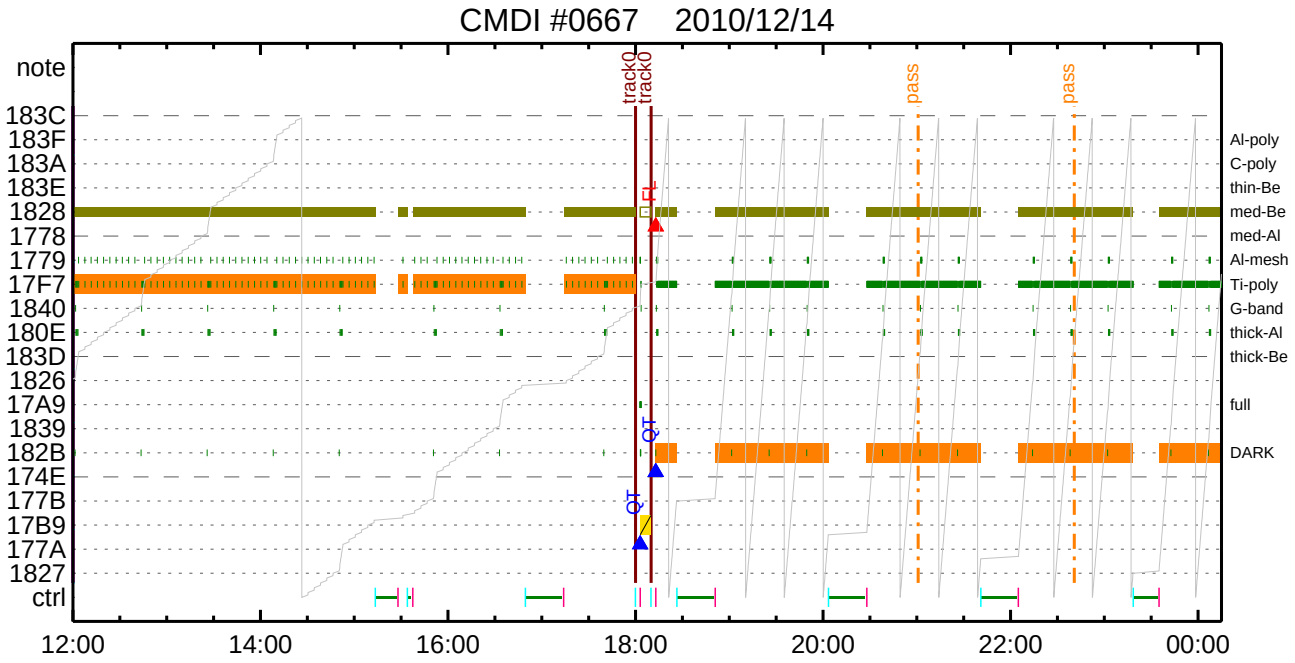
Flare Detection

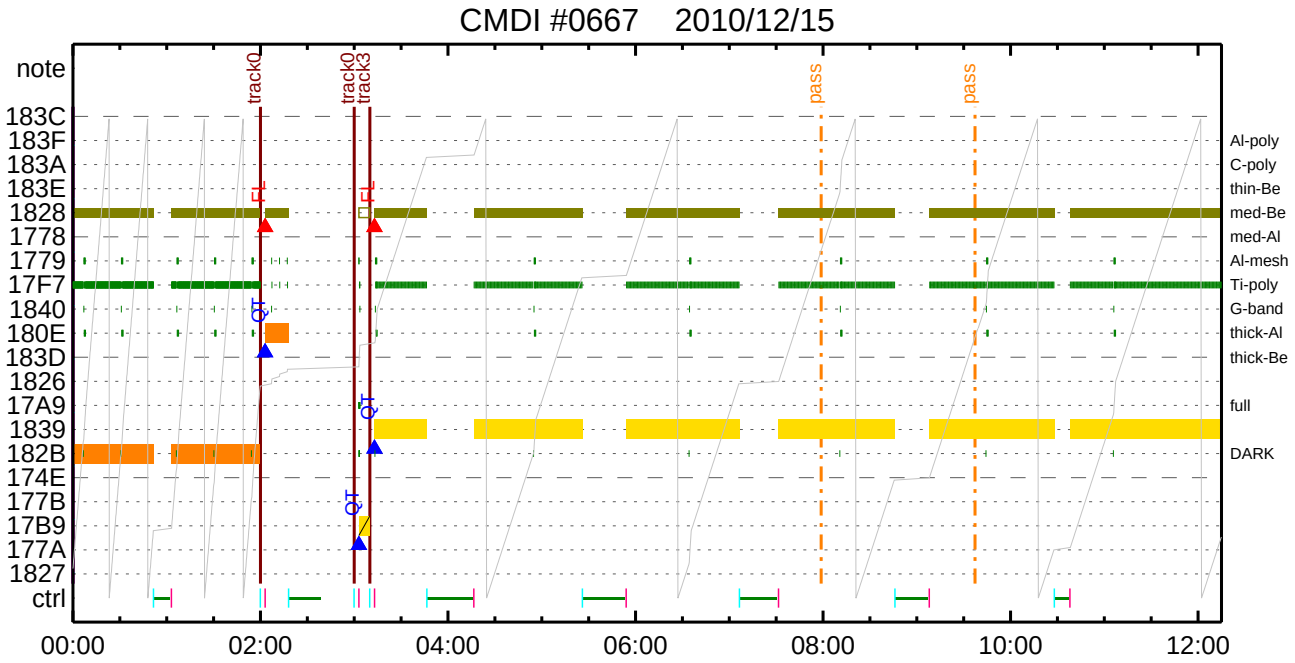
FLD Patrol

Term	Pointing (x, y)	Comment
12/14 09:32:16 - 12/14 18:00:16	Track (754.5, -482.6) @ 12/14 09:32:00	# OP start + 10min filament eruption
12/14 18:10:16 - 12/15 03:00:16	Fixed (810.7, 486.4)	# AR11131 obs
12/15 03:10:16 - 12/15 18:05:46	Track (-631.6, 346.1) @ 12/15 03:10:00	# HOP173 by tracking AR
12/15 18:15:46 - 12/16 03:15:16	Track (-530.1, 348.6) @ 12/15 18:15:30	# Track NE AR
12/16 03:25:16 - 12/18 10:44:00	Track (-463.1, 350.1) @ 12/16 03:25:00	# HOP173 by tracking

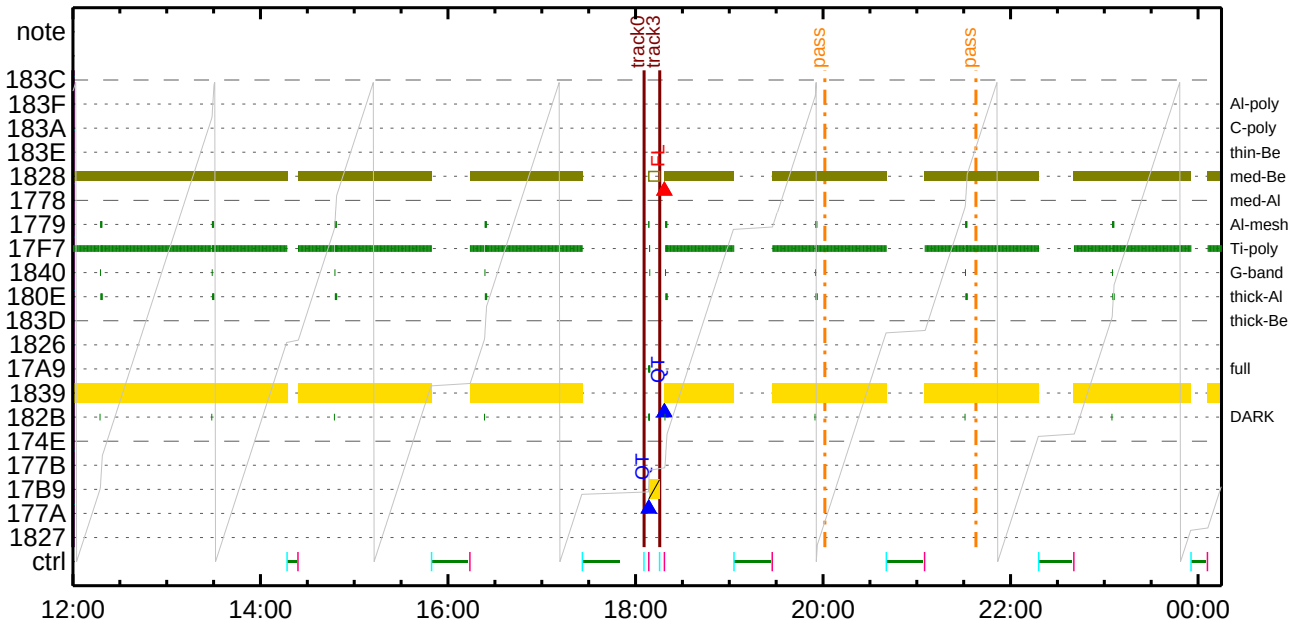
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0667 2010/12/14
note
before Tbl up
after Tbl up
20 183C 183C
19 183F 183F
18 183A 183A
17 183E 183E
16 1828 1828 : Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
15 1778 1778
14 1779 1779
13 183B 17F7 : AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 ...
12 1840 1840
11 16AC 180E : HOP173 prominence Al/mesh, Ti/poly, G-band-DPCM 1x ...
10 183D 183D
9 1826 1826
8 17A9 17A9
7 1839 1839 : AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
6 16AD 182B : AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
5 174E 174E
4 177B 177B
3 17B9 17B9 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
2 177A 177A
1 1827 1827
ctrl Table upload : 3rd pass (2010/12/14 09:01:05)
pass
pass
track4
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

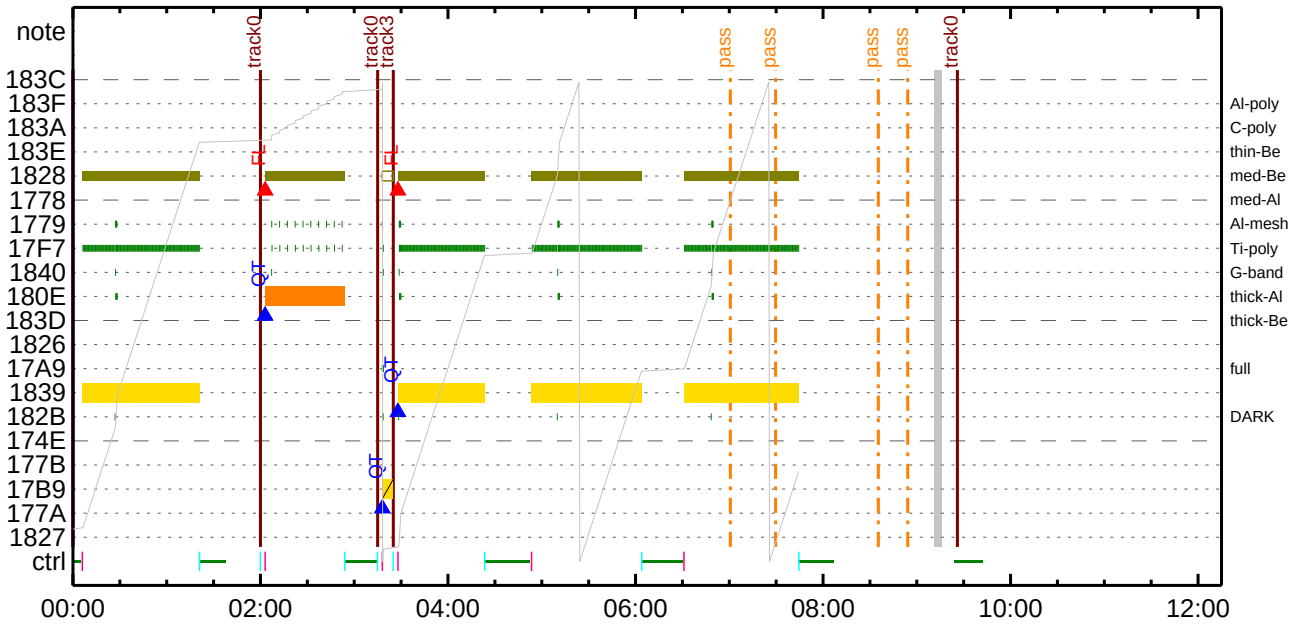

CMDI #0667 2010/12/14
note
track0
track0
pass
pass
FL
QT
QT
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0667 2010/12/15
note
track0
track0
track3
pass
pass
FL
FL
QT
QT
QT
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

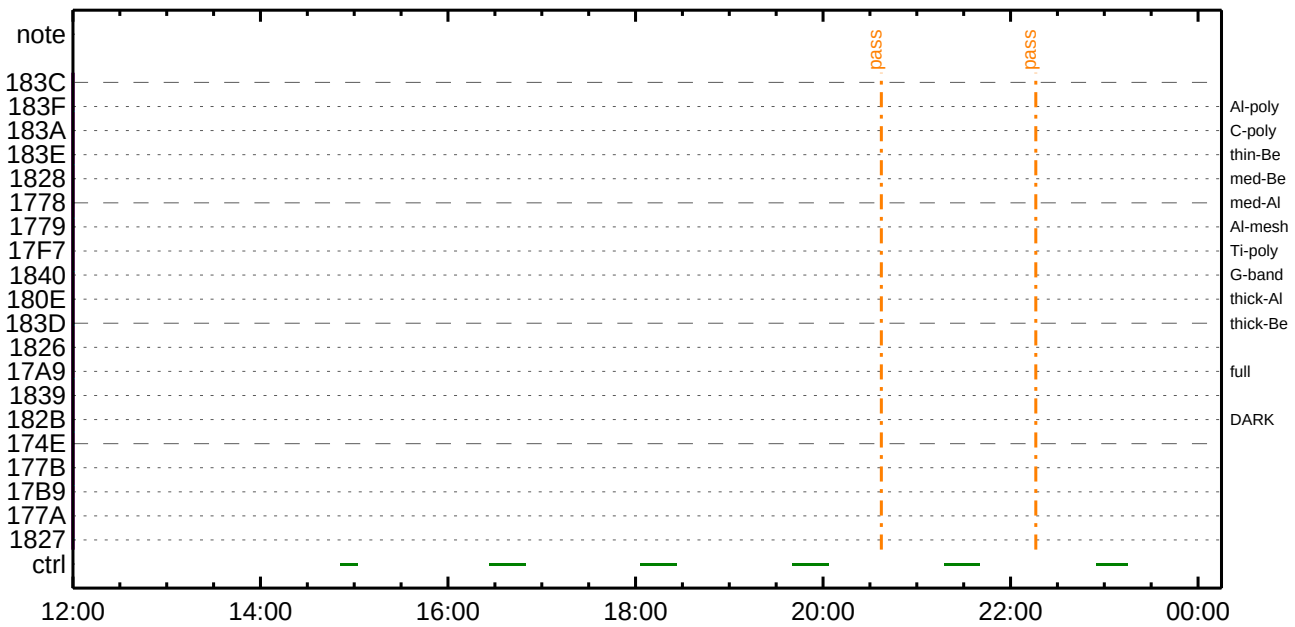
CMDI #0667 2010/12/15

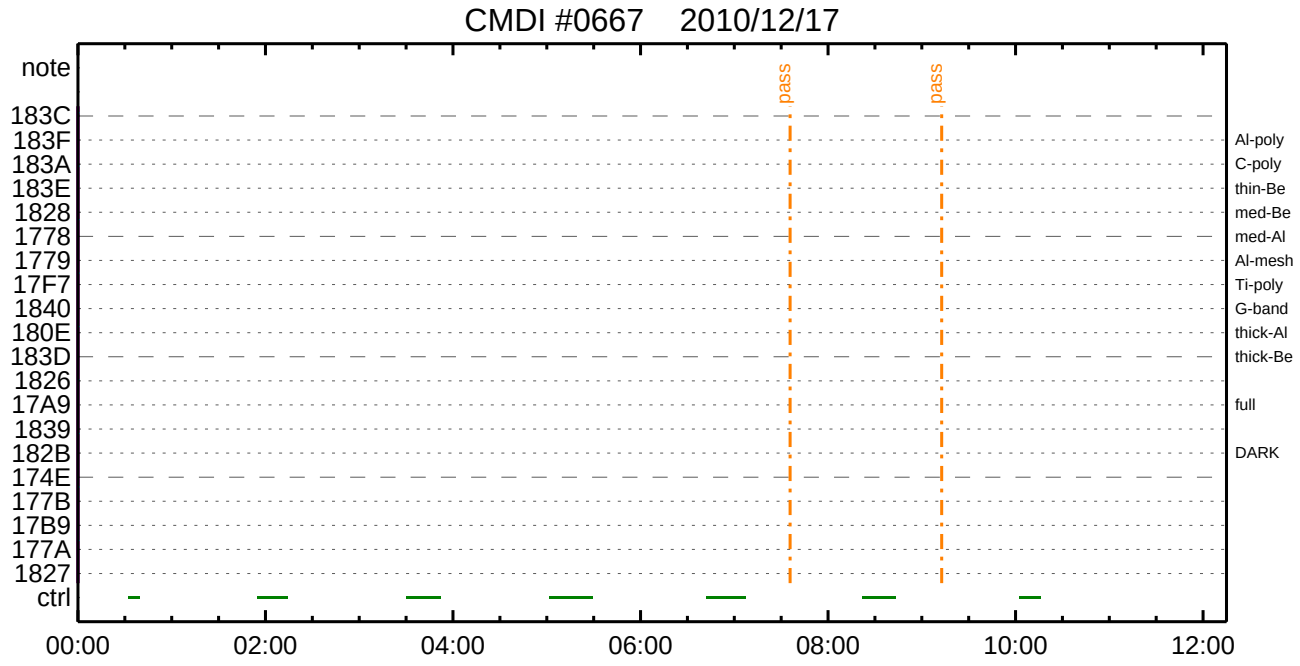


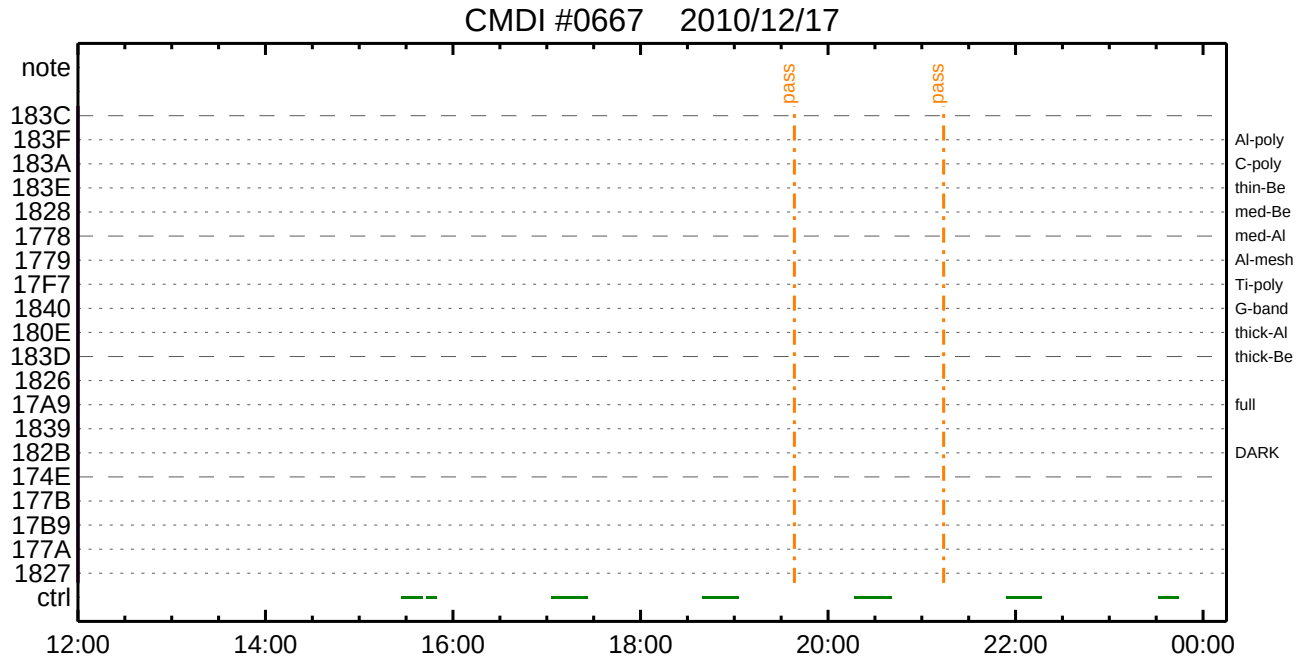
CMDI #0667 2010/12/16

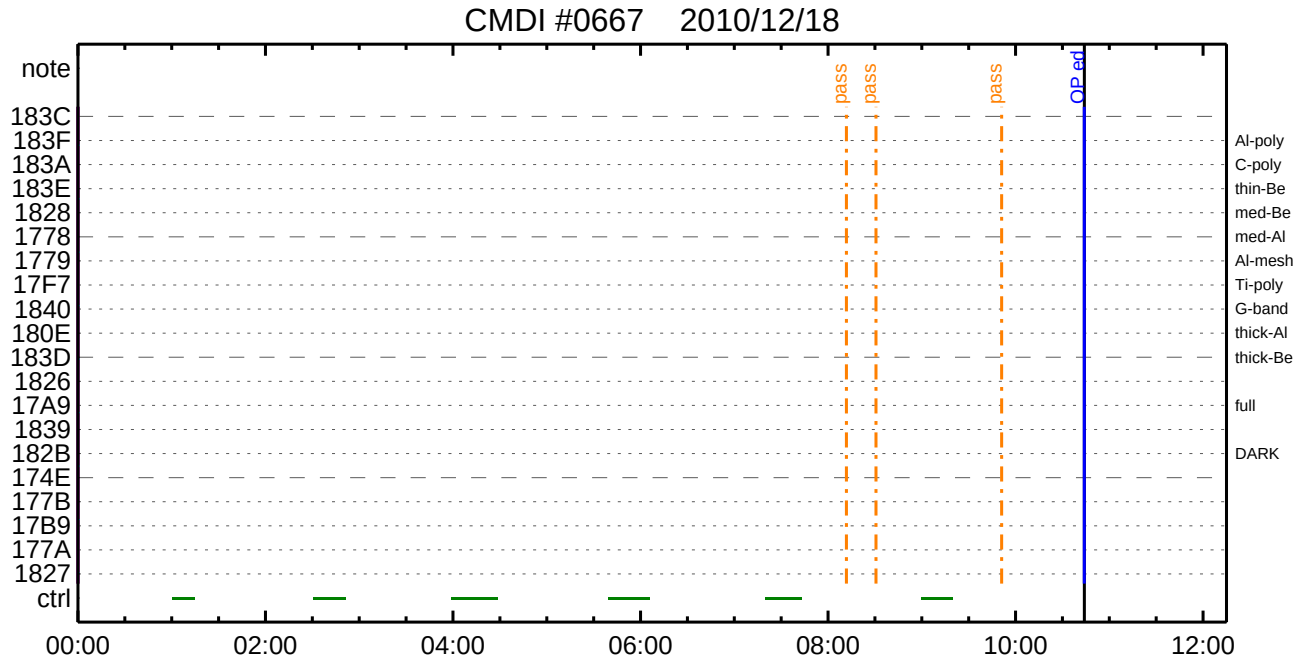


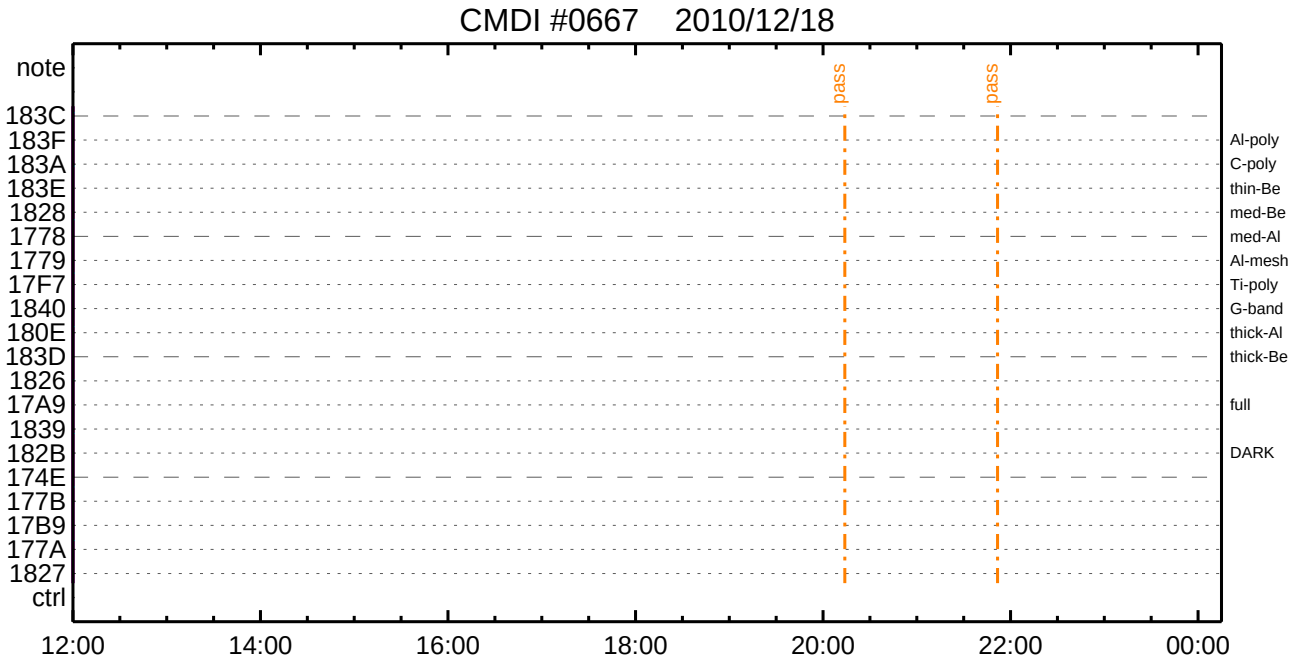
CMDI #0667 2010/12/16




CMDI #0667 2010/12/17
note
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0667 2010/12/17
note
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0667 2010/12/18
note
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0667 2010/12/18
note
183C
183F
183A
183E
1828
1778
1779
17F7
1840
180E
183D
1826
17A9
1839
182B
174E
177B
17B9
177A
1827
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-765 2010-12-14 13:15:39 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP    op-765:OP
     ()
   . S. OG    og-765:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎºè¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 00 7f 01 02)
     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 80 7f 01 02)
     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (21 00 41 01 02)
     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Çâ) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤Ìá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC       (02 0a f8)
     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2010-12-14 09:17:00.0
     DC 01-B3 DHU_OP_STOP
     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2010-12-14 09:17:01.0
     DC 01-B4 DHU_OP_COPY
     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2010-12-14 09:17:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2010-12-14 09:21:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2010-12-14 09:21:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2010-12-14 09:21:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2010-12-14 09:21:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2010-12-14 09:21:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-766 2010-12-14 13:15:39 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 7152887.1 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 7s
0059     C. *********************************
0060     C. EIS START OBSTBL LOAD
0061     C. *********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2010-12-14 09:21:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. *********************************
0074     C. EIS END OBSTBL LOAD
0075     C. *********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-767 2010-12-14 13:15:39 230 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +. DC 07-F8 XRT_STBY
0104     BC       (03 01)
0105  +. DC 07-F8 XRT_OPERATE
0106     BC       (03 02)
0107  +  DC 07-F0 MDP_XRT_MODE_STBY
0108     BC       (c3)
0109   . C. ----------- Success Verify ?             OK / NG____
0110     C.
0111     C. XRT Obs. Table Upload
0112   . S. RAM   ram-291:MDP_OBS_X
0113     ()
0114     C.
0115  +. DC 07-F0 MDP_DUMP_XRTTBL
0116     BC       (84 07 00 00 00 3a d4)
0117   . C. ----------- Comparison Check ?                      OK / ERR ____
0118     C.
0119     C.
0120  +. DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 01 b1 b1 04 04)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 02 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 03 b1 b1 08 08)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 04 b1 b1 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 05 85 83 06 06)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 06 85 83 08 08)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 07 80 80 20 20)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 08 80 80 20 08)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 09 80 80 08 20)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0a 85 83 06 06)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0b 80 80 08 08)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0f 80 80 06 06)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 10 80 80 08 08)
0146   . C. ---------- Success Verify ?           OK / NG ____
0147     C.
0148     C.
0149   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0150     C.
0151  +. DC 07-F0 MDP_XRT_MODE_OBSV
0152     BC       (c2)
0153  +. TI 2010-12-14 09:21:02.0
0154     DC 07-F0 MDP_XRT_MODE_OBSV
0155     BC       (c2)
0156   . C. ---------- Success Verify ?           OK / NG ____
0157     C.
0158     C. ************ XRT END ***********
0159   . C. ********************************
0160     C.  SOT table upload
0161     C. ********************************
0162   . C. < Stop FG table >
0163  +. DC 07-F0 MDP_FG_CTRL_MANU
0164     BC       (51)
0165   . C. ------------------------------------
0166     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0167     C. ------------------------------------
0168     C.
0169   . C. <Upload FG Observation Table>
0170   . S. RAM   ram-268:MDP_OBS_F
0171     ()
0172     C.
0173   . C. < Dump RAMID=MDP_OBS_F >
0174  +. DC 07-F0 MDP_DUMP_FGTBL
0175     BC       (82 07 00 00 00 38 b8)
0176     C. ------------------------------------
0177     C.  MDP_OBS_F verify = OK/NG        [ ]
0178     C. ------------------------------------
0179     C.
0180   . C. < Upload DPL table >
0181     C.
0182     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0183     C.
0184   . S. RAM   ram-271:MDP_DPL
0185     ()
0186     C.
0187   . C. < Dump RAMID=MDP_DPL >
0188  +. DC 07-F0 MDP_DUMP_FGTBL
0189     BC       (82 07 00 38 b8 00 40)
0190     C. ------------------------------------
0191     C.  MDP_DPL verify          = OK    [ ]
0192     C. ------------------------------------
0193     C.
```

```
0194     C. STS_CHK¤òON¤Ë¤¹¤ë
0195     C.
0196   . C. < Update MDP DSC PAR1 >
0197  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0198     BC       (4c)
0199     C.  MDP_CMD_CODE          = F04C0700[ ]
0200     C.  MDP_CMD_CNT        (count-up 1) [ ]
0201     C. ------------------------------------
0202     C.
0203   . C.
0204     C. ******************************
0205     C.  SOT TI command set
0206     C. ******************************
0207     C. Execute, after the success of TBL upload.
0208  +. TI 2010-12-14 09:21:18.0
0209     DC 07-F0 MDP_SOT_MODE_OBSV
0210     BC       (40)
0211   . C. ------------------------------------
0212     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0213     C. ------------------------------------
0214     C.
0215     C.
0216   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0217     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0218   . S. DC-BC dcbc-402:DCBC
0219     (MDP_known_event)
0220     C.
0221     C.
0222   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0223   . S. DC-BC dcbc-153:DCBC
0224     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0225     C.
0226     C.
0227   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0228     C.
0229   . C. ********** LOS **********
0230     C.
```

```
*** OP Sequence for XRT ***

2010/12/14  09:31:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  09:31:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/14  09:32:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/12/14  09:32:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/14  09:32:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/14  09:32:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/14  09:34:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  09:34:56.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2010/12/14  09:34:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/14  09:35:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  09:49:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  09:49:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  09:49:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  09:52:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  10:04:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  10:05:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  15:13:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  15:13:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  15:13:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  15:16:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  15:27:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  15:28:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  15:34:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  15:34:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  15:34:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  15:36:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  15:37:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  15:37:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  16:49:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  16:49:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  16:49:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  16:52:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  17:13:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  17:14:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  17:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  17:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/12/14  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/12/14  18:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/12/14  18:00:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/12/14  18:02:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/14  18:02:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/12/14  18:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  18:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  18:09:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/14  18:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 d4 c5 b7 f0
2010/12/14  18:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/14  18:10:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/14  18:10:20.0  XRT_ARS_DIS_420_OG [0x1a4]
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                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/14  18:12:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  18:12:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2010/12/14  18:12:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/14  18:13:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  18:26:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  18:26:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  18:26:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  18:29:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  18:50:00.5  XRT_Custom_418_OG [0x1a2]
2010/12/14  18:51:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  20:03:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  20:03:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  20:03:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  20:06:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  20:27:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  20:28:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  21:41:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  21:41:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  21:41:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  21:44:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  22:04:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  22:05:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/14  23:18:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/14  23:18:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/14  23:18:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/14  23:21:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/14  23:34:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/14  23:35:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  00:51:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  00:51:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  00:51:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  00:54:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  01:02:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  01:03:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  01:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  01:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/15  02:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 e9 cb 50 e5
2010/12/15  02:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/15  02:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/15  02:00:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/15  02:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  02:02:56.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/12/15  02:02:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/15  02:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  02:18:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  02:18:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  02:18:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  02:21:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  02:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
```

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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  02:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/12/15  03:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2010/12/15  03:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/12/15  03:00:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/12/15  03:02:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/15  03:02:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/12/15  03:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  03:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  03:09:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/15  03:10:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2010/12/15  03:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/15  03:10:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/15  03:10:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/15  03:12:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  03:12:56.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2010/12/15  03:12:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/15  03:13:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  03:46:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  03:46:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  03:46:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  03:49:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  04:15:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  04:16:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  05:26:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  05:26:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  05:26:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  05:29:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  05:53:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  05:54:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  07:06:30.5  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  07:06:32.5  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  07:06:34.5  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  07:09:44.5  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  07:30:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  07:31:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  08:46:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  08:46:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  08:46:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  08:49:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  09:07:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  09:08:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  10:28:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  10:28:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  10:28:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  10:31:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  10:37:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  10:38:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  14:17:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  14:17:02.0  XRT_FLD_RESET_412_OG [0x19c]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  14:17:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  14:20:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  14:23:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  14:24:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  15:49:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  15:49:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  15:49:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  15:52:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  16:13:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  16:14:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  17:26:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  17:26:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  17:26:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  17:29:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  18:05:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  18:05:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/12/15  18:05:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/12/15  18:05:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/12/15  18:05:48.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/12/15  18:08:26.0  XRT_ARS_DIS_447_OG [0x1bf]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/15  18:08:28.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/12/15  18:08:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  18:15:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  18:15:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/15  18:15:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/12/15  18:15:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/15  18:15:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/15  18:15:50.0  XRT_ARS_DIS_420_OG [0x1a4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/15  18:18:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  18:18:26.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2010/12/15  18:18:28.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/15  18:18:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  19:03:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  19:03:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  19:03:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  19:06:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  19:26:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  19:27:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  20:40:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  20:40:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  20:40:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  20:43:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/15  21:04:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  21:05:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  22:18:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  22:18:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  22:18:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  22:21:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
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2010/12/15  22:39:30.0  XRT_Custom_418_OG [0x1a2]
2010/12/15  22:40:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/15  23:55:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/15  23:55:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/15  23:55:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/15  23:58:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  00:05:00.0  XRT_Custom_418_OG [0x1a2]
2010/12/16  00:06:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  01:21:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  01:21:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  01:21:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/16  01:24:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  01:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  01:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/16  02:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 e9 cb 50 e5
2010/12/16  02:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/16  02:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/16  02:00:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/16  02:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  02:02:56.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/12/16  02:02:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/16  02:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  02:54:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  02:54:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  02:54:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/16  02:57:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  03:14:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  03:14:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/12/16  03:15:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/12/16  03:15:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/12/16  03:15:18.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/12/16  03:17:56.0  XRT_ARS_DIS_447_OG [0x1bf]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/16  03:17:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/12/16  03:18:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  03:24:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  03:24:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/12/16  03:25:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/12/16  03:25:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/12/16  03:25:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/12/16  03:25:20.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/12/16  03:27:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  03:27:56.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2010/12/16  03:27:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2010/12/16  03:28:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  04:23:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  04:23:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  04:23:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/16  04:26:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
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                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  04:52:30.5  XRT_Custom_418_OG [0x1a2]
2010/12/16  04:53:30.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  06:04:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  06:04:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  06:04:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/16  06:07:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  06:30:00.5  XRT_Custom_418_OG [0x1a2]
2010/12/16  06:31:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/12/16  07:44:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/12/16  07:44:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/12/16  07:44:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/12/16  07:47:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/12/16  09:26:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```