

XRT Timeline to be uploaded on 2010/12/21

Period: 2010/12/21 09:06:00 - 2010/12/25 10:08:00

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Normal mode

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XOB #180E: HOP173 prominence Al/mesh, Ti/poly, G-band-DPCM 1x1-512x512-FOV -AEC2-5min cadence													
Term		Pointing (x, y)						Comment					
12/21 09:33:30 - 12/21 10:56:00		Track (132.4, -761.0) ^{Ⓜ 12/21 09:16:00}						# OP start + 10min, filament observation.					
PROG= 11 Inf.-time(s)													
Subr= 2		1-time(s)		2.0sec									
Seqn= 57		1-time(s)		2.0sec									
Open/G-band		Open/G-band		open		Safe		Norm		63ms		Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 1		12-time(s)		300.0sec									
Seqn= 68		1-time(s)		2.0sec									
Open/Al-mesh		Open/Ti-poly		close		Safe		Norm		4.00s		Obs 1x1 512x512 (1024, 1024) DPCM 2 0 2.0sec	
Seqn= 34		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al		close		Safe		Norm		16.0s		Obs 1x1 512x512 (1024, 1024) DPCM 2 0 2.0sec	
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1826: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al (64s) - 2 loops													
Term		Pointing (x, y)						Comment					
12/21 11:35:00 - 12/21 14:41:00		Fixed (0.0, 0.0)						# 03/15					
PROG= 09 1-time(s)													
Subr= 1		2-time(s)		300.0sec									
Seqn= 9		1-time(s)		2.0sec									
Open/thick-Al		Open/thick-Al		close		Safe		Norm		64.0s		Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 12		1-time(s)		2.0sec									
Open/Al-mesh		Open/Al-mesh		close		Safe		Norm		125ms		Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh		Open/Al-mesh		close		Safe		Norm		2.00s		Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 11		1-time(s)		2.0sec									
Open/Ti-poly		Open/Ti-poly		close		Safe		Norm		250ms		Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Ti-poly		Open/Ti-poly		close		Safe		Norm		4.00s		Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 2		1-time(s)		2.0sec									
Seqn= 14		1-time(s)		2.0sec									
Open/Ti-poly		Open/Ti-poly		close		Safe		Dark		1.00s		Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term		Pointing (x, y)						Comment					
12/21 18:19:00 - 12/21 18:25:54		Fixed (0.0, 0.0)						synoptic, shifted 16.0 min					
12/22 06:04:30 - 12/22 06:11:24		Fixed (0.0, 0.0)						synoptic, shifted 1.5 min					
12/22 18:03:00 - 12/22 18:09:54		Fixed (0.0, 0.0)						synoptic					
12/23 06:08:30 - 12/23 06:15:24		Fixed (0.0, 0.0)						synoptic, shifted 5.5 min					
PROG= 03 1-time(s)													
Subr= 1		1-time(s)		12.0sec									
Seqn= 7		1-time(s)		4.0sec									
Open/Al-mesh		Open/Al-mesh		close		Safe		Norm		16ms		Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh		Open/Al-mesh		close		Safe		Norm		1.00s		Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 5		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		500ms		Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		500ms		Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		500ms		Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		500ms		Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		500ms		Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
Seqn= 8		1-time(s)		4.0sec									
Open/Ti-poly		Open/Ti-poly		close		Safe		Norm		32ms		Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Ti-poly		Open/Ti-poly		close		Safe		Norm		2.00s		Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 4		1-time(s)		2.0sec									
Open/G-band		Open/G-band		open		Safe		Norm		16ms		Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 90sec-cad													
Term		Pointing (x, y)						Comment					
12/21 18:29:00 - 12/22 06:01:24		Track (629.1, 356.0) ^{Ⓜ 12/21 18:26:00}						# AR 11135					
12/22 06:14:30 - 12/22 17:59:54		Track (699.5, 354.3) ^{Ⓜ 12/22 06:11:30}						# AR 11135					
12/22 18:13:00 - 12/23 06:05:24		Track (762.2, 352.1) ^{Ⓜ 12/22 18:10:00}						# AR 11135					
12/23 06:18:30 - 12/23 08:48:30		Track (815.5, 349.4) ^{Ⓜ 12/23 06:15:30}						# AR 11135					
PROG= 12 Inf.-time(s)													
Subr= 2		1-time(s)		2.0sec									
Seqn= 19		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al		close		Safe		Dark		16.0s		Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
Open/G-band		Open/G-band		open		Safe		Norm		63ms		Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
Seqn= 95		4-time(s)		2.0sec									
Open/Al-mesh		Open/Ti-poly		close		Safe		Norm		250ms		Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Open/Ti-poly		Open/thick-Al		close		Safe		Norm		500ms		Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Open/thick-Al		Open/thick-Be		close		Safe		Norm		1.00s		Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 3	20-time(s)	90.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
12/21 09:33:30 - 12/21 10:56:00	Track (132.4, -761.0) ^{Ⓜ 12/21 09:16:00}	# OP start + 10min, filament observation.
12/21 18:29:00 - 12/22 06:01:24	Track (629.1, 356.0) ^{Ⓜ 12/21 18:26:00}	# AR 11135
12/22 06:14:30 - 12/22 17:59:54	Track (699.5, 354.3) ^{Ⓜ 12/22 06:11:30}	# AR 11135
12/22 18:13:00 - 12/23 06:05:24	Track (762.2, 352.1) ^{Ⓜ 12/22 18:10:00}	# AR 11135
12/23 06:18:30 - 12/23 08:48:30	Track (815.5, 349.4) ^{Ⓜ 12/23 06:15:30}	# AR 11135

PROG= 16	1-time(s)											
└─ Subr= 1	30-time(s)	20.0sec										
└─ Seqn= 87	1-time(s)	2.0sec										
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 90	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3	30-time(s)	60.0sec										
└─ Seqn= 87	1-time(s)	2.0sec										
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 90	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3	30-time(s)	60.0sec										
└─ Seqn= 87	1-time(s)	2.0sec										
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Subr= 4	24-time(s)	600.0sec										
└─ Seqn= 89	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

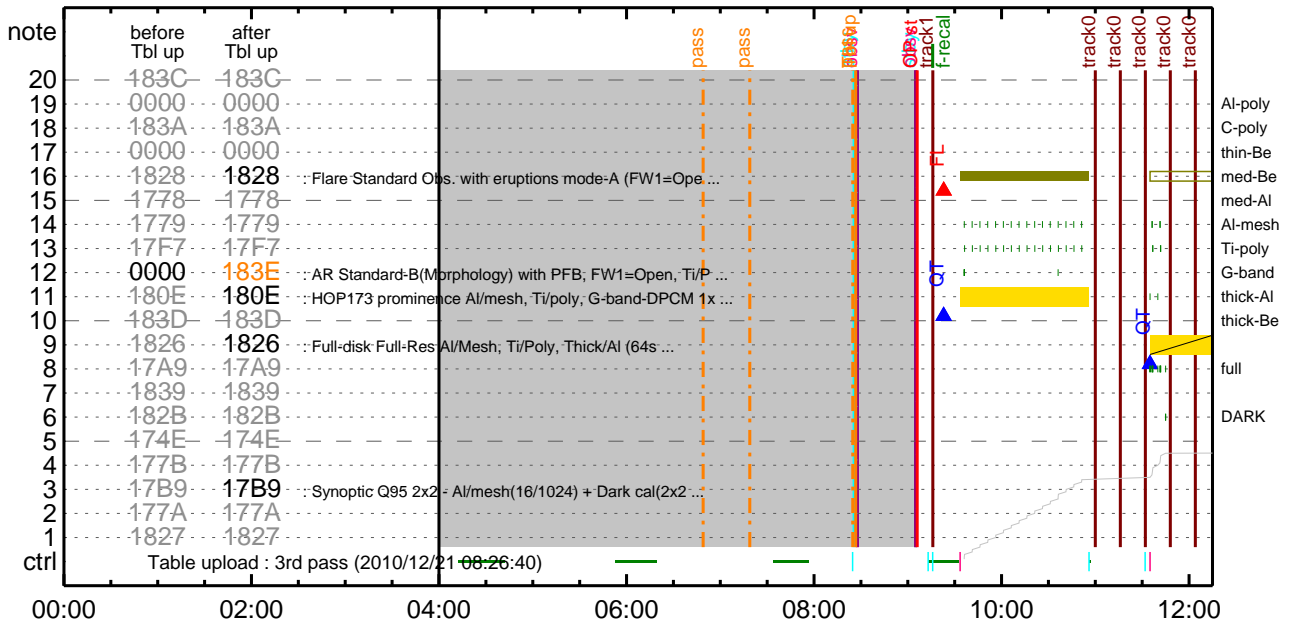
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FLD Patrol												
Term	Pointing (x, y)	Comment										
12/21 09:20:16 - 12/21 11:32:16	Track (132.4, -761.0) ^{Ⓜ 12/21 09:16:00}	# OP start + 10min, filament observation.										

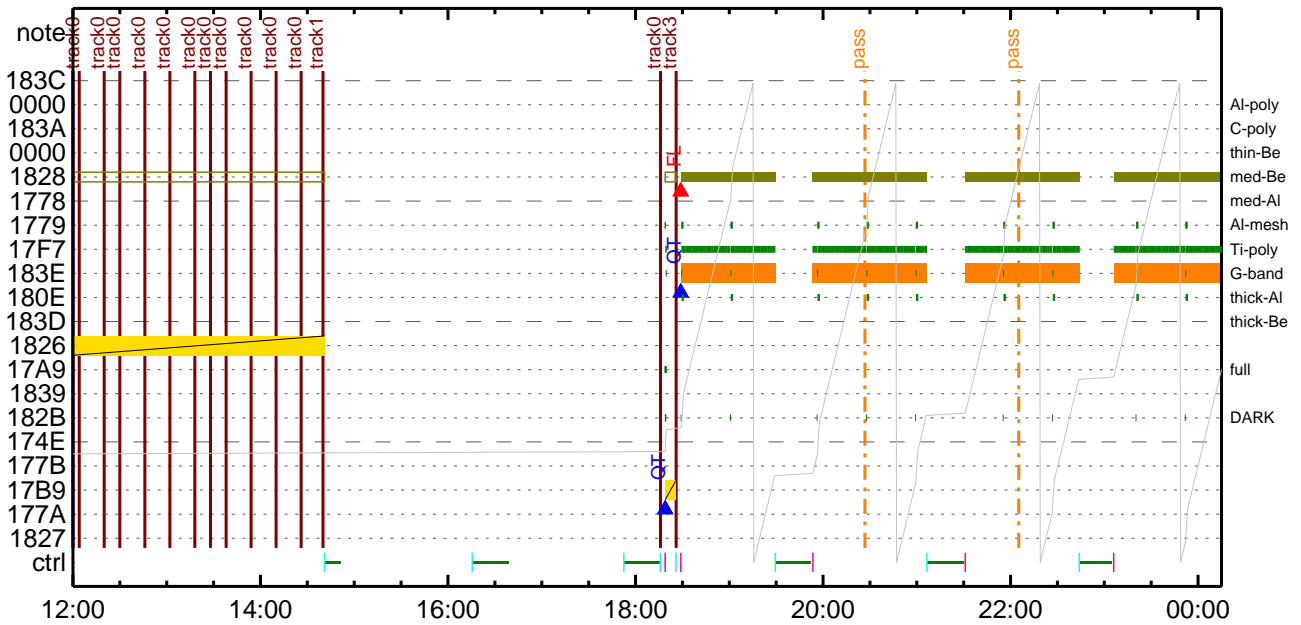
12/21 18:26:16 - 12/22 06:01:46 Track (629.1, 356.0) @ 12/21 18:26:00 # AR 11135
 12/22 06:11:46 - 12/22 18:00:16 Track (699.5, 354.3) @ 12/22 06:11:30 # AR 11135
 12/22 18:10:16 - 12/23 06:05:46 Track (762.2, 352.1) @ 12/22 18:10:00 # AR 11135
 12/23 06:15:46 - 12/25 10:08:00 Track (815.5, 349.4) @ 12/23 06:15:30 # AR 11135

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

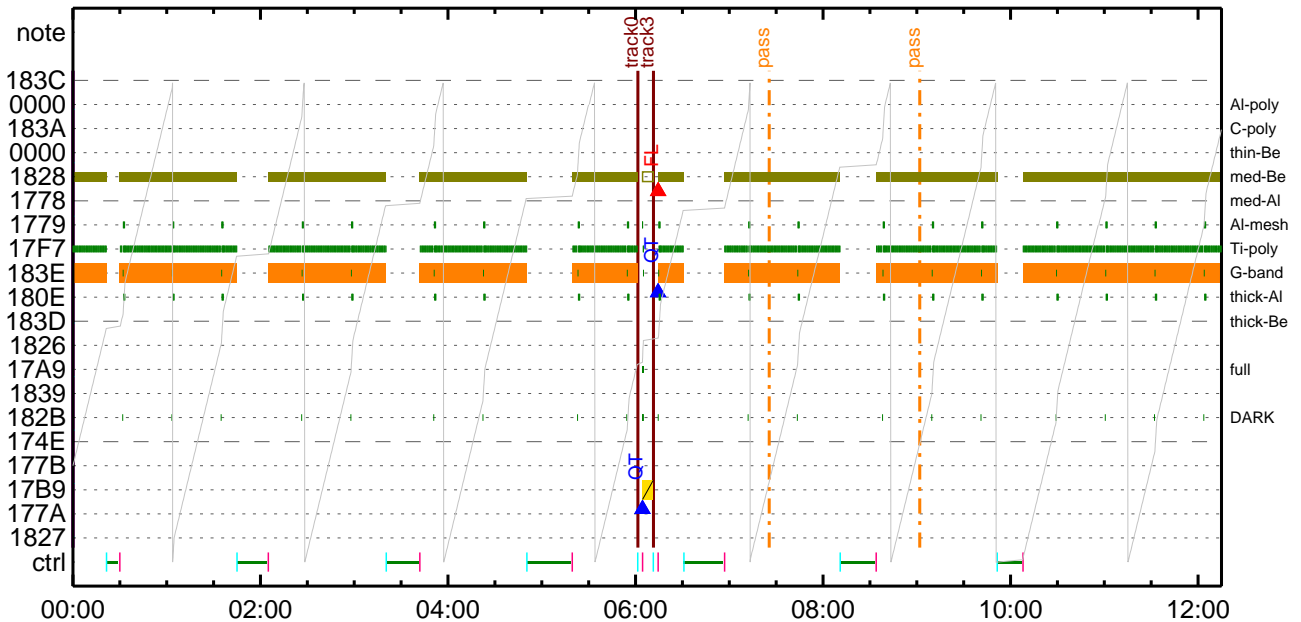
CMDI #0677 2010/12/21



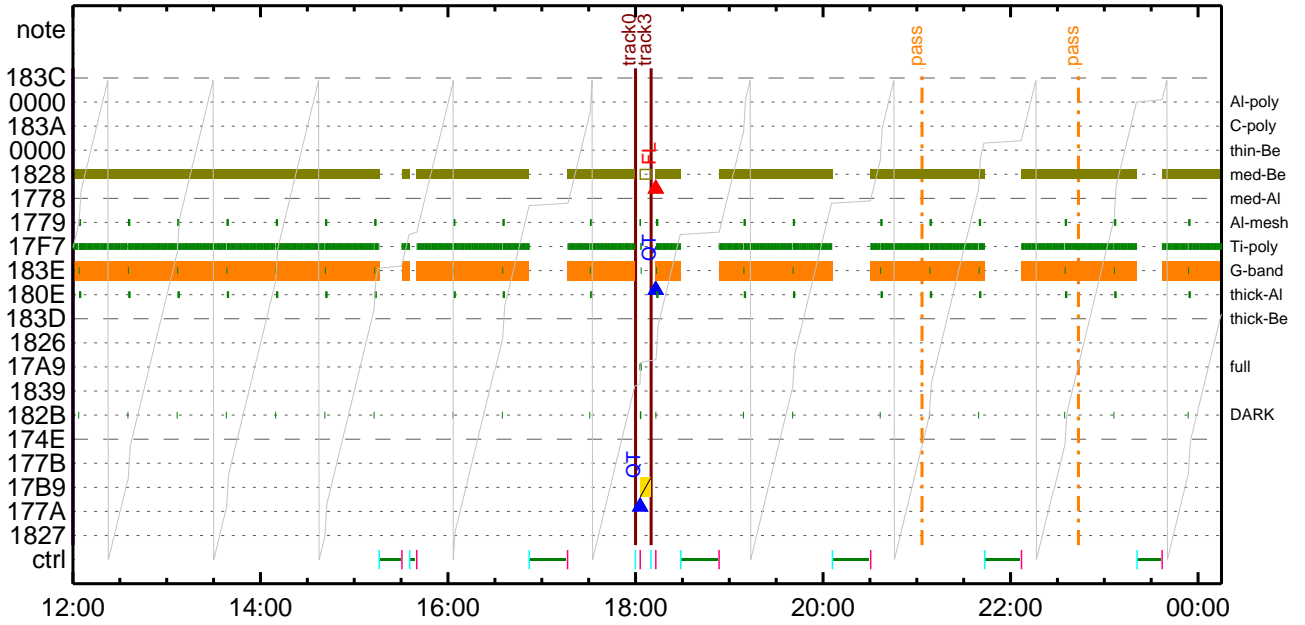
CMDI #0677 2010/12/21



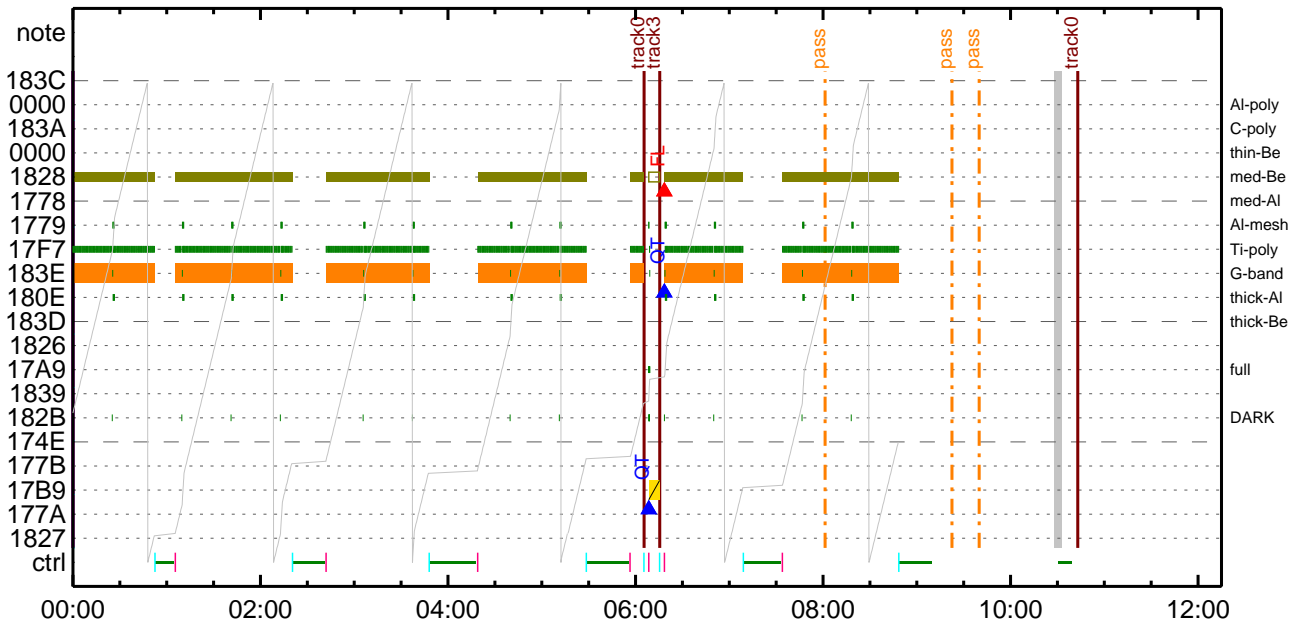
CMDI #0677 2010/12/22



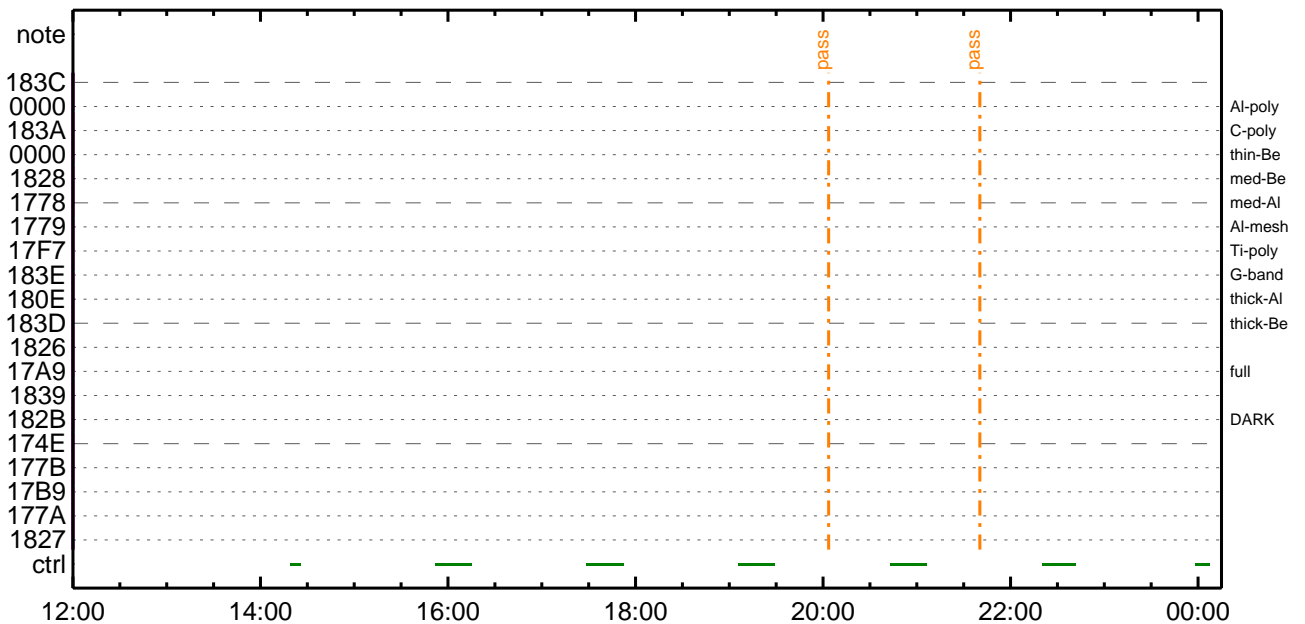
CMDI #0677 2010/12/22



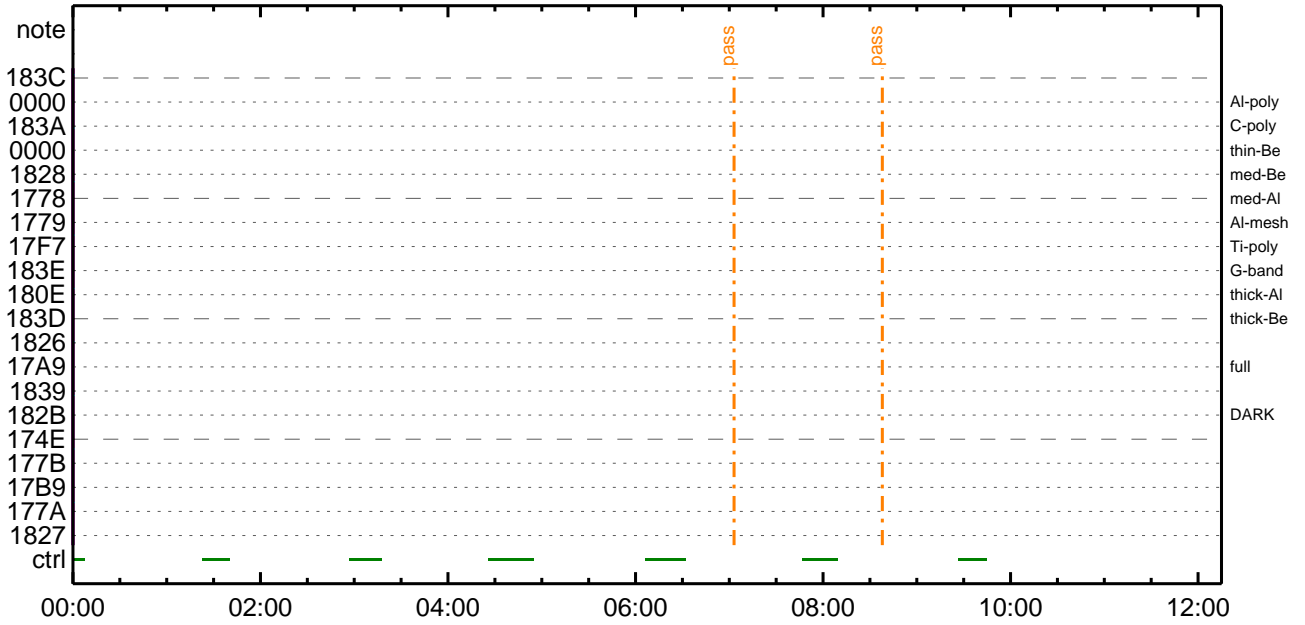
CMDI #0677 2010/12/23



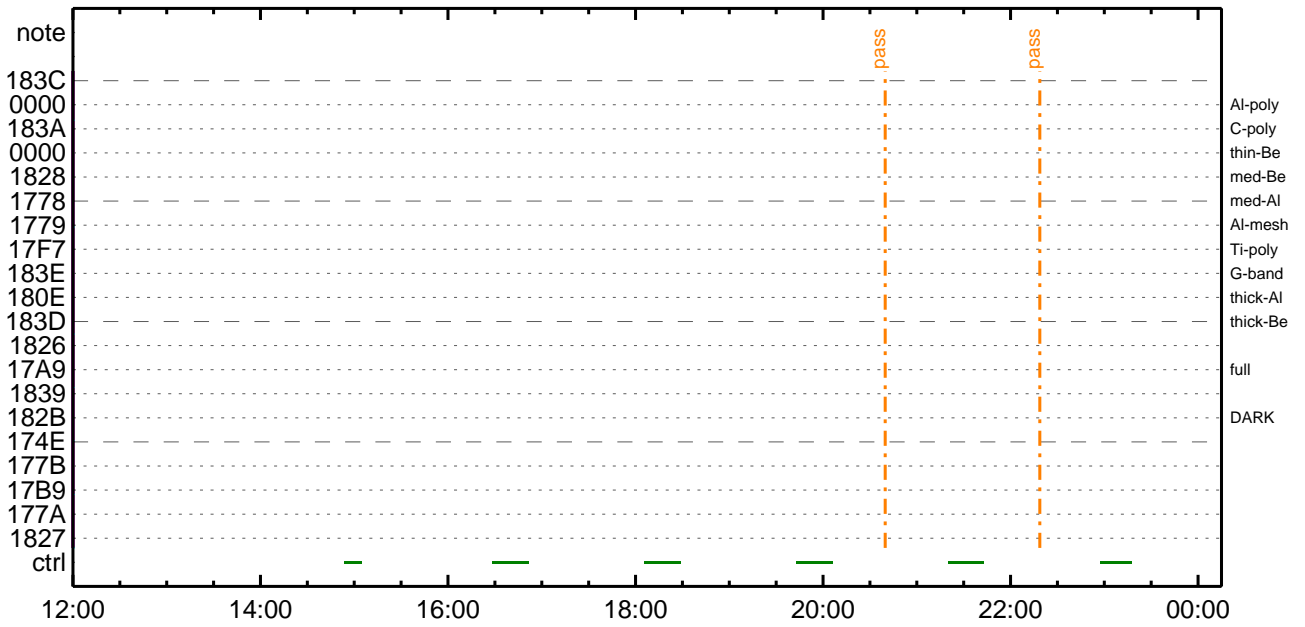
CMDI #0677 2010/12/23



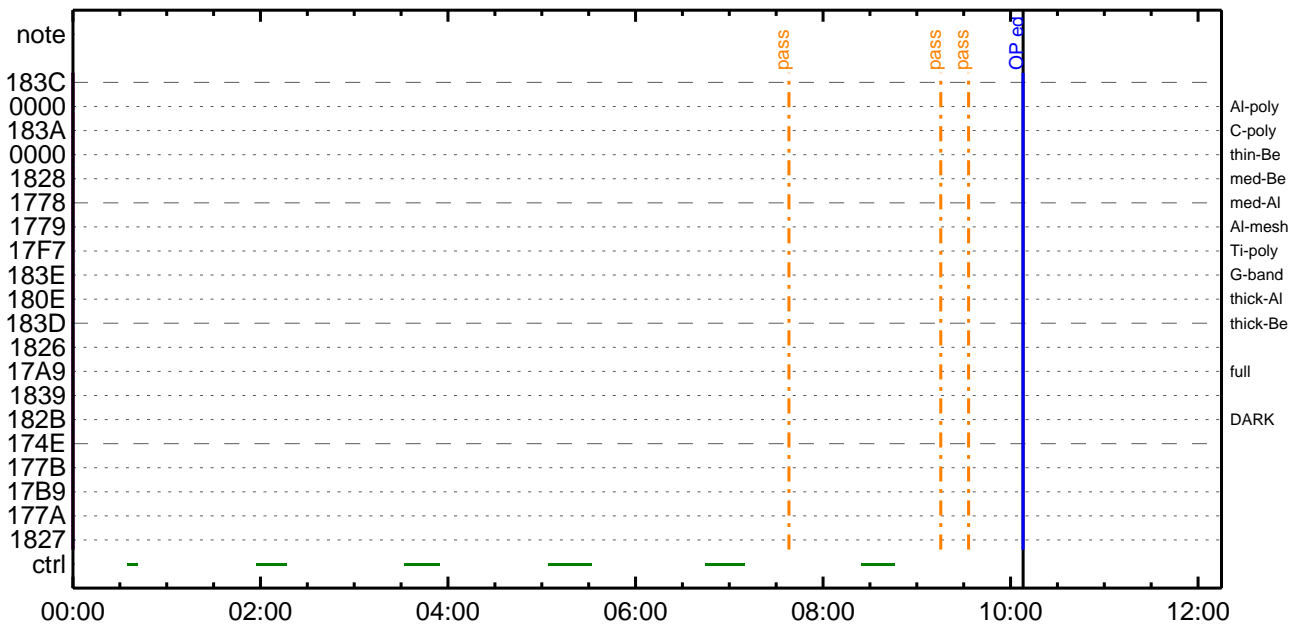
CMDI #0677 2010/12/24



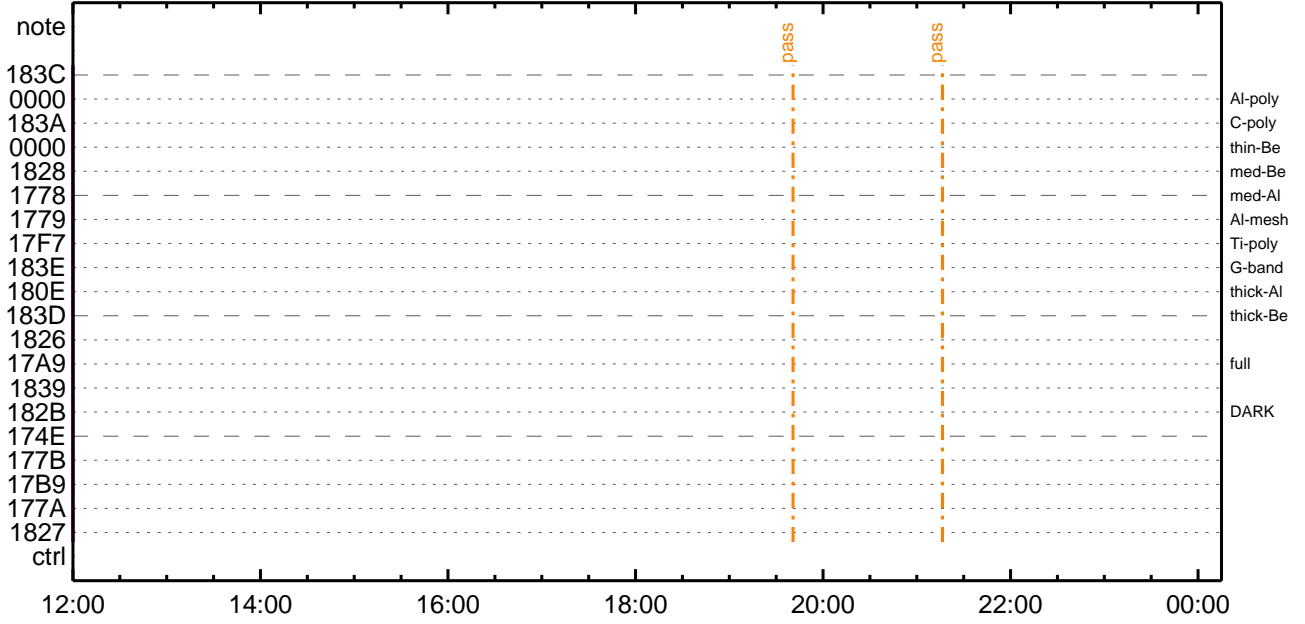
CMDI #0677 2010/12/24



CMDI #0677 2010/12/25



CMDI #0677 2010/12/25




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0096 C.                0302; SET0EDUMP01E±0iYNY¹0Ç¹00|0³0E;f
0097 C.
0098 . C. TIY³YF¥ÖYÉ00dADİ¿ (UT)
0099 +. TI 2010-12-21 09:01:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2010-12-21 09:01:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2010-12-21 09:01:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2010-12-21 09:05:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °Ê²¼0İÄê%îÍÑ0İYÁ¥§YÁY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]              EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]             EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]               EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]               EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]             EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]            EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]               EQ        EXEC
0140 C.
0141 . C. YÄYÖY×½ªİ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%Ê¹Ç•ê²İOK0ð³İÇ§
0145 C.
0146 . C. DHU¥â;¼YÉ;Ê¼Y½;¥İ;¼YÈ;Ë0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]               EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]             EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]            EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2010-12-21 09:05:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC                (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2010-12-21 09:05:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC                (c3)
0171 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 . C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2010-12-21 09:05:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC                (21 02)
0183 +. TI 2010-12-21 09:05:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC                (22)
0186 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0187 C. ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C. ***** MDP `ÜÄİ0İ»ö¼Y0ËÄ0¹0èDCBC•x²è *****
0192 C. (%â0İYÖYÄYÉY¥P¥ÉYÁYÇYè0E¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-780 2010-12-21 11:51:54 91 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã  
0005 C.  
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 C. Áí;Èö¿òÁö•µ°È»ÍxÁÇöíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈöÈ¼°ÇÖö••¿¼i¹çöí;çÁ®), ùñ¹èèöëöçÁ+¿®ö•öÈöö³öÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 (  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCs Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. ***** AOCs Commands (Orbital Element Update) *****  
0046 C. Update the orbital element  
0047 +. DC 02-50 AOCU_ORB_PRPGT_START  
0048 BC (16)  
0049 + DC 02-8E AOCU_ORB_UPD  
0050 C.  
0051 C. <A_ORB>[ORBIT] EPC = 7757691.3 +- 1.0 (s) [ ]  
0052 C.  
0053 C.  
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0055 +. DC 07-FC EIS_MODE_MANU  
0056 BC (21 02)  
0057 . C. Verify EIS in MANUAL mode  
0058 C. Estimated OBSTBL upload time is 27s  
0059 C. *****  
0060 C. EIS START OBSTBL LOAD  
0061 C. *****  
0062 . S. RAM ram-820:EIS_OBSTBL  
0063 (  
0064 +. DC 07-FC EIS_DUMP_OBSTBL  
0065 BC (07 07 07 00 00 70 00)  
0066 C.  
0067 C. Execute, after the success of OBSTBL upload.  
0068 C. Set EIS TI-commands  
0069 +. TI 2010-12-21 09:05:50.0  
0070 DC 07-FC EIS_MODE_CHG_ENA  
0071 BC (20)  
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0073 C. *****  
0074 C. EIS END OBSTBL LOAD  
0075 C. *****  
0076 C.  
0077 . C. ***** MDP `ûÁíöí`ò¼YöÉÁö¹öèDCBC•x²è *****  
0078 C. (¼á°íYóYÁYÉYÞYÉYáYçYèöE¾¼ö¼Á»Ûö¹öè)  
0079 . S. DC-BC dcbc-402:DCBC  
0080 (MDP_known_event)  
0081 C.  
0082 C.  
0083 . C. ***** YÐY¹•İ Daily±;îÑöÈ´Øö¹öèDCBC•x²è *****  
0084 . S. DC-BC dcbc-153:DCBC  
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0086 C.  
0087 C.  
0088 . C. ;ãLOSŸÁY$YÁY-¼Á»Û;ã  
0089 C.  
0090 . C. ***** LOS *****  
0091 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 . C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C.
0143 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0144 C.
0145 +. DC 07-F0 MDP_XRT_MODE_OBSV
0146 BC (c2)
0147 +. TI 2010-12-21 09:05:02.0
0148 DC 07-F0 MDP_XRT_MODE_OBSV
0149 BC (c2)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C. ***** XRT END *****
0153 . C. *****
0154 C. SOT table upload
0155 C. *****
0156 . C. < Stop FG table >
0157 +. DC 07-F0 MDP_FG_CTRL_MANU
0158 BC (51)
0159 . C. -----
0160 C. MDP_FG_CTRL_MODE = MANU [ ]
0161 C. -----
0162 C.
0163 . C. <Upload FG Observation Table>
0164 . S. RAM ram-262:MDP_OBS_F
0165 ( )
0166 C.
0167 . C. < Dump RAMID=MDP_OBS_F >
0168 +. DC 07-F0 MDP_DUMP_FGTBL
0169 BC (82 07 00 00 00 38 b8)
0170 C. -----
0171 C. MDP_OBS_F verify = OK/NG [ ]
0172 C. -----
0173 C.
0174 . C. < Upload DPL table >
0175 C.
0176 C. ¥ç¥Ã¥×¥í;¼¥É¥î¥°¥ESTS_CHK¥ð¥OFF¥È¥¹¥ë
0177 C.
0178 . S. RAM ram-271:MDP_DPL
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_DPL >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 38 b8 00 40)
0184 C. -----
0185 C. MDP_DPL verify = OK [ ]
0186 C. -----
0187 C.
0188 C. STS_CHK¥ð¥ON¥È¥¹¥ë
0189 C.
0190 . C. < Update MDP DSC PAR1 >
0191 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0192 BC (4c)
0193 C. MDP_CMD_CODE = F04C0700[ ]

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0194 C. MDP_CMD_CNT (count-up 1) [ ]
0195 C. -----
0196 C.
0197 . C.
0198 C. *****
0199 C. SOT TI command set
0200 C. *****
0201 C. Execute, after the success of TBL upload.
0202 +. TI 2010-12-21 09:05:18.0
0203 DC 07-F0 MDP_SOT_MODE_OBSV
0204 BC (40)
0205 . C. -----
0206 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0207 C. -----
0208 C.
0209 C.
0210 . C. ***** MDP `uAÎaÎ»ö¼YâEÂDâ¹æDCBC•x²è *****
0211 C. (¼â°îYÔYÄYËYËYËYâYçYèæ¼¼¼¼¼»Üâ¹æè)
0212 . S. DC-BC dcbc-402:DCBC
0213 (MDP_known_event)
0214 C.
0215 C.
0216 . C. ***** YDY¹•İ Daily±;İÑâE'Øâ¹æDCBC•x²è *****
0217 . S. DC-BC dcbc-153:DCBC
0218 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0219 C.
0220 C.
0221 . C. ;ãLOSŸÁŸSŸÄŸ-¼Ä»Ü;ã
0222 C.
0223 . C. ***** LOS *****
0224 C.
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*** OP Sequence for XRT ***

2010/12/21	09:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	09:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	09:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	09:15:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	09:15:56.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2010/12/21	09:16:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2010/12/21	09:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	09:19:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/12/21	09:20:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/21	09:20:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/21	09:20:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/21	09:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	09:22:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2010/12/21	09:22:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/12/21	09:32:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/21	09:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	10:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	10:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	10:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	10:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	11:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 ac cd				
2010/12/21	11:16:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 d6 67				
2010/12/21	11:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	11:31:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/21	11:32:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00 00				
2010/12/21	11:32:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/21	11:32:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/21	11:32:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/21	11:34:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2010/12/21	11:35:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	11:48:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 00 29 99				
2010/12/21	12:04:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 00 53 33				
2010/12/21	12:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 d6 36 b7 8e				
2010/12/21	12:30:00.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2010/12/21	12:46:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2010/12/21	13:02:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2010/12/21	13:18:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2010/12/21	13:28:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 29 ca b7 8e				
2010/12/21	13:38:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2010/12/21	13:54:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2010/12/21	14:10:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2010/12/21	14:26:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2010/12/21	14:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00 00				
2010/12/21	14:41:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	14:41:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	14:41:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2010/12/21	14:44:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	16:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	16:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	16:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	16:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	17:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	17:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	17:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	17:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	18:15:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	18:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/21	18:16:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/21	18:16:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/21	18:16:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/21	18:16:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/21	18:18:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/12/21	18:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	18:25:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	18:25:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/12/21	18:26:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/12/21	18:26:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/21	18:26:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/21	18:26:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/21	18:28:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	18:28:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2010/12/21	18:28:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/12/21	18:29:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	19:52:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/21	19:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	21:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	21:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	21:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	21:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	21:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/21	21:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/21	22:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/21	22:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/21	22:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/21	22:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/21	23:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/21	23:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	00:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	00:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	00:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2010/12/22	00:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/22	00:29:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/22	00:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	01:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	01:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	01:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/22	01:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/22	02:04:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/22	02:05:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	03:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	03:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	03:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/22	03:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/22	03:41:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/22	03:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	04:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	04:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	04:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/22	04:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/22	05:18:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/22	05:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	06:01:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	06:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/22	06:01:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/22	06:01:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/22	06:01:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/22	06:01:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/22	06:04:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/12/22	06:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	06:11:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	06:11:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/12/22	06:11:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/12/22	06:11:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/22	06:11:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/22	06:11:50.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/22	06:14:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	06:14:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2010/12/22	06:14:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2010/12/22	06:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	06:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	06:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	06:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/22	06:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/22	06:56:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/22	06:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/22	08:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/22	08:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/22	08:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/22	08:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2010/12/22	08:33:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	08:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	09:51:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	09:51:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	09:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/22	09:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/22	10:07:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	10:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	15:16:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	15:16:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	15:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/22	15:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/22	15:29:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	15:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	15:35:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	15:35:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	15:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/22	15:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/22	15:39:00.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	15:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	16:52:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	16:52:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	16:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/22	16:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/22	17:15:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	17:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2010/12/22	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/12/22	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2010/12/22	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2010/12/22	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/22	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2010/12/22	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/12/22	18:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2010/12/22	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/12/22	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/12/22	18:10:20.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/12/22	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	18:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2010/12/22	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2010/12/22	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/12/22	18:29:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/12/22	18:29:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/12/22	18:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2010/12/22	18:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2010/12/22	18:52:30.0	XRT_Custom_418_OG [0x1a2]					
2010/12/22	18:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					

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2010/12/22	20:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/22	20:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/22	20:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/22	20:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/22	20:29:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/22	20:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/22	21:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/22	21:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/22	21:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/22	21:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/22	22:06:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/22	22:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/22	23:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/22	23:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/22	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/22	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/22	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/22	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/22	23:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/22	23:36:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/22	23:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	00:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	00:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	00:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	00:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	01:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	01:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	02:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	02:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	02:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	02:41:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	02:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	03:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	03:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	03:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	03:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	04:18:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	04:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	05:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	05:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	05:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	05:55:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	05:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	06:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	06:05:30.0	XRT_FOCUS_POSITION	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	06:05:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	06:05:30.0	AOCU_NM	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	06:05:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	06:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	06:08:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	06:08:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	06:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	2	07-F0	c4 03

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2010/12/23	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	06:15:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	06:15:30.0	AOCS_OrE-point_Start_17_OG [0x0a7]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/12/23	06:15:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2010/12/23	06:15:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/12/23	06:15:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/12/23	06:18:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/12/23	06:18:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	06:18:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2010/12/23	06:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2010/12/23	07:09:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	07:09:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	07:09:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	07:12:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	07:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/23	07:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2010/12/23	08:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/23	08:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/23	08:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/23	08:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/23	10:43:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM	AOCS_OrE-point_Start_4_OG [0x09a]	5	02-76	00 00 00 00 00