

XRT Timeline to be uploaded on 2010/12/23

Period: 2010/12/23 10:33:00 - 2010/12/27 09:32:00

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Normal mode

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XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term		Pointing (x, y)					Comment						
12/23 14:46:00 - 12/23 17:53:54		Track (846.3, 347.3) ^{® 12/23 14:43:00}					AR 11135 monitoring						
PROG= 12 Inf.-time(s)													
Subr= 2		1-time(s)		2.0sec									
Seqn= 19		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 95		4-time(s)		2.0sec									
Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1		1-time(s)		2.0sec									
Seqn= 3		30-time(s)		80.0sec									
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16												
Term		Pointing (x, y)					Comment					
12/23 17:57:00 - 12/23 19:05:30		Fixed (0.0, 0.0)					synoptic, shifted -6.0 min					
12/24 05:42:00 - 12/24 06:06:30		Fixed (0.0, 0.0)					synoptic, shifted -21.0 min					
12/24 17:45:00 - 12/24 18:05:30		Fixed (0.0, 0.0)					synoptic, shifted -18.0 min					
12/25 06:03:00 - 12/25 06:44:30		Fixed (0.0, 0.0)					synoptic					

XOB #184: Prominence, FW1=Open, Al/mesh, Ti/Poly, 768x384 at 1064 1048													
Term		Pointing (x, y)					Comment						
12/24 08:10:30 - 12/24 17:41:54		Fixed (870.0, 346.0)					#AR 11135 at limb monitoring						
PROG= 19 Inf.-time(s)													
Subr= 2		1-time(s)		2.0sec									
Seqn= 26		1-time(s)		2.0sec									
Open/Ti-poly		Open/Ti-poly	close	Safe	Dark	4.00s	Obs	2x2	768x384 (1256, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 1		120-time(s)		30.0sec									
Seqn= 91		1-time(s)		2.0sec									
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	768x384 (1256, 1048)	Q=98	0	0	2.0sec
Seqn= 35		1-time(s)		2.0sec									
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	768x384 (1256, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)													
Term		Pointing (x, y)					Comment						
12/23 14:46:00 - 12/23 17:53:54		Track (846.3, 347.3) ^{® 12/23 14:43:00}					AR 11135 monitoring						
12/24 08:10:30 - 12/24 17:41:54		Fixed (870.0, 346.0)					#AR 11135 at limb monitoring						
PROG= 16 1-time(s)													
Subr= 1		30-time(s)		20.0sec									
Seqn= 87		1-time(s)		2.0sec									
Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 90		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3		30-time(s)	60.0sec									
Seqn= 87		1-time(s)	2.0sec									
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 90		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3		30-time(s)	60.0sec									
Seqn= 87		1-time(s)	2.0sec									
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 90		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3		30-time(s)	60.0sec									
Seqn= 87		1-time(s)	2.0sec									
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4		24-time(s)	600.0sec									
Seqn= 89		1-time(s)	2.0sec									
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

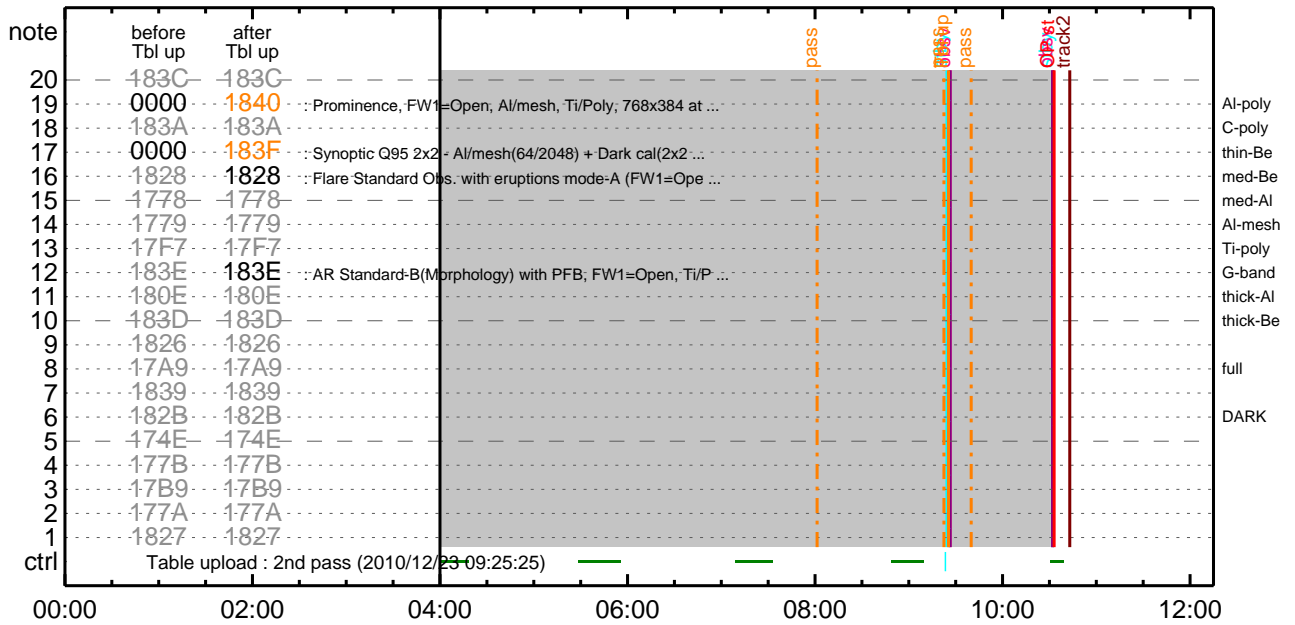
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Flare Detection

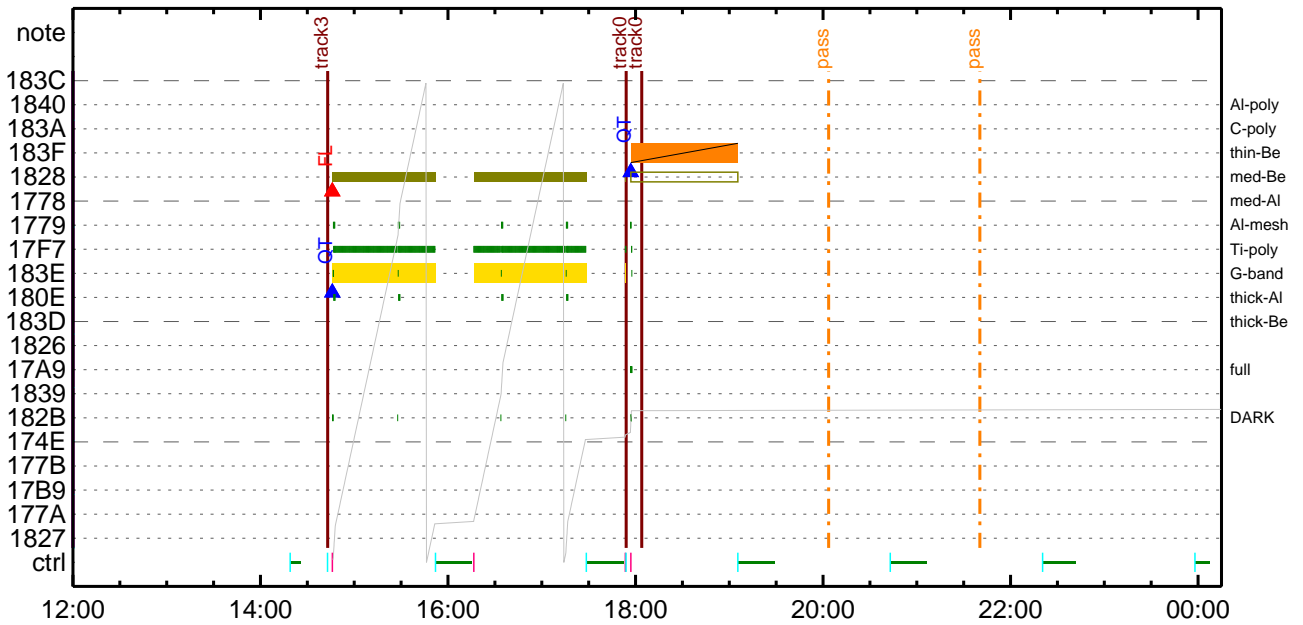
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FLD Patrol											
Term	Pointing (x, y)				Comment						
12/23 14:43:16 - 12/23 17:54:16	Track (846.3, 347.3) @ 12/23 14:43:00				AR 11135 monitoring						
12/24 08:07:46 - 12/24 17:42:16	Fixed (870.0, 346.0)				#AR 11135 at limb monitoring						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

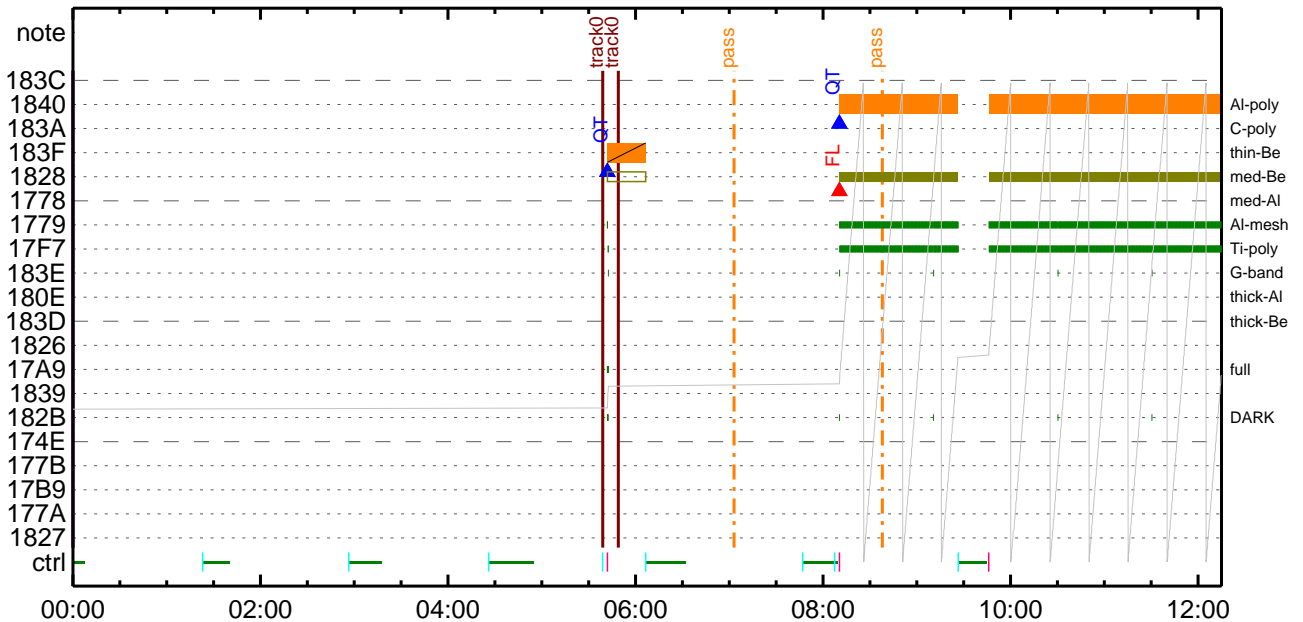
CMDI #0681 2010/12/23



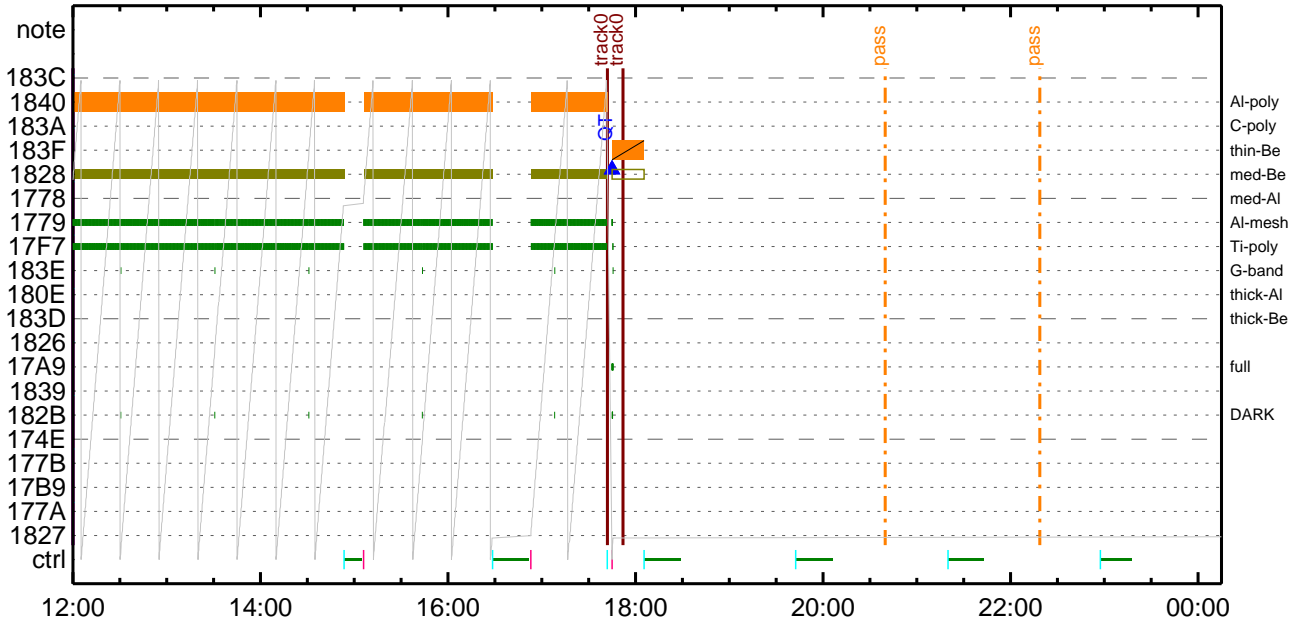
CMDI #0681 2010/12/23



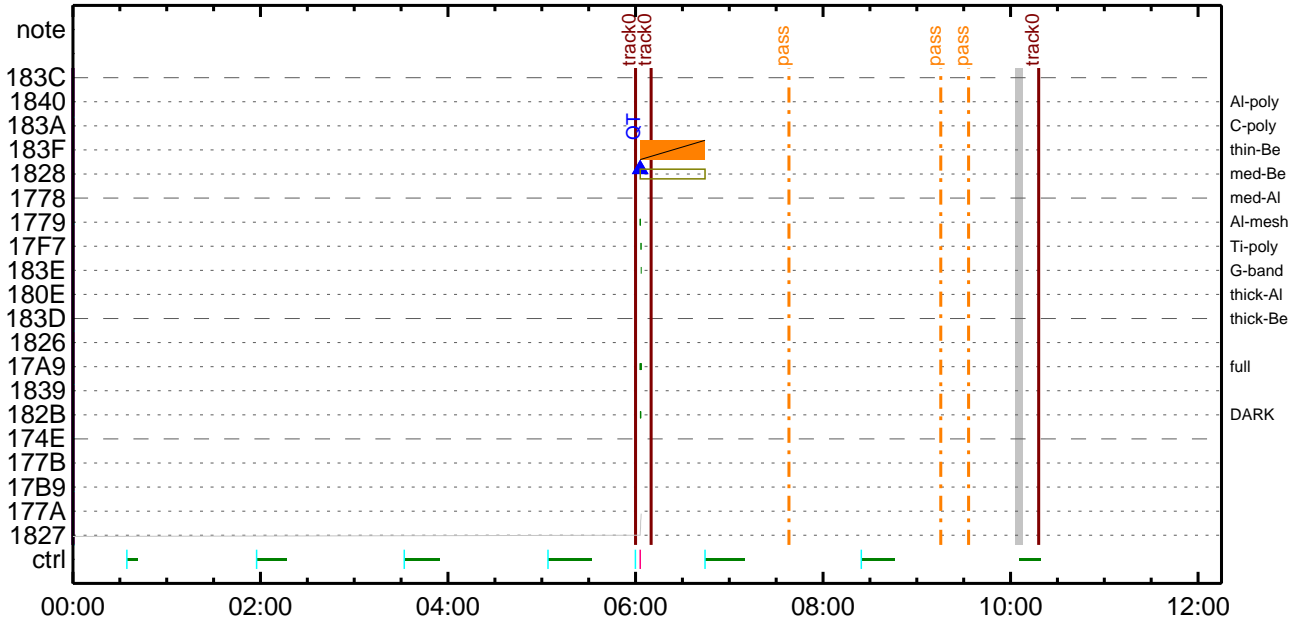
CMDI #0681 2010/12/24



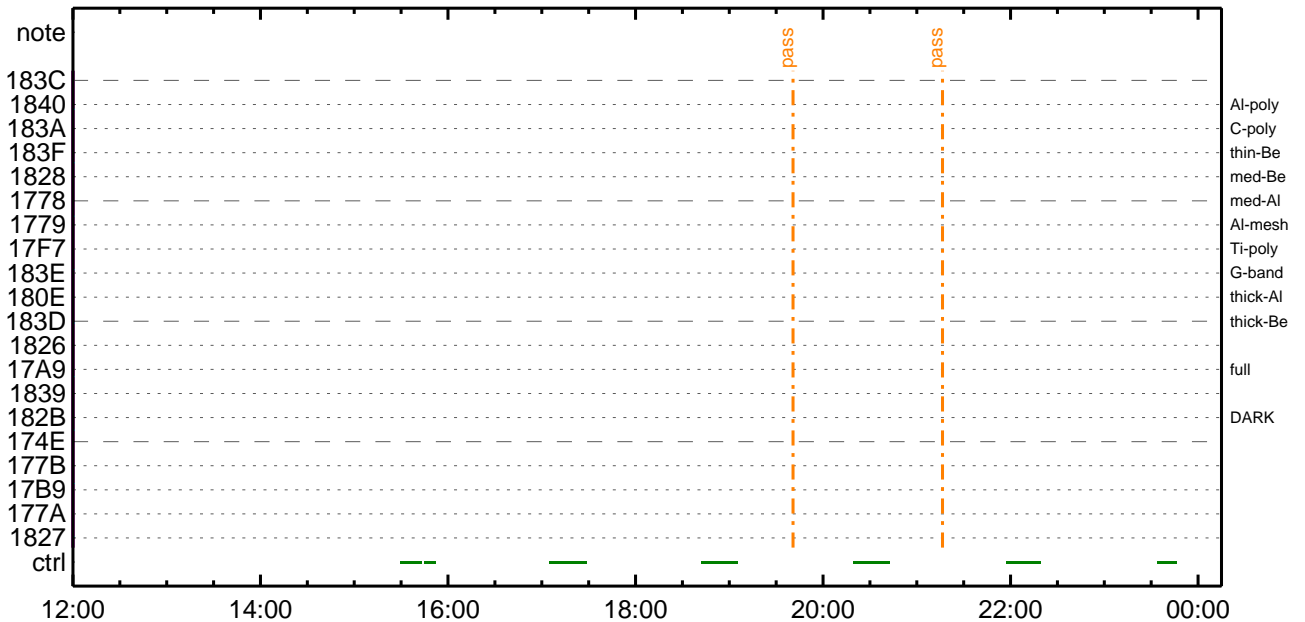
CMDI #0681 2010/12/24



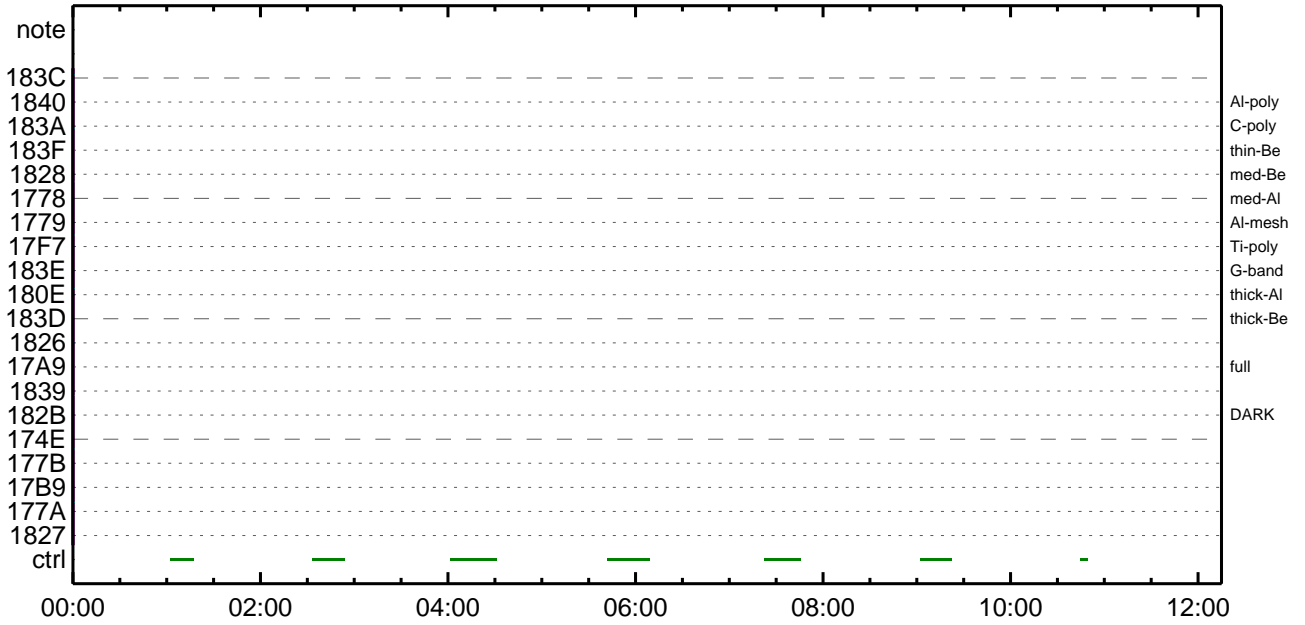
CMDI #0681 2010/12/25



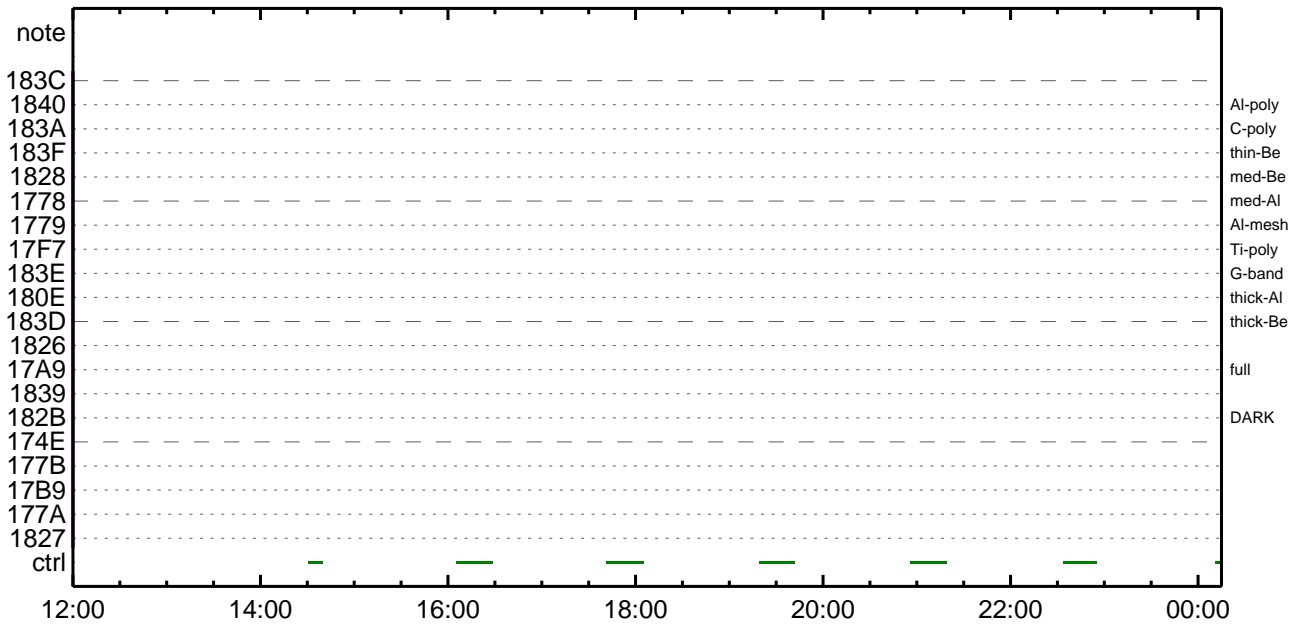
CMDI #0681 2010/12/25



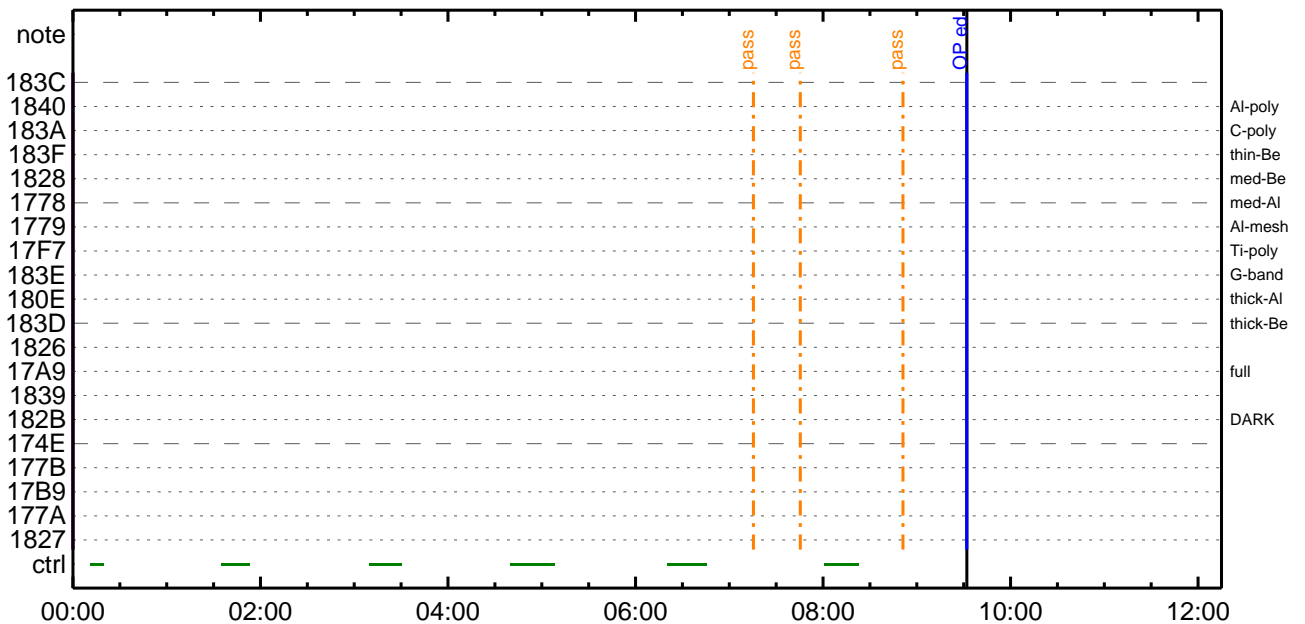
CMDI #0681 2010/12/26



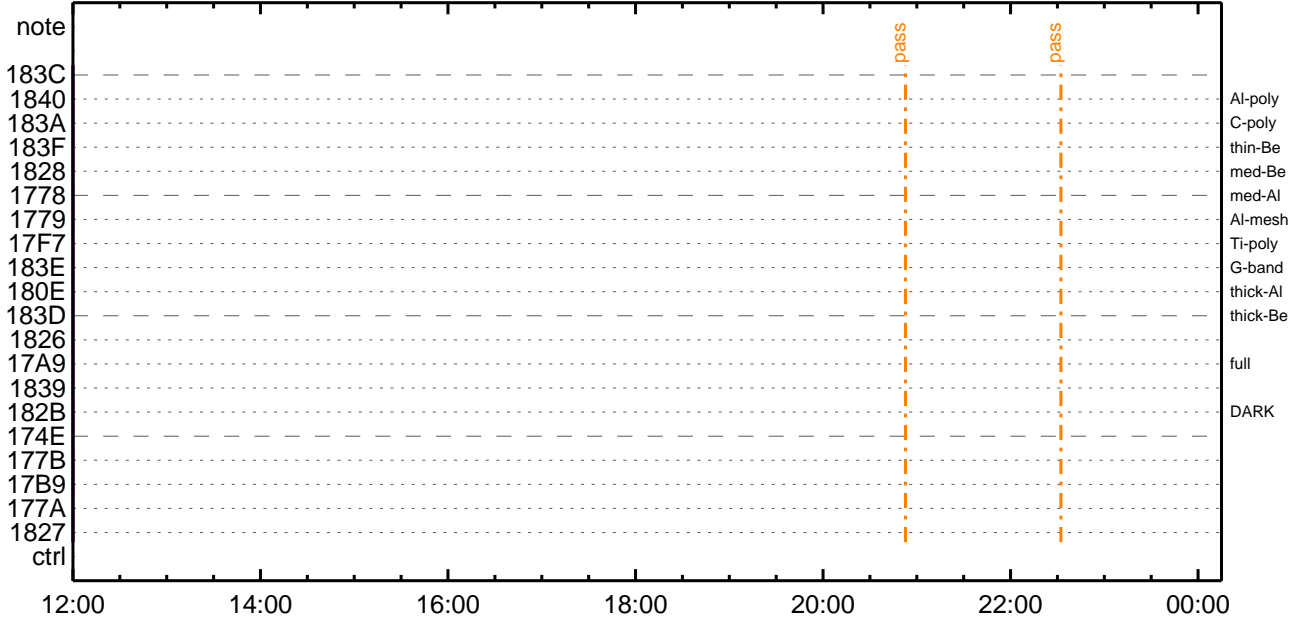
CMDI #0681 2010/12/26



CMDI #0681 2010/12/27



CMDI #0681 2010/12/27




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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;4YE;1;YAYOYx
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-785:OP
0104 . C. ( )
0105 . S. OG og-785:OG
0106 . C. ( )
0107 . C.
0108 . C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 . C. ;çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 . C. ;çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 . C. ;çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 . C. ;çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 . C. ;çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 . C. ;çç[HK1_PKT_FORM_NO] EQ 7
0120 . C. ;çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 . C. ;çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 . C. ;çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 . C. ;çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOYx½ªî»ò³îÇ§
0125 . C. ;çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGîî¼E¹ç.è²îOKò³îÇ§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 . C. ;çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 . C. ;çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 . C. ;çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 . C. ;çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 . C. ;çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 . C. ;çç[HK1_PKT_FORM_NO] EQ 7
0139 . C. ;çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 . C. ;çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 . C. ;çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 . C. ;çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOYx½ªî»ò³îÇ§
0144 . C. ;çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGîî¼E¹ç.è²îOKò³îÇ§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 . C. ;çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 . C. ;çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 . C. ;çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 . C. ;çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 . C. ;çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 . C. ;çç[HK1_PKT_FORM_NO] EQ 7
0158 . C. ;çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 . C. ;çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 . C. ;çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 . C. ;çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOYx½ªî»ò³îÇ§
0163 . C. ;çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPîî¼E¹ç.è²îOKò³îÇ§
0165 . C.
0166 . C. ***** òE²¼òî¼Ã´¶ÁºòEÉ-òºÁ÷¿@ (¼âµ-YAYOYx½ªî»ò³îÇ§òÁÓÃæòÇ¼ªººè¼i¹çòÇòâ) *****
0167 . C. DHUÿâ;¼YE;E¼Y½.¥i;¼YE;Eòðîãº¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 . C. ;çç[HK1_PKT_FORM_NO] EQ 2
0171 . C. ;çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 . C. ;çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 . C. ;çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;§ OPOG UPLOADò-Á÷¿@NGòî¼i¹ç;çºE²¼òî¼TI-CMDÁ÷¿@òî¼Á¹Òº.ºEòººººE;f
0180 . C. òPò¿;çSETòEDUMPAîE±ºiYNY¹ç¹Òò|º³òE;f
0181 . C.
0182 . C. TIY³YpYóYEòðÁDî¿¿(UT)
0183 +. TI 2010-12-23 10:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C. ;çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 . C.
0187 +. TI 2010-12-23 10:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C. ;çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 . C.
0191 +. TI 2010-12-23 10:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C. ;çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-12-23 10:32:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °È²¼áîÄè%îîñáîŷÄŷ\$ŷÄŷ¹àîŷ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÔŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÔŷx½¹î»à³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLáîŷÈ¹ç·è²îOKà³îç§
0229 C.
0230 C. DHUŷä;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èàðîáà¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-12-23 10:32:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2010-12-23 10:32:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2010-12-23 10:32:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP []
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-12-23 10:32:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîáî»ö¼ŷáàÈÀð¹àèDCBC·x²è *****
0276 C. (%á°îŷÄŷÈŷŷŷÄŷçŷèÈ¼áà¼Ä»ŷá¹àè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÄŷ¹·î Daily±çîñáè'Øà¹àèDCBC·x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ\$ŷÄŷ¹¼Ä»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-786 2010-12-23 11:24:38 137 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°È»Í×ÁÇαÍYçYÁY×YÍ;¼YÉ;ÈÈ%μ•fÍÉ;ÈÈE¼°ÇÔα•α¿¼l¹çαÍ;çÁ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 C. ***** XRT START *****
0047 C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 BC (c1)
0050 + DC 07-F0 MDP_XRT_MODE_STBY
0051 BC (c3)
0052 . C. ----- Success Verify ? OK / NG_____
0053 C.
0054 C. XRT Obs. Table Upload
0055 . S. RAM ram-291:MDP_OBS_X
0056 ( )
0057 C.
0058 +. DC 07-F0 MDP_DUMP_XRTTBL
0059 BC (84 07 00 00 00 3a d4)
0060 . C. ----- Comparison Check ? OK / ERR _____
0061 C.
0062 C.
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 01 b1 b1 04 04)
0065 + DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 02 b1 b1 08 08)
0067 + DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 03 b1 b1 08 08)
0069 + DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 04 b1 b1 06 06)
0071 + DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 05 85 83 06 06)
0073 + DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 06 85 83 06 06)
0075 + DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 07 80 80 20 20)
0077 + DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 08 80 80 20 08)
0079 + DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 09 80 80 08 20)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 0a 9d 83 0c 06)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 0f 80 80 06 06)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 10 80 80 08 08)
0087 . C. ----- Success Verify ? OK / NG _____
0088 C.
0089 C.
0090 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0091 C.
0092 +. DC 07-F0 MDP_XRT_MODE_OBSV
0093 BC (c2)
0094 +. TI 2010-12-23 10:32:02.0
0095 DC 07-F0 MDP_XRT_MODE_OBSV
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0096 BC (c2)
0097 . C. ----- Success Verify ? OK / NG ____
0098 C.
0099 C. ***** XRT END *****
0100 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0101 +. DC 07-FC EIS_MODE_MANU
0102 BC (21 02)
0103 . C. Verify EIS in MANUAL mode
0104 . C. Estimated OBSTBL upload time is 15s
0105 C. *****
0106 C. EIS START OBSTBL LOAD
0107 C. *****
0108 . S. RAM ram-820:EIS_OBSTBL
0109 ( )
0110 +. DC 07-FC EIS_DUMP_OBSTBL
0111 BC (07 07 07 00 00 70 00)
0112 C.
0113 C. Execute, after the success of OBSTBL upload.
0114 C. Set EIS TI-commands
0115 +. TI 2010-12-23 10:32:50.0
0116 DC 07-FC EIS_MODE_CHG_ENA
0117 BC (20)
0118 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0119 C. *****
0120 C. EIS END OBSTBL LOAD
0121 C. *****
0122 C.
0123 . C. ***** MDP `úÃîñî»ö%ÝðÊÃð¹ñèDCBC•x²è *****
0124 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Ã»Û¹ñè)
0125 . S. DC-BC dcbc-402:DCBC
0126 (MDP_known_event)
0127 C.
0128 C.
0129 . C. ***** ¥DÝ¹•İ Daily±¿İññÈ´Ø¹ñèDCBC•x²è *****
0130 . S. DC-BC dcbc-153:DCBC
0131 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0132 C.
0133 C.
0134 . C. ;ãLOS¥Á¥$¥Ã¥~¼Ã»Û;ã
0135 C.
0136 . C. ***** LOS *****
0137 C.

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*** OP Sequence for XRT ***

2010/12/23	10:43:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2010/12/23	14:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	14:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	14:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/23	14:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/23	14:42:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	14:42:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/12/23	14:43:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/12/23	14:43:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/23	14:43:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/23	14:43:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/23	14:45:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	14:45:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2010/12/23	14:45:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/12/23	14:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/23	15:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	15:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	15:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/23	15:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/23	16:15:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/23	16:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/23	17:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	17:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	17:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/23	17:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/23	17:52:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/23	17:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/23	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/12/23	17:54:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/12/23	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/23	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/23	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/23	17:56:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2010/12/23	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/23	18:04:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	e1	40	b2	a8
2010/12/23	19:05:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	19:05:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	19:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/23	19:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/23	20:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	20:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	20:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/23	20:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/23	22:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/23	22:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/23	22:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2010/12/23	22:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/23	23:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/23	23:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/23	23:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	00:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	01:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	01:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	01:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	01:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	02:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	02:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	02:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	02:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	04:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	04:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	04:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	04:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	05:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/12/24	05:39:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/12/24	05:39:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/12/24	05:39:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/12/24	05:39:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/12/24	05:41:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2010/12/24	05:42:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/24	05:49:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 e1 40 b2 a8	
2010/12/24	06:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	06:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	06:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	06:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	07:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	07:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	07:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/12/24	07:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/12/24	08:07:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	08:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/12/24	08:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/12/24	08:07:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/12/24	08:07:50.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/12/24	08:10:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	08:10:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2010/12/24	08:10:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2010/12/24	08:10:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/12/24	09:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/12/24	09:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/12/24	09:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2010/12/24	09:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	09:45:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/24	09:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/24	14:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	14:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	14:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	14:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	15:05:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/24	15:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/24	16:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	16:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	16:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	16:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	16:52:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/24	16:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/24	17:41:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	17:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/24	17:42:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/24	17:42:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/24	17:42:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/24	17:42:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/24	17:44:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2010/12/24	17:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/24	17:52:00.0	AOCS_Or-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e1 40 b2 a8				
2010/12/24	18:05:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	18:05:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	18:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	18:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	19:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	19:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	19:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	19:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	21:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	21:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	21:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	21:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/24	22:57:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/24	22:57:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/24	22:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/24	23:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	00:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	00:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	00:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	00:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	01:57:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	01:57:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	01:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	02:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2010/12/25	03:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	03:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	03:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	03:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	05:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	05:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	05:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	05:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/12/25	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/12/25	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/25	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/25	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/25	06:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2010/12/25	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/25	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e1 40 b2 a8				
2010/12/25	06:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	06:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	06:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	06:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	08:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/25	08:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/25	08:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/25	08:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/25	10:18:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				