

XRT Timeline to be uploaded on 2010/12/29

Period: 2010/12/29 10:13:00 - 2011/01/04 12:47:00

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Normal mode

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XOB #1827: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 120s cad												
Term	Pointing (x, y)						Comment					
12/29 14:03:00 - 12/29 17:55:00	Track (524.7, 247.5) ^{@ 12/29 14:00:00}						track AR11138					
12/29 18:31:30 - 12/30 06:03:54	Track (558.6, 246.6) ^{@ 12/29 18:28:30}						track AR11138					
12/30 06:17:00 - 12/30 09:52:00	Track (642.4, 244.0) ^{@ 12/30 06:14:00}						track AR11138					
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 20 4-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 15 20-time(s) 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(181/4096)												
Term	Pointing (x, y)						Comment					
12/29 18:21:30 - 12/29 18:28:24	Fixed (0.0, 0.0)						synoptic, shifted 18.5 min					
12/30 06:07:00 - 12/30 06:13:54	Fixed (0.0, 0.0)						synoptic, shifted 4.0 min					

PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 28 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)						Comment					
12/29 14:03:00 - 12/29 17:55:00	Track (524.7, 247.5) ^{@ 12/29 14:00:00}						track AR11138					
12/29 18:31:30 - 12/30 06:03:54	Track (558.6, 246.6) ^{@ 12/29 18:28:30}						track AR11138					
12/30 06:17:00 - 12/30 09:52:00	Track (642.4, 244.0) ^{@ 12/30 06:14:00}						track AR11138					
PROG= 16 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 60 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└─ Subr= 3 30-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 88 1-time(s) 2.0sec												

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 2	1-time(s)		2.0sec										
	Seqn= 90		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subbr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 2	1-time(s)		2.0sec										
	Seqn= 90		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subbr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 4	24-time(s)		600.0sec										
	Seqn= 89		1-time(s) 2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

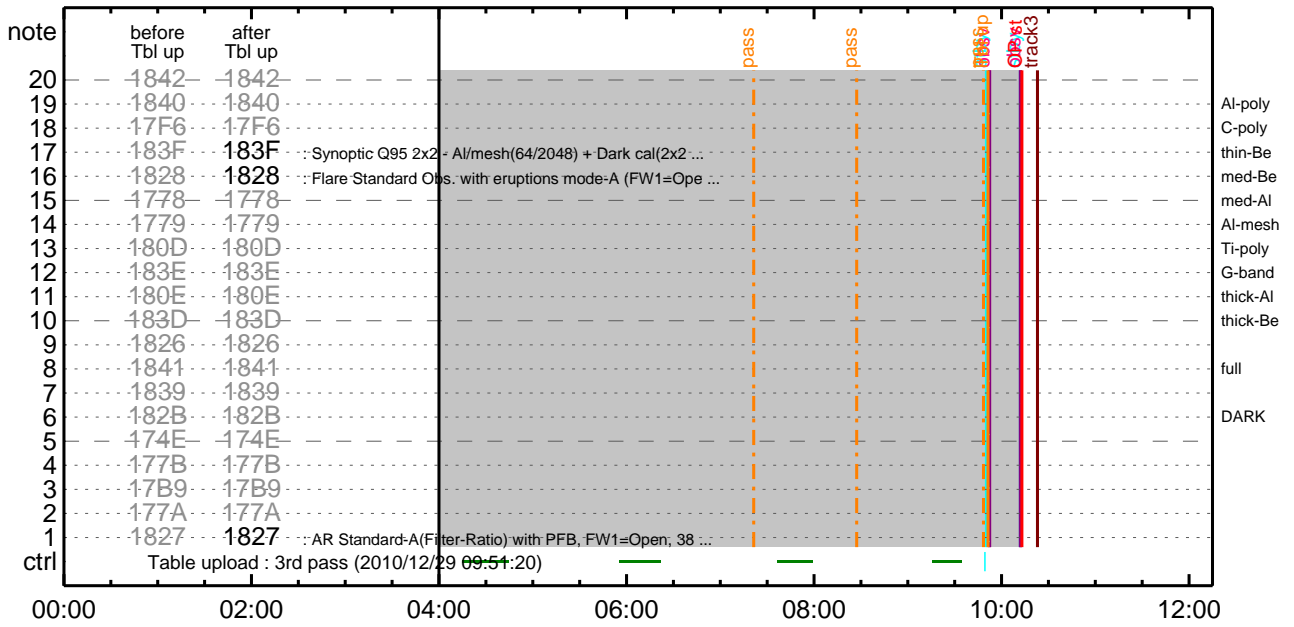
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Flare Detection

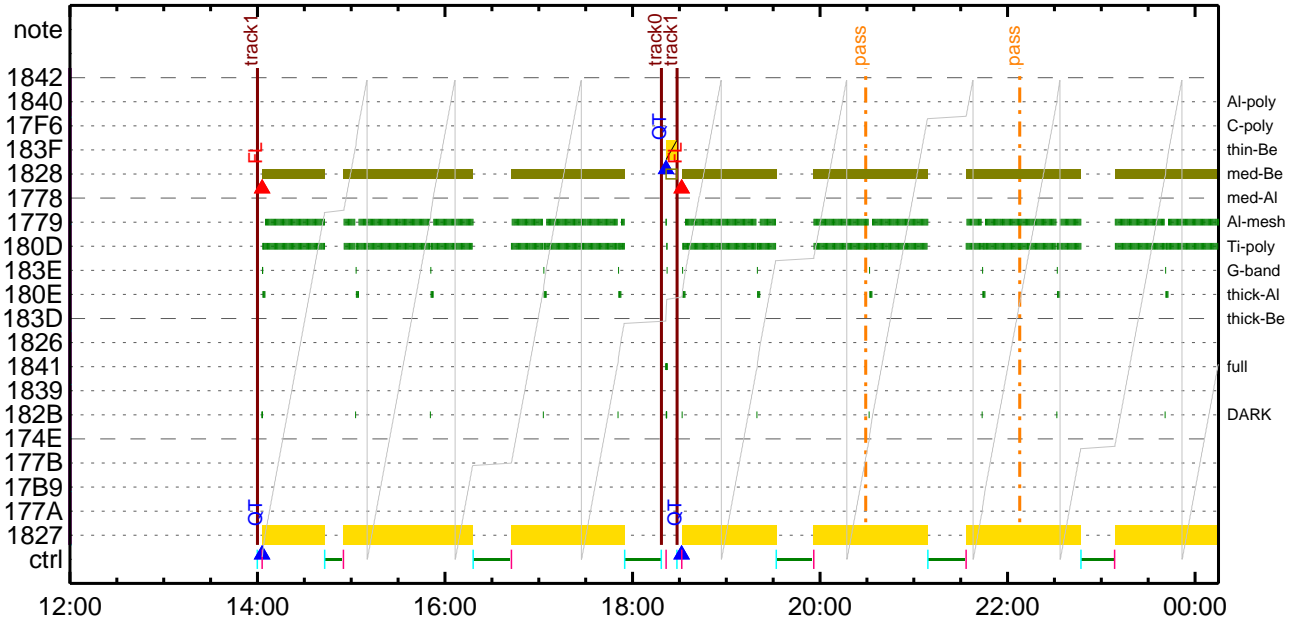
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FLD Patrol												
Term	Pointing (x, y)								Comment			
12/29 14:00:16 - 12/29 18:18:46	Track (524.7,	247.5)	@ 12/29 14:00:00	track	AR11138						
12/29 18:28:46 - 12/30 06:04:16	Track (558.6,	246.6)	@ 12/29 18:28:30	track	AR11138						
12/30 06:14:16 - 01/04 12:47:00	Track (642.4,	244.0)	@ 12/30 06:14:00	track	AR11138						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

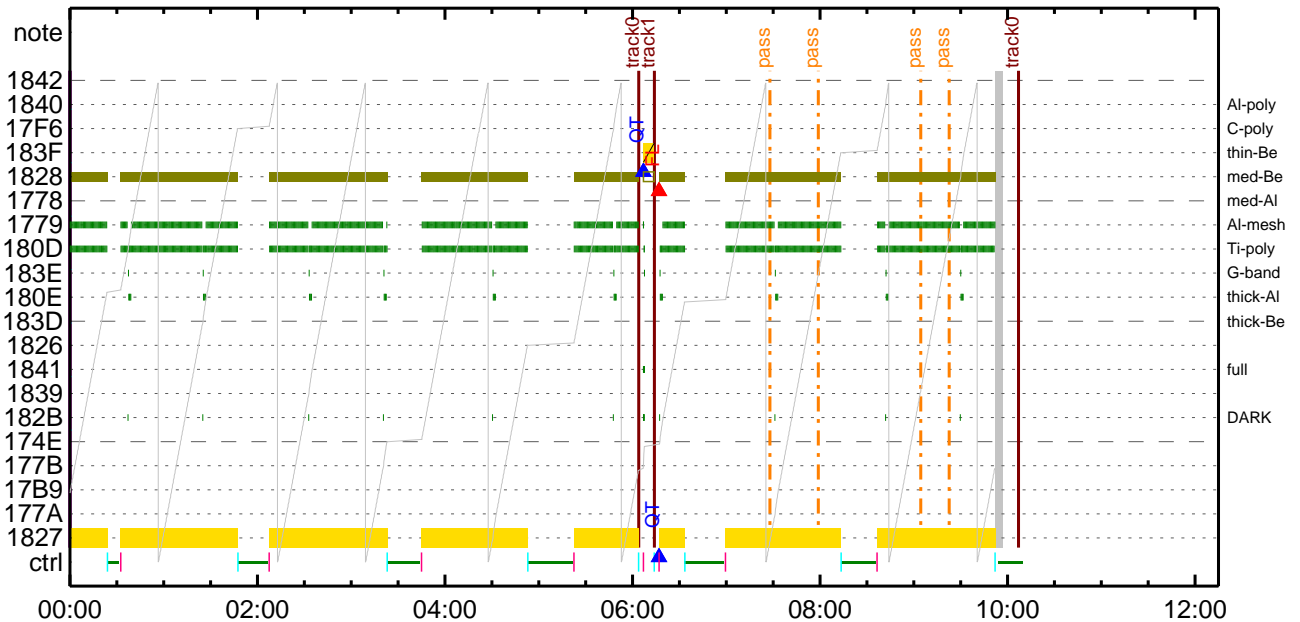
CMDI #0690 2010/12/29



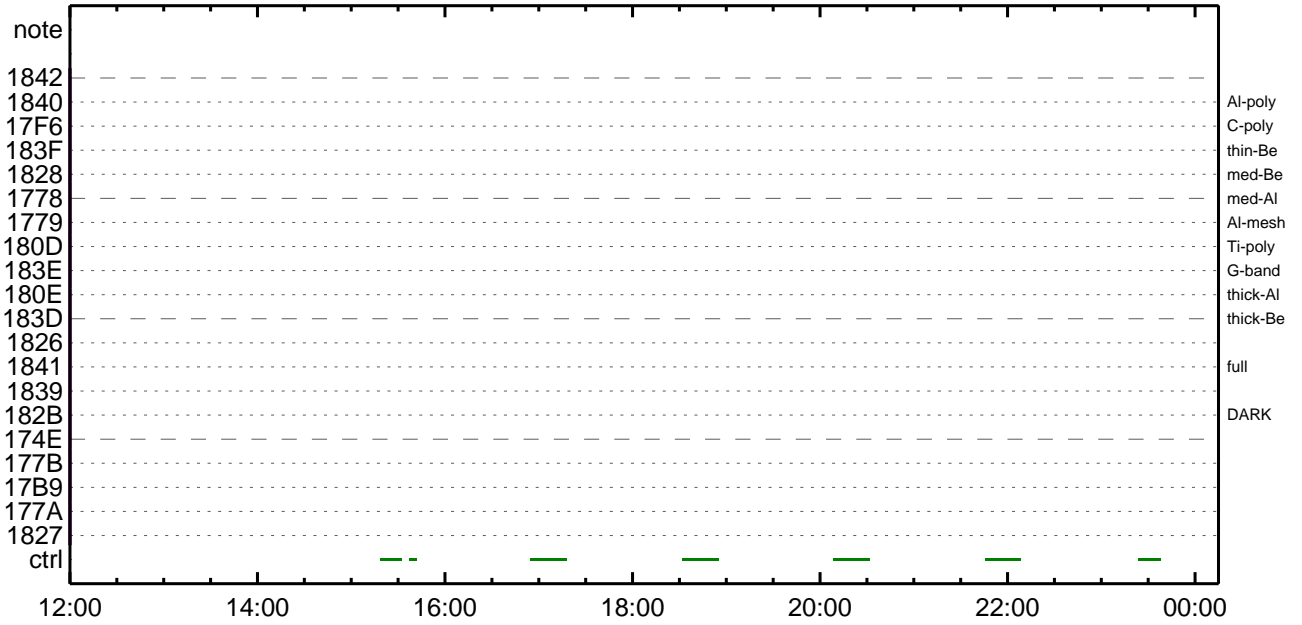
CMDI #0690 2010/12/29



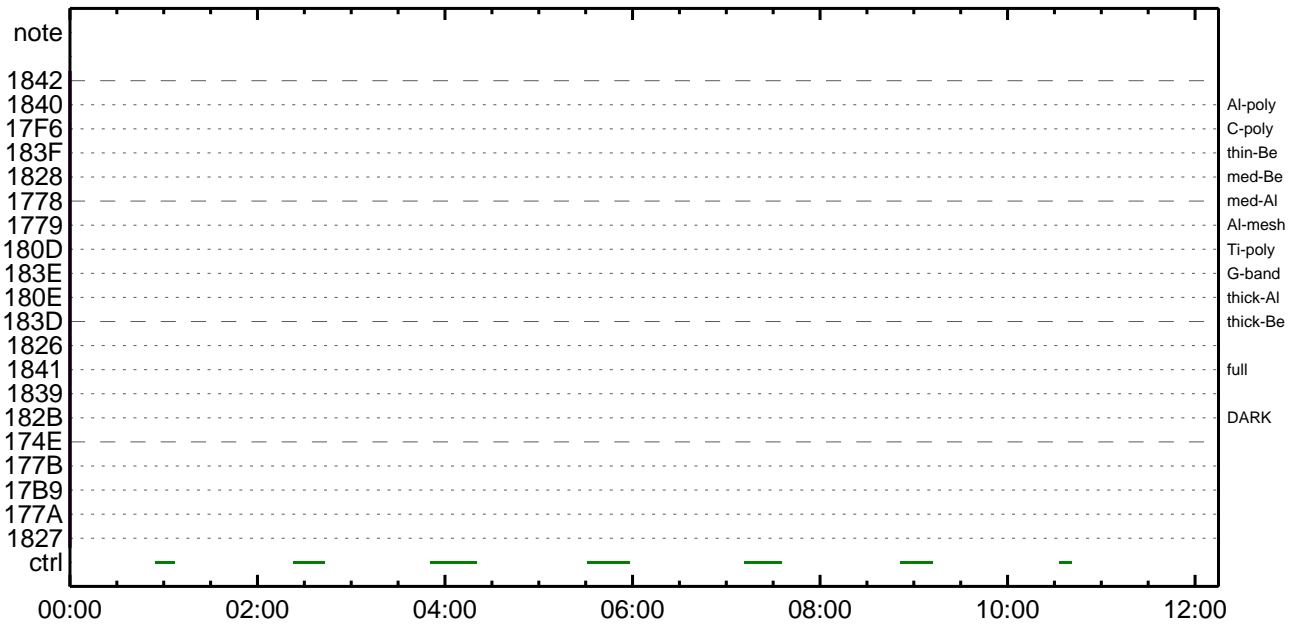
CMDI #0690 2010/12/30



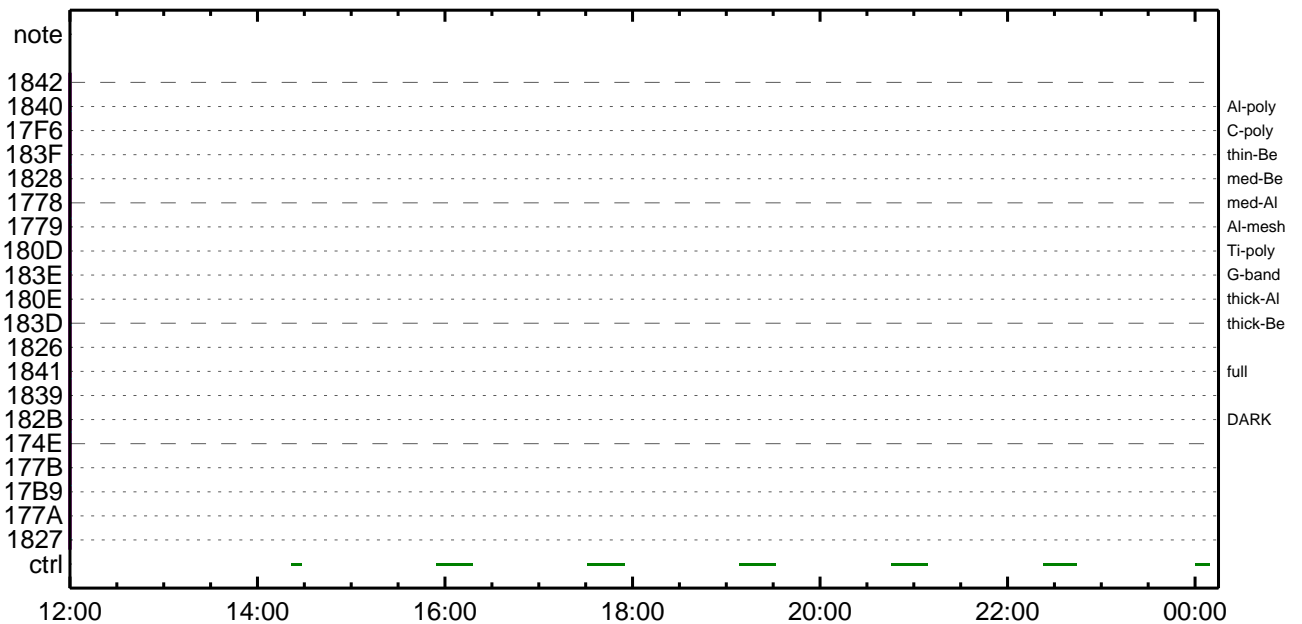
CMDI #0690 2010/12/30



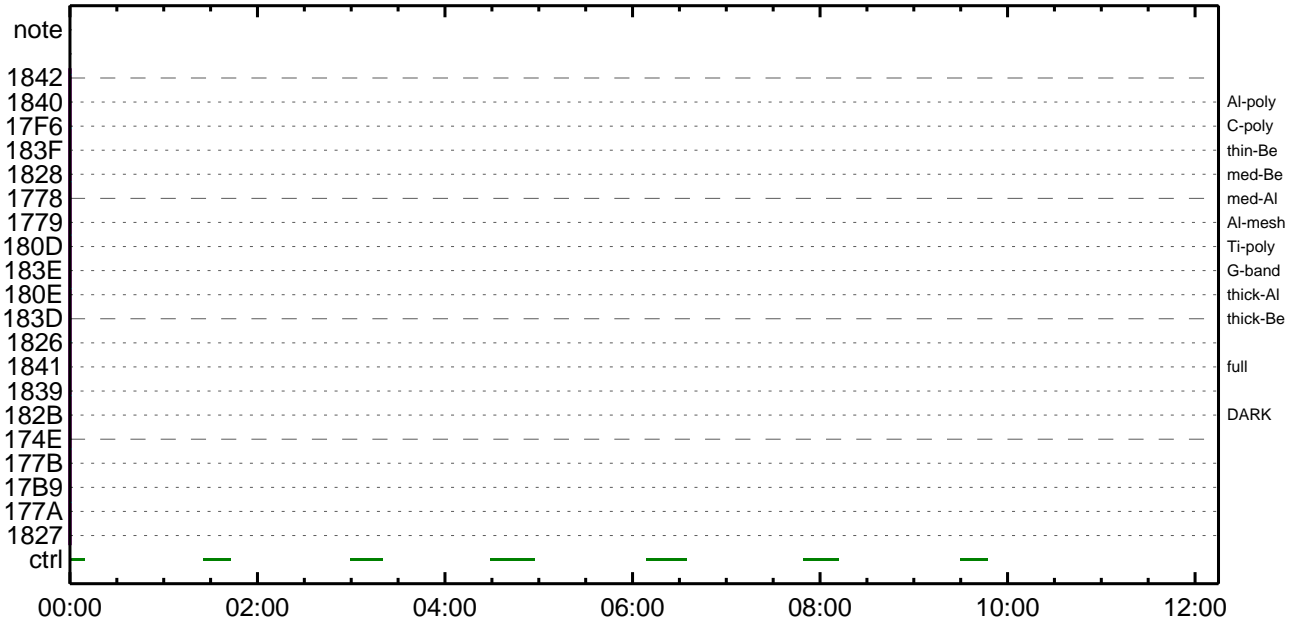
CMDI #0690 2010/12/31



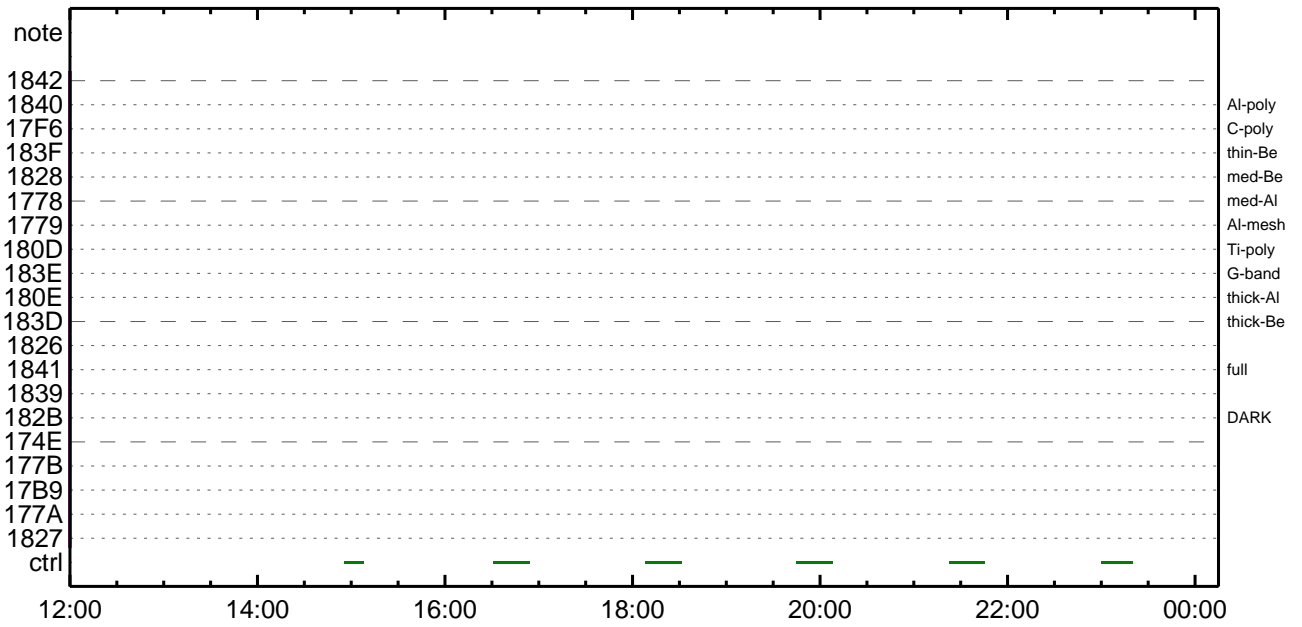
CMDI #0690 2010/12/31



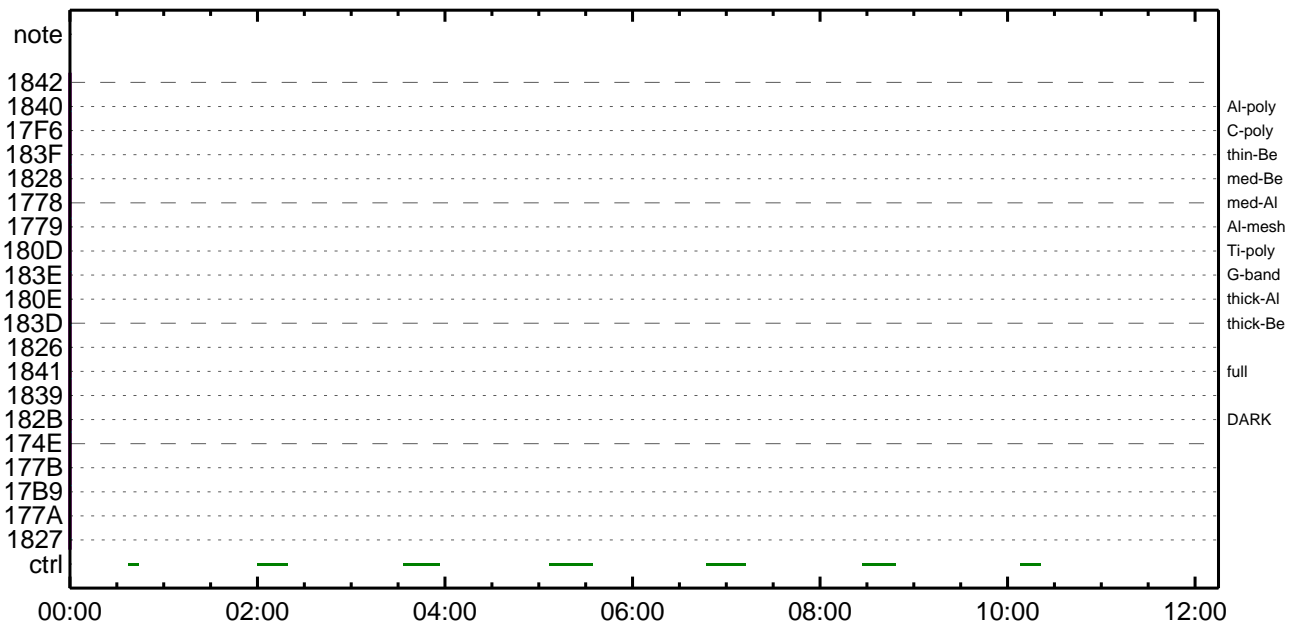
CMDI #0690 2011/01/01



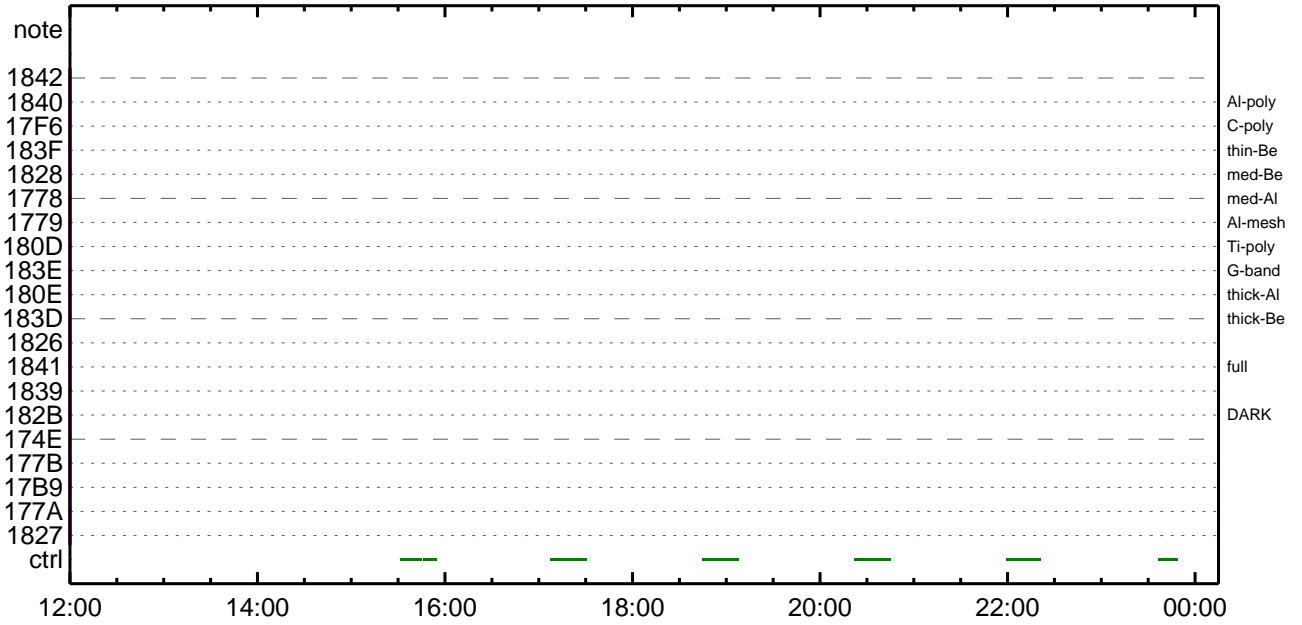
CMDI #0690 2011/01/01



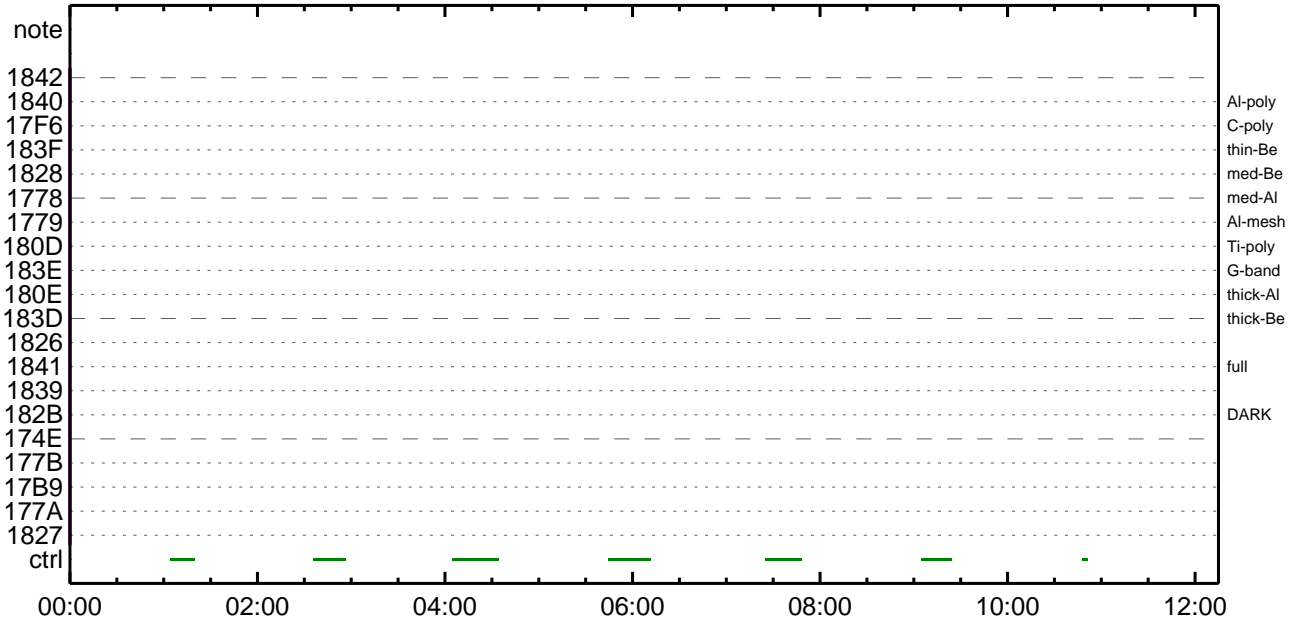
CMDI #0690 2011/01/02



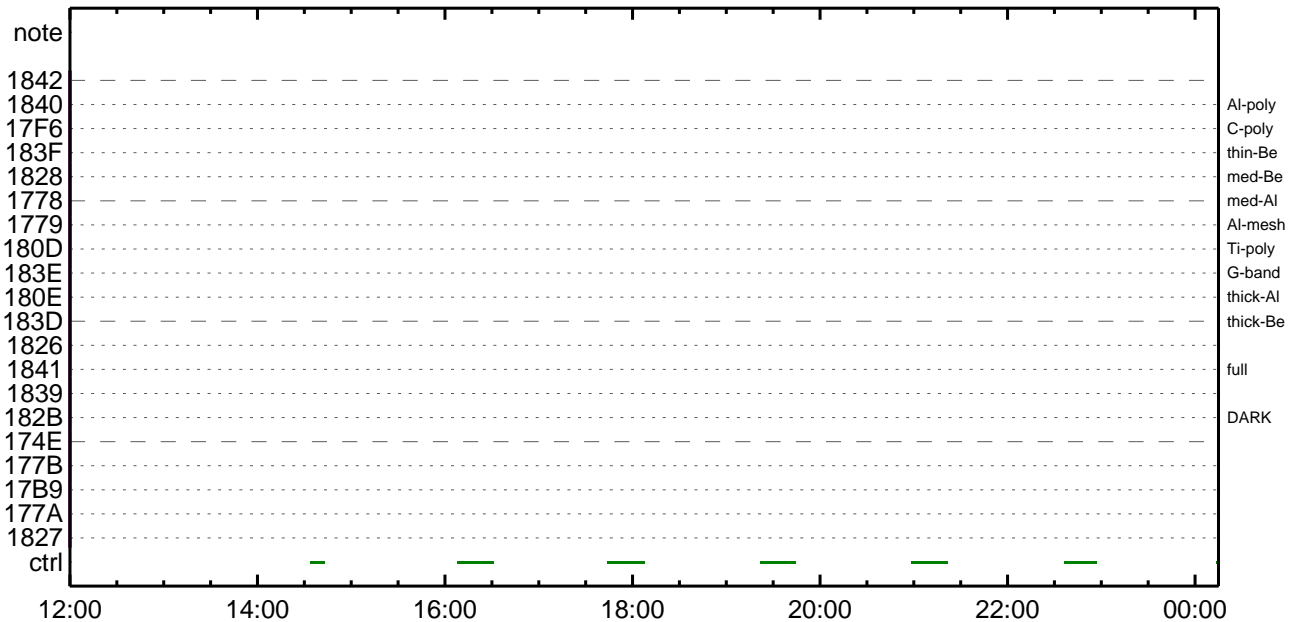
CMDI #0690 2011/01/02



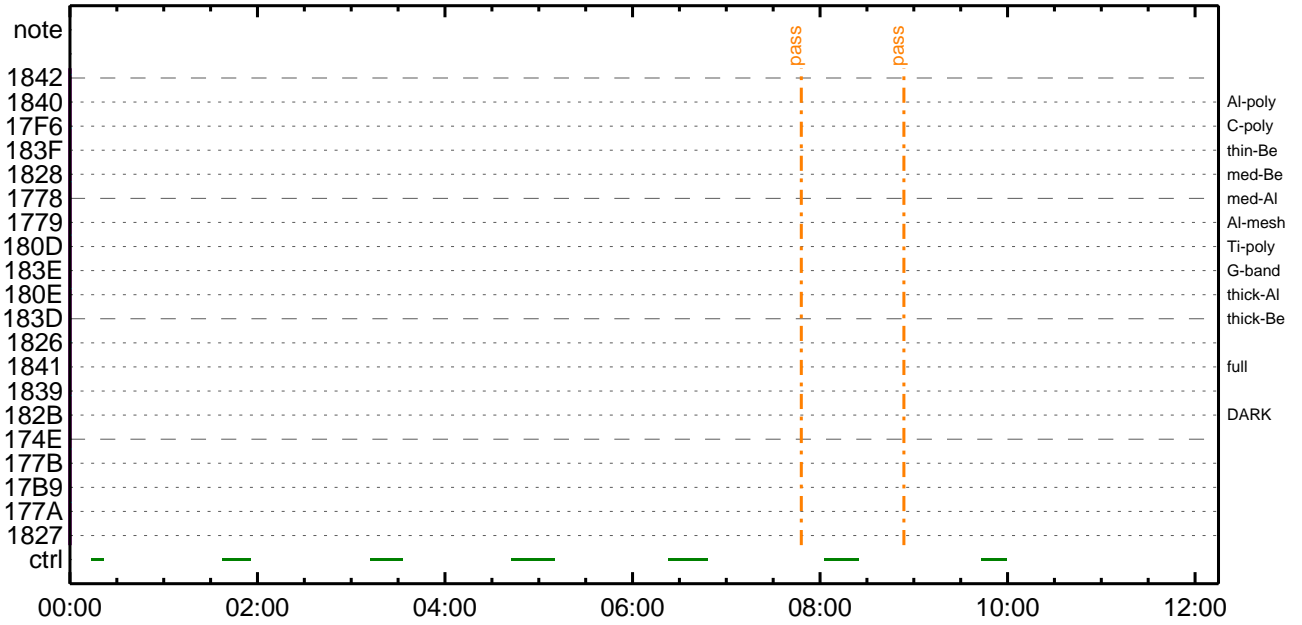
CMDI #0690 2011/01/03



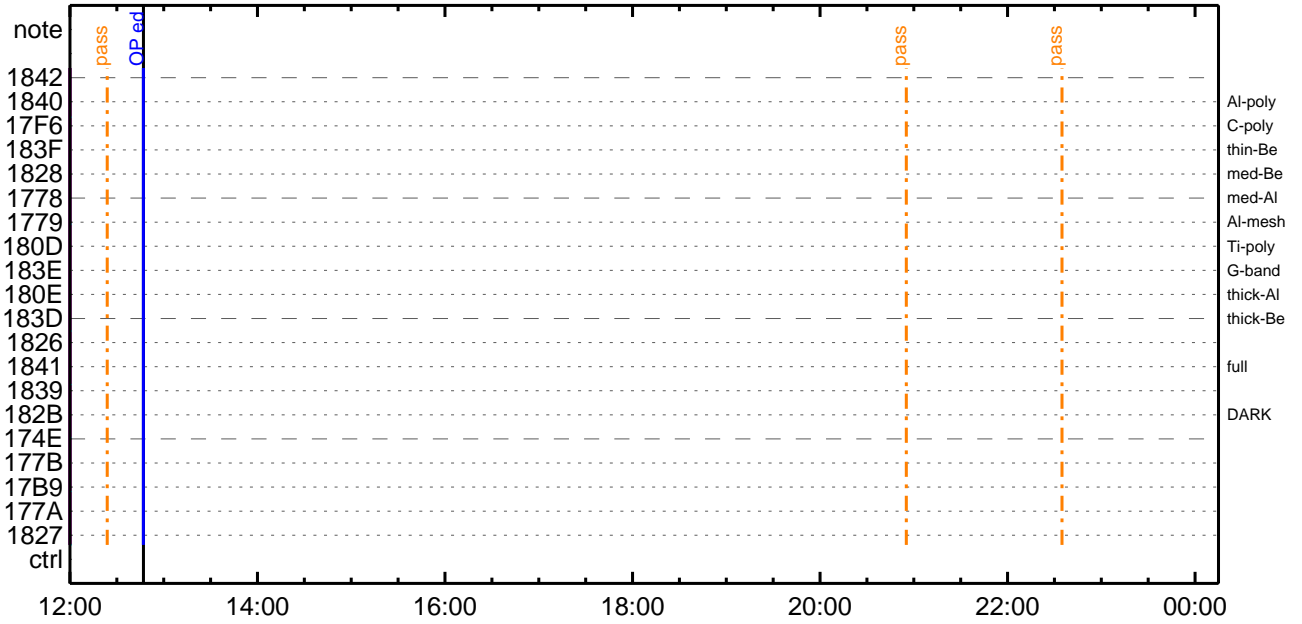
CMDI #0690 2011/01/03



CMDI #0690 2011/01/04



CMDI #0690 2011/01/04



(a) Spacecraft Operation Procedure (real-commands)

```
main-798 2010-12-29 13:38:34 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥6YÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•µ°È»Í×ÁÇ¿ÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•ííÉ;ÈðÈ¼°ÇÒð•¼¿¼l¹ç¿Í;çÀ®, ù¹ðèððçÁ+¿®ð•ðÈððð³ðÈ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ;|YÁY6Y×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-798:OP
0020 ( )
0021 . S. OG og-798:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁY6Y×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁY6Y×¼ªª î»ðð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðÍ¼È¹ç•è² ÌOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁY6Y×¼ªª î»ðð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðÍ¼È¹ç•è² ÌOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁY6Y×¼ªª î»ðð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPðÍ¼È¹ç•è² ÌOKðð³ îÇ§
0081 C.
0082 . C. ***** òÈ²¼òÍ¼Á´ ¶¼ðèÈ-òÀ+¿® (¼áµ-YÁY6Y×¼è¼çððÁÓÃæç¼ªª ð¼ðè¼ì¹ç¿çðâ) *****
0083 C. DHU¿á;¼YÉ;È¼Y¼; YÍ;¼YÉ;ÈððÍá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADð-Á+¿®NGðÍ¼¹ç; ç°È²¼ðÍTI-CMDÁ+¿®ðÍ¼Á¹Ôð•ðÈððð³ðÈ; Æ
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0096 C.                0300;çSET0EDUMP01A±°iYÑY¹0ç¹Ô0|0³0E;E
0097 C.
0098 . C. TIY³FYÖYÉ00ðÅDİ¿(UT)
0099 +. TI 2010-12-29 10:08:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0102 C.
0103 +. TI 2010-12-29 10:08:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0106 C.
0107 +. TI 2010-12-29 10:08:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0110 C.
0111 +. TI 2010-12-29 10:12:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0114 C.
0115 C. 0E²¼0İÄè%îíÑ0İYÁYŞYÁY-¹àîÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0117 C.                çç[HK1_TI_CMD_NUM]              EQ      4
0118 C.                çç[HK1_NEXT_EXEC_PIM]            EQ      DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]            EQ      0xB3
0120 C.
0121 . C. *****
0122 C. TIİî°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]            EQ      07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]            EQ      2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]            EQ      3
0131 C.                çç[HK1_DMP_REPEAT_NUM]          EQ      0
0132 C.                çç[HK1_DMA_DMP_PIM]             EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]              EQ      7
0136 C.                çç[HK1_PKT_GEN_TIME]             EQ      0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0139 C.                çç[HK1_DMP_CHK_FLG]             EQ      EXEC
0140 C.
0141 . C. YÄYÖY×¼¹¹»0ð³İÇŞ
0142 C.                çç[HK1_DMP_CHK_FLG]             EQ      NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹ç•è²İOK0ð³İÇŞ
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¾,Yî;¼YÈ;Ë0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]              EQ      2
0150 C.                çç[HK1_PKT_GEN_TIME]             EQ      0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2010-12-29 10:12:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC                (21 02)
0163 +. TI 2010-12-29 10:12:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC                (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2010-12-29 10:12:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC                (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2010-12-29 10:12:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´üÄî0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÄYÈYÏYÉYÁYçYè0E%¼00¼Ä»Ü0¹0è)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-800 2010-12-29 13:38:34 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÀø•µ°È»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÇø•ø¿¼í¹çøí;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 . C. ----- Success Verify ? OK / NG ____
0055 C.
0056 C.
0057 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0058 C.
0059 +. DC 07-F0 MDP_XRT_MODE_OBSV
0060 BC (c2)
0061 +. TI 2010-12-29 10:12:02.0
0062 DC 07-F0 MDP_XRT_MODE_OBSV
0063 BC (c2)
0064 . C. ----- Success Verify ? OK / NG ____
0065 C.
0066 C. ***** XRT END *****
0067 . C. *****
0068 C. SOT table upload
0069 C. *****
0070 . C. < Stop FG table >
0071 +. DC 07-F0 MDP_FG_CTRL_MANU
0072 BC (51)
0073 . C. -----
0074 C. MDP_FG_CTRL_MODE = MANU [ ]
0075 C. -----
0076 C.
0077 . C. <Upload FG Observation Table>
0078 . S. RAM ram-265:MDP_OBS_F
0079 ( )
0080 C.
0081 . C. < Dump RAMID=MDP_OBS_F >
0082 +. DC 07-F0 MDP_DUMP_FGTBL
0083 BC (82 07 00 00 00 38 b8)
0084 C. -----
0085 C. MDP_OBS_F verify = OK/NG [ ]
0086 C. -----
0087 C.
0088 . C. < Upload DPL table >
0089 C.
0090 C. YçYÁY×Yí;¼YÉøíÁ°øÈSTS_CHKøøOFFøÈø¹øÈ
0091 C.
0092 . S. RAM ram-271:MDP_DPL
0093 ( )
0094 C.
0095 . C. < Dump RAMID=MDP_DPL >
```

```
0096 +. DC 07-F0 MDP_DUMP_FGTBL
0097 BC (82 07 00 38 b8 00 40)
0098 C. -----
0099 C. MDP_DPL verify = OK [ ]
0100 C. -----
0101 C.
0102 C. STS_CHKαδONαÈα¹αè
0103 C.
0104 . C. < Update MDP DSC PAR1 >
0105 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0106 BC (4c)
0107 C. MDP_CMD_CODE = F04C0700[ ]
0108 C. MDP_CMD_CNT (count-up 1) [ ]
0109 C. -----
0110 C.
0111 . C.
0112 C. *****
0113 C. SOT TI command set
0114 C. *****
0115 C. Execute, after the success of TBL upload.
0116 +. TI 2010-12-29 10:12:18.0
0117 DC 07-F0 MDP_SOT_MODE_OBSV
0118 BC (40)
0119 . C. -----
0120 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0121 C. -----
0122 C.
0123 C.
0124 . C. ***** MDP `ûÃîαî»ò%ÿαÈÃDα¹αèDCBC•x²è *****
0125 C. (%â°îÿÔÿÃÿÈÿPÿËÿáÿçÿèαÈ%¼α¼Ã»Ûα¹αè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹•î Daily±;îÑαÈ'Øα¹αèDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿÿÿÿ-¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
```

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*** OP Sequence for XRT ***

2010/12/29	10:23:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2010/12/29	13:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	13:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/12/29	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/12/29	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/29	14:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/29	14:00:20.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/29	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/29	14:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2010/12/29	14:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/12/29	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/29	14:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	14:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/29	14:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/29	14:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/29	14:54:00.0	XRT_Custom_418_OG [0x1a2]							
2010/12/29	14:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/29	16:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	16:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/29	16:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/29	16:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/29	16:41:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/29	16:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/29	17:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	17:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/29	17:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/12/29	17:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/12/29	18:18:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	18:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2010/12/29	18:18:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2010/12/29	18:18:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/12/29	18:18:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/12/29	18:18:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/29	18:21:28.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2010/12/29	18:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/29	18:28:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	18:28:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/12/29	18:28:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2010/12/29	18:28:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/12/29	18:28:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/12/29	18:28:50.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/12/29	18:31:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/12/29	18:31:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2010/12/29	18:31:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2010/12/29	18:31:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/12/29	19:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/12/29	19:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							

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2010/12/29	19:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/29	19:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/29	19:55:00.0	XRT_Custom_418_OG [0x1a2]				
2010/12/29	19:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/29	21:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/29	21:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/29	21:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/29	21:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/29	21:32:30.0	XRT_Custom_418_OG [0x1a2]				
2010/12/29	21:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/29	22:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/29	22:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/29	22:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/29	22:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/29	23:07:30.0	XRT_Custom_418_OG [0x1a2]				
2010/12/29	23:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	00:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	00:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/30	00:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/30	00:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/30	00:31:30.0	XRT_Custom_418_OG [0x1a2]				
2010/12/30	00:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	01:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	01:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/30	01:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/30	01:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/30	02:06:30.0	XRT_Custom_418_OG [0x1a2]				
2010/12/30	02:07:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	03:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	03:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/30	03:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/30	03:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/30	03:44:00.0	XRT_Custom_418_OG [0x1a2]				
2010/12/30	03:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	04:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	04:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/12/30	04:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/12/30	04:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/12/30	05:21:30.0	XRT_Custom_418_OG [0x1a2]				
2010/12/30	05:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	06:03:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	06:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/12/30	06:04:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2010/12/30	06:04:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/12/30	06:04:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/12/30	06:04:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/12/30	06:06:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2010/12/30	06:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/12/30	06:13:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/12/30	06:13:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				

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2010/12/30	06:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	01	00	00	00
2010/12/30	06:14:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/12/30	06:14:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/12/30	06:14:20.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/12/30	06:16:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/12/30	06:16:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2010/12/30	06:16:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2010/12/30	06:17:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/12/30	06:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/12/30	06:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/12/30	06:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/12/30	06:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/12/30	06:58:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/30	06:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/12/30	08:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/12/30	08:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2010/12/30	08:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2010/12/30	08:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2010/12/30	08:35:30.0	XRT_Custom_418_OG [0x1a2]							
2010/12/30	08:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/12/30	09:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/12/30	10:07:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00