

XRT Timeline to be uploaded on 2011/01/06

Period: 2011/01/06 09:44:00 - 2011/01/11 09:33:00

* * * * *

Normal mode

* * * * *

XOB #181D: HOP81 FW1=Al/poly - 8s - AR Present - NO G-band - 384x384											
Term	Pointing (x, y)	Comment									
01/06 09:57:00 - 01/06 14:45:30	Fixed (0.0, 940.0)	# OP start + 10min ; HOP 81 - North Pole									
01/07 10:14:36 - 01/07 14:59:54	Fixed (0.0, -940.0)	HOP 81 - South pole									
PROG= 09 Inf.-time(s)											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 80 60-time(s) 30.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1844: AR Standard-A(Filter-Ratio) with PFB, thin-Be context, 384x384 at 1064 1048, 120s cad											
Term	Pointing (x, y)	Comment									
01/06 15:03:00 - 01/06 17:57:30	Track (92.8, 560.6) ^{Ⓜ 01/06 15:00:00}	track AR 11140									
01/07 06:19:30 - 01/07 09:57:00	Track (518.3, -190.0) ^{Ⓜ 01/07 06:16:30}	# track AR 11142									
01/07 15:03:00 - 01/07 17:59:54	Track (582.9, -192.4) ^{Ⓜ 01/07 15:00:00}	track AR 11142									
01/07 18:13:00 - 01/08 06:10:24	Track (605.3, -193.4) ^{Ⓜ 01/07 18:10:00}	# track AR 11142									
01/08 06:23:30 - 01/08 10:36:00	Track (686.1, -197.6) ^{Ⓜ 01/08 06:20:30}	# track AR 11142									
PROG= 11 Inf.-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
└─ Seqn= 82 4-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 15 20-time(s) 2.0sec											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16											
Term	Pointing (x, y)	Comment									
01/06 18:24:00 - 01/06 18:30:54	Fixed (0.0, 0.0)	synoptic, shifted 21.0 min									
01/07 06:09:30 - 01/07 06:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min									
01/07 18:03:00 - 01/07 18:09:54	Fixed (0.0, 0.0)	synoptic									
01/08 06:13:30 - 01/08 06:20:24	Fixed (0.0, 0.0)	synoptic, shifted 10.5 min									
PROG= 17 1-time(s)											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 69 1-time(s) 4.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 28 1-time(s) 4.0sec											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1845: AR Standard-B(Morphology) with PFB, Ti-Poly, thin-Be context, 512x512 at 1064 1048, 60sec-cad											
Term	Pointing (x, y)	Comment									
01/06 18:34:00 - 01/07 03:25:30	Track (432.5, -304.7) ^{Ⓜ 01/06 18:31:00}	# HOP 150 - track filament									
PROG= 12 Inf.-time(s)											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 17 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
└─ Seqn= 85 4-time(s) 2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec

	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1	1-time(s)	2.0sec											
Seqn= 77	45-time(s)	30.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)													
Term	Pointing (x, y)								Comment				
01/06 09:57:00 - 01/06 14:45:30	Fixed (0.0, 940.0)								# OP start + 10min ; HOP 81 - North Pole				
01/06 15:03:00 - 01/06 17:57:30	Track (92.8, 560.6) @ 01/06 15:00:00								track AR 11140				
01/06 18:34:00 - 01/07 03:25:30	Track (432.5, -304.7) @ 01/06 18:31:00								# HOP 150 - track filament				
01/07 06:19:30 - 01/07 09:57:00	Track (518.3, -190.0) @ 01/07 06:16:30								# track AR 11142				
01/07 10:14:36 - 01/07 14:59:54	Fixed (0.0, -940.0)								HOP 81 - South pole				
01/07 15:03:00 - 01/07 17:59:54	Track (582.9, -192.4) @ 01/07 15:00:00								track AR 11142				
01/07 18:13:00 - 01/08 06:10:24	Track (605.3, -193.4) @ 01/07 18:10:00								# track AR 11142				
01/08 06:23:30 - 01/08 10:36:00	Track (686.1, -197.6) @ 01/08 06:20:30								# track AR 11142				
PROG= 16	1-time(s)												
Subr= 1	30-time(s)	20.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
Seqn= 89	1-time(s)	2.0sec											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

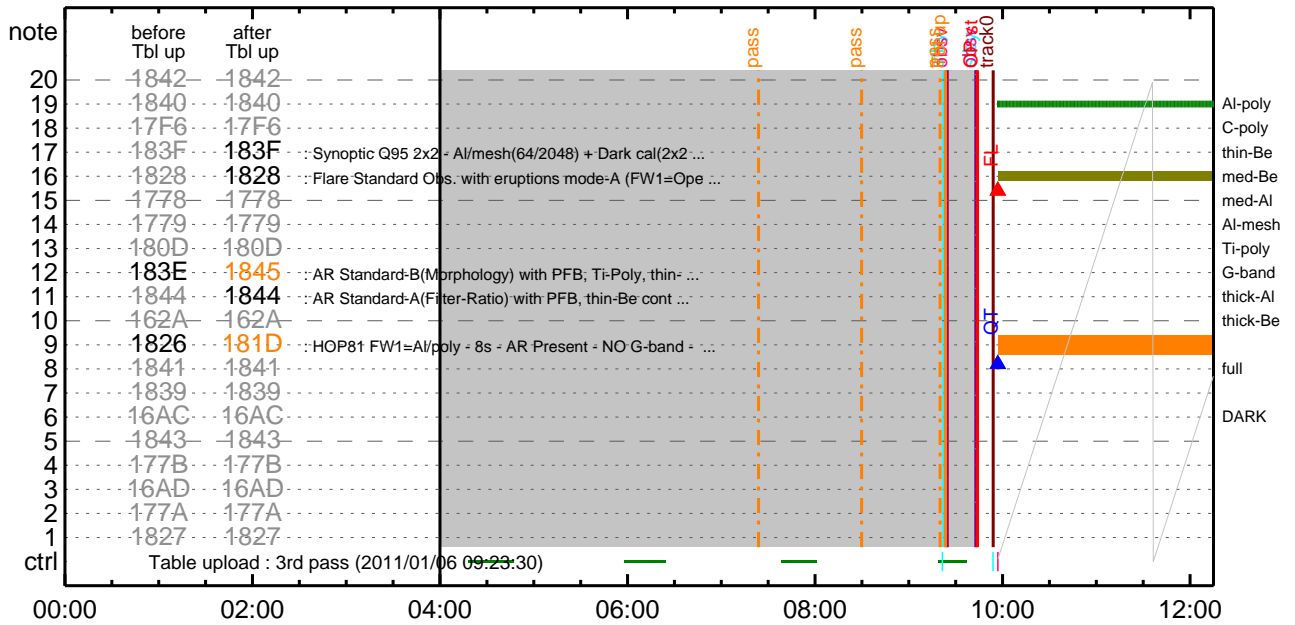
* * * * *

Flare Detection

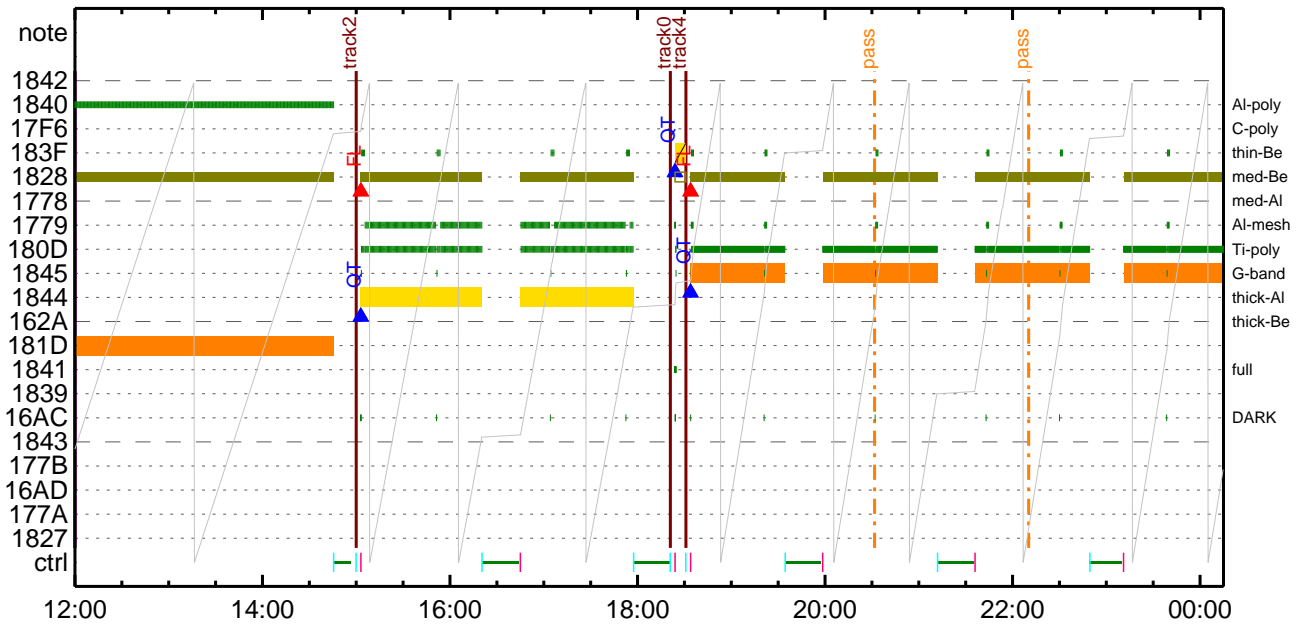
* * * * *

FLD Patrol											
Term		Pointing (x, y)						Comment			
01/06 09:54:16 - 01/06 18:21:16		Fixed (0.0, 940.0)						# OP start + 10min ; HOP 81 - North Pole			
01/06 18:31:16 - 01/07 06:06:46		Track (432.5, -304.7) @ 01/06 18:31:00						# HOP 150 - track filament			
01/07 06:16:46 - 01/07 18:00:16		Track (518.3, -190.0) @ 01/07 06:16:30						# track AR 11142			
01/07 18:10:16 - 01/08 06:10:46		Track (605.3, -193.4) @ 01/07 18:10:00						# track AR 11142			
01/08 06:20:46 - 01/11 09:33:00		Track (686.1, -197.6) @ 01/08 06:20:30						# track AR 11142			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

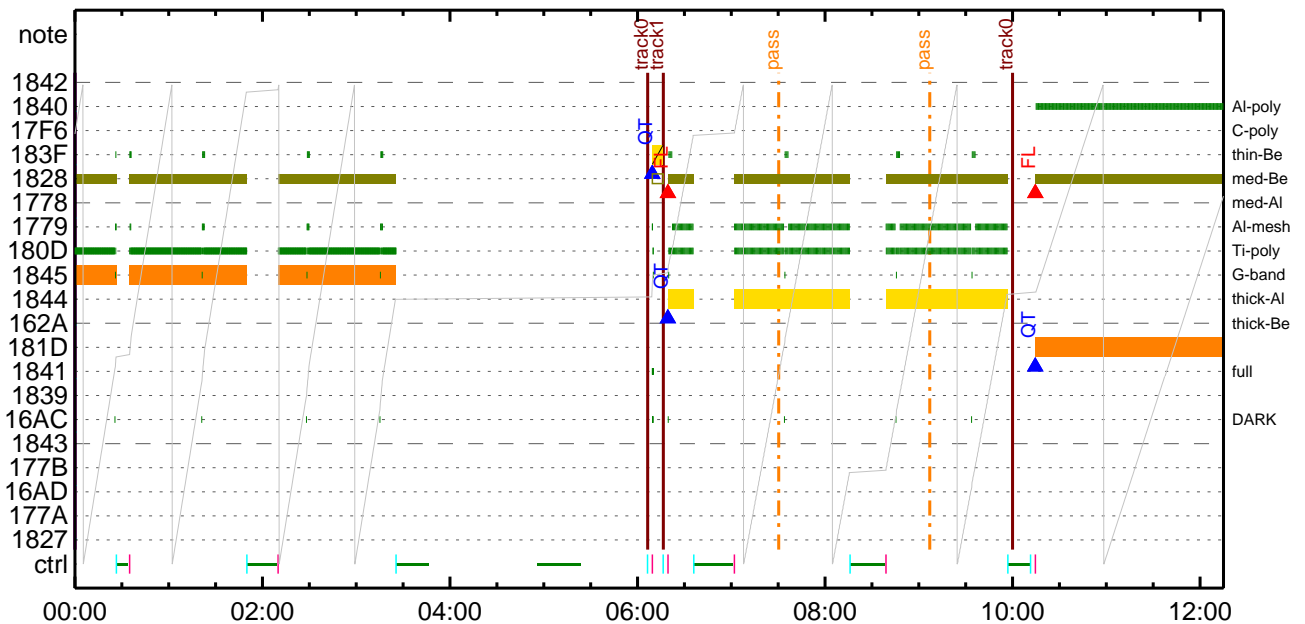
CMDI #0699 2011/01/06



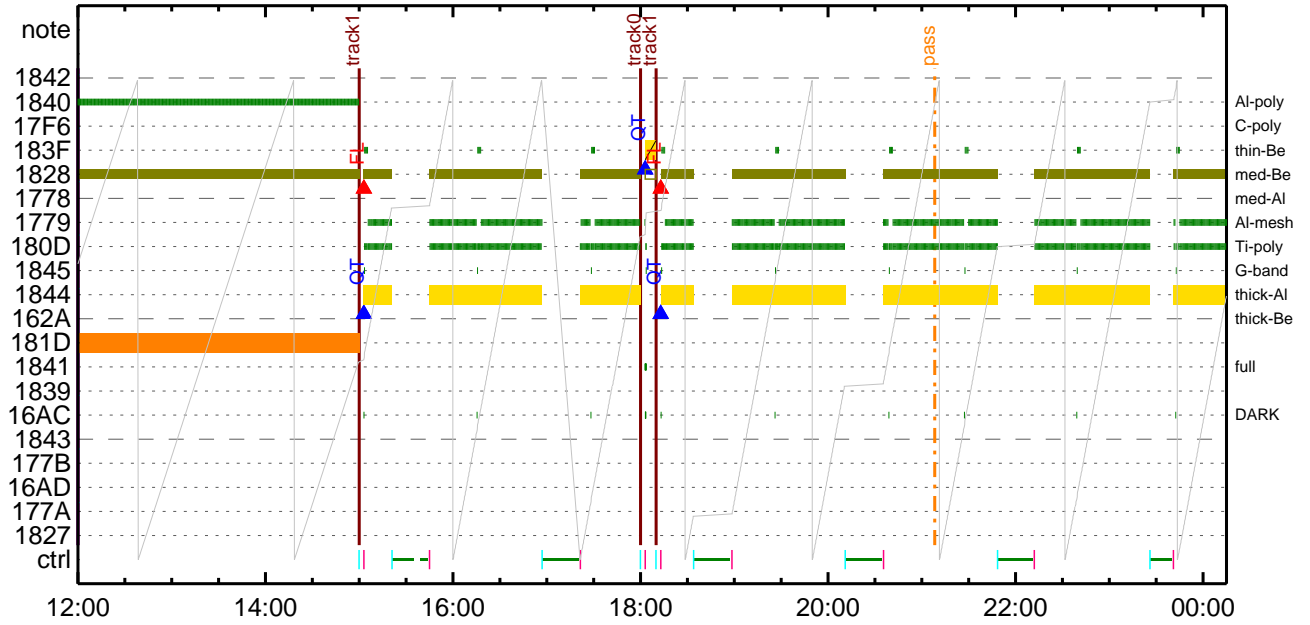
CMDI #0699 2011/01/06



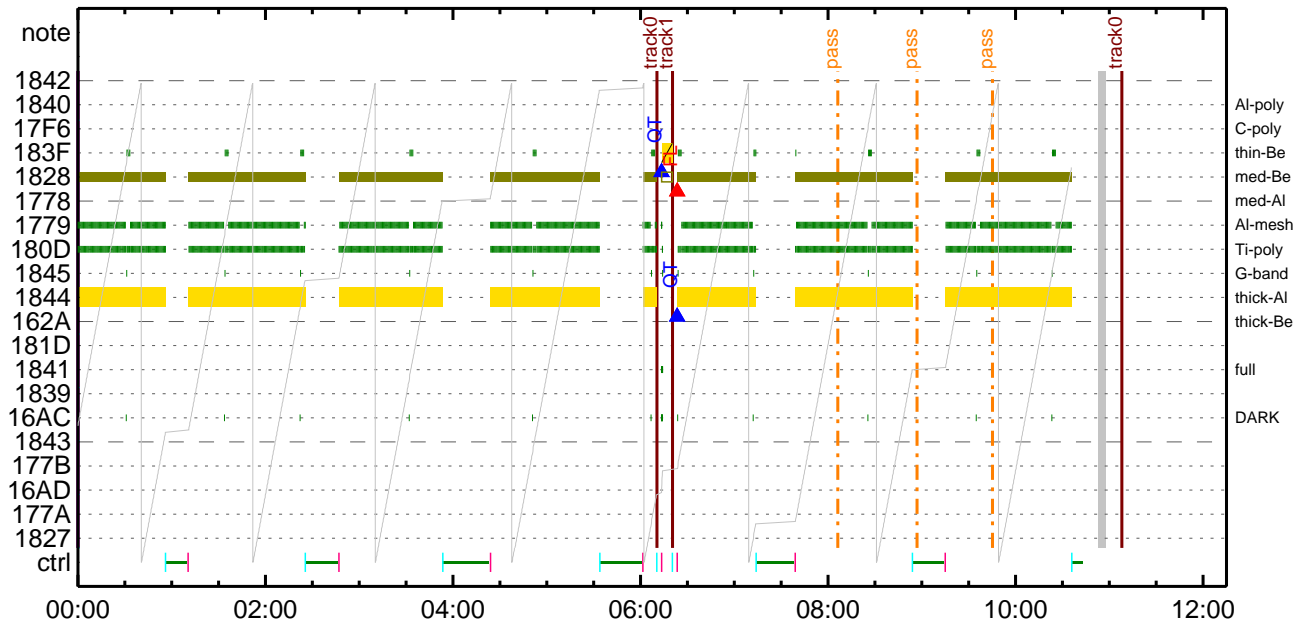
CMDI #0699 2011/01/07



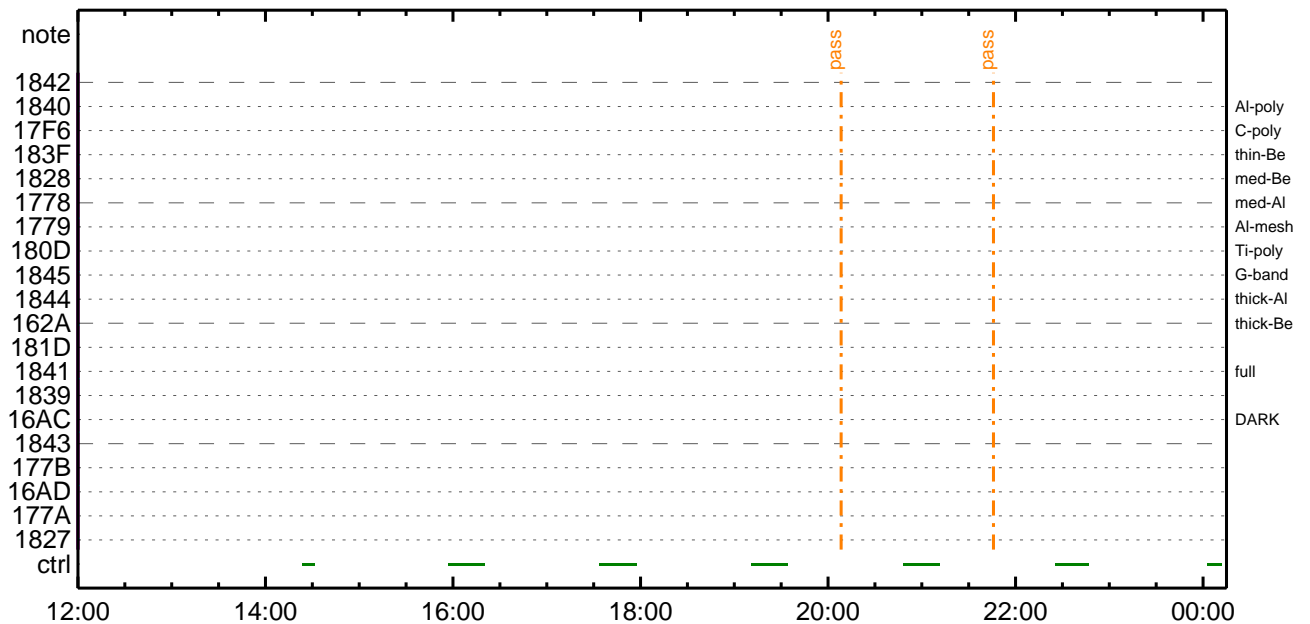
CMDI #0699 2011/01/07



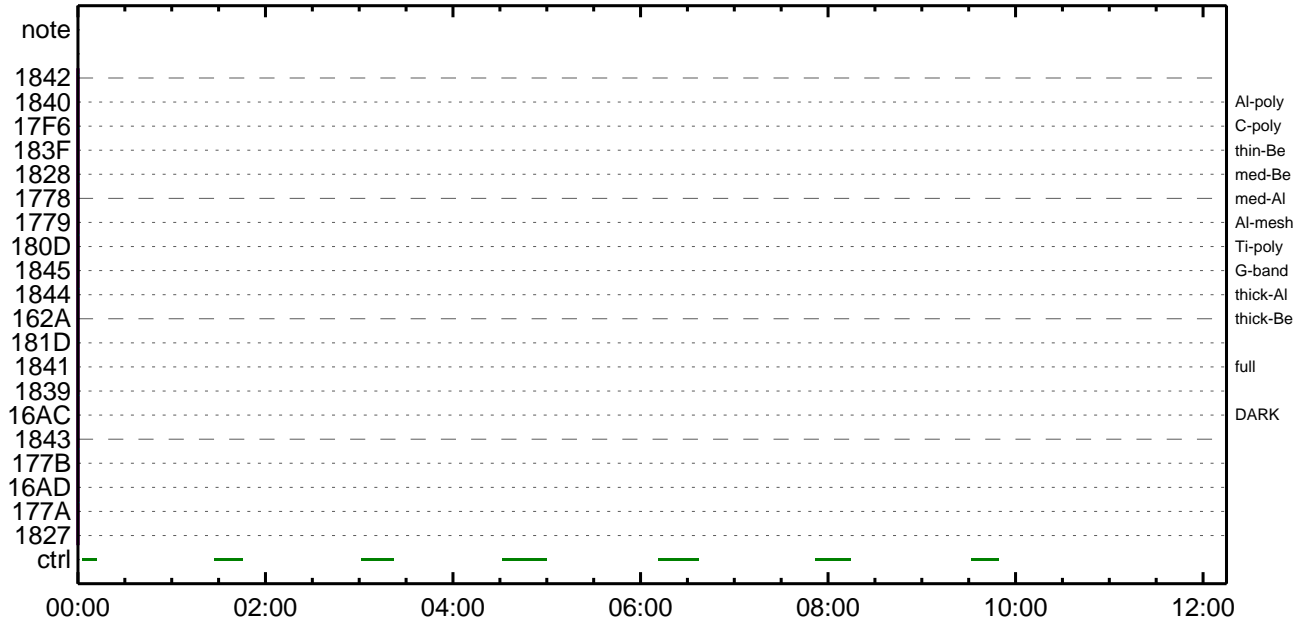
CMDI #0699 2011/01/08



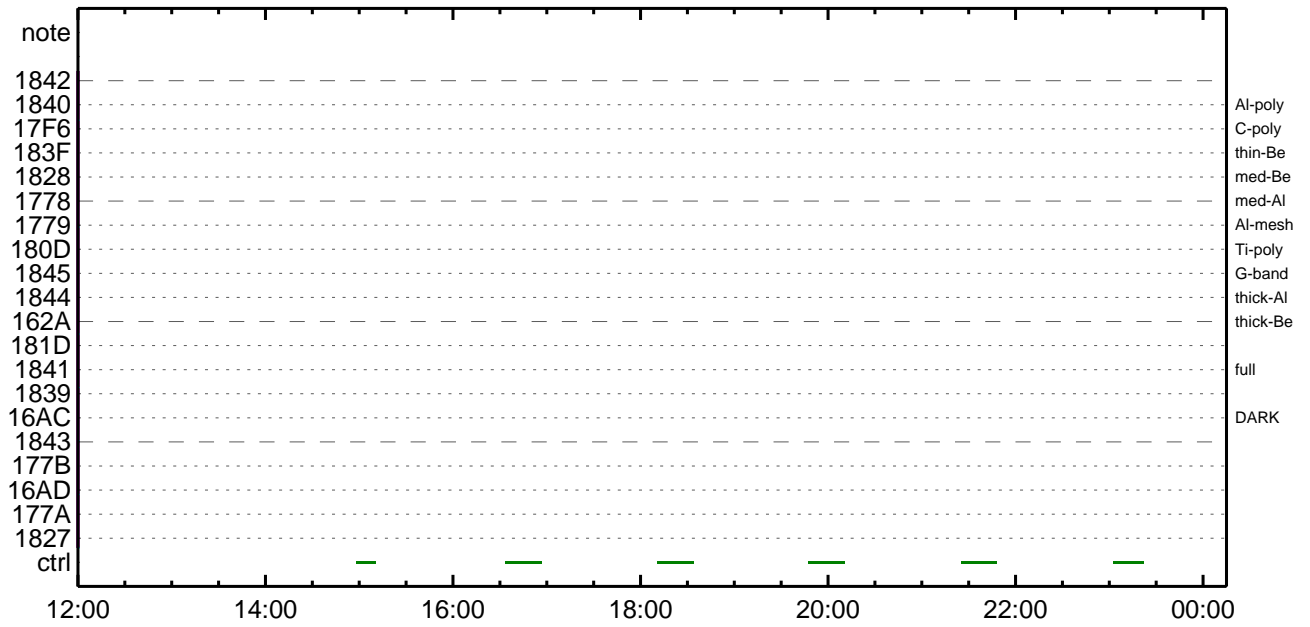
CMDI #0699 2011/01/08



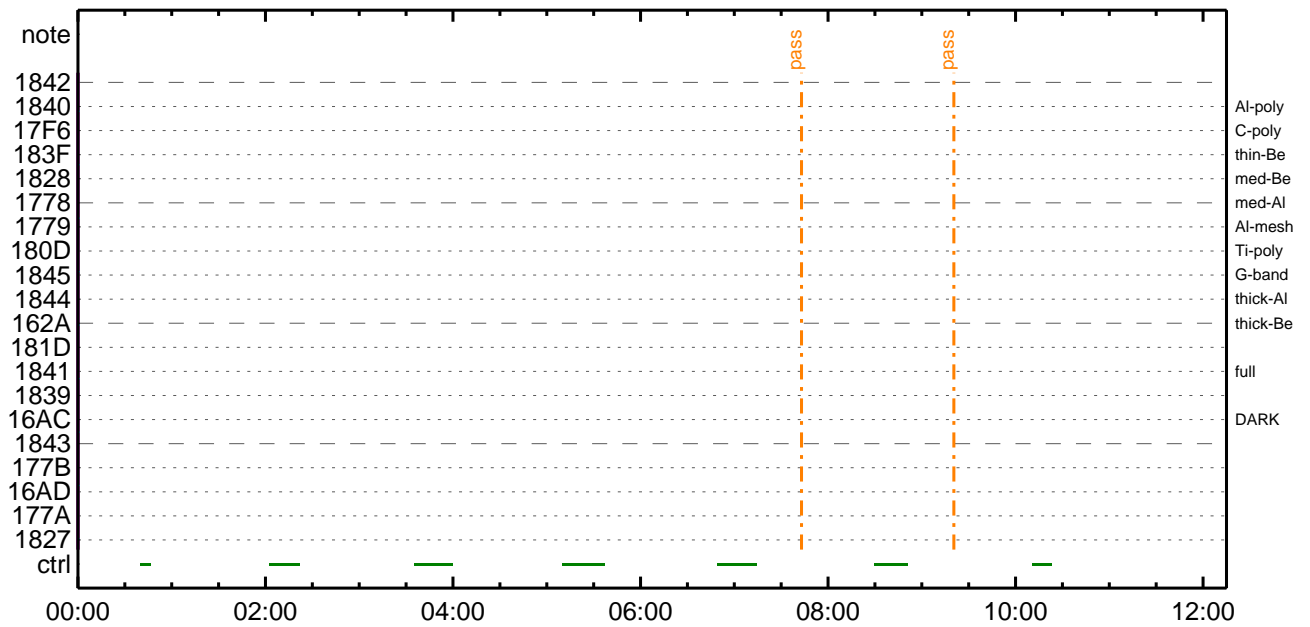
CMDI #0699 2011/01/09



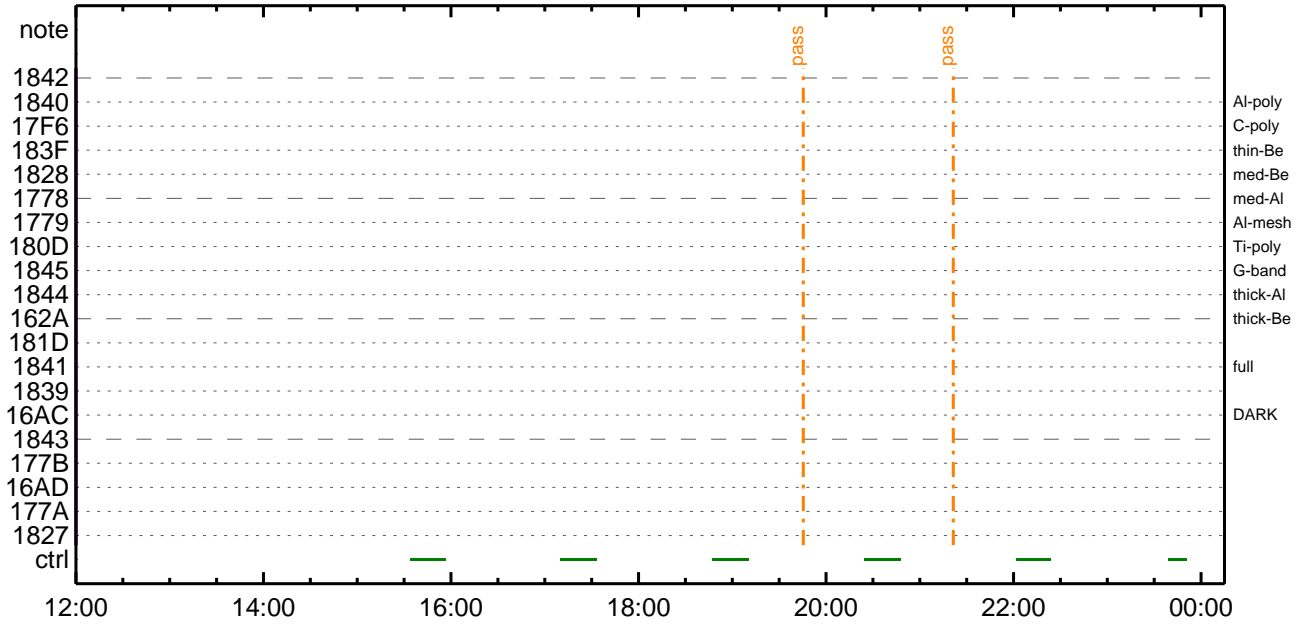
CMDI #0699 2011/01/09



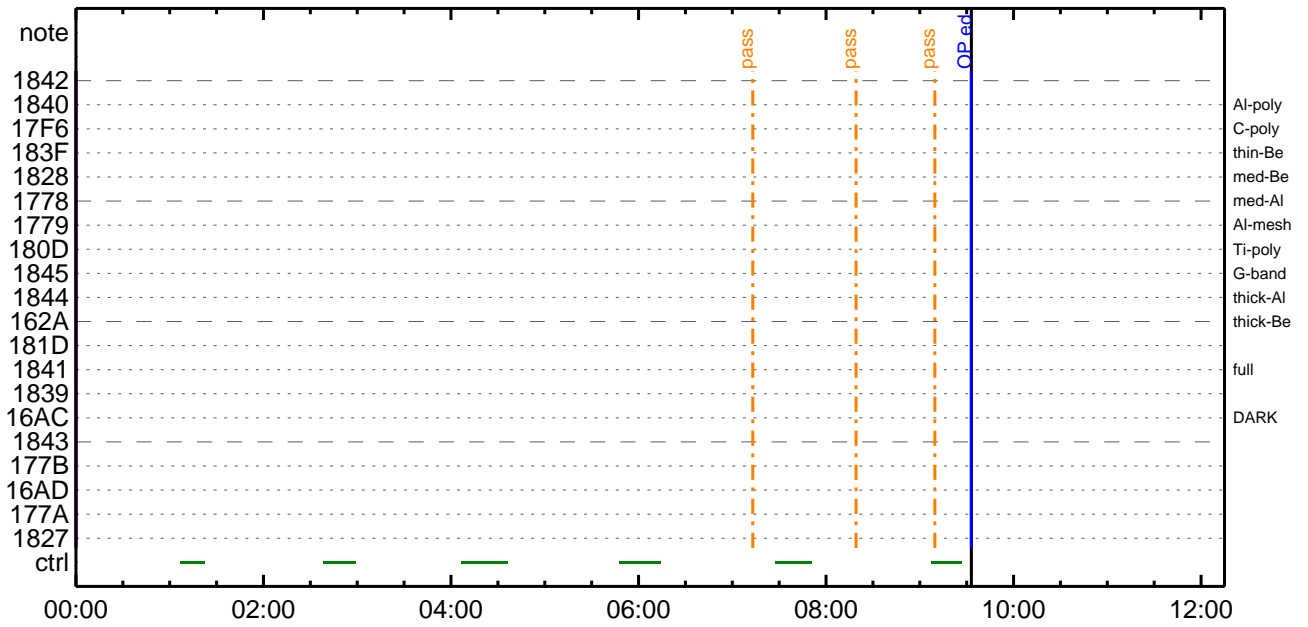
CMDI #0699 2011/01/10



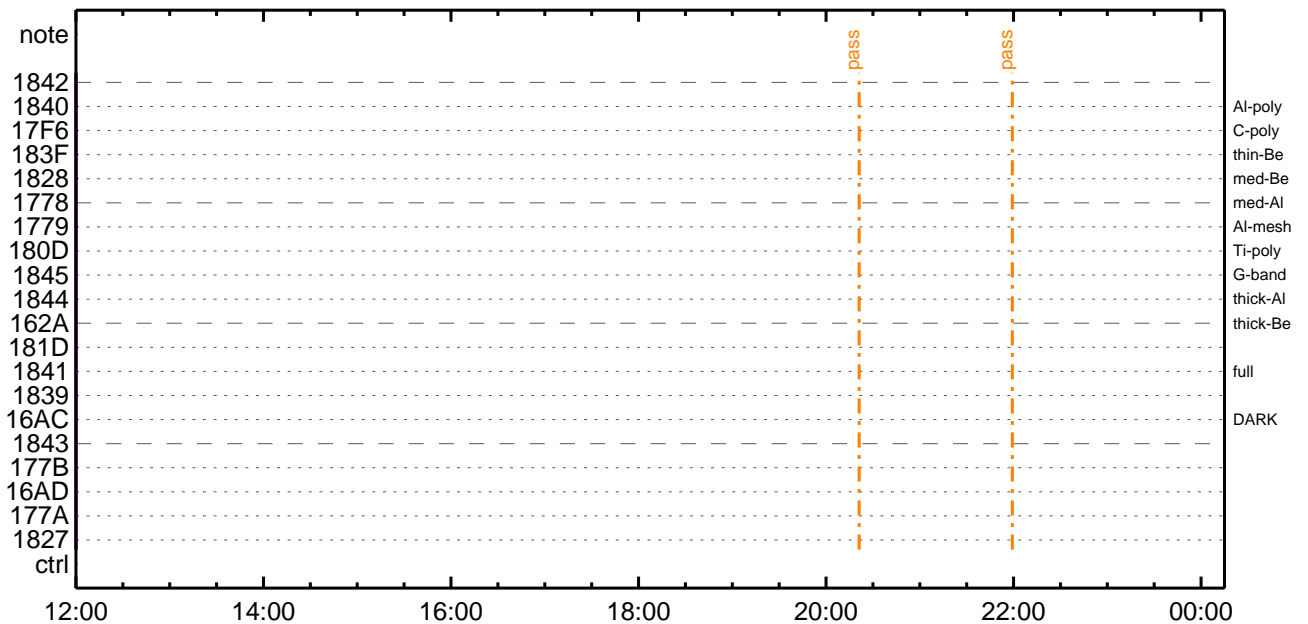
CMDI #0699 2011/01/10



CMDI #0699 2011/01/11



CMDI #0699 2011/01/11



(a) Spacecraft Operation Procedure (real-commands)

```
main-810 2011-01-06 11:44:34 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÃY$YÃY`¼Ä»Û;ä
0005 C.
0006 C. YÃYB;¼Y³YF¥ÖYÉA+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÅÍ;È¿¿ðÃâ•µ°Ë»Í×ÁÇ¿ÍY¿YÃY×YÍ;¼YÉ;ÈÈè¾µ•ÍÊ;ÈðÈ¼°ÇÔâ•µ¿¼l¹ç¿Í;¿Ä®.Ûâ¹âÈðß¿Á+¿®â•µÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ; |YÃYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-810:OP
0020 ( )
0021 . S. OG og-810:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÏ°èYÃYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ        40
0029 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ          0
0030 C.        ¿¿[HK1_DMP_BLOCK_NUM]           EQ        127
0031 C.        ¿¿[HK1_DMP_REPEAT_NUM]         EQ          0
0032 C.        ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.        ¿¿[HK1_PKT_FORM_NO]             EQ          7
0036 C.        ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0037 C.        ¿¿[HK1_S_TLM_BIT_RATE]         EQ        32k
0038 C.        ¿¿[HK1_X_TLM_BIT_RATE]         EQ         4M
0039 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        EXEC
0040 . C. YÃYÖY×¾²¹Î»ðð³ÍÇ$
0041 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0042 . C. RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ÍÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ        41
0048 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ          0
0049 C.        ¿¿[HK1_DMP_BLOCK_NUM]           EQ        127
0050 C.        ¿¿[HK1_DMP_REPEAT_NUM]         EQ          0
0051 C.        ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.        ¿¿[HK1_PKT_FORM_NO]             EQ          7
0055 C.        ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0056 C.        ¿¿[HK1_S_TLM_BIT_RATE]         EQ        32k
0057 C.        ¿¿[HK1_X_TLM_BIT_RATE]         EQ         4M
0058 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        EXEC
0059 . C. YÃYÖY×¾²¹Î»ðð³ÍÇ$
0060 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0061 . C. RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ÍÇ$
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ        42
0067 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ          0
0068 C.        ¿¿[HK1_DMP_BLOCK_NUM]           EQ        65
0069 C.        ¿¿[HK1_DMP_REPEAT_NUM]         EQ          0
0070 C.        ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.        ¿¿[HK1_PKT_FORM_NO]             EQ          7
0074 C.        ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0075 C.        ¿¿[HK1_S_TLM_BIT_RATE]         EQ        32k
0076 C.        ¿¿[HK1_X_TLM_BIT_RATE]         EQ         4M
0077 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        EXEC
0078 . C. YÃYÖY×¾²¹Î»ðð³ÍÇ$
0079 C.        ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0080 . C. RAM ID=NMOG, RAM ID=OPðÍ¼È¹ç•è²ÍOKðð³ÍÇ$
0081 C.
0082 . C. ***** òÈ²¼ðÍ¼Ã´¶Ì°òÈÈ-ò°Ã+¿®(¼ãµ-YÃYÖY×¼È¼çððÄÓÃæç¾²¹µ-ò¼¼l¹ç¿ç¿â) *****
0083 C. DHUã;¼YÉ;È¾Y¾. YÍ;¼YÉ;ÈððÍáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.        ¿¿[HK1_PKT_FORM_NO]             EQ          2
0087 C.        ¿¿[HK1_PKT_GEN_TIME]            EQ        0.5S
0088 C.        ¿¿[HK1_S_TLM_BIT_RATE]         EQ        32k
0089 C.        ¿¿[HK1_X_TLM_BIT_RATE]         EQ         4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADâ-Ã+¿®NGðÍ¼l¹ç¿ç•è²¼¼ðÍTI-CMDÃ+¿®ðÍ¼Ä¹Ôâ•µÈððð³ðÈ;f
```


0096 C. ... ; SET EDUMPA ... ;
0097 C.
0098 . C. TI Y3 Y P Y6 Y E p d A D i (UT)
0099 +. TI 2011-01-06 09:39:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2011-01-06 09:39:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2011-01-06 09:39:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2011-01-06 09:43:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. ...
0116 C. ... [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C. ... [HK1_TI_CMD_NUM] EQ 4
0118 C. ... [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C. ... [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 . C. *****
0122 C. TI I I ^ o e y a y o y x
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; \$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. ... [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C. ... [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C. ... [HK1_DMP_BLOCK_NUM] EQ 3
0131 C. ... [HK1_DMP_REPEAT_NUM] EQ 0
0132 C. ... [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. ... [HK1_PKT_FORM_NO] EQ 7
0136 C. ... [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C. ... [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C. ... [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C. ... [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. Y A Y O Y x 1/2 a I > o d 3 I C S
0142 C. ... [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL o I % E 1 q . e 2 I O K o d 3 I C S
0145 C.
0146 . C. DHU Y a ; 1/2 Y E ; E 1/2 Y % , Y i ; 1/2 Y E ; E o d I a o 1
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. ... [HK1_PKT_FORM_NO] EQ 2
0150 C. ... [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C. ... [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C. ... [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-01-06 09:43:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP []
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-01-06 09:43:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2011-01-06 09:43:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-01-06 09:43:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ' u A I o I ' o 3/4 y o E A D a 1 o e D C B C . x 2 e *****
0192 C. (% a o i y o y a y e y p y e y a y c y e o e % / 4 o o 4 A > U o 1 o e)
0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-812 2011-01-06 11:44:34 116 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSÿÁÿSÿYÁÿ-¼Á»Û;ä
0005  C.
0006  C. ÿÀÿB;¼ÿÿ³ÿFÿÿóÿÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCS : Reload orbital element (send every contact) *****
0010  C.  Áí;Èö¿òÁò•µ°È»Í×ÁÇóíÿçÿYÁÿ×ÿÿí;¼ÿÿÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼ì¹çòí;çÀ®. ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. SOT table upload
0016  C. *****
0017  . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019  BC (51)
0020  C. -----
0021  C. MDP_FG_CTRL_MODE = MANU [ ]
0022  C. -----
0023  C.
0024  . C. <Upload FG Observation Table>
0025  . S. RAM ram-268:MDP_OBS_F
0026  C.
0027  C.
0028  . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030  BC (82 07 00 00 00 38 b8)
0031  C. -----
0032  C. MDP_OBS_F verify = OK/NG [ ]
0033  C. -----
0034  C.
0035  C. *****
0036  C. SOT TI command set
0037  C. *****
0038  C. Execute, after the success of TBL upload.
0039 +. TI 2011-01-06 09:43:18.0
0040  DC 07-F0 MDP_SOT_MODE_OBSV
0041  BC (40)
0042  C. -----
0043  C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044  C. -----
0045  C.
0046  C.
0047  C. ***** XRT START *****
0048  C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050  BC (c1)
0051 +. DC 07-F0 MDP_XRT_MODE_STBY
0052  BC (c3)
0053  . C. ----- Success Verify ? OK / NG____
0054  C.
0055  C. XRT Obs. Table Upload
0056  . S. RAM ram-291:MDP_OBS_X
0057  C.
0058  C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060  BC (84 07 00 00 00 3a d4)
0061  . C. ----- Comparison Check ? OK / ERR _____
0062  C.
0063  C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065  BC (cd 01 b1 b1 04 04)
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067  BC (cd 02 b1 b1 08 08)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069  BC (cd 03 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071  BC (cd 04 b1 b1 06 06)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073  BC (cd 05 85 83 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075  BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077  BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079  BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081  BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083  BC (cd 0a 85 83 08 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085  BC (cd 0f 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087  BC (cd 10 80 80 08 08)
0088  . C. ----- Success Verify ? OK / NG _____
0089  C.
0090  C.
0091  . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0092  C.
0093 +. DC 07-F0 MDP_XRT_MODE_OBSV
0094  BC (c2)
0095 +. TI 2011-01-06 09:43:02.0
```

```
0096 DC 07-F0 MDP_XRT_MODE_OBSV
0097 BC (c2)
0098 . C. ----- Success Verify ? OK / NG ____
0099 C.
0100 C. ***** XRT END *****
0101 C.
0102 . C. ***** MDP `úÃîñî»ò¼ŸñĒĀĐñ¹ñĒDCBC•x²Ě *****
0103 C. (¼á°îŸÓŸĀŸĒŸŸĒŸĀŸçŸĚñĒĒ¼ññ¼Ā»Ūñ¹ñĚ)
0104 . S. DC-BC dcbc-402:DCBC
0105 (MDP_known_event)
0106 C.
0107 C.
0108 . C. ***** ŸĐŸ¹•Ī Daily±ĵĪññĒ`Øñ¹ñĒDCBC•x²Ě *****
0109 . S. DC-BC dcbc-153:DCBC
0110 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0111 C.
0112 C.
0113 . C. ĵãLOSŸĀŸŸŸĀŸ-¼Ā»Ūĵã
0114 C.
0115 . C. ***** LOS *****
0116 C.
```

Jan 06, 11 11:44

XRT_OGLIST_0699.chk

Page 1/6

*** OP Sequence for XRT ***

2011/01/06	09:53:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	09:53:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/01/06	09:54:00.0	AOCS_ORe-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	00 ac 73 00 00	
2011/01/06	09:54:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/01/06	09:54:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/01/06	09:54:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/01/06	09:54:22.0	XRT_ARS_DIS_437_OG [0x1b5]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/01/06	09:56:54.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/06	09:56:56.0	XRT_QT_PROG_SET_414_OG [0x19e]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2011/01/06	09:56:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2011/01/06	09:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/06	14:45:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	14:45:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/06	14:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/01/06	14:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/01/06	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	14:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/01/06	15:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	02 00 00 00 00	
2011/01/06	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/01/06	15:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/01/06	15:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/01/06	15:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/01/06	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/06	15:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/01/06	15:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2011/01/06	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/06	16:20:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	16:20:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/06	16:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/01/06	16:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/01/06	16:44:00.0	XRT_Custom_418_OG [0x1a2]				
2011/01/06	16:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/06	17:57:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	17:57:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/06	17:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/01/06	18:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/01/06	18:20:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	18:20:56.0	XRT_FOCUS_POSITION_401_OG [0x191]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/01/06	18:21:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2011/01/06	18:21:16.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/01/06	18:21:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/01/06	18:21:20.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/01/06	18:23:58.0	XRT_QT_PROG_SET_403_OG [0x193]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2011/01/06	18:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/06	18:30:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/06	18:30:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	

2011/01/06	18:31:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	04 00 00 00 00
2011/01/06	18:31:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2011/01/06	18:31:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/01/06	18:31:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0
2011/01/06	18:31:22.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2011/01/06	18:33:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/06	18:33:56.0	XRT_QT_PROG_SET_415_OG [0x19f] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/01/06	18:33:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/01/06	18:34:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/06	19:34:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/06	19:34:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/06	19:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/06	19:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/06	19:57:30.0	XRT_Custom_418_OG [0x1a2]			
2011/01/06	19:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/06	21:12:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/06	21:12:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/06	21:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/06	21:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/06	21:35:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/06	21:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/06	22:49:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/06	22:49:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/06	22:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/06	22:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/06	23:10:00.5	XRT_Custom_418_OG [0x1a2]			
2011/01/06	23:11:00.5	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/07	00:26:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/07	00:26:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/07	00:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/07	00:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/07	00:34:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/07	00:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/07	01:50:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/07	01:50:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/07	01:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/07	01:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/07	02:09:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/07	02:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/07	03:25:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/07	03:25:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/07	03:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/07	03:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/07	06:06:24.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/07	06:06:26.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/01/07	06:06:30.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2011/01/07	06:06:46.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9
2011/01/07	06:06:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/01/07	06:06:50.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5

Jan 06, 11 11:44

XRT_OGLIST_0699.chk

Page 3/6

2011/01/07	06:09:28.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2011/01/07	06:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	06:16:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	06:16:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/01/07	06:16:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/01/07	06:16:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/07	06:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/07	06:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/07	06:16:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/07	06:19:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	06:19:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/01/07	06:19:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2011/01/07	06:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	06:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	06:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	06:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/07	06:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/07	07:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/07	07:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	08:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	08:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	08:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/07	08:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/07	08:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/07	08:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	09:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	09:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	09:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/07	10:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	53	8d	00	00
2011/01/07	10:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/07	10:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	10:11:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/01/07	10:11:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/07	10:11:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/07	10:11:56.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/07	10:11:58.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/07	10:14:30.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	10:14:32.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2011/01/07	10:14:34.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2011/01/07	10:14:36.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	14:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/01/07	15:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/01/07	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/07	15:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/07	15:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/07	15:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/07	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							

Jan 06, 11 11:44

XRT_OGLIST_0699.chk

Page 4/6

2011/01/07	15:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2011/01/07	15:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2011/01/07	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	15:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	15:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/07	15:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/07	15:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/07	15:44:00.0	XRT_Custom_418_OG [0x1a2]						
2011/01/07	15:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	16:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	16:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/07	16:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/07	17:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/07	17:20:30.0	XRT_Custom_418_OG [0x1a2]						
2011/01/07	17:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2011/01/07	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00 00 00
2011/01/07	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/01/07	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/01/07	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/07	18:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2011/01/07	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2011/01/07	18:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01	00	00 00 00
2011/01/07	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/01/07	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/01/07	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/01/07	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/07	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/07	18:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2011/01/07	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2011/01/07	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	18:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	18:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/07	18:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/07	18:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/07	18:57:30.0	XRT_Custom_418_OG [0x1a2]						
2011/01/07	18:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	20:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	20:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/07	20:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/07	20:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/07	20:34:30.0	XRT_Custom_418_OG [0x1a2]						
2011/01/07	20:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/07	21:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/07	21:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		

2011/01/07	21:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/07	21:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/07	22:11:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/07	22:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/07	23:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/07	23:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/07	23:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/07	23:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/07	23:40:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/07	23:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	00:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	00:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/08	00:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/08	00:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/08	01:09:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/08	01:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	02:25:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	02:25:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/08	02:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/08	02:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/08	02:46:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/08	02:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	03:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	03:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/08	03:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/08	03:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/08	04:23:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/08	04:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	05:34:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	05:34:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/08	05:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/08	05:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/08	06:00:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/08	06:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	06:10:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	06:10:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/01/08	06:10:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/01/08	06:10:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/01/08	06:10:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/01/08	06:10:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/08	06:13:28.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/01/08	06:13:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/08	06:20:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/08	06:20:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/01/08	06:20:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/01/08	06:20:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/08	06:20:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/08	06:20:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/08	06:20:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/08	06:23:24.0	XRT_FLD_RESET_412_OG [0x19c]							

Jan 06, 11 11:44

XRT_OGLIST_0699.chk

Page 6/6

2011/01/08	06:23:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/08	06:23:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2011/01/08	06:23:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2011/01/08	07:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/08	07:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/08	07:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/08	07:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/01/08	07:38:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/01/08	07:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]				
2011/01/08	08:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/01/08	08:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/08	08:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/08	08:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/01/08	09:14:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/01/08	09:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/01/08	10:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]				
2011/01/08	10:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/01/08	10:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/01/08	10:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/01/08	11:08:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_FLD_RESET	1	07-F0	da	
		AOCU_NM	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			AOCU_NM	5	02-76	00 00 00 00 00	