

XRT Timeline to be uploaded on 2011/01/15

Period: 2011/01/15 10:02:00 - 2011/01/20 09:40:00

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Normal mode

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XOB #1844: AR Standard-A(Filter-Ratio) with PFB, thin-Be context, 384x384 at 1064 1048, 120s cad												
Term	Pointing (x, y)	Comment										
01/15 10:15:00 - 01/15 17:59:54	Fixed (-880.0, 400.0)	# OP start + 10min, flaring active region at the limb.										
01/15 18:13:00 - 01/16 06:12:54	Fixed (-880.0, 400.0)	# flaring active region at the limb.										
01/17 09:13:00 - 01/17 17:49:24	Track (-634.5, 399.9) <small>© 01/17 09:10:00</small>	# Observe flaring AR (pointing to be adjusted on Monday)										
01/17 18:02:30 - 01/18 05:59:54	Track (-577.4, 404.6) <small>© 01/17 17:59:30</small>	# Return to AR										
PROG= 11 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 82 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 15 20-time(s) 2.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16												
Term	Pointing (x, y)	Comment										
01/15 18:03:00 - 01/15 18:09:54	Fixed (0.0, 0.0)	synoptic										
01/16 06:16:00 - 01/16 06:22:54	Fixed (0.0, 0.0)	synoptic, shifted 13.0 min										
01/16 18:03:00 - 01/16 18:09:54	Fixed (0.0, 0.0)	synoptic										
01/17 05:49:30 - 01/17 05:56:24	Fixed (0.0, 0.0)	synoptic, shifted -13.5 min										
01/17 17:52:30 - 01/17 17:59:24	Fixed (0.0, 0.0)	synoptic, shifted -10.5 min										
01/18 06:03:00 - 01/18 06:09:54	Fixed (0.0, 0.0)	synoptic										
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 28 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1850: CH-QS-Boundary Ti/poly+Thin-Be-384x384-2min cad												
Term	Pointing (x, y)	Comment										
01/16 06:26:00 - 01/16 17:36:00	Track (343.6, -430.7) <small>© 01/16 06:23:00</small>	# Continue CH observation										
PROG= 07 Inf.-time(s)												
└─ Subr= 1 1-time(s) 30.0sec												
└─ Seqn= 41 1-time(s) 2.0sec												
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	32.0s	Obs	1x1	384x384 (1064, 1024)	DPCM	2	0	2.0sec
└─ Subr= 2 5-time(s) 120.0sec												
└─ Seqn=100 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1064, 1024)	Q=95	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1852: AR (Filter-Ratio) with PFB, thin-Be context, 384x384, 240s cad, offset												
Term	Pointing (x, y)	Comment										
01/16 18:13:00 - 01/17 05:46:24	Fixed (-880.0, 400.0)	# flaring active region at the limb										
01/17 05:59:30 - 01/17 09:09:54	Fixed (-880.0, 400.0)	# Observe AR with fixed pointing										
01/18 06:13:00 - 01/18 10:36:00	Track (-491.9, 410.5) <small>© 01/18 06:10:00</small>	# Return to AR										
PROG= 05 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												

┌	Seqn= 82	4-time(s)	2.0sec																
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec						
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec						
└	Subr= 2	1-time(s)	2.0sec																
┌	Seqn= 13	20-time(s)	2.0sec																
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec						
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	58.0sec						
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec						
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	58.0sec						
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec						
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	58.0sec						
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec						
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	58.0sec						
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)																			
Term		Pointing (x, y)										Comment							
01/15	10:15:00 - 01/15	17:59:54	Fixed (-880.0, 400.0)							# OP start + 10min, flaring active region at the limb.									
01/15	18:13:00 - 01/16	06:12:54	Fixed (-880.0, 400.0)							# flaring active region at the limb.									
01/16	06:26:00 - 01/16	17:36:00	Track (343.6, -430.7)		@ 01/16 06:23:00					# Continue CH observation									
01/16	18:13:00 - 01/17	05:46:24	Fixed (-880.0, 400.0)							# flaring active region at the limb									
01/17	05:59:30 - 01/17	09:09:54	Fixed (-880.0, 400.0)							# Observe AR with fixed pointing									
01/17	09:13:00 - 01/17	17:49:24	Track (-634.5, 399.9)		@ 01/17 09:10:00					# Observe flaring AR (pointing to be adjusted on Monday)									
01/17	18:02:30 - 01/18	05:59:54	Track (-577.4, 404.6)		@ 01/17 17:59:30					# Return to AR									
01/18	06:13:00 - 01/18	10:36:00	Track (-491.9, 410.5)		@ 01/18 06:10:00					# Return to AR									
PROG= 16 1-time(s)																			
Subr= 1		30-time(s)		20.0sec															
┌		Seqn= 87		1-time(s)		2.0sec													
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
└		Seqn= 60		1-time(s)		2.0sec													
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
┌		Subr= 2		1-time(s)		2.0sec													
└		Seqn= 90		1-time(s)		2.0sec													
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec					
┌		Subr= 3		30-time(s)		60.0sec													
└		Seqn= 87		1-time(s)		2.0sec													
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
└		Seqn= 88		1-time(s)		2.0sec													
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
┌		Subr= 2		1-time(s)		2.0sec													
└		Seqn= 90		1-time(s)		2.0sec													
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec					
┌		Subr= 3		30-time(s)		60.0sec													
└		Seqn= 87		1-time(s)		2.0sec													
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
└		Seqn= 88		1-time(s)		2.0sec													
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
┌		Subr= 2		1-time(s)		2.0sec													
└		Seqn= 90		1-time(s)		2.0sec													
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec					
┌		Subr= 3		30-time(s)		60.0sec													
└		Seqn= 87		1-time(s)		2.0sec													
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
└		Seqn= 88		1-time(s)		2.0sec													
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
┌		Subr= 4		24-time(s)		600.0sec													
└		Seqn= 89		1-time(s)		2.0sec													
		Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec					
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec					
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
		Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

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Active Region Search

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NOT USED

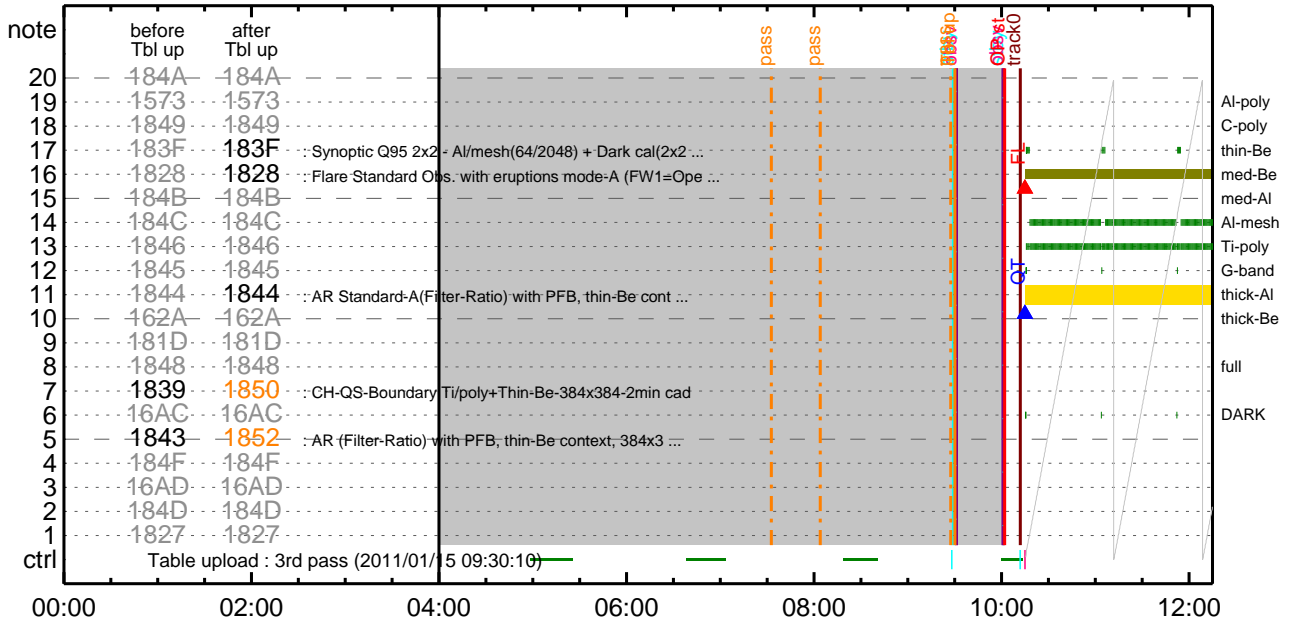
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Flare Detection

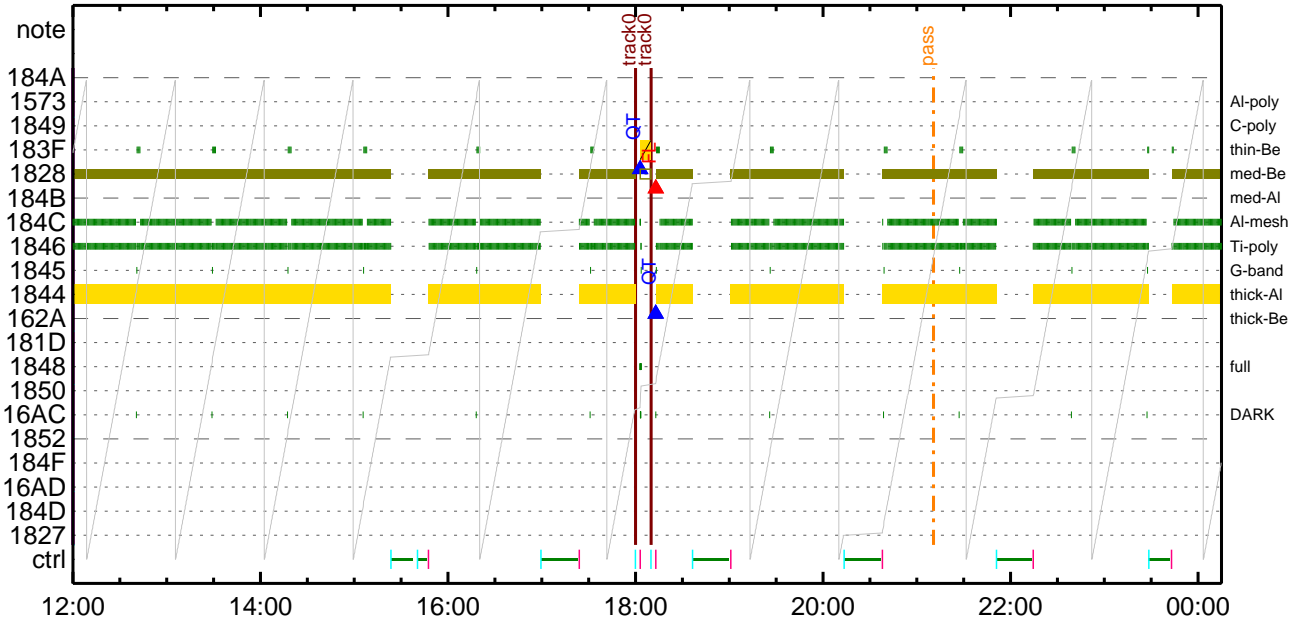
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FLD Patrol											
Term		Pointing (x, y)					Comment				
01/15 10:12:16 - 01/15 18:00:16		Fixed (-880.0, 400.0)					# OP start + 10min, flaring active region at the limb.				
01/15 18:10:16 - 01/16 06:13:16		Fixed (-880.0, 400.0)					# flaring active region at the limb.				
01/16 06:23:16 - 01/16 18:00:16		Track (343.6, -430.7) @ 01/16 06:23:00					# Continue CH observation				
01/16 18:10:16 - 01/17 05:46:46		Fixed (-880.0, 400.0)					# flaring active region at the limb				
01/17 05:56:46 - 01/17 17:49:46		Fixed (-880.0, 400.0)					# Observe AR with fixed pointing				
01/17 17:59:46 - 01/18 06:00:16		Track (-577.4, 404.6) @ 01/17 17:59:30					# Return to AR				
01/18 06:10:16 - 01/20 09:40:00		Track (-491.9, 410.5) @ 01/18 06:10:00					# Return to AR				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

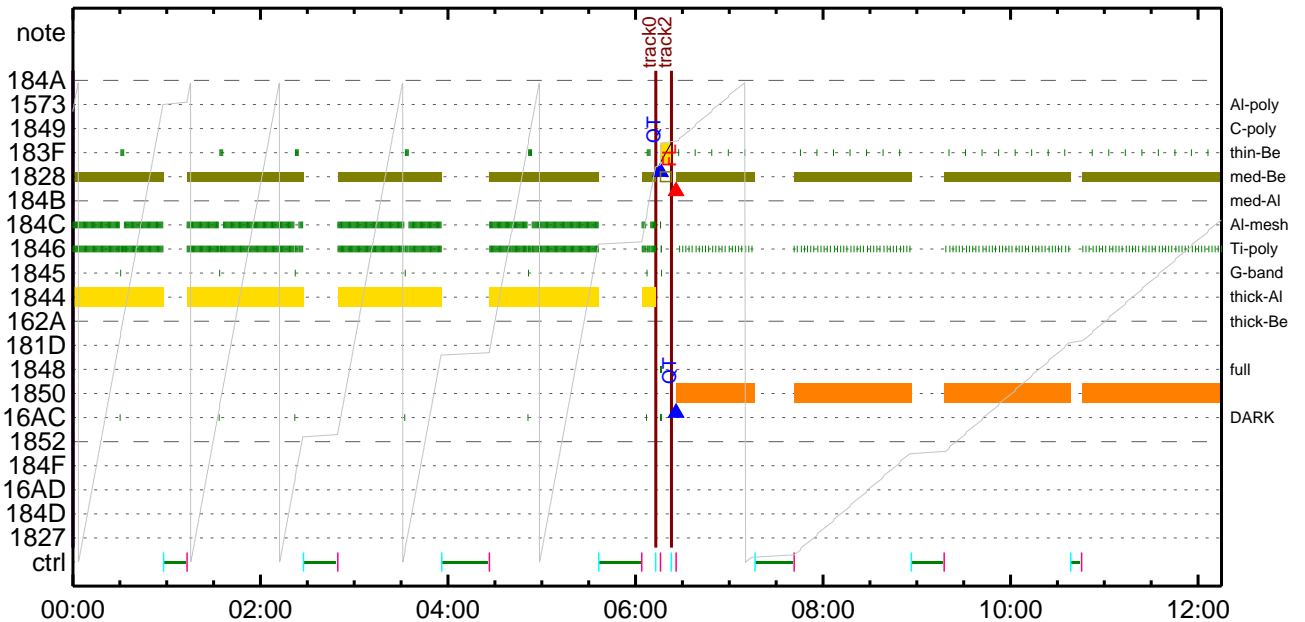
CMDI #0718 2011/01/15



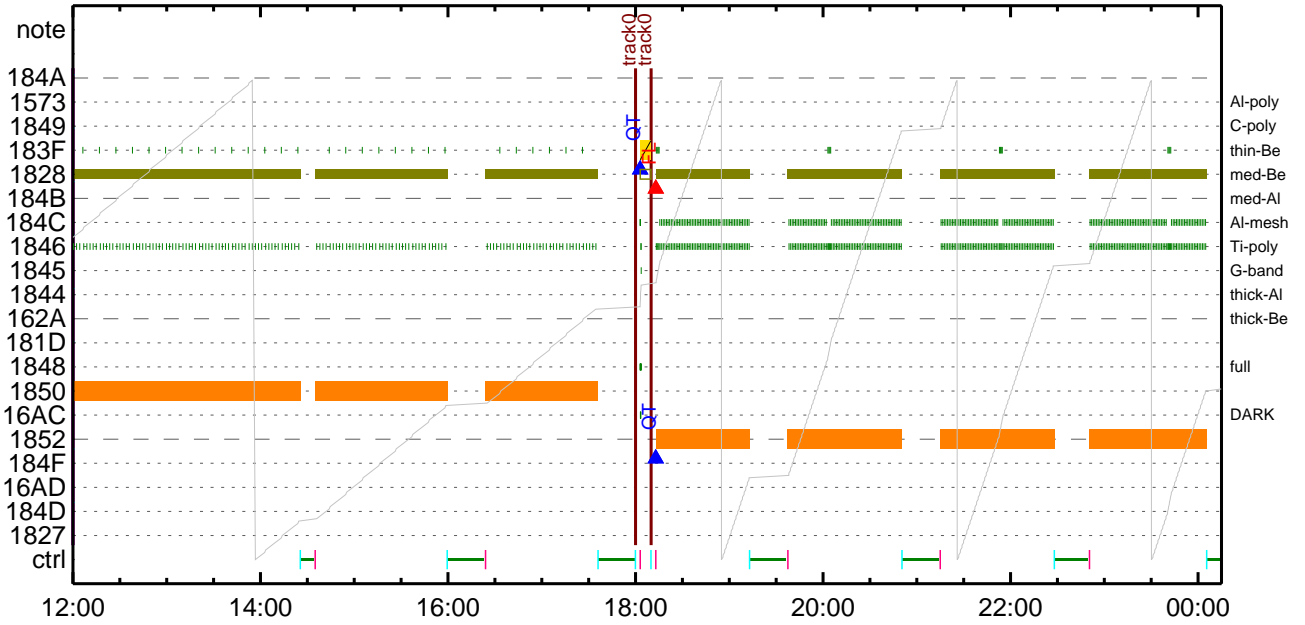
CMDI #0718 2011/01/15



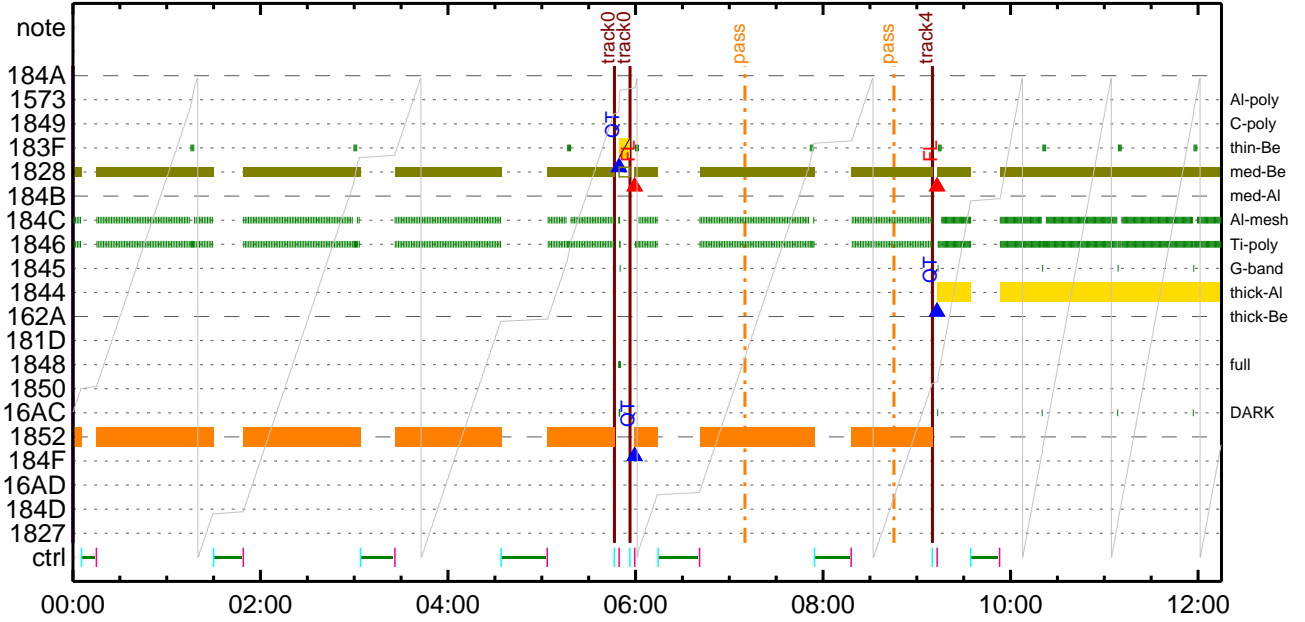
CMDI #0718 2011/01/16



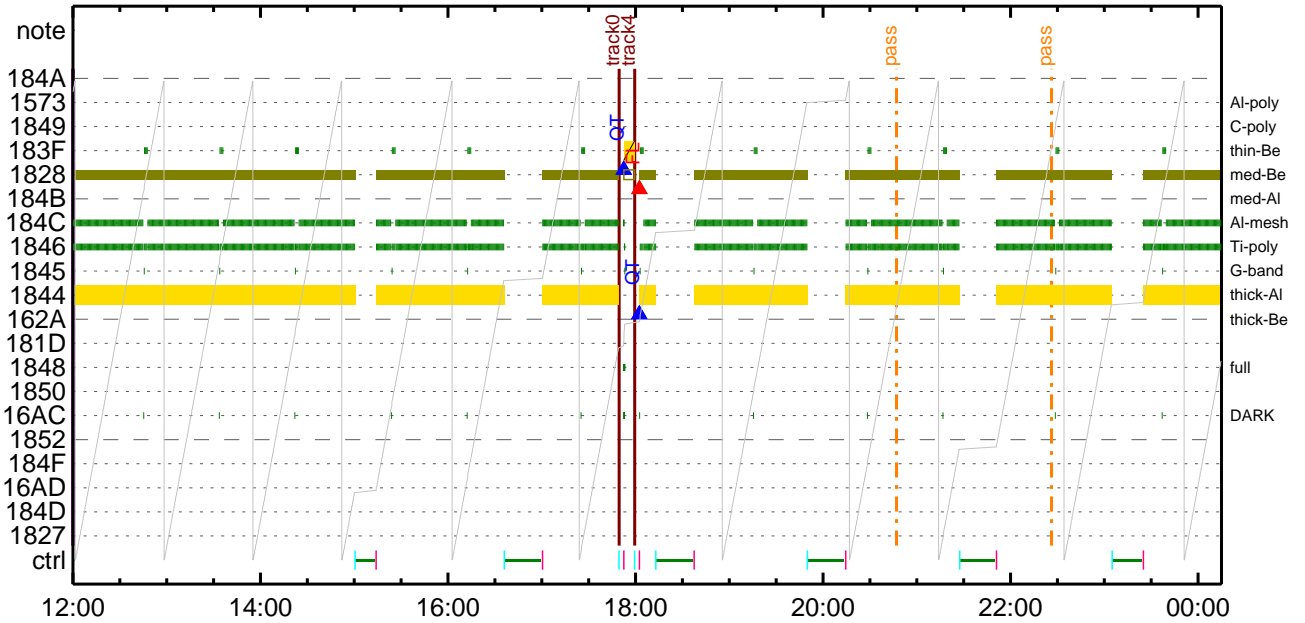
CMDI #0718 2011/01/16



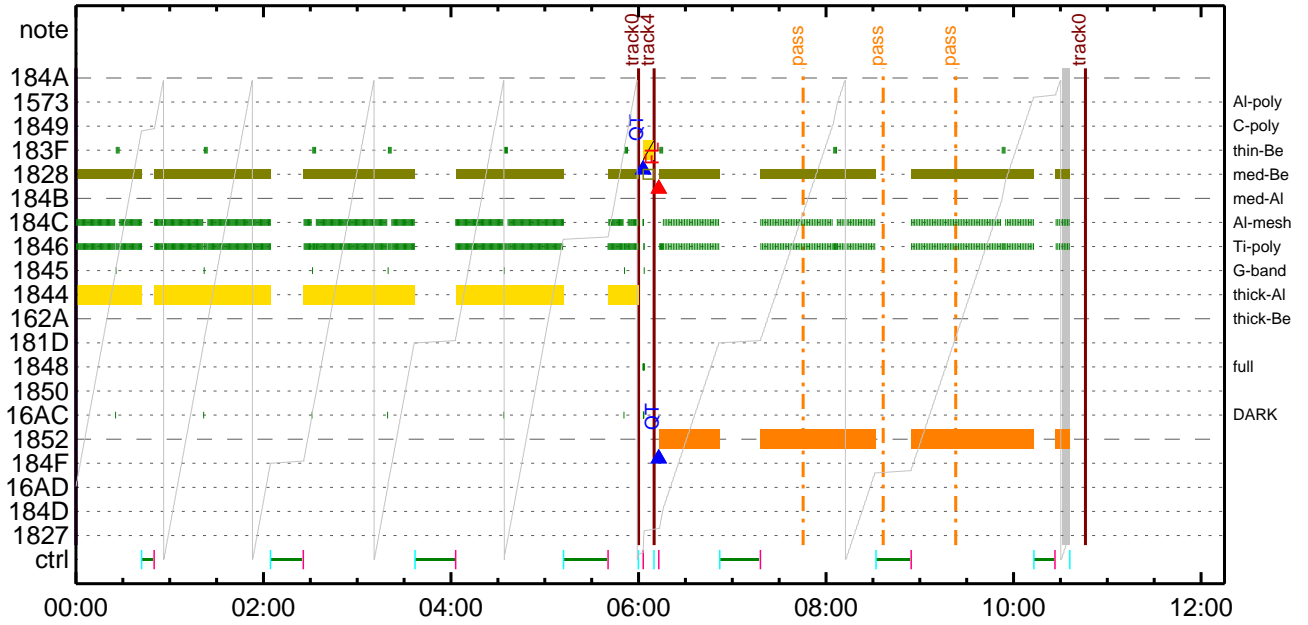
CMDI #0718 2011/01/17



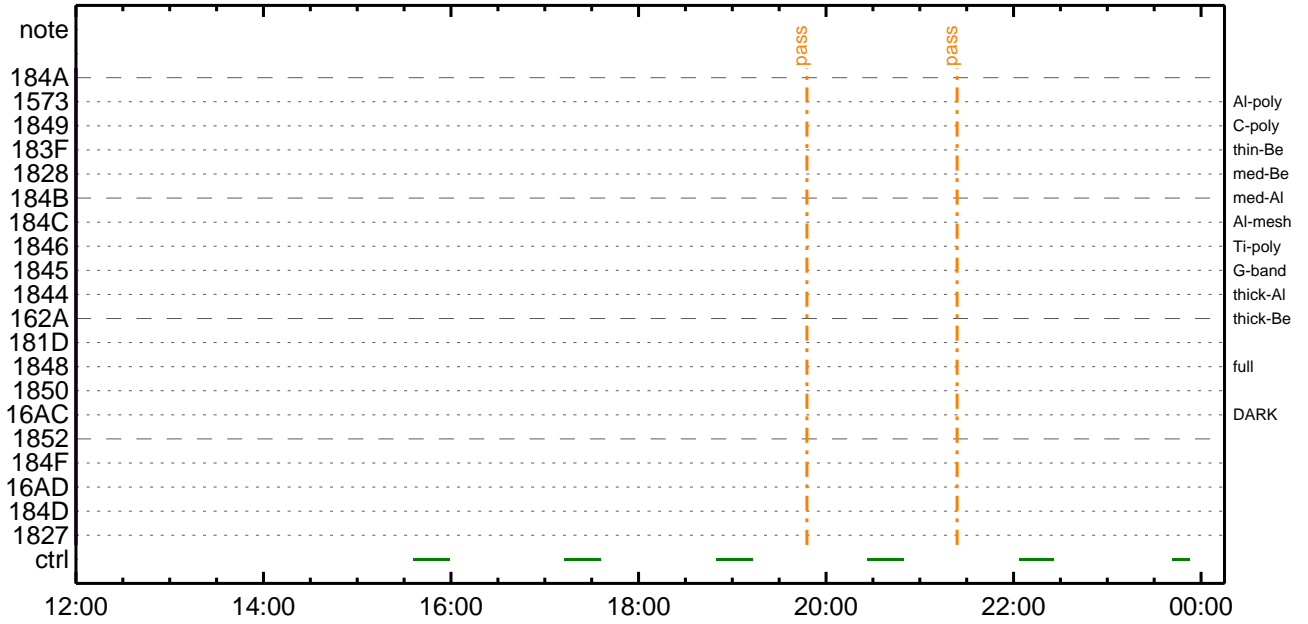
CMDI #0718 2011/01/17



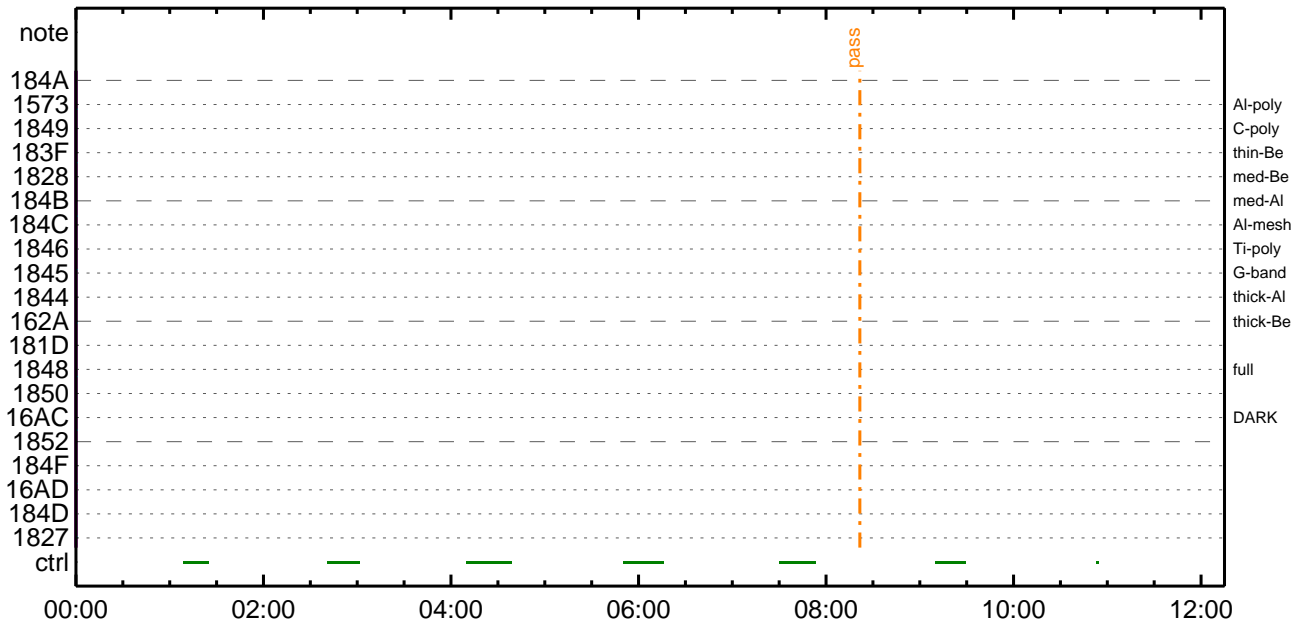
CMDI #0718 2011/01/18



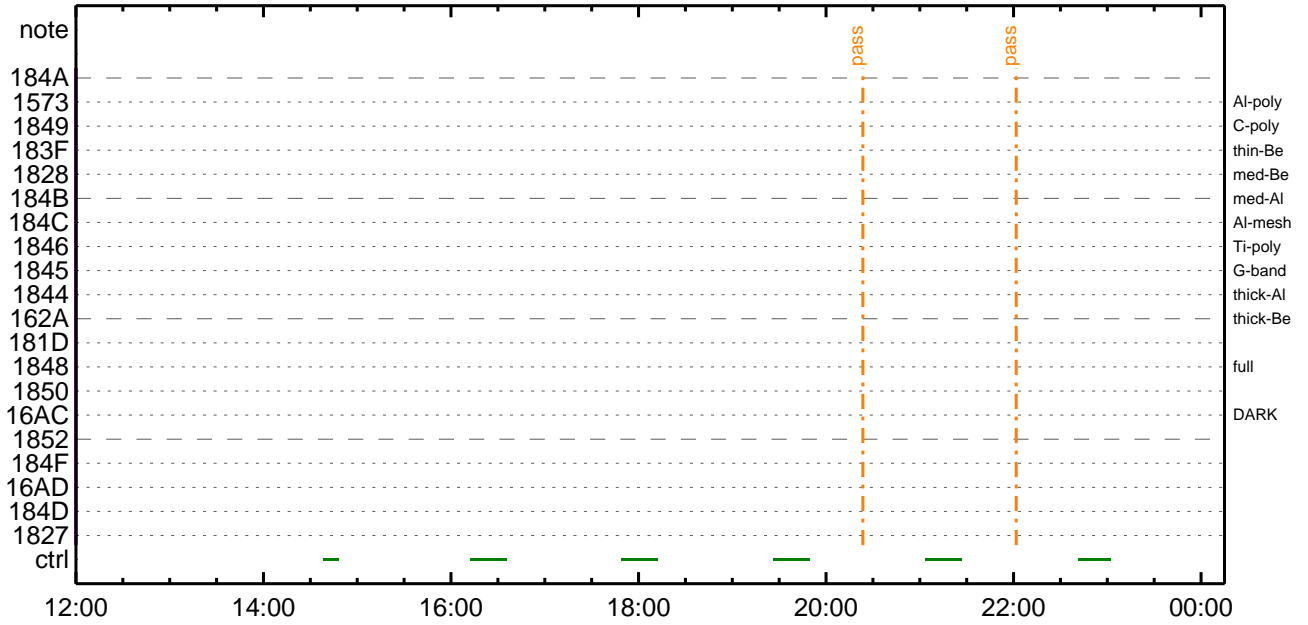
CMDI #0718 2011/01/18



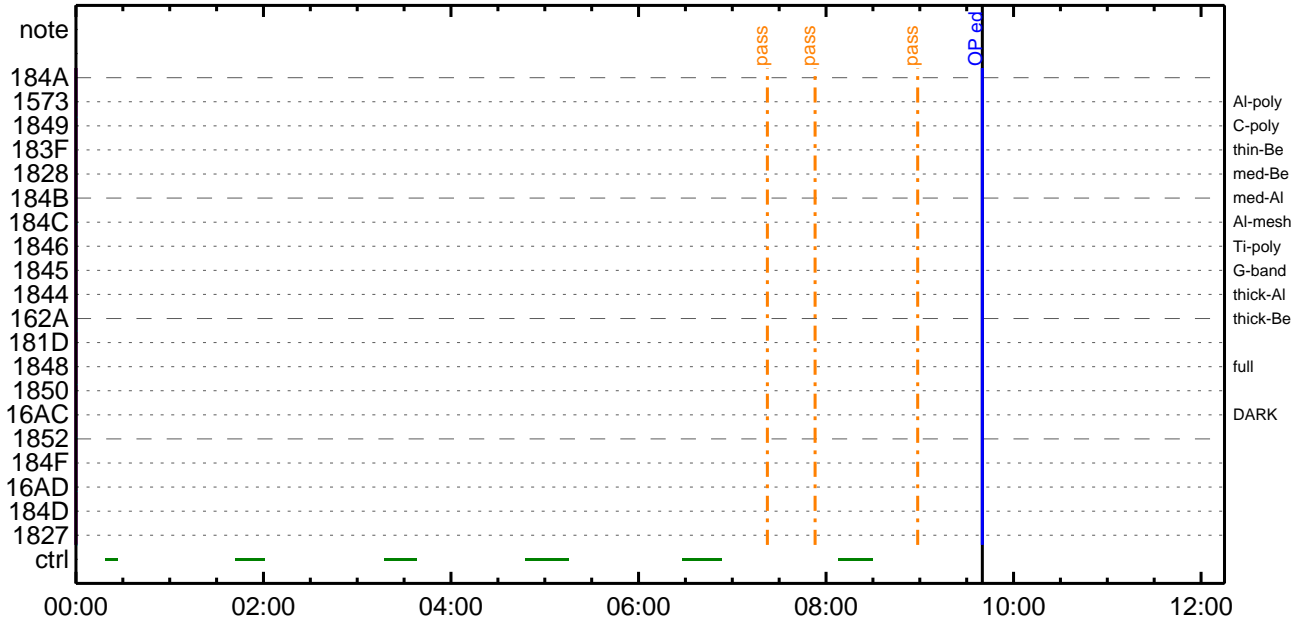
CMDI #0718 2011/01/19



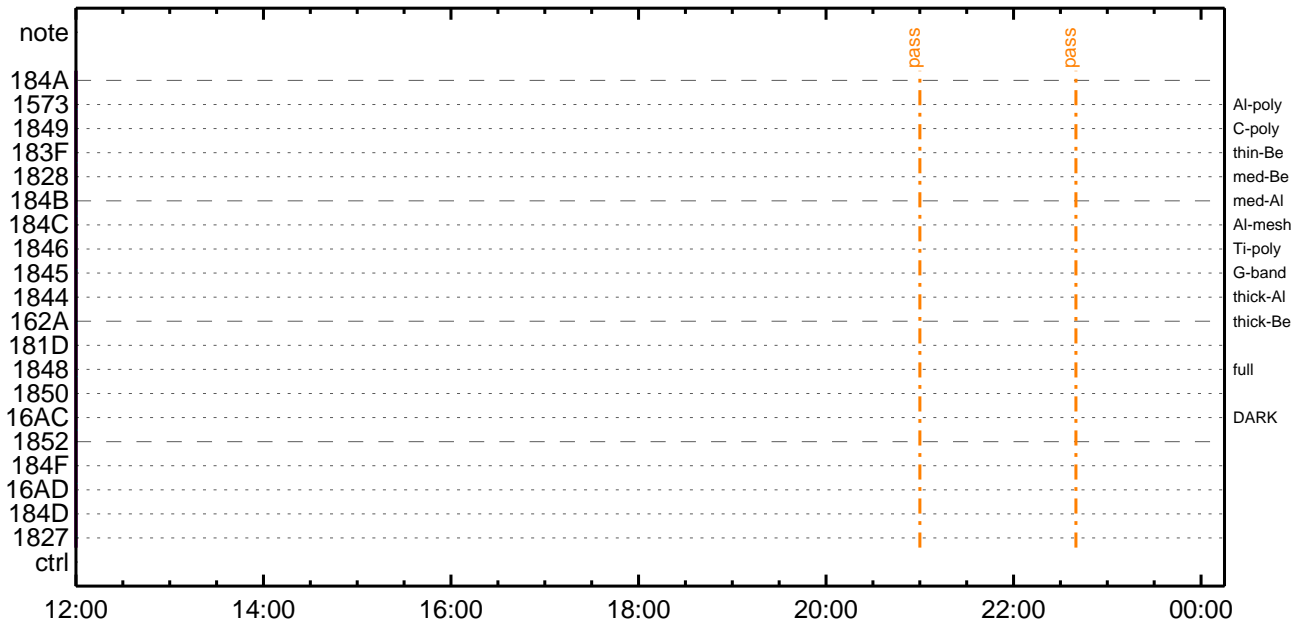
CMDI #0718 2011/01/19



CMDI #0718 2011/01/20



CMDI #0718 2011/01/20



(a) Spacecraft Operation Procedure (real-commands)

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main-830 2011-01-15 14:59:27 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÿóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëð¿ðÁð•µ°Æ»Í×ÁÇðÍYÇYÁY×YÍ;¼YÉ;ËÈÈ¼µ•ííÉ;ËðÈ¼°ÇÖð•µ¿¼í¹ÇðÍ; ÇÀ®, ùñ¹ðÈððÇÁ+¿®ð•ðÈððð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. Ç" °ÆÀ, í×ÈYðáLOSpðÇðÍ»p´Öðð¹íí, µ•; ÇÉÖÍ×ðÈXÁÖONðÍ¹ÖðÈð¹ðÈððð³ðÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÿóYÉYÍYÁY-¾ÖÁÖð-°ÁÀðð•µ¿ðé; Ç°È²¼ðÍ°ÆÀ, ¼È¼Çðð¼Á¹Öð¹ðé; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. Ç" RESTART;ÈPT1;Ëð•µ¿ð¼í¹ÇðÍ; Ç°È²¼ðÍ¼Á¹Öð»ð°; ÇDCBC-150ðØ¿Èðà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYóYÉYÈÀÙÁÖ;ÈÁ•Á°²óÈð;Ë, áðí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ð-¼«Æ°Áá»ßð•µ¿, á; Ç°È²¼ð¼Á¹Öð¹ðé; f
0055 C. YÇYóYÉYÈÀÙÁÖðáÁ•Á°²óÈðð-¶áð¼í¹ÇðÍ´°í»ð¹ðÈððÇÁÖðÁ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. Ç" RESTART;ÈPT2;Ëð•µ¿ð¼í¹ÇðÍ; Ç°È²¼ðÍ¼Á¹Öð»ð°; ÇDCBC-151ðØ¿Èðà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYóYÉYÈÀÙÁÖ;ÈÁ•Á°²óÈð;Ë, áðí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß; ÇXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;4YE;ä
0100 . C. *****
0101 . C.
0102 . C. ;äOP/OGY1;4YE;ä
0103 . S. OP op-830:OP
0104 . C. (
0105 . S. OG og-830:OG
0106 . C. (
0107 . C.
0108 . C. ;äNMOG&OPîî°èYÄYóYx;ä
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 . C. çç[HK1_PKT_FORM_NO] EQ 7
0120 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YÄYóYx½ªî»ò³îÇ§
0125 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGαî¼È¹ç•è²îOKαò³îÇ§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 . C. çç[HK1_PKT_FORM_NO] EQ 7
0139 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YÄYóYx½ªî»ò³îÇ§
0144 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGαî¼È¹ç•è²îOKαò³îÇ§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 . C. çç[HK1_PKT_FORM_NO] EQ 7
0158 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YÄYóYx½ªî»ò³îÇ§
0163 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPαî¼È¹ç•è²îOKαò³îÇ§
0165 . C.
0166 . C. ***** °È²¼αî¼Ä´¶Ä°ÈÈ¬α°Ä÷¿@ (¼åµ-YÄYóYx½ê½çαðÄÖæαÇ¼ª°¬ðè¼i¹ççαâ) *****
0167 . C. DHUÿâ;¼YE;È¼Y½;Yi;¼YE;Èòîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 . C. çç[HK1_PKT_FORM_NO] EQ 2
0171 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;§ OPOG UPLOADα¬Ä÷¿@NGµî¼i¹ç;ç°È²¼αîTI-CMDÄ÷¿@αî¼Ä¹Ôα•αÈααα³αÈ;f
0180 . C. αβα¿;çSETαÈDUMPαîÆ±°iYNY¹αÇ¹Ôα|α³αÈ;f
0181 . C.
0182 . C. TIY³YpYóYÈαðÄî¿¿(UT)
0183 +. TI 2011-01-15 09:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 . C.
0187 +. TI 2011-01-15 09:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 . C.
0191 +. TI 2011-01-15 09:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-01-15 10:01:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ºíÄë%ííñºîŷÄŷ$ŷÄŷ¹àîÛ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]       EQ      3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»º³îÇ$
0226 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîî³Ê¹çªë²îOKº³îÇ$
0229 C.
0230 C. DHUŷª;¼ŷÊ;Ê¼ŷ¼.ŷî;¼ŷÊ;Êº³îª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-01-15 10:01:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-01-15 10:01:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]       EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2011-01-15 10:01:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]       EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÃîî»ò¼ŷºÊÄº¹ºèDCBC•x²è *****
0265 C. (¼ªºîŷÖŷÄŷËŷŷŷËŷªŷçŷëº¼ºª¼ª»Ûº¹ºè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.î Daily±;îñºîË¹ºèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ðãLOSŷÄŷ$ŷÄŷ¹¼ª»Û;ª
0276 C.
0277 C. ***** LOS *****
0278 C.
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*** OP Sequence for XRT ***

2011/01/15	10:11:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	10:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/01/15	10:12:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 dc 73 4e 35		
2011/01/15	10:12:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/01/15	10:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/01/15	10:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/01/15	10:12:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/15	10:14:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/15	10:14:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2011/01/15	10:14:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2011/01/15	10:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	15:23:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	15:23:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/15	15:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/15	15:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/15	15:40:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	15:40:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/15	15:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/15	15:40:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/15	15:43:44.0	XRT_CUSTOM_418_OG [0x1a2]					
		MDP_XRT_CUSTOM_418	1	07-F0	c0		
2011/01/15	15:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	15:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	16:59:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	16:59:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/15	16:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/15	16:59:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/15	17:02:44.0	XRT_CUSTOM_418_OG [0x1a2]					
		MDP_XRT_CUSTOM_418	1	07-F0	c0		
2011/01/15	17:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	17:24:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/01/15	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/01/15	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/01/15	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/01/15	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/15	18:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2011/01/15	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/15	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/01/15	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 dc 73 4e 35		
2011/01/15	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/01/15	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/01/15	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/01/15	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/15	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/15	18:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2011/01/15	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2011/01/15	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/15	18:36:30.0	XRT_CTRL_MANU_408_OG [0x198]					

2011/01/15	18:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	18:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/15	18:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/15	19:00:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/15	19:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/15	20:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	20:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/15	20:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/15	20:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/15	20:37:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/15	20:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	21:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/15	21:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	21:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/15	21:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/15	22:13:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/15	22:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/15	23:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	23:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/15	23:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/15	23:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/15	23:42:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/15	23:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	00:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	00:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	00:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/16	01:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/16	01:12:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/16	01:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	02:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	02:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	02:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/16	02:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/16	02:48:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/16	02:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	03:56:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	03:56:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	03:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/16	03:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/16	04:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/16	04:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	05:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	05:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	05:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/16	05:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/16	06:03:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/16	06:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/16	06:12:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	06:12:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/16	06:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
		AOCU_NM		5	02-76	00 00 00 00 00

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2011/01/16	06:13:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/01/16	06:13:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/01/16	06:13:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/16	06:15:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/01/16	06:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	06:22:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	06:22:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/01/16	06:23:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/01/16	06:23:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/16	06:23:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/16	06:23:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/16	06:23:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/16	06:25:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	06:25:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/01/16	06:25:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/01/16	06:26:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	07:16:30.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	07:16:32.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	07:16:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/16	07:19:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/16	07:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/16	07:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	08:56:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	08:56:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	08:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/16	08:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/16	09:16:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/16	09:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	10:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	10:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	10:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/16	10:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/16	10:44:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/16	10:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	14:25:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	14:25:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	14:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/16	14:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/16	14:34:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/16	14:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	15:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	15:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	15:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/16	16:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/16	16:23:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/16	16:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/16	17:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/16	17:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/16	17:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2011/01/16	17:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/16	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/16	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/01/16	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/01/16	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/01/16	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/01/16	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/16	18:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2011/01/16	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/16	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/16	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/01/16	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 dc 73 4e 35		
2011/01/16	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/01/16	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/01/16	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/01/16	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/01/16	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/16	18:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2011/01/16	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2011/01/16	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/16	19:13:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/16	19:13:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/16	19:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/16	19:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/16	19:36:30.0	XRT_Custom_418_OG [0x1a2]					
2011/01/16	19:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/16	20:50:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/16	20:50:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/16	20:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/16	20:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/16	21:14:00.0	XRT_Custom_418_OG [0x1a2]					
2011/01/16	21:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/16	22:28:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/16	22:28:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/16	22:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/16	22:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/16	22:49:30.0	XRT_Custom_418_OG [0x1a2]					
2011/01/16	22:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/17	00:05:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/17	00:05:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/17	00:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/17	00:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/17	00:14:00.0	XRT_Custom_418_OG [0x1a2]					
2011/01/17	00:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/01/17	01:30:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/01/17	01:30:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/01/17	01:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/01/17	01:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/01/17	01:48:00.0	XRT_Custom_418_OG [0x1a2]					

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2011/01/17	01:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	03:04:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	03:04:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/17	03:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/17	03:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/17	03:25:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/17	03:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	04:34:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	04:34:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/17	04:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/17	04:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/17	05:02:30.0	XRT_Custom_418_OG [0x1a2]			
2011/01/17	05:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	05:46:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	05:46:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/01/17	05:46:30.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/01/17	05:46:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/01/17	05:46:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/01/17	05:46:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/01/17	05:49:28.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/01/17	05:49:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	05:56:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	05:56:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/01/17	05:56:30.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 dc 73 4e 35
2011/01/17	05:56:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/01/17	05:56:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/01/17	05:56:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/01/17	05:56:52.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/01/17	05:59:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/17	05:59:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2011/01/17	05:59:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/01/17	05:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	06:14:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	06:14:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/17	06:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/17	06:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/17	06:40:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/17	06:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	07:54:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	07:54:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/17	07:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/17	07:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/17	08:17:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/17	08:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/17	09:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/17	09:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/01/17	09:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/01/17	09:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/01/17	09:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			

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2011/01/17	09:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/17	09:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/17	09:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	09:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/01/17	09:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2011/01/17	09:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	09:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	09:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	09:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	09:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	09:52:00.0	XRT_Custom_418_OG [0x1a2]								
2011/01/17	09:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	15:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	15:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	15:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	15:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	15:13:00.0	XRT_Custom_418_OG [0x1a2]								
2011/01/17	15:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	16:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	16:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	16:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	16:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	16:59:30.0	XRT_Custom_418_OG [0x1a2]								
2011/01/17	17:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	17:49:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	17:49:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/01/17	17:49:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2011/01/17	17:49:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/01/17	17:49:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/01/17	17:49:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/17	17:52:28.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2011/01/17	17:52:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	17:59:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	17:59:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/01/17	17:59:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	00	00	00	00
2011/01/17	17:59:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/01/17	17:59:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/01/17	17:59:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/01/17	17:59:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/17	18:02:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	18:02:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/01/17	18:02:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2011/01/17	18:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	18:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	18:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	18:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	18:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	18:36:30.0	XRT_Custom_418_OG [0x1a2]								

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2011/01/17	18:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	19:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	19:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	19:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	19:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	20:13:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/17	20:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	21:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	21:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	21:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	21:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	21:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/17	21:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/17	23:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/17	23:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/17	23:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/17	23:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/17	23:24:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/17	23:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/18	00:42:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/18	00:42:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/18	00:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/18	00:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/18	00:49:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/18	00:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/18	02:04:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/18	02:04:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/18	02:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/18	02:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/18	02:24:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/18	02:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/18	03:37:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/18	03:37:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/18	03:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/18	03:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/18	04:02:00.0	XRT_Custom_418_OG [0x1a2]							
2011/01/18	04:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/18	05:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/18	05:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/01/18	05:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/01/18	05:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/01/18	05:39:30.0	XRT_Custom_418_OG [0x1a2]							
2011/01/18	05:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/01/18	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/01/18	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/01/18	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/01/18	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/01/18	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/01/18	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/01/18	06:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				

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2011/01/18	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/18	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/18	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/01/18	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/01/18	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/01/18	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/01/18	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/01/18	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/01/18	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/18	06:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2011/01/18	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/01/18	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/18	06:52:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/18	06:52:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/18	06:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/18	06:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/18	07:17:00.0	XRT_Custom_418_OG [0x1a2]			
2011/01/18	07:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/18	08:32:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/18	08:32:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/18	08:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/18	08:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/18	08:53:30.0	XRT_Custom_418_OG [0x1a2]			
2011/01/18	08:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/18	10:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/18	10:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/01/18	10:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/01/18	10:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/01/18	10:25:30.0	XRT_Custom_418_OG [0x1a2]			
2011/01/18	10:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/01/18	10:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/01/18	10:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00