XRT Timeline to be uploaded on 2011/02/01

Period: 2011/02/01 11:05:00 - 2011/02/04 10:12:00

Normal mode

XOB #184D: Full Sun 5 Filter- 1x1 Q98 -FW1/FW2

Term	Pointing (x, y)	Comment
02/01 11:50:00 - 02/01 14:30:00	Fixed (0.0, 0.0)	# 3/15

PROG= 02 1-time(s)

Subr= 1 1-time(s) 180.0sec

Seqn= 75 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 81 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 84 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 79 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 96 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 360.0sec

Seqn= 83 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 4 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16

Term	Pointing (x, y)	Comment
02/01 18:07:30 - 02/01 19:18:00	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min
02/02 05:54:30 - 02/02 06:01:24	Fixed (0.0, 0.0)	synoptic, shifted -8.5 min

PROG= 17 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 69 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 28 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #180D: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 2.5min-cad

Term	Pointing (x, y)	Comment
02/01 23:48:02 - 02/02 05:51:24	Track (-200.5, -251.6) @ 02/01 23:45:00	# Track AR 11150.
02/02 06:04:32 - 02/02 09:41:54	Track (-147.2, -250.3) @ 02/02 06:01:30	# Track AR 11150.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 6 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 86 16-time(s) 150.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Flare mode

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
02/01 23:48:02 - 02/02 05:51:24	Track (-200.5, -251.6) @ 02/01 23:45:00	# Track AR 11150.
02/02 06:04:32 - 02/02 09:41:54	Track (-147.2, -250.3) @ 02/02 06:01:30	# Track AR 11150.

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 16 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Active Region Search

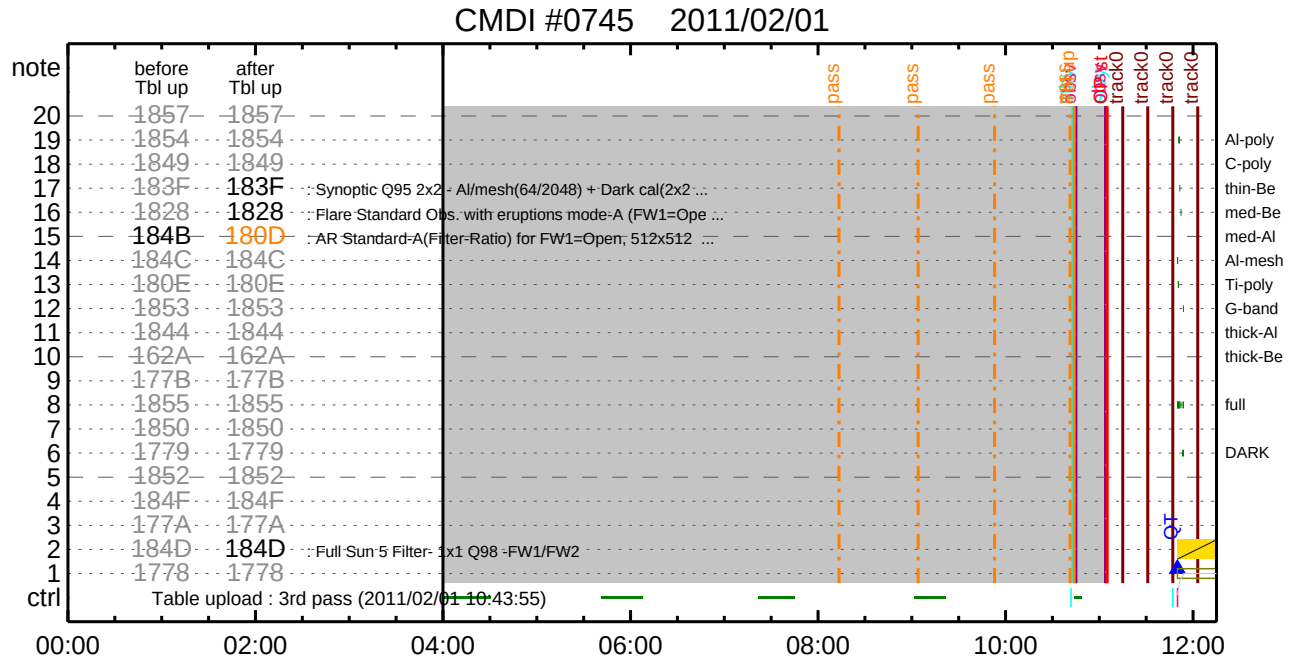
NOT USED

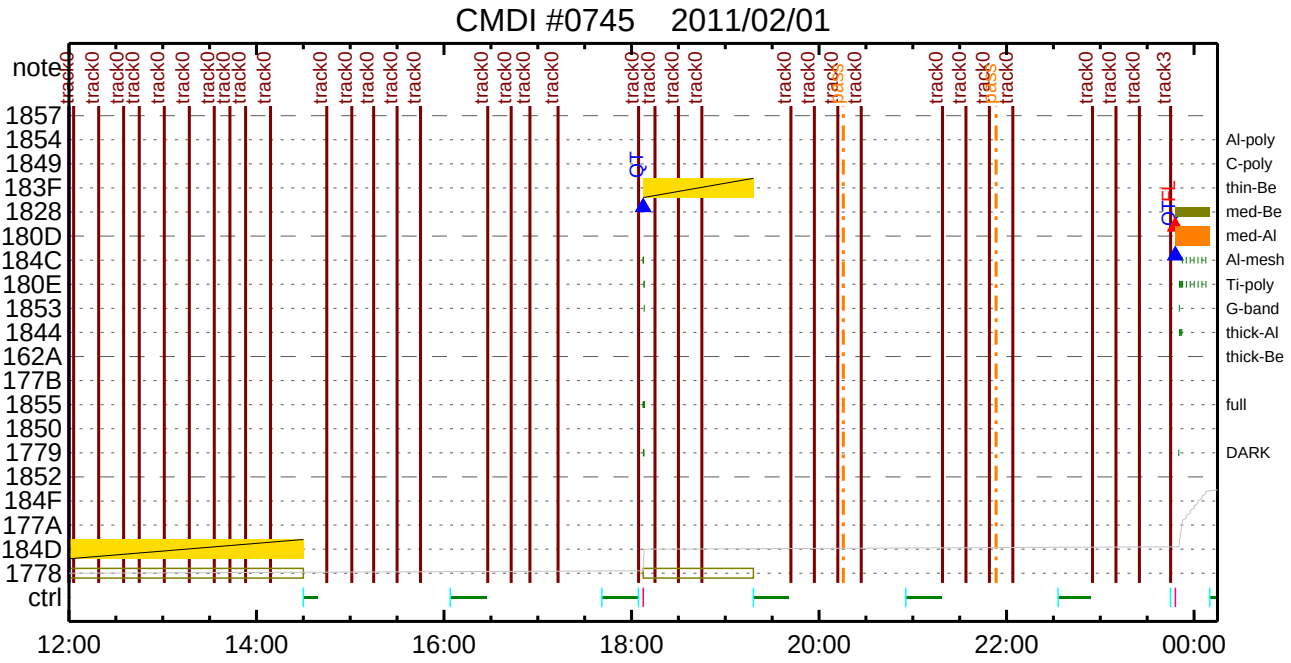
Flare Detection

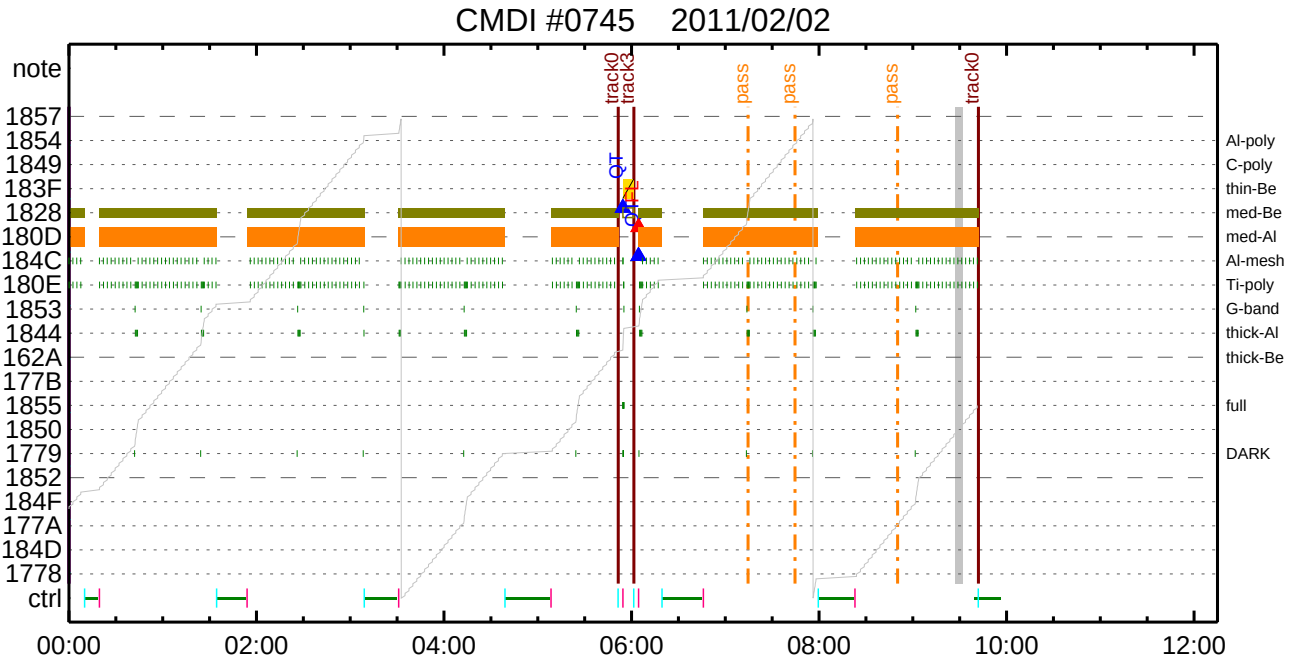
FLD Patrol

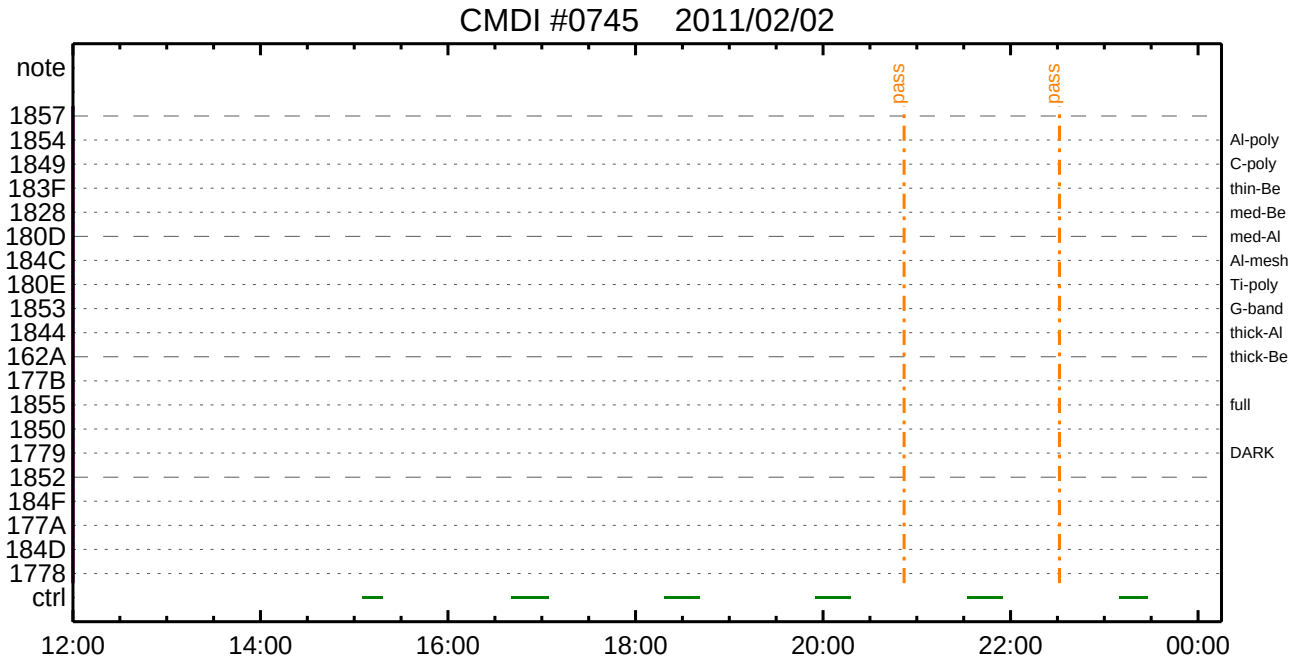
Term	Pointing (x, y)	Comment
02/01 23:45:16 - 02/02 05:51:46	Track (-200.5, -251.6) $^{@\ 02/01\ 23:45:00}$	# Track AR 11150.
02/02 06:01:46 - 02/04 10:12:00	Track (-147.2, -250.3) $^{@\ 02/02\ 06:01:30}$	# Track AR 11150.

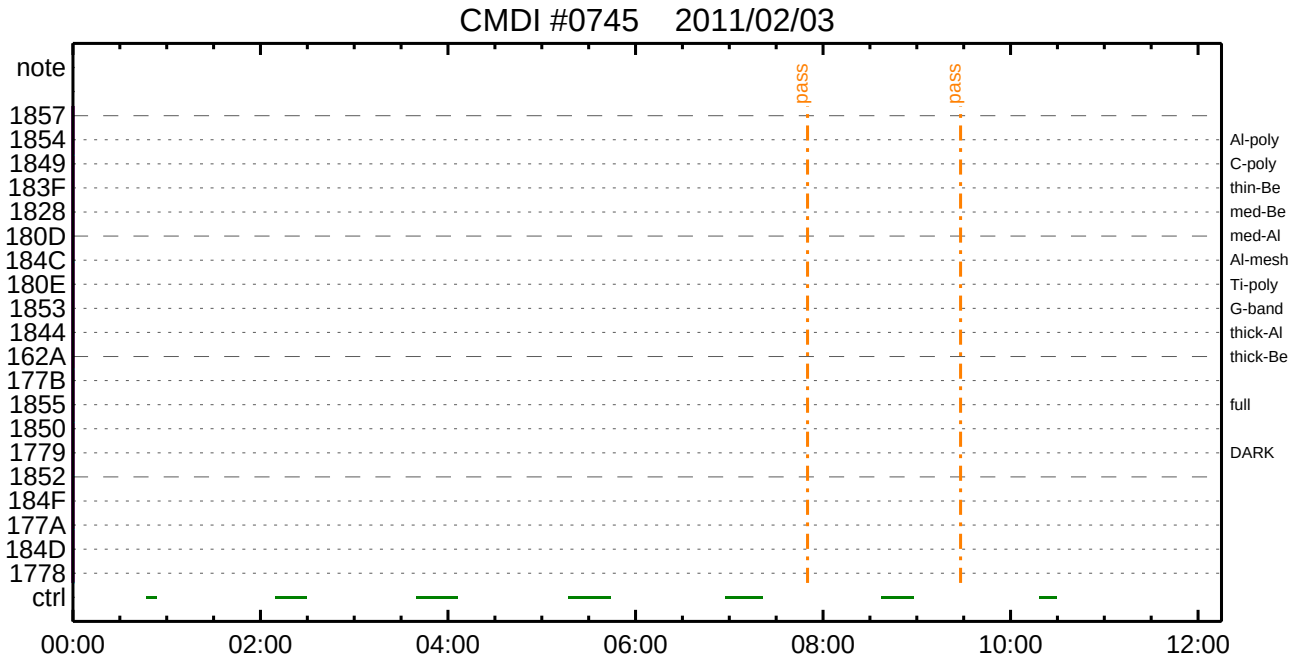
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec

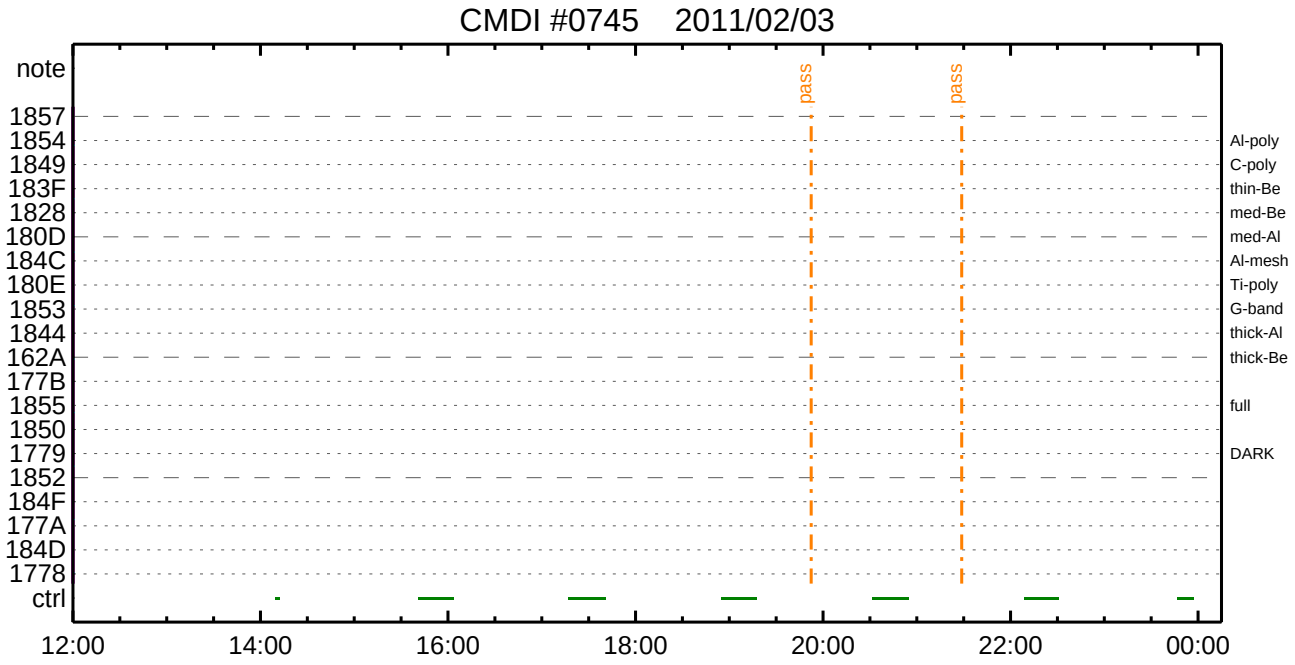

CMDI #0745 2011/02/01
note
before Tbl up
after Tbl up
: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 ...
: Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 ...
: Full Sun 5 Filter- 1x1 Q98 -FW1/FW2
Table upload : 3rd pass (2011/02/01 10:43:55)
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

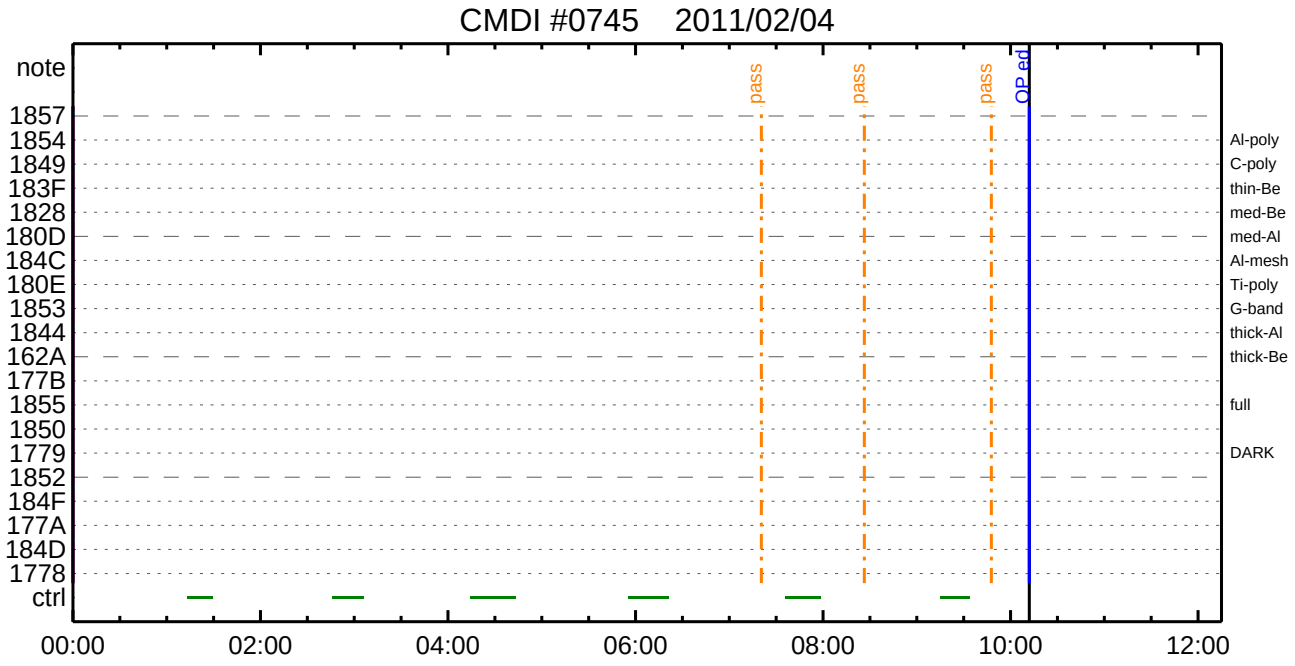

CMDI #0745 2011/02/01
note
track0
track3
pass
QT
QTFL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

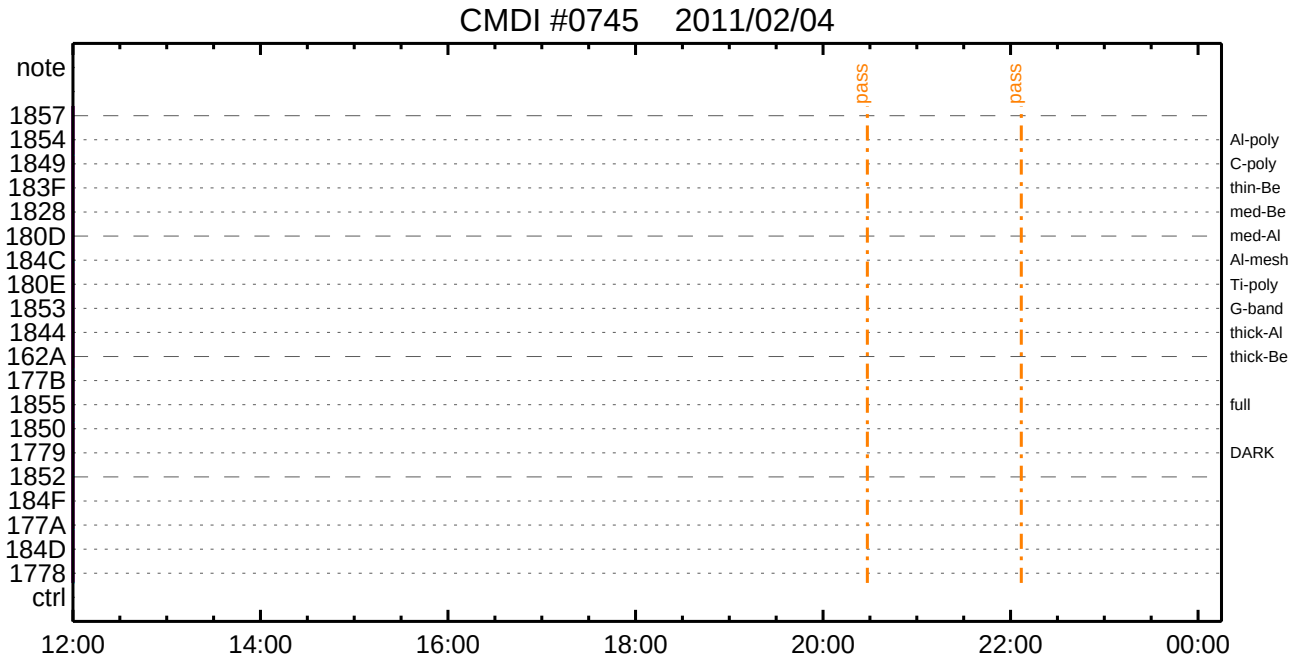

CMDI #0745 2011/02/02
note
track0
track3
pass
QT
QTFL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0745 2011/02/02
note
1857
1854
1849
183F
1828
180D
184C
180E
1853
1844
162A
177B
1855
1850
1779
1852
184F
177A
184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0745 2011/02/03
note
1857
1854
1849
183F
1828
180D
184C
180E
1853
1844
162A
177B
1855
1850
1779
1852
184F
177A
184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0745 2011/02/03
note
1857
1854
1849
183F
1828
180D
184C
180E
1853
1844
162A
177B
1855
1850
1779
1852
184F
177A
184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0745 2011/02/04
note
1857
1854
1849
183F
1828
180D
184C
180E
1853
1844
162A
177B
1855
1850
1779
1852
184F
177A
184D
1778
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0745 2011/02/04
note
1857
1854
1849
183F
1828
180D
184C
180E
1853
1844
162A
177B
1855
1850
1779
1852
184F
177A
184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-864 2011-02-01 13:13:32 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP    op-864:OP
     ()
   . S. OG    og-864:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 00 7f 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 80 7f 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (21 00 41 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC       (02 0a f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2011-02-01 11:00:00.0
     DC 01-B3 DHU_OP_STOP
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2011-02-01 11:00:01.0
     DC 01-B4 DHU_OP_COPY
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2011-02-01 11:00:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2011-02-01 11:04:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C.
0239     C. ************ XRT START ***********
0240     C. Execute, after the success of OP upload.
0241   +. TI 2011-02-01 11:04:00.0
0242     DC 07-F0 MDP_XRT_MODE_STBY
0243     BC       (c3)
0244    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0245     C.
0246     C. ************ XRT END ***********
0247    . C. Stop EIS observation and temporarily disable EIS mode changes
0248     C.
0249     C.
0250     C. ********** Start EIS operation (TI set) **********
0251     C. Execute, after the success of OP upload.
0252     C. Set EIS TI-commands
0253   +. TI 2011-02-01 11:04:30.0
0254     DC 07-FC EIS_MODE_MANU
0255     BC       (21 02)
0256   +. TI 2011-02-01 11:04:40.0
0257     DC 07-FC EIS_MODE_CHG_DIS
0258     BC       (22)
0259    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0260     C. *********** End EIS operation (TI set) ***********
0261     C.
0262     C.
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-865 2011-02-01 13:13:32 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 2958308.8 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 44s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2011-02-01 11:04:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-866 2011-02-01 13:13:32 112 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 20 20)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 20 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 08 20)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 85 83 08 08)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0f 80 80 06 06)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 10 80 80 08 08)
0052   . C. ---------- Success Verify ?           OK / NG ____
0053     C.
0054     C.
0055   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0056     C.
0057   +. DC 07-F0 MDP_XRT_MODE_OBSV
0058     BC       (c2)
0059   +. TI 2011-02-01 11:04:02.0
0060     DC 07-F0 MDP_XRT_MODE_OBSV
0061     BC       (c2)
0062   . C. ---------- Success Verify ?           OK / NG ____
0063     C.
0064     C. ************ XRT END ***********
0065   . C. ********************************
0066     C.  SOT table upload
0067     C. ********************************
0068   . C. < Upload DPL table >
0069   +. DC 07-F0 MDP_FG_CTRL_MANU
0070     BC       (51)
0071   . C. ------------------------------------
0072     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0073     C. ------------------------------------
0074     C.
0075     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0076     C.
0077   . S. RAM   ram-271:MDP_DPL
0078     ()
0079     C.
0080   . C. < Dump RAMID=MDP_DPL >
0081   +. DC 07-F0 MDP_DUMP_FGTBL
0082     BC       (82 07 00 38 b8 00 40)
0083     C. ------------------------------------
0084     C.  MDP_DPL verify          = OK    [ ]
0085     C. ------------------------------------
0086     C.
0087     C. STS_CHK¤òON¤Ë¤¹¤ë
0088     C.
0089   . C. < Update MDP DSC PAR1 >
0090   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0091     BC       (4c)
0092     C.  MDP_CMD_CODE          = F04C0700[ ]
0093     C.  MDP_CMD_CNT        (count-up 1) [ ]
0094     C. ------------------------------------
0095     C.
```

```
0096   . C.
0097     C.
0098   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0099     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0100   . S. DC-BC dcbc-402:DCBC
0101     (MDP_known_event)
0102     C.
0103     C.
0104   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0105   . S. DC-BC dcbc-153:DCBC
0106     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0107     C.
0108     C.
0109   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0110     C.
0111   . C. *********** LOS ***********
0112     C.
```

```
*** OP Sequence for XRT ***

2011/02/01  11:15:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 00 00 ac cd
2011/02/01  11:31:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 d6 67
2011/02/01  11:46:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/01  11:46:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2011/02/01  11:47:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 00 00 00 00
2011/02/01  11:47:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2011/02/01  11:47:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2011/02/01  11:47:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2011/02/01  11:49:58.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 02
2011/02/01  11:50:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2011/02/01  12:03:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM               5 02-76 00 00 00 29 99
2011/02/01  12:19:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM               5 02-76 00 00 00 53 33
2011/02/01  12:35:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM               5 02-76 00 d6 36 b7 8e
2011/02/01  12:45:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM               5 02-76 00 b4 b5 db 75
2011/02/01  13:01:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM               5 02-76 00 ac 5b 00 00
2011/02/01  13:17:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM               5 02-76 00 b4 b5 24 8b
2011/02/01  13:33:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM               5 02-76 00 d6 36 48 72
2011/02/01  13:43:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM               5 02-76 00 29 ca b7 8e
2011/02/01  13:53:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM               5 02-76 00 4b 4b db 75
2011/02/01  14:09:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM               5 02-76 00 53 a5 00 00
2011/02/01  14:30:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/01  14:30:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2011/02/01  14:30:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2011/02/01  14:33:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2011/02/01  14:45:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM               5 02-76 00 4b 4b 24 8b
2011/02/01  15:01:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM               5 02-76 00 29 db 48 72
2011/02/01  15:15:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM               5 02-76 00 00 e5 51 b2
2011/02/01  15:30:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM               5 02-76 00 00 e5 40 00
2011/02/01  15:45:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM               5 02-76 00 00 e5 2e 4d
2011/02/01  16:04:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/01  16:04:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2011/02/01  16:04:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2011/02/01  16:07:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2011/02/01  16:28:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM               5 02-76 00 00 e5 1c 9b
2011/02/01  16:43:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM               5 02-76 00 00 e5 0a f1
2011/02/01  16:55:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM               5 02-76 00 00 e5 f9 40
2011/02/01  17:13:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM               5 02-76 00 00 e5 e7 8e
2011/02/01  17:41:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/01  17:41:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2011/02/01  17:41:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2011/02/01  17:44:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2011/02/01  18:04:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/01  18:04:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2011/02/01  18:04:30.5  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 00 00 00 00
2011/02/01  18:04:46.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2011/02/01  18:04:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
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                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/01  18:04:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/01  18:07:28.0  XRT_QT_PROG_SET_403_OG [0x193]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/01  18:07:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/01  18:15:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                    AOCU_NM              5 02-76 00 00 e5 d5 cb
2011/02/01  18:30:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                    AOCU_NM              5 02-76 00 00 e5 c4 19
2011/02/01  18:45:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                    AOCU_NM              5 02-76 00 00 e5 b2 67
2011/02/01  19:18:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/01  19:18:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/01  19:18:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/01  19:21:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/01  19:42:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                    AOCU_NM              5 02-76 00 14 18 51 58
2011/02/01  19:57:00.0  AOCS_ORe-point_Start_27_OG [0x0b1]
                                    AOCU_NM              5 02-76 00 11 db 43 64
2011/02/01  20:12:00.0  AOCS_ORe-point_Start_28_OG [0x0b2]
                                    AOCU_NM              5 02-76 00 10 41 33 74
2011/02/01  20:27:00.0  AOCS_ORe-point_Start_29_OG [0x0b3]
                                    AOCU_NM              5 02-76 00 0f 33 23 02
2011/02/01  20:55:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/01  20:55:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/01  20:55:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/01  20:58:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/01  21:19:00.0  AOCS_ORe-point_Start_30_OG [0x0b4]
                                    AOCU_NM              5 02-76 00 0e a7 12 24
2011/02/01  21:34:00.0  AOCS_ORe-point_Start_31_OG [0x0b5]
                                    AOCU_NM              5 02-76 00 0e 7e 00 cc
2011/02/01  21:49:00.0  AOCS_ORe-point_Start_32_OG [0x0b6]
                                    AOCU_NM              5 02-76 00 0e a7 f0 00
2011/02/01  22:04:00.0  AOCS_ORe-point_Start_33_OG [0x0b7]
                                    AOCU_NM              5 02-76 00 0f 33 df 1b
2011/02/01  22:33:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/01  22:33:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/01  22:33:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/01  22:36:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/01  22:55:00.0  AOCS_ORe-point_Start_34_OG [0x0b8]
                                    AOCU_NM              5 02-76 00 10 41 ce a8
2011/02/01  23:10:00.0  AOCS_ORe-point_Start_35_OG [0x0b9]
                                    AOCU_NM              5 02-76 00 11 db be c1
2011/02/01  23:25:00.0  AOCS_ORe-point_Start_36_OG [0x0ba]
                                    AOCU_NM              5 02-76 00 14 fd af bf
2011/02/01  23:44:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/01  23:44:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/01  23:45:00.0  AOCS_ORe-point_Start_37_OG [0x0bb]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2011/02/01  23:45:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/01  23:45:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/01  23:45:20.0  XRT_AEC_RESET_432_OG [0x1b0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/01  23:45:22.0  XRT_ARS_DIS_434_OG [0x1b2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/01  23:47:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/01  23:47:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2011/02/01  23:48:00.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2011/02/01  23:48:02.5  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  00:10:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  00:10:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  00:10:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  00:13:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  00:18:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  00:19:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  01:34:30.0  XRT_CTRL_MANU_408_OG [0x198]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  01:34:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  01:34:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  01:37:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  01:53:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  01:54:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  03:09:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  03:09:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  03:09:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  03:12:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  03:30:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  03:31:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  04:39:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  04:39:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  04:39:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  04:42:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  05:07:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  05:08:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  05:51:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  05:51:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/02/02  05:51:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2011/02/02  05:51:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2011/02/02  05:51:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/02  05:51:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/02  05:54:28.0  XRT_QT_PROG_SET_403_OG [0x193]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/02  05:54:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  06:01:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  06:01:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/02  06:01:30.0  AOCS_ORe-point_Start_37_OG [0x0bb]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2011/02/02  06:01:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/02  06:01:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/02  06:01:50.0  XRT_AEC_RESET_432_OG [0x1b0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/02  06:01:52.0  XRT_ARS_DIS_434_OG [0x1b2]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/02  06:04:26.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  06:04:28.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2011/02/02  06:04:30.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2011/02/02  06:04:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  06:19:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  06:19:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  06:19:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  06:22:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  06:45:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  06:46:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  07:59:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/02  07:59:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/02  07:59:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/02  08:02:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/02  08:22:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/02  08:23:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/02  09:41:54.0  XRT_CTRL_MANU_400_OG [0x190]
```

```
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2011/02/02  09:42:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM               5 02-76 00 00 00 00 00
```