

XRT Timeline to be uploaded on 2011/02/02

Period: 2011/02/02 09:32:00 - 2011/02/05 09:56:00

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Normal mode

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XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16

Term	Pointing (x, y)	Comment
02/02 14:10:00 - 02/02 15:05:00	Fixed (-12.0, 35.0)	synoptic, shifted -5.5 min, and a few hours, and HOP 79 11/20.
02/03 06:03:00 - 02/03 06:09:54	Fixed (0.0, 0.0)	synoptic
02/03 18:01:00 - 02/03 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
02/04 06:34:00 - 02/04 06:40:54	Fixed (0.0, 0.0)	synoptic, shifted 31.0 min

PROG= 17 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 69 1-time(s) 4.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 28 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 4 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1850: CH-QS-Boundary Ti/poly+Thin-Be-384x384-2min cad

Term	Pointing (x, y)	Comment
02/02 20:48:00 - 02/03 01:59:54	Track (200.1, 180.0) ^{02/02 20:45:00} # EIS Coronal Hole Boundary Observation.	

PROG= 07 Inf.-time(s)

- Subr= 1 1-time(s) 30.0sec
 - Seqn= 41 1-time(s) 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 32.0s Obs 1x1 384x384 (1064, 1024) DPCM 2 0 2.0sec
- Subr= 2 5-time(s) 120.0sec
 - Seqn=100 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 1x1 384x384 (1064, 1024) Q=95 1 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1844: AR Standard-A(Filter-Ratio) with PFB, thin-Be context, 384x384 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
02/03 02:03:00 - 02/03 05:59:54	Track (19.5, -257.8) ^{02/03 02:00:00} # Observe AR 11150	
02/03 06:13:00 - 02/03 17:57:54	Track (55.3, -257.7) ^{02/03 06:10:00} # AR 11150.	
02/03 18:11:00 - 02/04 01:59:54	Track (157.7, -258.4) ^{02/03 18:08:00} # AR 11150.	

PROG= 11 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 19 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 82 4-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 15 20-time(s) 2.0sec
 - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec
 - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec
 - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
 - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec
 - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
 - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #180D: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 2.5min-cad

Term	Pointing (x, y)	Comment
02/04 02:03:02 - 02/04 06:30:54	Track (96.7, 592.5) ^{02/04 02:00:00} # track AR in North.	
02/04 06:44:02 - 02/04 10:21:54	Track (132.3, 592.1) ^{02/04 06:41:00} # track AR in North.	

PROG= 15 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 17 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 6 4-time(s) 2.0sec

	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 86	16-time(s)	150.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)													
Term	Pointing (x, y)		Comment										
02/02 20:48:00 - 02/03 01:59:54	Track (200.1, 180.0) ^{Ⓜ 02/02 20:45:00}		# EIS Coronal Hole Boundary Observation.										
02/03 02:03:00 - 02/03 05:59:54	Track (19.5, -257.8) ^{Ⓜ 02/03 02:00:00}		# Observe AR 11150										
02/03 06:13:00 - 02/03 17:57:54	Track (55.3, -257.7) ^{Ⓜ 02/03 06:10:00}		# AR 11150.										
02/03 18:11:00 - 02/04 01:59:54	Track (157.7, -258.4) ^{Ⓜ 02/03 18:08:00}		# AR 11150.										
02/04 02:03:02 - 02/04 06:30:54	Track (96.7, 592.5) ^{Ⓜ 02/04 02:00:00}		# track AR in North.										
02/04 06:44:02 - 02/04 10:21:54	Track (132.3, 592.1) ^{Ⓜ 02/04 06:41:00}		# track AR in North.										
PROG= 16	1-time(s)												
Subr= 1	30-time(s)	20.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
Seqn= 87	1-time(s)	2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
Seqn= 89	1-time(s)	2.0sec											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

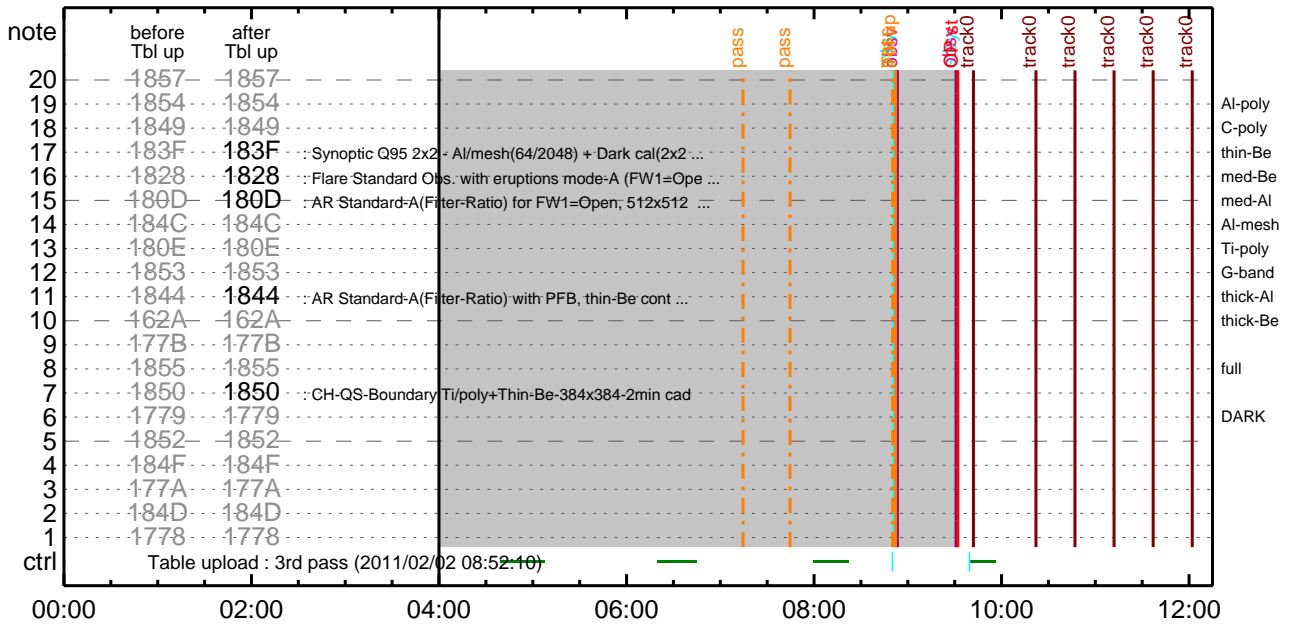
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FLD Patrol													
Term	Pointing (x, y)		Comment										

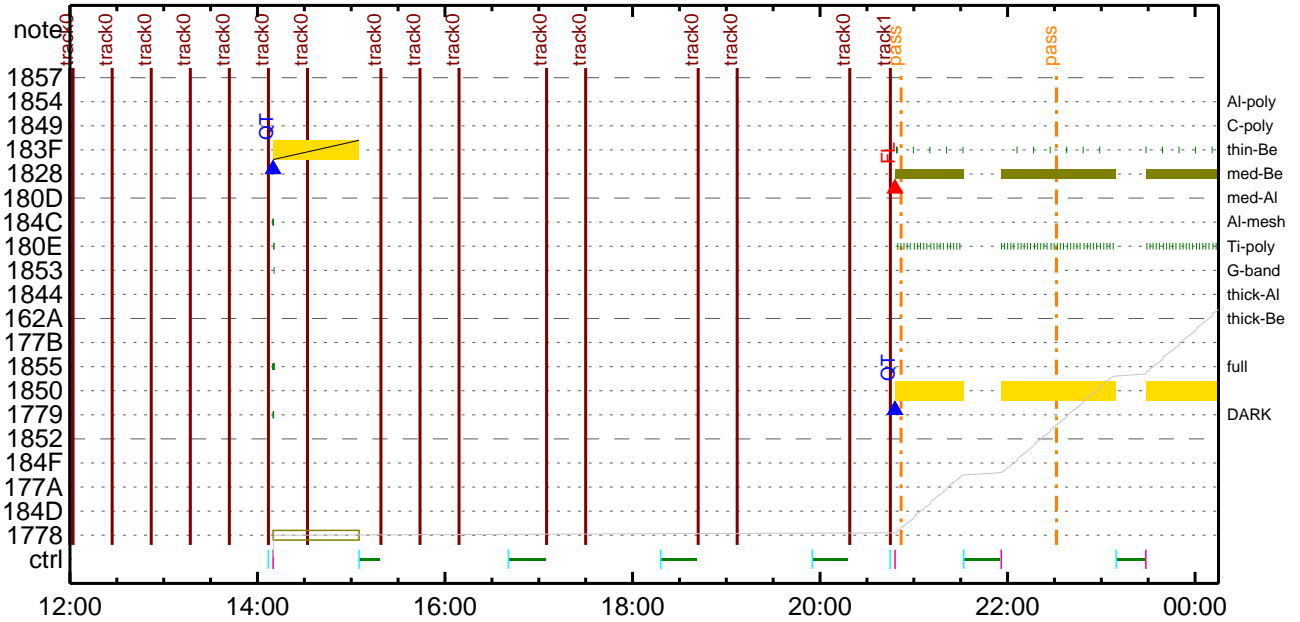
02/02 20:45:16 - 02/03 06:00:16 Track (200.1, 180.0) @ 02/02 20:45:00 # EIS Coronal Hole Boundary Observation.
 02/03 06:10:16 - 02/03 17:58:16 Track (55.3, -257.7) @ 02/03 06:10:00 # AR 11150.
 02/03 18:08:16 - 02/04 06:31:16 Track (157.7, -258.4) @ 02/03 18:08:00 # AR 11150.
 02/04 06:41:16 - 02/05 09:56:00 Track (132.3, 592.1) @ 02/04 06:41:00 # track AR in North.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

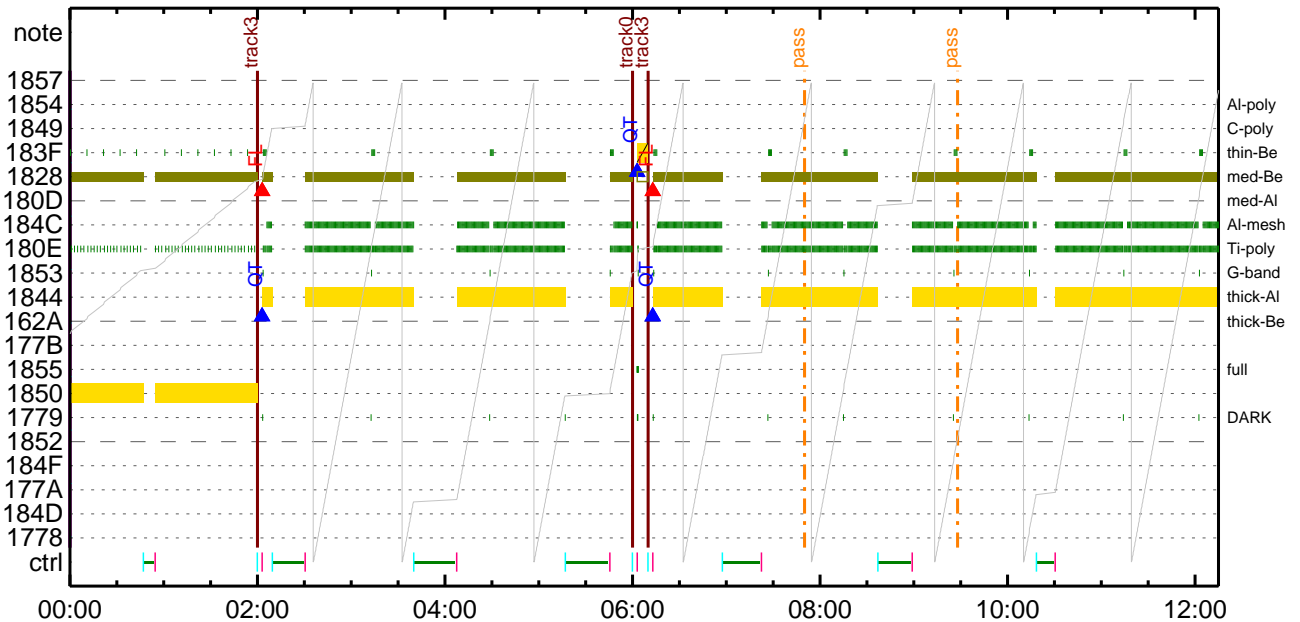
CMDI #0748 2011/02/02



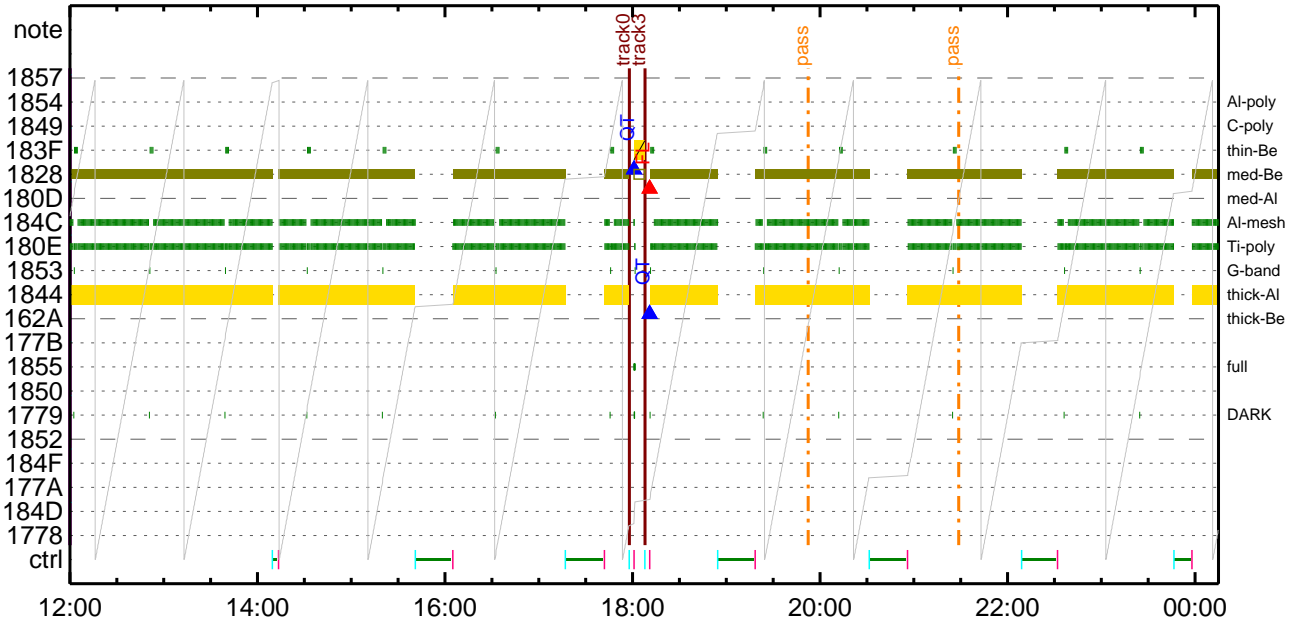
CMDI #0748 2011/02/02



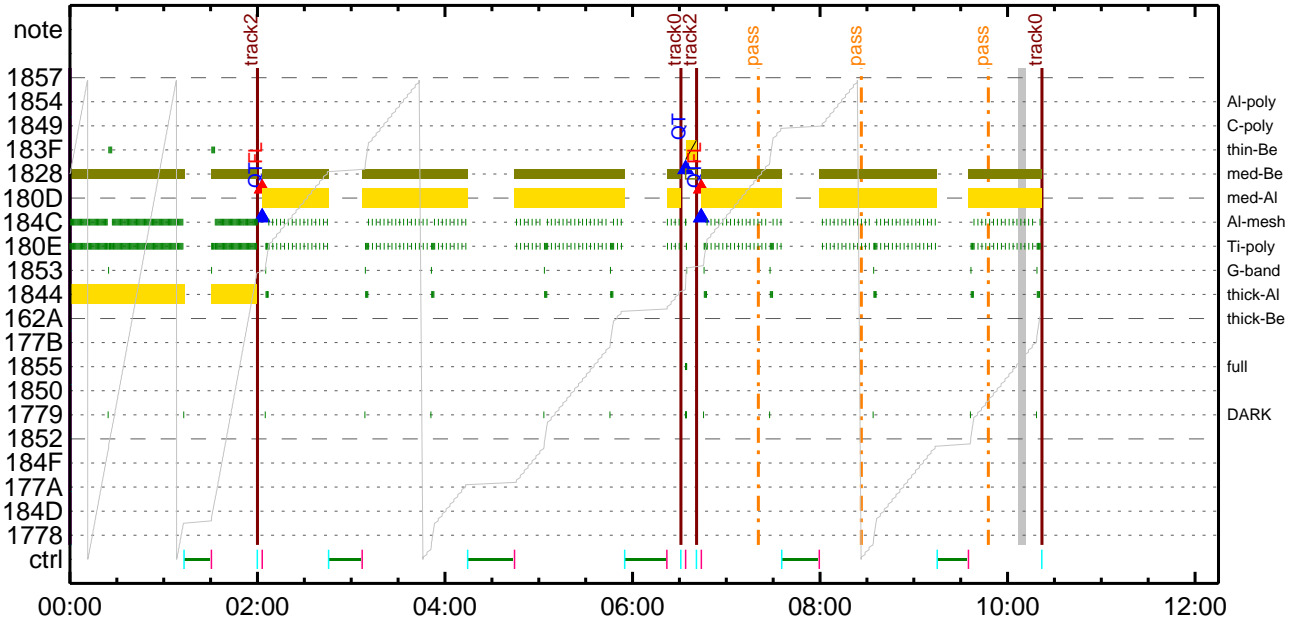
CMDI #0748 2011/02/03



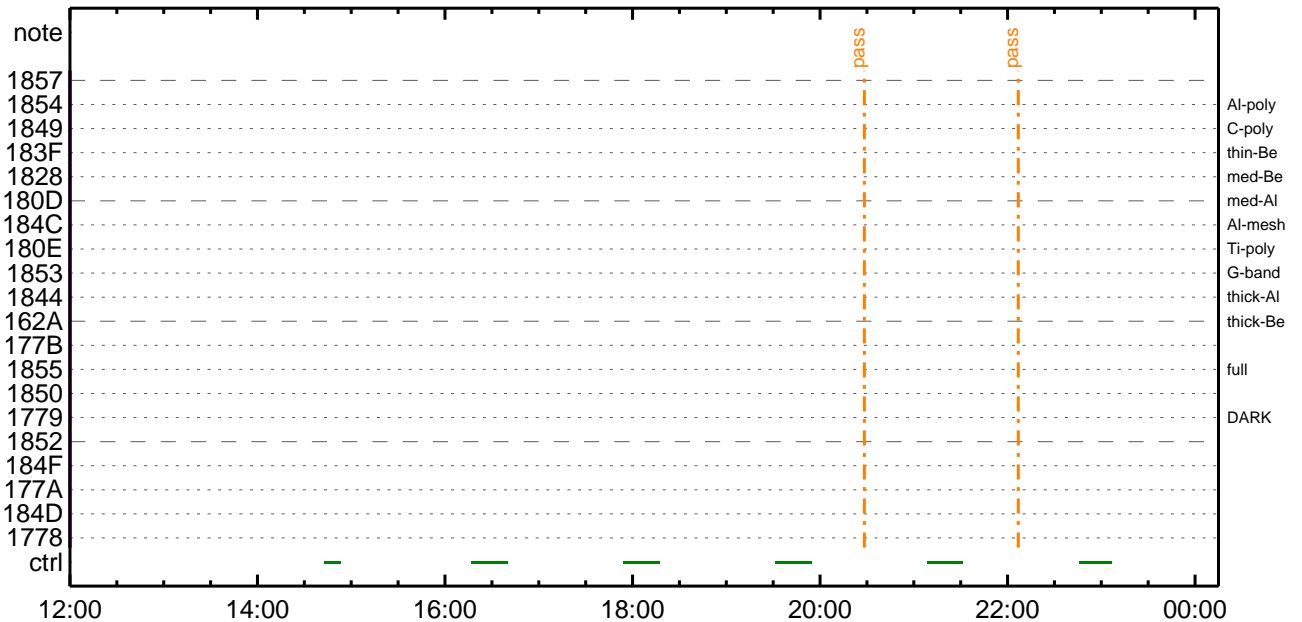
CMDI #0748 2011/02/03



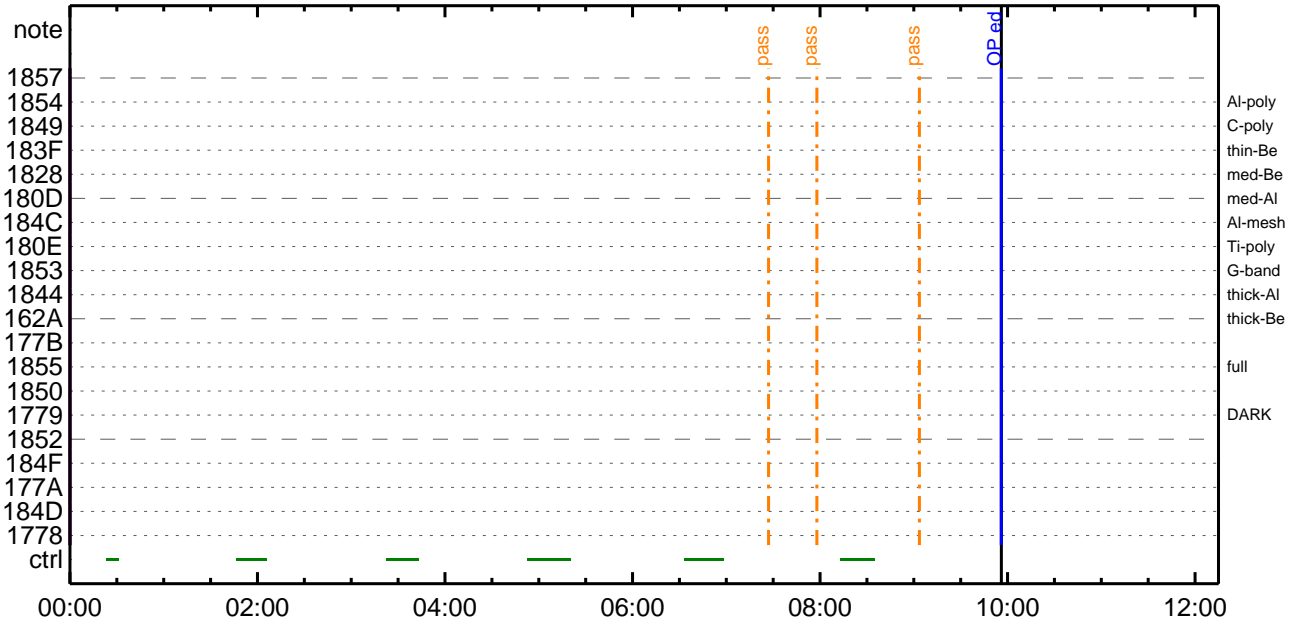
CMDI #0748 2011/02/04



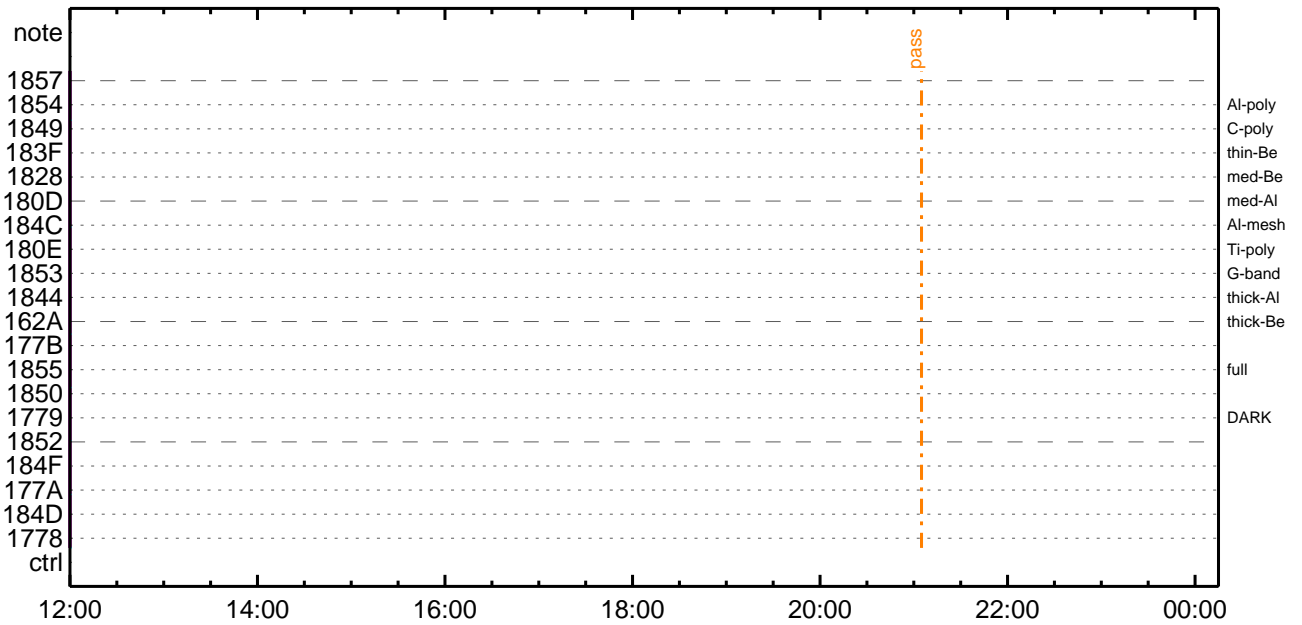
CMDI #0748 2011/02/04



CMDI #0748 2011/02/05



CMDI #0748 2011/02/05




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-867:OP
0104 ( )
0105 S. OG og-867:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     127
0115 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0116 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.      çç[HK1_PKT_FORM_NO]         EQ      7
0120 C.      çç[HK1_PKT_GEN_TIME]        EQ     0.25 s
0121 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0122 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0123 C.      çç[HK1_DMP_CHK_FLG]        EQ     EXEC
0124 C. YAYOXx½^aÎ»ò³ÎÇ§
0125 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0126 C. RAM ID=NMOG□Î½Ê¹ç•ë²İOKò³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     127
0134 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0135 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.      çç[HK1_PKT_FORM_NO]         EQ      7
0139 C.      çç[HK1_PKT_GEN_TIME]        EQ     0.25 s
0140 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0141 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0142 C.      çç[HK1_DMP_CHK_FLG]        EQ     EXEC
0143 C. YAYOXx½^aÎ»ò³ÎÇ§
0144 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0145 C. RAM ID=NMOG□Î½Ê¹ç•ë²İOKò³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     65
0153 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0154 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.      çç[HK1_PKT_FORM_NO]         EQ      7
0158 C.      çç[HK1_PKT_GEN_TIME]        EQ     0.25 s
0159 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0160 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0161 C.      çç[HK1_DMP_CHK_FLG]        EQ     EXEC
0162 C. YAYOXx½^aÎ»ò³ÎÇ§
0163 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0164 C. RAM ID=NMOG,RAM ID=OP□Î½Ê¹ç•ë²İOKò³ÎÇ§
0165 C.
0166 C. ***** °Ê²¼□İ½Ä´¶Á°ÊÊ¬□°Á÷¿@ (¼âµ-YAYOXx½ê½çòÄŒç¼^a°□è¼i¹çççâ) *****
0167 C. DHUYâ;4YE;Ê½½, Y;½YE;Êòİâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.      çç[HK1_PKT_FORM_NO]         EQ      2
0171 C.      çç[HK1_PKT_GEN_TIME]        EQ     0.5S
0172 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32K
0173 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD□-Á÷¿@NG□İ½i¹ç;ç°Ê²¼□İTI-CMDÁ÷¿@□İ½Ä¹Œ□□è□□³□è;f
0180 C.      □è□;çSET□EDUMP□İ±°iYNY¹ç¹Œ□□³□è;f
0181 C.
0182 C. TIY³YpYóYEòòÄİ¿¿(UT)
0183 +. TI 2011-02-02 09:27:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP
0186 C.
0187 +. TI 2011-02-02 09:27:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP
0190 C.
0191 +. TI 2011-02-02 09:27:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP

```



```

0194 C.
0195 +. TI 2011-02-02 09:31:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼αîÄè%îíñαîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-02-02 09:31:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2011-02-02 09:31:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2011-02-02 09:31:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC          (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÃîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèαÈ¼αα¼Ä»Űα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷŷÄŷ-¼Ä»Ű;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-868 2011-02-02 13:04:42 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È;ø;ç;ç;ø;µ;°;È;»í;×;Á;ç;ø;í;ç;ç;Y;Á;Y;×;Y;í;¼;Y;É;Ë;È;¾;µ;í;í;È;Ë;ø;È;¼;°;ç;Ö;ø;ø;¿;¼;í;¹;ç;ø;í;ç;Ä;®,;ù;ø¹;ø;È;ø;ø;ø;ç;Ä;+;¿;®;ø;ø;È;ø;ø;ø;ø;ø;È;í;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_ST51>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 1m26s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2011-02-02 09:31:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÄí;ø;í;õ;¾;Y;ø;È;Ä;ø;¹;ø;È;C;B;C;•;×;²;è *****
0069 C. (¾;ä;°;í;Y;ø;Y;Ä;Y;È;Y;Þ;Y;È;Y;Ä;Y;ç;Y;È;ø;È;¾;¾;ø;ø;¾;Ä;»;Û;ø;¹;ø;è)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹;Ì Daily±;¿;í;Ñ;ø;È;´;ø;ø;¹;ø;È;D;C;B;C;•;×;²;è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATING_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÝÁYŞYÄY-¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 08 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 85 80 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 85 83 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2011-02-02 09:31:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C. ***** XRT END *****
0155 C.
0156 . C. ***** MDP `úÃîñï»ö%ÝñÊÄð¹ñèDCBC•x²è *****
0157 C. (%á°îÿÓÿÄÿÈÿËÿáÿçÿèñ%¼ññ%Ä»Ûñ¹ñè)
0158 . S. DC-BC dcbc-402:DCBC
0159 (MDP_known_event)
0160 C.
0161 C.
0162 . C. ***** ¥Dÿ¹•İ Daily±¿İññÊ´Øñ¹ñèDCBC•x²è *****
0163 . S. DC-BC dcbc-153:DCBC
0164 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0165 C.
0166 C.
0167 . C. ¡ãLOS¥Á¥$ÿÄÿ-¼Ä»Û;ä
0168 C.
0169 . C. ***** LOS *****
0170 C.

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*** OP Sequence for XRT ***

2011/02/02	09:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	09:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/02	09:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/02	09:42:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 54 00 01 0e				
2011/02/02	09:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/02	10:22:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 4b ff 01 0e				
2011/02/02	10:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 43 1a 01 0e				
2011/02/02	11:12:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 3a 35 01 0e				
2011/02/02	11:37:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 31 58 01 0e				
2011/02/02	12:02:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 28 72 01 0e				
2011/02/02	12:27:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 1f 8d 01 0e				
2011/02/02	12:52:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 16 a7 01 0e				
2011/02/02	13:17:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 0d ca 01 0e				
2011/02/02	13:42:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 04 e5 01 0e				
2011/02/02	14:06:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	14:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/02	14:07:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 fc e6 01 0e				
2011/02/02	14:07:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/02	14:07:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/02	14:07:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/02	14:09:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/02/02	14:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/02	14:32:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 f4 01 01 0e				
2011/02/02	15:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	15:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/02	15:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/02	15:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/02	15:19:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 eb 1b 01 0e				
2011/02/02	15:44:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 e2 36 01 0e				
2011/02/02	16:09:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 d9 59 01 0e				
2011/02/02	16:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	16:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/02	16:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/02	16:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/02	17:05:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 d0 73 01 0e				
2011/02/02	17:30:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 c7 8e 01 0e				
2011/02/02	18:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	18:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/02	18:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/02	18:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/02	18:42:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 be a8 01 0e				
2011/02/02	19:07:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 b5 cb 01 0e				
2011/02/02	19:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/02	19:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/02	19:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/02	19:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2011/02/02	20:19:00.0	AOCS_OrE-point_Start_20_OG [0x0aa]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
		AOCU_NM		5	02-76	00 ac e6 01 0e	
2011/02/02	20:44:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/02	20:44:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/02/02	20:45:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]					
		AOCU_NM		5	02-76	01 00 00 00 00	
2011/02/02	20:45:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/02/02	20:45:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/02/02	20:45:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/02/02	20:45:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/02	20:47:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/02	20:47:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2011/02/02	20:47:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2011/02/02	20:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/02	21:32:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/02	21:32:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/02	21:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/02	21:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/02	21:55:00.0	XRT_Custom_418_OG [0x1a2]					
2011/02/02	21:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/02	23:09:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/02	23:09:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/02	23:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/02	23:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/02	23:27:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/02	23:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	00:47:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	00:47:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	00:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	00:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	00:53:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	00:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	01:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	01:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/02/03	02:00:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]					
		AOCU_NM		5	02-76	03 00 00 00 00	
2011/02/03	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/02/03	02:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/02/03	02:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/02/03	02:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/03	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	02:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/02/03	02:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2011/02/03	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	02:09:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	02:09:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	02:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	02:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	02:29:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	02:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	03:40:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/02/03	03:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	03:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	03:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	04:06:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	04:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	05:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	05:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	05:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	05:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	05:44:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	05:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/02/03	06:00:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/02/03	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/02/03	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/02/03	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/03	06:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2011/02/03	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/02/03	06:10:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	03 00 00 00 00	
2011/02/03	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/02/03	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/02/03	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/02/03	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/03	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	06:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/02/03	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2011/02/03	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	06:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	06:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	06:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	07:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	07:21:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	07:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	08:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	08:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	08:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	08:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	08:58:00.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	08:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	10:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/03	10:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/03	10:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/03	10:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/03	10:29:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/03	10:30:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/03	14:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/02/03	14:09:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	14:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	14:12:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	14:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	14:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	15:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	15:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	15:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	15:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	16:04:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	16:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	17:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	17:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	17:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	17:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	17:41:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	17:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	17:57:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	17:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/03	17:58:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/03	17:58:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/03	17:58:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/03	17:58:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/03	18:00:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/02/03	18:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	18:07:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	18:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/03	18:08:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/02/03	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/03	18:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/03	18:08:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/03	18:08:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/03	18:10:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	18:10:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/02/03	18:10:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/02/03	18:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	18:54:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	18:54:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	18:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	18:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	19:17:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	19:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	20:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	20:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	20:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	20:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	20:55:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	20:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	22:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2011/02/03	22:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	22:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	22:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	22:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	22:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/03	23:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/03	23:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/03	23:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/03	23:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/03	23:57:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/03	23:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	01:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	01:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/04	01:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/04	01:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/04	01:29:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/04	01:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	01:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	01:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/04	02:00:00.0	AOCS_OrE-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/02/04	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/04	02:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/04	02:00:20.0	XRT_AEC_RESET_432_OG [0x1b0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/04	02:00:22.0	XRT_ARS_DIS_434_OG [0x1b2]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/04	02:02:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/04	02:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2011/02/04	02:03:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/02/04	02:03:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	02:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	02:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/04	02:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/04	02:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/04	03:06:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/04	03:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	04:14:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	04:14:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/04	04:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/04	04:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/04	04:43:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/04	04:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	05:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	05:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/04	05:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/04	05:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/04	06:21:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/04	06:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/04	06:30:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/04	06:30:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/04	06:31:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/04	06:31:16.0	XRT_FLD_DIS_402_OG [0x192]							

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2011/02/04	06:31:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/02/04	06:31:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/04	06:33:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2011/02/04	06:34:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/04	06:40:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/04	06:40:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/02/04	06:41:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02	00 00 00 00
2011/02/04	06:41:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/02/04	06:41:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/02/04	06:41:20.0	XRT_AEC_RESET_432_OG [0x1b0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/02/04	06:41:22.0	XRT_ARS_DIS_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/02/04	06:43:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/04	06:43:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2011/02/04	06:44:00.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2011/02/04	06:44:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/04	07:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/04	07:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/04	07:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/04	07:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/04	07:58:30.0	XRT_Custom_418_OG [0x1a2]					
2011/02/04	07:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/04	09:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/04	09:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/02/04	09:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/02/04	09:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/02/04	09:34:00.0	XRT_Custom_418_OG [0x1a2]					
2011/02/04	09:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/02/04	10:21:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/02/04	10:22:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00 00 00 00