

# XRT Timeline to be uploaded on 2011/02/05

Period: 2011/02/05 09:56:00 - 2011/02/10 09:34:00

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Normal mode

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XOB #181D: HOP81 FW1=Al/poly - 8s - AR Present - NO G-band - 384x384													
Term	Pointing (x, y)	Comment											
02/05 10:09:30 - 02/05 16:05:54	Fixed ( 0.0, -940.0)	# OP start + 10min: HOP81(Day2) South Pole - Fast Scan Mode											
<b>PROG= 14 Inf.-time(s)</b>													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 80 60-time(s) 30.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1844: AR Standard-A(Filter-Ratio) with PFB, thin-Be context, 384x384 at 1064 1048, 120s cad													
Term	Pointing (x, y)	Comment											
02/05 16:09:00 - 02/05 17:59:54	Track ( 520.3, -272.6) <sup>Ⓢ 02/05 16:06:00</sup>	AR11150											
02/05 18:13:00 - 02/06 06:06:54	Track ( 534.8, -273.7) <sup>Ⓢ 02/05 18:10:00</sup>	AR11150(cont.)											
02/06 06:20:00 - 02/06 17:30:00	Track ( 615.2, -280.4) <sup>Ⓢ 02/06 06:17:00</sup>	AR11150(cont.)											
02/06 18:08:30 - 02/07 05:40:24	Track ( 685.6, -288.0) <sup>Ⓢ 02/06 18:05:30</sup>	AR11150(cont.)											
02/08 07:14:06 - 02/08 10:19:54	Track ( 846.0, -316.8) <sup>Ⓢ 02/08 06:45:00</sup>	AR11150											
<b>PROG= 11 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 82 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 15 20-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16													
Term	Pointing (x, y)	Comment											
02/05 18:03:00 - 02/05 18:09:54	Fixed ( 0.0, 0.0)	synoptic											
02/06 06:10:00 - 02/06 06:16:54	Fixed ( 0.0, 0.0)	synoptic, shifted 7.0 min											
02/06 17:58:30 - 02/06 18:05:24	Fixed ( 0.0, 0.0)	synoptic, shifted -4.5 min											
02/07 05:43:30 - 02/07 05:50:24	Fixed ( 0.0, 0.0)	synoptic, shifted -19.5 min											
02/08 05:58:00 - 02/08 06:04:54	Fixed ( 0.0, 0.0)	synoptic											
<b>PROG= 17 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 69 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 28 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT													
Term	Pointing (x, y)	Comment											
02/07 06:05:30 - 02/07 07:50:24	Fixed ( 0.0, 945.0)	Co-alignment N-limb											
<b>PROG= 18 1-time(s)</b>													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 21 24-time(s) 300.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT</b>												
Term		Pointing (x, y)					Comment					
02/07 08:05:30 - 02/07 09:50:24		Fixed ( -945.0, 0.0)					Co-alignment E-limb					
<b>PROG= 04 1-time(s)</b>												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 22 15-time(s) 480.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

<b>XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long</b>												
Term		Pointing (x, y)					Comment					
02/08 06:08:05 - 02/08 06:14:54		Fixed ( -528.4, -528.4)					Quadrant (1/4)					
<b>PROG= 01 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 38 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

<b>XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long</b>												
Term		Pointing (x, y)					Comment					
02/08 06:18:00 - 02/08 06:24:54		Fixed ( 528.4, -528.4)					Quadrant (2/4)					
<b>PROG= 06 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 36 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

<b>XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long</b>												
Term		Pointing (x, y)					Comment					
02/08 06:28:00 - 02/08 06:34:54		Fixed ( 528.4, 528.4)					Quadrant (3/4)					
<b>PROG= 03 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 39 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

<b>XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long</b>												
Term		Pointing (x, y)					Comment					
02/08 06:37:55 - 02/08 07:11:00		Fixed ( -528.4, 528.4)					Quadrant (4/4)					
<b>PROG= 09 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 40 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x512	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x512	(1024, 1024)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x512	(1024, 1024)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x512	(1024, 1024)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

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Flare mode

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
02/05 10:09:30 - 02/05 16:05:54	Fixed ( 0.0, -940.0)	# OP start + 10min: HOP81(Day2) South Pole - Fast Scan Mode
02/05 16:09:00 - 02/05 17:59:54	Track ( 520.3, -272.6) <sup>Ⓜ 02/05 16:06:00</sup>	AR11150
02/05 18:13:00 - 02/06 06:06:54	Track ( 534.8, -273.7) <sup>Ⓜ 02/05 18:10:00</sup>	AR11150(cont.)
02/06 06:20:00 - 02/06 17:30:00	Track ( 615.2, -280.4) <sup>Ⓜ 02/06 06:17:00</sup>	AR11150(cont.)
02/06 18:08:30 - 02/07 05:40:24	Track ( 685.6, -288.0) <sup>Ⓜ 02/06 18:05:30</sup>	AR11150(cont.)
02/08 07:14:06 - 02/08 10:19:54	Track ( 846.0, -316.8) <sup>Ⓜ 02/08 06:45:00</sup>	AR11150
<b>PROG= 16 1-time(s)</b>		
<b>Subr= 1 30-time(s) 20.0sec</b>		
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/thick-Al	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 90 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/thick-Al	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 90 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/thick-Al	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 90 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/thick-Al	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>		
<b>Seqn= 89 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

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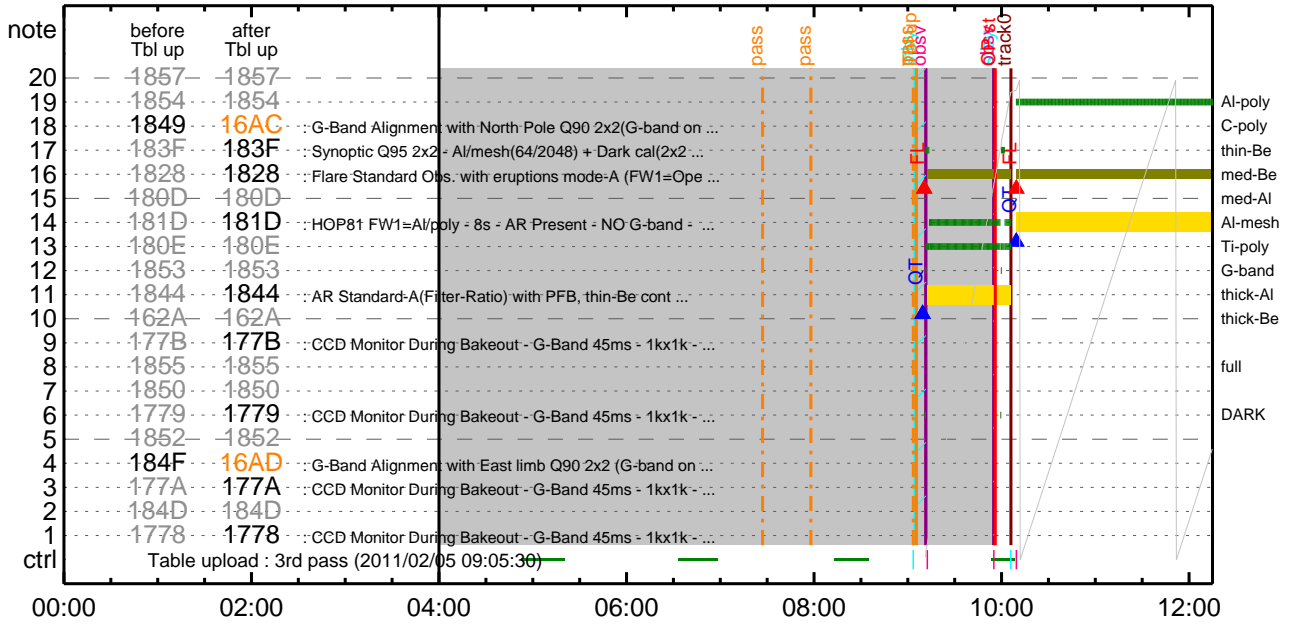
**Flare Detection**

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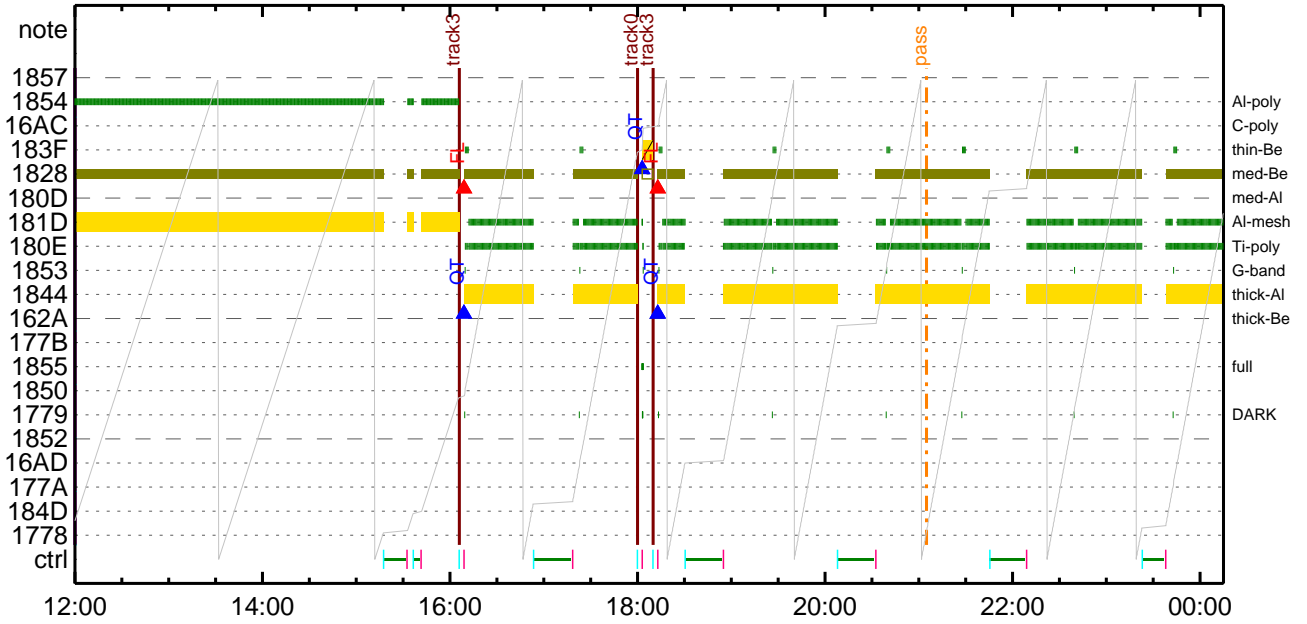
**FLD Patrol**

Term	Pointing (x, y)	Comment
02/05 18:10:16 - 02/06 06:07:16	Track ( 534.8, -273.7) <sup>Ⓜ 02/05 18:10:00</sup>	AR11150(cont.)
02/06 06:17:16 - 02/06 17:55:46	Track ( 615.2, -280.4) <sup>Ⓜ 02/06 06:17:00</sup>	AR11150(cont.)
02/06 18:05:46 - 02/07 05:40:46	Track ( 685.6, -288.0) <sup>Ⓜ 02/06 18:05:30</sup>	AR11150(cont.)
02/08 07:11:22 - 02/10 09:34:00	Track ( 846.0, -316.8) <sup>Ⓜ 02/08 06:45:00</sup>	AR11150
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

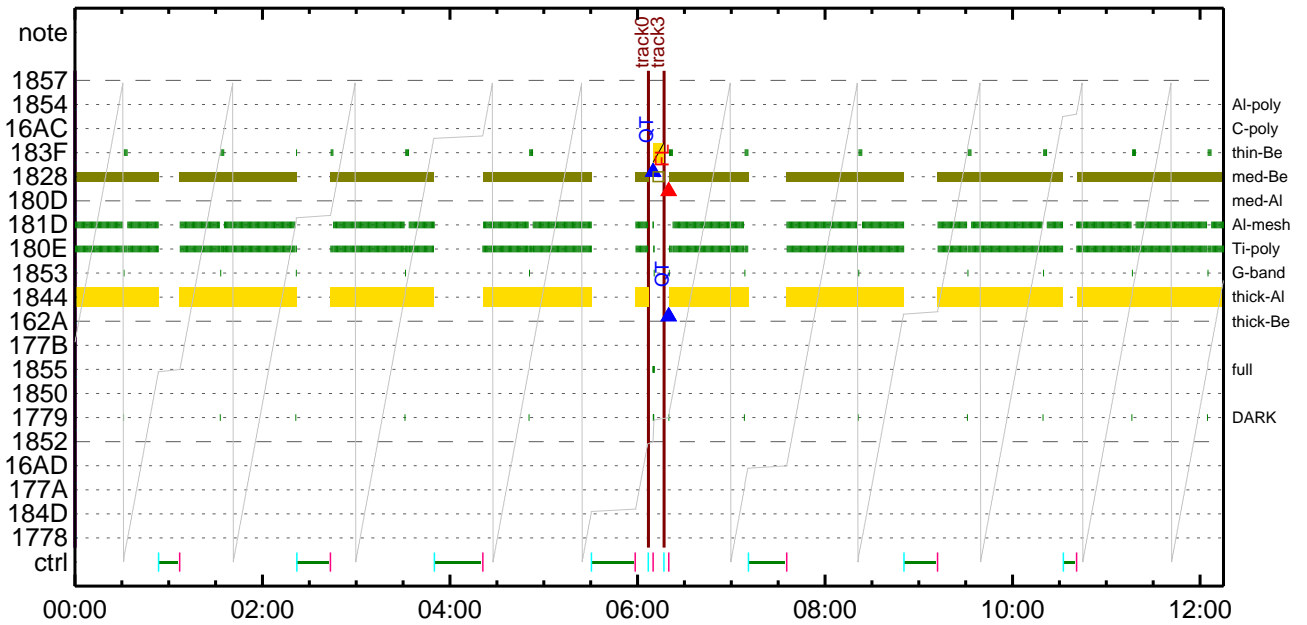
### CMDI #0754 2011/02/05



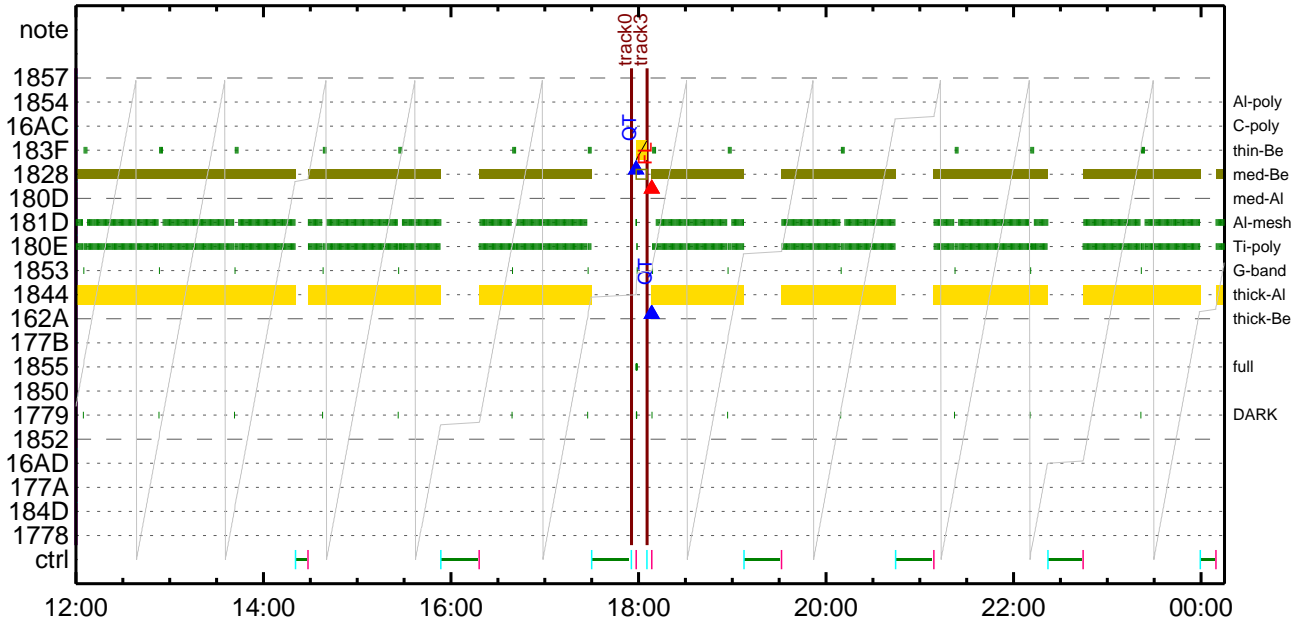
### CMDI #0754 2011/02/05



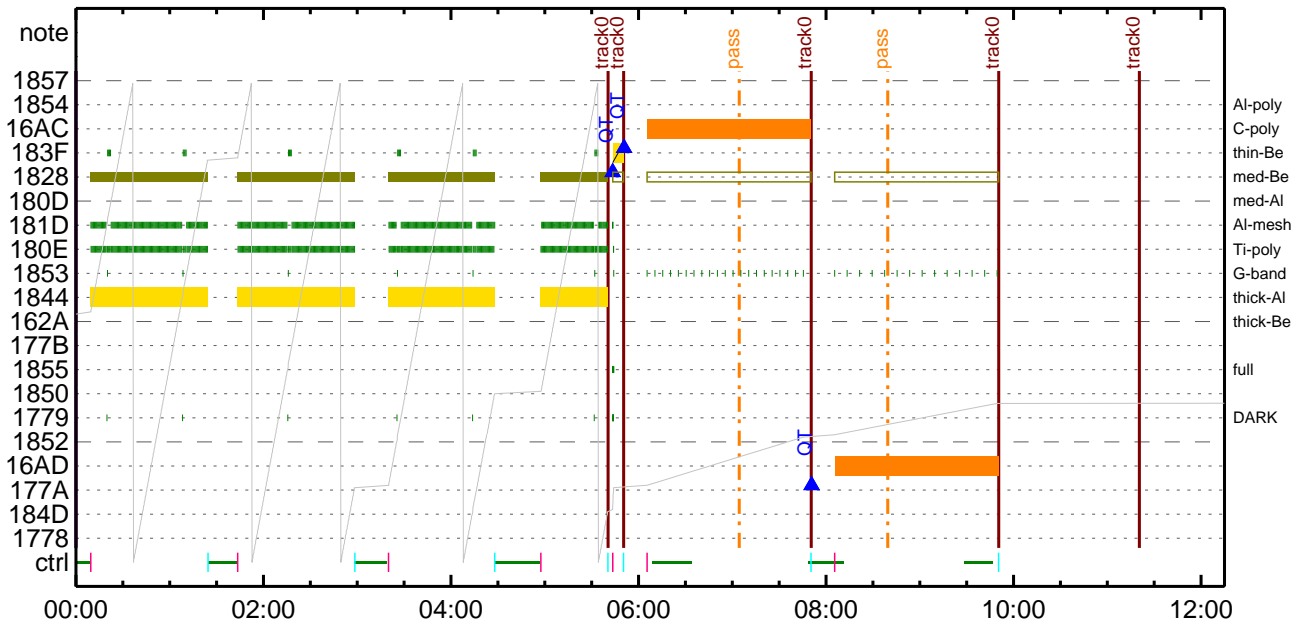
### CMDI #0754 2011/02/06



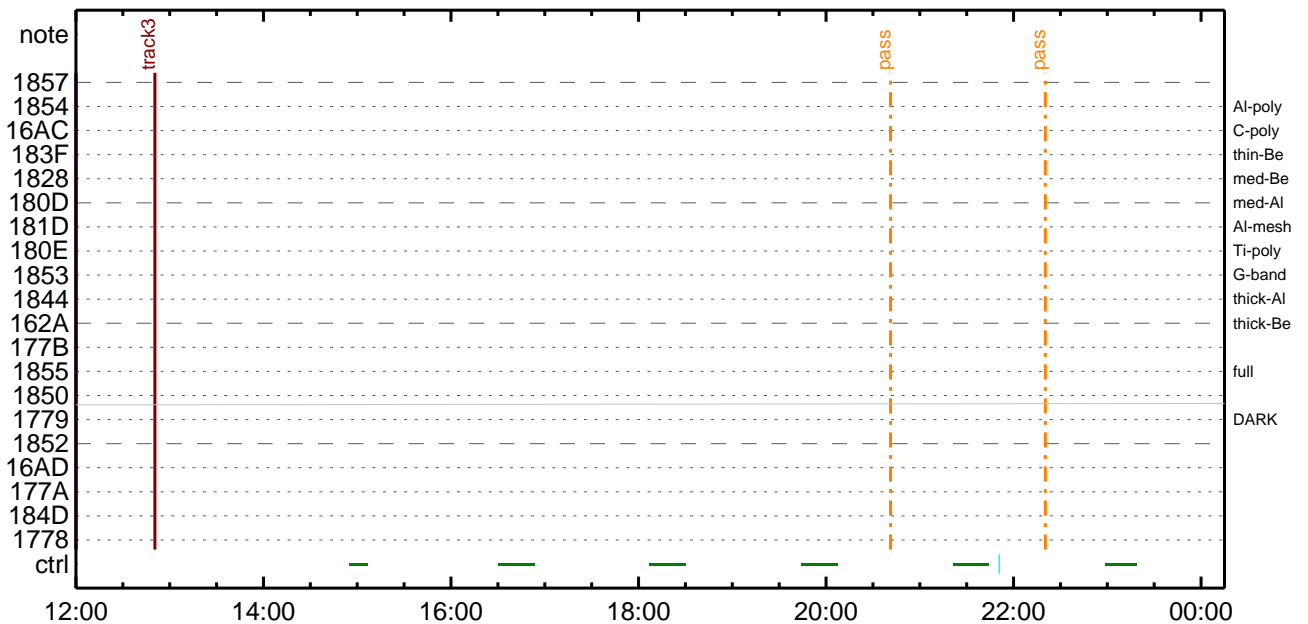
CMDI #0754 2011/02/06



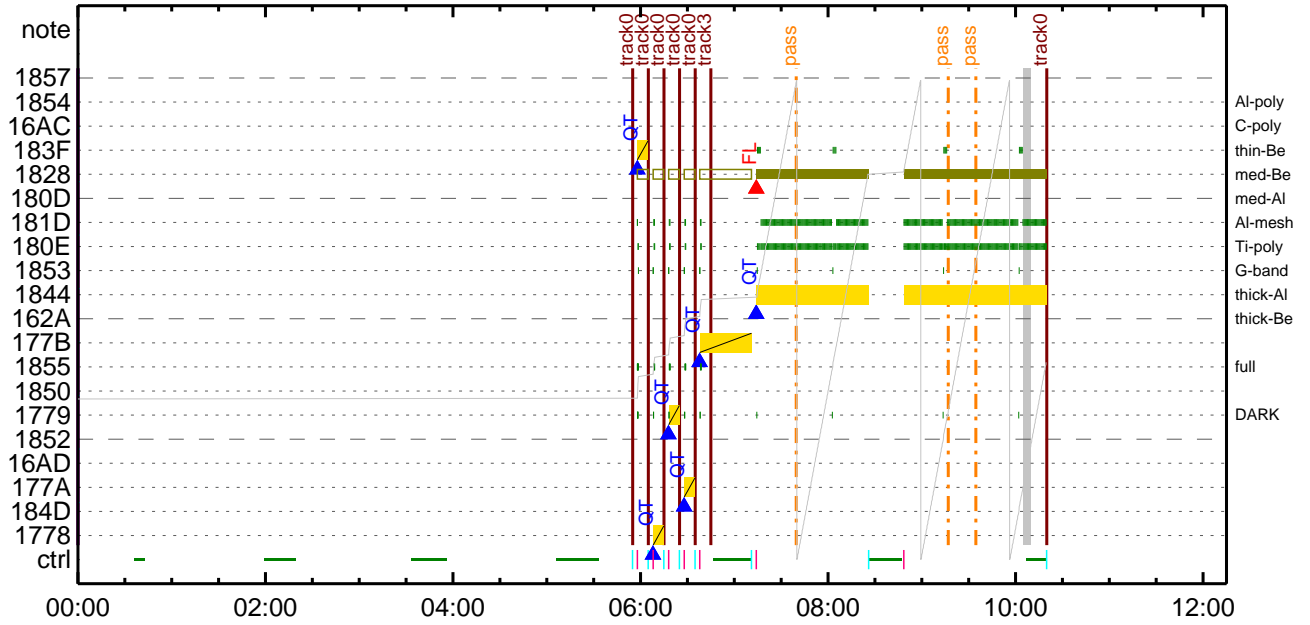
CMDI #0754 2011/02/07



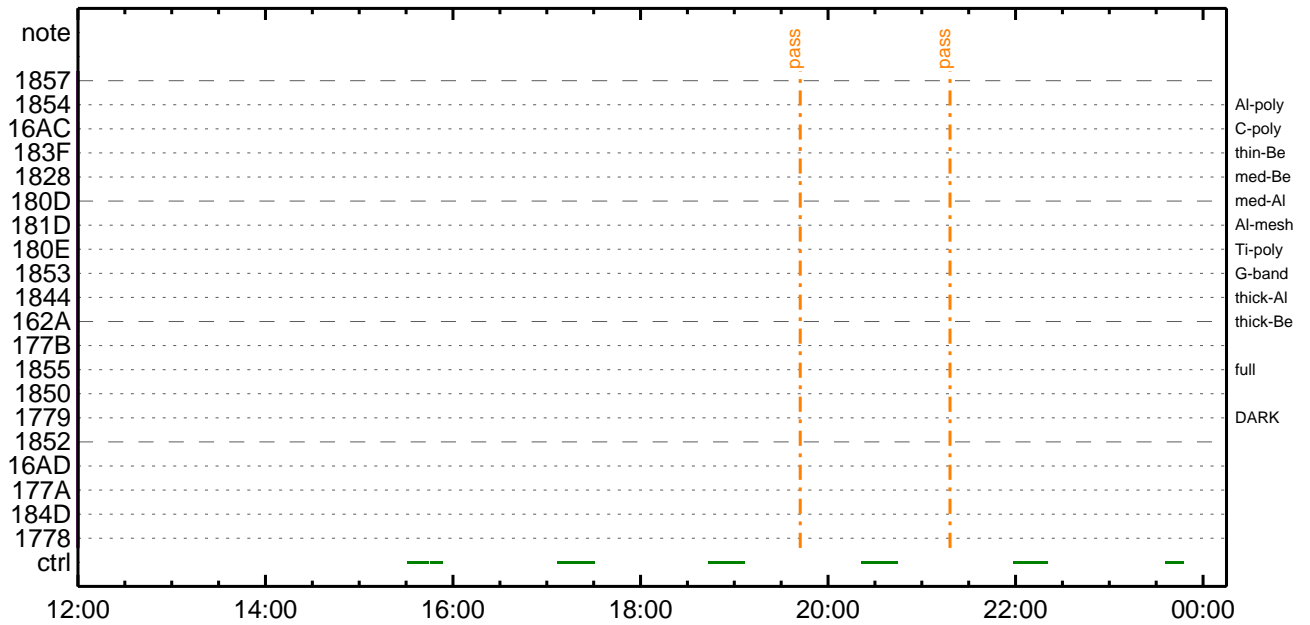
CMDI #0754 2011/02/07



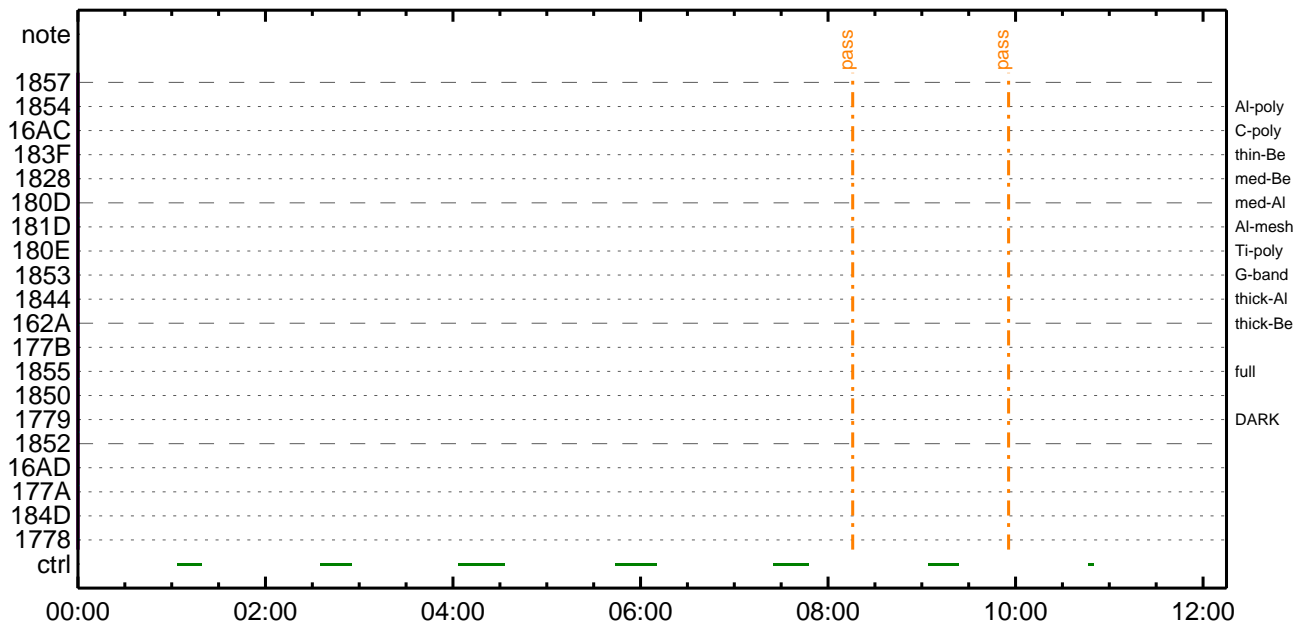
### CMDI #0754 2011/02/08



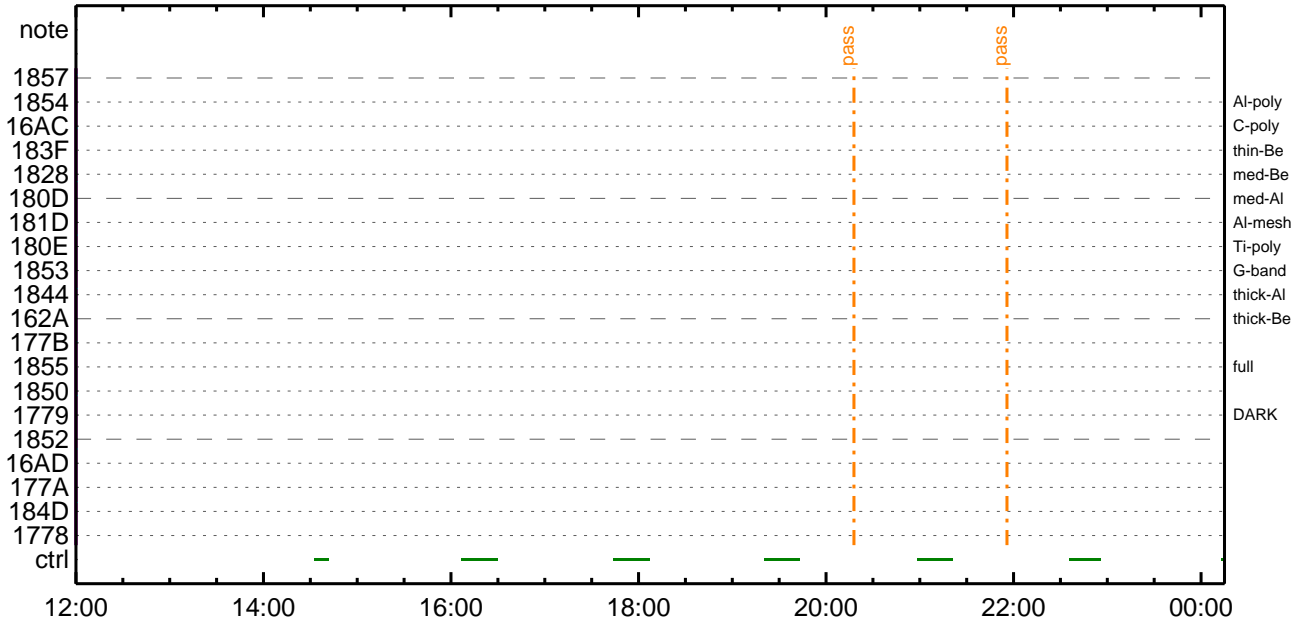
### CMDI #0754 2011/02/08



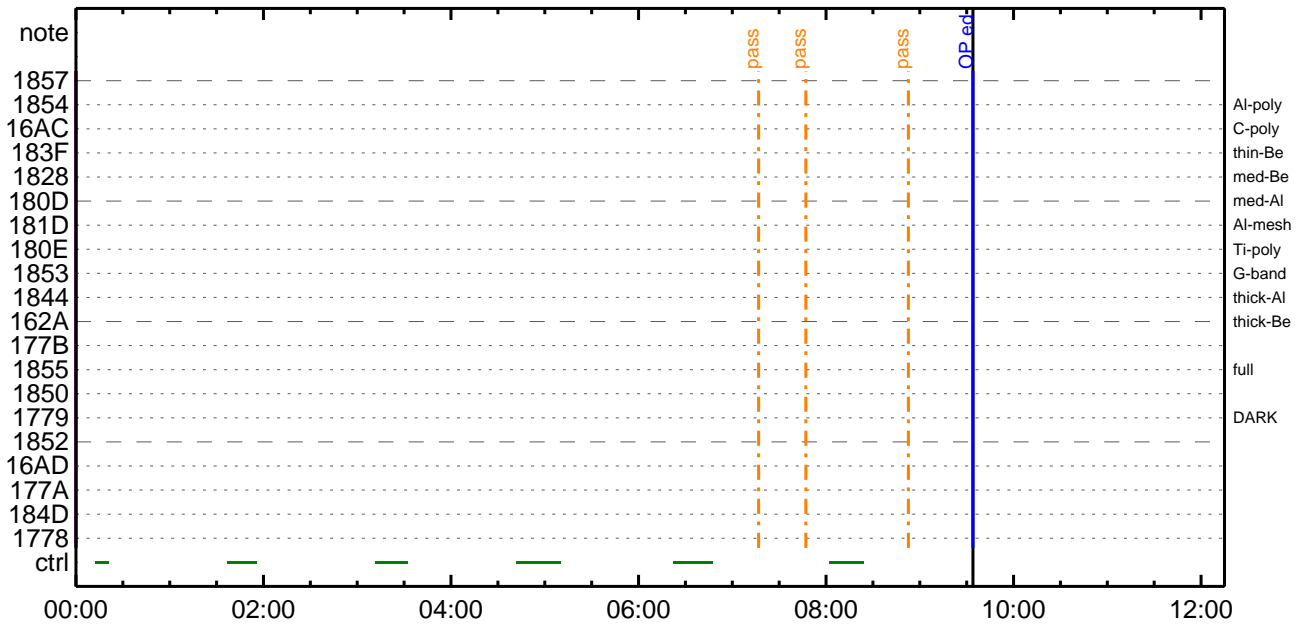
### CMDI #0754 2011/02/09



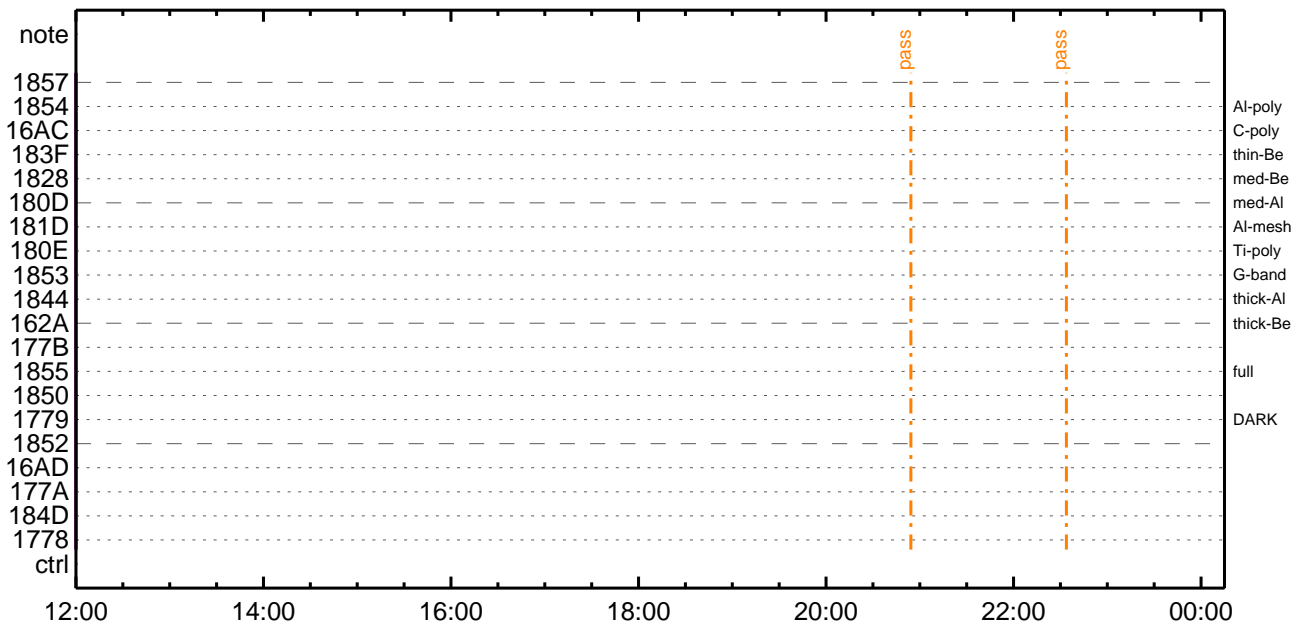
CMDI #0754 2011/02/09



CMDI #0754 2011/02/10



CMDI #0754 2011/02/10







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉ;ã
0103 S. OP op-875:OP
0104 ( )
0105 S. OG og-875:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÎÎ°èYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYÓYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½Ê¹ç•è²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYÓYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½Ê¹ç•è²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYÓYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½Ê¹ç•è²îOKð³îÇ§
0165 C.
0166 C. ***** òÊ²¼òî½Ã´¶Á°òÊÈ¬ò°Á÷¿@ (½âµ-YAYÓYx½ê½çòðÁÔÃæç½ª°¬ðè½î¹çççðâ) *****
0167 C. DHUYã;½YÉ;Ê½Y½;Yi;½YÉ;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿@NG½î½¹ç;ç°Ê²¼òîTI-CMDÁ÷¿@ãî½Á¹Ô°¬ðÊðð³ðÊ;f
0180 C. ð³ð¿;çSETòEDUMPAîÆ±òîYÑY¹ç¹Ô°¬ð³ðÊ;f
0181 C.
0182 C. TIY³Y½YÓYÉòðÁDî¿(UT)
0183 +. TI 2011-02-05 09:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-02-05 09:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-02-05 09:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-02-05 09:55:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °È²¼ïãĬÄē%īîñòîŷÄŷ§ŷÄŷ-¹àĭŭ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]                EQ      4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIĭĭ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]           EQ      07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]             EQ      7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªĭ»ò³ĭÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLòĭ¾È¹ç•è²ĬOKò³ĭÇ§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¾ŷ¼. ŷĭ;¼ŷÈ;Èòòĭãò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]             EQ      2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-02-05 09:55:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-02-05 09:55:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-02-05 09:55:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-02-05 09:55:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úĀĭòĭ»ö¼ŷòĬÄĐò¹òèDCBC•x²è *****
0276 C. (%ã°ĭŷÖŷÄŷÈŷŷŷÄŷçŷèè¾¼òò¼Ā»Űò¹è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•Ĭ Daily±çĬñòĬĖ'Øò¹òèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ĭãLOSŷÄŷ§ŷÄŷ-¼Ā»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 60 20 18)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b a0 80 18 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c c0 c0 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 40 c0 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 40 40 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0b)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 10)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0166 BC (c0)
0167 +. TI 2011-02-05 09:55:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 +. TI 2011-02-05 09:55:04.0
0171 DC 07-F0 MDP_XRT_CTRL_AUTO
0172 BC (c0)
0173 . C. ----- Success Verify ? OK / NG ____
0174 C.
0175 C. ***** XRT END *****
0176 . C. *****
0177 C. SOT table upload
0178 C. *****
0179 . C. < Stop FG table >
0180 +. DC 07-F0 MDP_FG_CTRL_MANU
0181 BC (51)
0182 . C. -----
0183 C. MDP_FG_CTRL_MODE = MANU [ ]
0184 C. -----
0185 C.
0186 . C. <Upload FG Observation Table>
0187 . S. RAM ram-264:MDP_OBS_F
0188 ( )
0189 C.
0190 . C. < Dump RAMID=MDP_OBS_F >
0191 +. DC 07-F0 MDP_DUMP_FGTBL
0192 BC (82 07 00 00 00 38 b8)
0193 C. -----

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0194 C. MDP_OBS_F verify = OK/NG [ ]
0195 C. -----
0196 C.
0197 . C. < Stop SP table >
0198 +. DC 07-F0 MDP_SP_CTRL_MANU
0199 BC (61)
0200 C. -----
0201 C. MDP_SP_CTRL_MODE = MANU [ ]
0202 C. -----
0203 C.
0204 . C. <Upload SP Observation Table>
0205 . S. RAM ram-287:MDP_OBS_S
0206 ( )
0207 C.
0208 . C. < Dump RAMID=MDP_OBS_S >
0209 +. DC 07-F0 MDP_DUMP_SPTBL
0210 BC (83 07 00 00 00 38 b8)
0211 C. -----
0212 C. MDP_OBS_S verify = OK/NG [ ]
0213 C. -----
0214 C.
0215 C. *****
0216 C. SOT TI command set
0217 C. *****
0218 C. Execute, after the success of TBL upload.
0219 +. TI 2011-02-05 09:55:18.0
0220 DC 07-F0 MDP_SOT_MODE_OBSV
0221 BC (40)
0222 . C. -----
0223 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0224 C. -----
0225 C.
0226 C.
0227 . C. ***** MDP `uãîñî»ö%ýñÉÂðñ¹ñèDCBC•x²è *****
0228 C. (%á°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÉ%¼ñ¼Â»Ûñ¹ñè)
0229 . S. DC-BC dcbc-402:DCBC
0230 (MDP_known_event)
0231 C.
0232 C.
0233 . C. ***** ÿDÿ¹•Ï Daily±¿íññÉ´Øñ¹ñèDCBC•x²è *****
0234 . S. DC-BC dcbc-153:DCBC
0235 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0236 C.
0237 C.
0238 . C. ;ãLOSÿÁÿ$ÿÃÿ~¼Â»Û;ã
0239 C.
0240 . C. ***** LOS *****
0241 C.

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Feb 05, 11 14:45

XRT\_OGLIST\_0754.chk

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\*\*\* OP Sequence for XRT \*\*\*

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2011/02/05 10:05:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 10:05:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/02/05 10:06:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 53 8d 00 00
2011/02/05 10:06:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/02/05 10:06:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/02/05 10:06:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/02/05 10:06:22.0 XRT_ARS_DIS_407_OG [0x197]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/02/05 10:09:24.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/02/05 10:09:26.0 XRT_QT_PROG_SET_448_OG [0x1c0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2011/02/05 10:09:28.0 XRT_FL_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2011/02/05 10:09:30.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/02/05 15:17:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 15:17:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/02/05 15:17:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/02/05 15:20:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/02/05 15:31:30.0 XRT_Custom_418_OG [0x1a2]
2011/02/05 15:32:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/02/05 15:36:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 15:36:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/02/05 15:36:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/02/05 15:39:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/02/05 15:40:30.0 XRT_Custom_418_OG [0x1a2]
2011/02/05 15:41:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/02/05 16:05:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 16:05:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/02/05 16:06:00.0 AOCs_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 00 00 00 00
2011/02/05 16:06:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/02/05 16:06:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/02/05 16:06:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/02/05 16:06:22.0 XRT_ARS_DIS_437_OG [0x1b5]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/02/05 16:08:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/02/05 16:08:56.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2011/02/05 16:08:58.0 XRT_FL_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2011/02/05 16:09:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/02/05 16:53:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 16:53:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/02/05 16:53:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/02/05 16:56:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/02/05 17:17:30.0 XRT_Custom_418_OG [0x1a2]
2011/02/05 17:18:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/02/05 17:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/02/05 17:59:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/02/05 18:00:00.0 AOCs_Ore-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/02/05 18:00:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2011/02/05 18:00:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2011/02/05 18:00:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/02/05 18:02:58.0 XRT_QT_PROG_SET_403_OG [0x193]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11

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2011/02/05	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/05	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/05	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/05	18:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/02/05	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/05	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/02/05	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/05	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/05	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/05	18:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/02/05	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/02/05	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/05	18:30:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/05	18:30:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/05	18:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/05	18:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/05	18:54:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/05	18:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/05	20:08:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/05	20:08:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/05	20:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/05	20:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/05	20:31:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/05	20:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/05	21:45:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/05	21:45:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/05	21:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/05	21:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/05	22:08:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/05	22:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/05	23:23:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/05	23:23:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/05	23:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/05	23:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/05	23:37:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/05	23:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	00:53:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	00:53:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	00:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	00:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	01:06:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/06	01:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	02:22:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	02:22:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	02:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	02:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	02:42:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/06	02:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	03:50:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	03:50:02.0	XRT_FLD_RESET_412_OG [0x19c]			



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2011/02/06	03:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/06	03:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	04:20:00.0	XRT_Custom_418_OG [0x1a2]								
2011/02/06	04:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	05:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	05:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	05:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/06	05:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	05:57:30.0	XRT_Custom_418_OG [0x1a2]								
2011/02/06	05:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	06:06:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	06:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/06	06:07:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/06	06:07:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/06	06:07:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/06	06:07:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/06	06:09:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/02/06	06:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	06:16:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	06:16:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/06	06:17:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00				
2011/02/06	06:17:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/06	06:17:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/06	06:17:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/06	06:17:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/06	06:19:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	06:19:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/02/06	06:19:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/02/06	06:20:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	07:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	07:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	07:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/06	07:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	07:34:30.0	XRT_Custom_418_OG [0x1a2]								
2011/02/06	07:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	08:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	08:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	08:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/06	08:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	09:11:00.0	XRT_Custom_418_OG [0x1a2]								
2011/02/06	09:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	10:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	10:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	10:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/06	10:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	10:40:00.0	XRT_Custom_418_OG [0x1a2]								
2011/02/06	10:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	14:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	14:20:32.0	XRT_FLD_RESET_412_OG [0x19c]								

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2011/02/06	14:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	14:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	14:27:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	14:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	15:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	15:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	15:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	15:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	16:17:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	16:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	17:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	17:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	17:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	17:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/02/06	17:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2011/02/06	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/02/06	17:55:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/02/06	17:55:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/06	17:58:28.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/02/06	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	18:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/06	18:05:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2011/02/06	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/06	18:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/02/06	18:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/06	18:05:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/06	18:08:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	18:08:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/02/06	18:08:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/02/06	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	19:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	19:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	19:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	19:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	19:30:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	19:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	20:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	20:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	20:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/06	20:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/06	21:08:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/06	21:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	22:22:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/06	22:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/06	22:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2011/02/06	22:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/06	22:43:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/06	22:44:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/06	23:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/06	23:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/06	23:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/07	00:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/07	00:08:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/07	00:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	01:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	01:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/07	01:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/07	01:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/07	01:42:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/07	01:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	02:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	02:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/07	02:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/07	03:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/07	03:19:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/07	03:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	04:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	04:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/07	04:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/07	04:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/07	04:56:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/07	04:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	05:40:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	05:40:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/07	05:40:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/07	05:40:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/07	05:40:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/07	05:40:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/07	05:43:28.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/02/07	05:43:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	05:50:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	05:50:26.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/07	05:50:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2011/02/07	05:50:46.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/02/07	05:50:48.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/07	05:50:50.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/07	05:50:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/07	06:05:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	07:50:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	07:50:26.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/07	07:50:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2011/02/07	07:50:46.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/02/07	07:50:48.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/07	07:50:50.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				

2011/02/07	07:50:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/07	08:05:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/07	09:50:24.0	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	09:50:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 53 1a				
2011/02/07	09:50:54.0	XRT_TCIB_XRT_S_HTR_A_ENA_427_OG [0x1ab]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/02/07	11:20:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/07	11:50:54.0	XRT_Custom_429_OG [0x1ad]							
2011/02/07	12:50:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/02/07	13:50:54.0	XRT_Custom_429_OG [0x1ad]							
2011/02/07	15:50:54.0	XRT_Custom_429_OG [0x1ad]							
2011/02/07	17:50:54.0	XRT_Custom_440_OG [0x1b8]							
2011/02/07	17:51:04.0	XRT_Custom_429_OG [0x1ad]							
2011/02/07	19:51:04.0	XRT_Custom_442_OG [0x1ba]							
2011/02/07	21:50:42.0	XRT_Custom_440_OG [0x1b8]							
2011/02/07	21:50:52.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/07	21:50:54.0	XRT_TCIB_XRT_S_HTR_A_DIS_420_OG [0x1a4]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2011/02/08	05:54:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	05:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/08	05:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/08	05:55:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/08	05:55:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/08	05:55:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	05:57:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/02/08	05:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	06:04:54.0	XRT_CTRL_MANU_430_OG [0x1ae]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	06:05:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2011/02/08	06:07:32.0	XRT_ROI_A_426_OG [0x1aa]							
		MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 20 20				
		MDP_XRT_ROI_SET	6	07-F0	cd 08 c0 40 10 10				
		MDP_XRT_ROI_SET	6	07-F0	cd 0c c0 c0 10 10				
		MDP_XRT_ROI_SET	6	07-F0	cd 0d 40 c0 10 10				
		MDP_XRT_ROI_SET	6	07-F0	cd 0e 40 40 10 10				
		MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06				
2011/02/08	06:07:32.5	XRT_ROI_B_431_OG [0x1af]							
		MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08				
2011/02/08	06:07:37.5	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/08	06:07:57.5	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/02/08	06:07:59.5	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/08	06:08:01.5	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/08	06:08:03.5	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	06:08:05.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	06:14:54.0	XRT_CTRL_MANU_430_OG [0x1ae]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	06:15:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2011/02/08	06:17:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/08	06:17:52.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2011/02/08	06:17:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/08	06:17:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/08	06:17:58.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	06:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	06:24:54.0	XRT_CTRL_MANU_430_OG [0x1ae]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	06:25:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 d1 07 d1 07				
2011/02/08	06:27:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/08	06:27:52.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				

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2011/02/08	06:27:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/08	06:27:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/08	06:27:58.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	06:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	06:34:54.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	06:35:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2011/02/08	06:37:27.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/08	06:37:47.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2011/02/08	06:37:49.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/08	06:37:51.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/08	06:37:53.0	XRT_ARS_DIS_447_OG [0x1bf]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	06:37:55.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	06:45:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/02/08	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	07:11:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/08	07:11:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/08	07:11:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/08	07:11:26.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/08	07:11:28.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/08	07:14:00.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/08	07:14:02.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/02/08	07:14:04.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/02/08	07:14:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	08:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	08:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/08	08:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/08	08:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/08	08:47:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/08	08:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/08	10:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/08	10:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				