

# XRT Timeline to be uploaded on 2011/02/15

Period: 2011/02/15 09:56:00 - 2011/02/19 09:08:00

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## Normal mode

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XOB #185A: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad												
Term	Pointing (x, y)				Comment							
02/15 10:09:00 - 02/15 17:45:54	Track ( 258.0, -232.6) <sup>Ⓢ 02/15 10:06:00</sup>				# OP start + 10min   Track AR 11158 (HOP180 10:10-14:36UT)							
02/15 17:59:00 - 02/16 05:59:54	Track ( 322.5, -234.9) <sup>Ⓢ 02/15 17:56:00</sup>				# Cont.							
02/16 06:13:00 - 02/16 18:03:54	Track ( 419.3, -239.6) <sup>Ⓢ 02/16 06:10:00</sup>				# Cont. (HOP180 10:10-14:36 UT)							
02/16 18:17:00 - 02/17 06:22:24	Track ( 509.2, -245.6) <sup>Ⓢ 02/16 18:14:00</sup>				# Cont.							
02/17 06:35:30 - 02/17 10:12:54	Track ( 593.9, -253.0) <sup>Ⓢ 02/17 06:32:30</sup>				# Cont.							
<b>PROG= 12 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 10 4-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 30 20-time(s) 2.0sec												
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec												
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec												
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec												
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16												
Term	Pointing (x, y)				Comment							
02/15 17:49:00 - 02/15 17:55:54	Fixed ( 0.0, 0.0)				synoptic, shifted -14.0 min							
02/16 06:03:00 - 02/16 06:09:54	Fixed ( 0.0, 0.0)				synoptic							
02/16 18:07:00 - 02/16 18:13:54	Fixed ( 0.0, 0.0)				synoptic, shifted 4.0 min							
02/17 06:25:30 - 02/17 06:32:24	Fixed ( 0.0, 0.0)				synoptic, shifted 22.5 min							
<b>PROG= 17 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 28 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

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## Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)				Comment							
02/15 10:09:00 - 02/15 17:45:54	Track ( 258.0, -232.6) <sup>Ⓢ 02/15 10:06:00</sup>				# OP start + 10min   Track AR 11158 (HOP180 10:10-14:36UT)							
02/15 17:59:00 - 02/16 05:59:54	Track ( 322.5, -234.9) <sup>Ⓢ 02/15 17:56:00</sup>				# Cont.							
02/16 06:13:00 - 02/16 18:03:54	Track ( 419.3, -239.6) <sup>Ⓢ 02/16 06:10:00</sup>				# Cont. (HOP180 10:10-14:36 UT)							
02/16 18:17:00 - 02/17 06:22:24	Track ( 509.2, -245.6) <sup>Ⓢ 02/16 18:14:00</sup>				# Cont.							
02/17 06:35:30 - 02/17 10:12:54	Track ( 593.9, -253.0) <sup>Ⓢ 02/17 06:32:30</sup>				# Cont.							
<b>PROG= 16 1-time(s)</b>												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 60 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												

	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 90</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 90</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4</b>	<b>24-time(s)</b>	<b>600.0sec</b>											
	<b>Seqn= 89</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

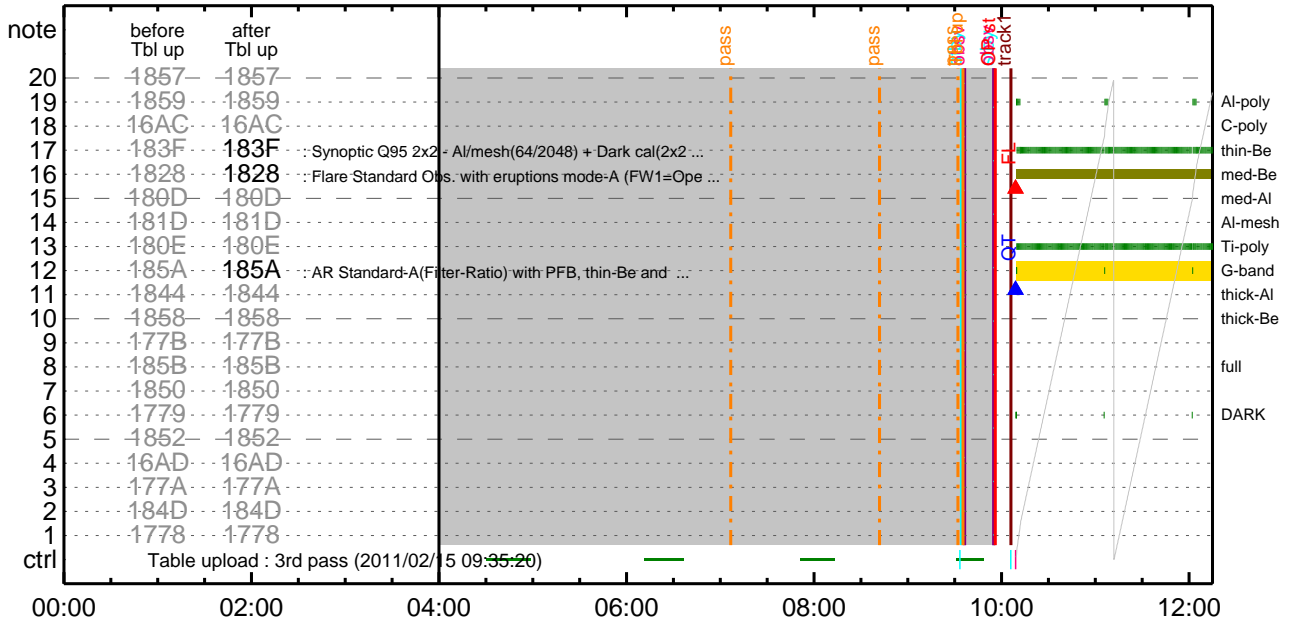
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### Flare Detection

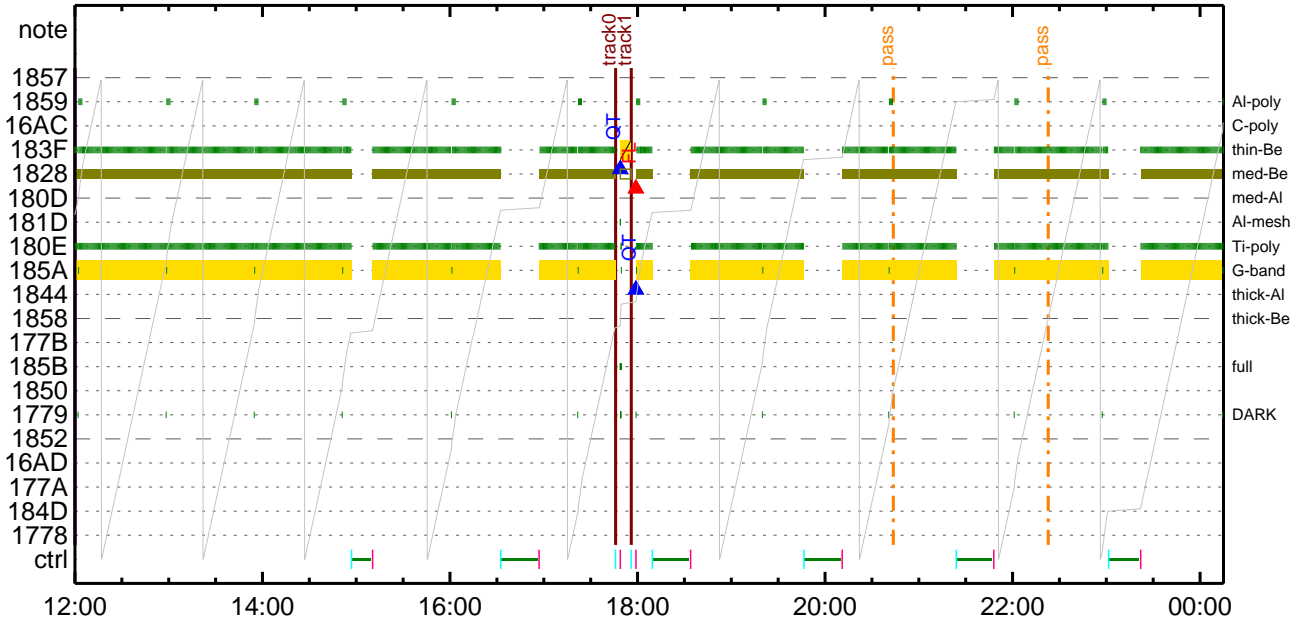
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FLD Patrol												
Term	Pointing (x, y)						Comment					
02/15 10:06:16 - 02/15 17:46:16	Track (	258.0,	-232.6)	@ 02/15 10:06:00	#	OP start + 10min	Track AR 11158 (HOP180 10:10-14:36UT)					
02/15 17:56:16 - 02/16 06:00:16	Track (	322.5,	-234.9)	@ 02/15 17:56:00	#	Cont.						
02/16 06:10:16 - 02/16 18:04:16	Track (	419.3,	-239.6)	@ 02/16 06:10:00	#	Cont. (HOP180 10:10-14:36 UT)						
02/16 18:14:16 - 02/17 06:22:46	Track (	509.2,	-245.6)	@ 02/16 18:14:00	#	Cont.						
02/17 06:32:46 - 02/19 09:08:00	Track (	593.9,	-253.0)	@ 02/17 06:32:30	#	Cont.						
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

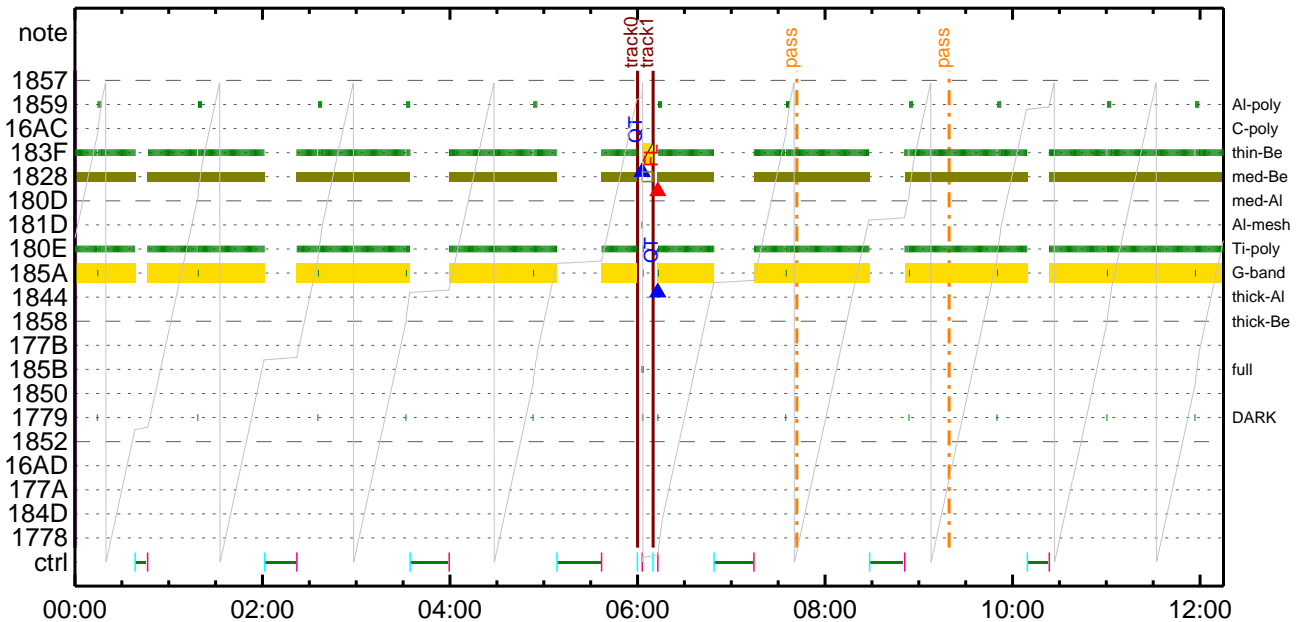
### CMDI #0776 2011/02/15



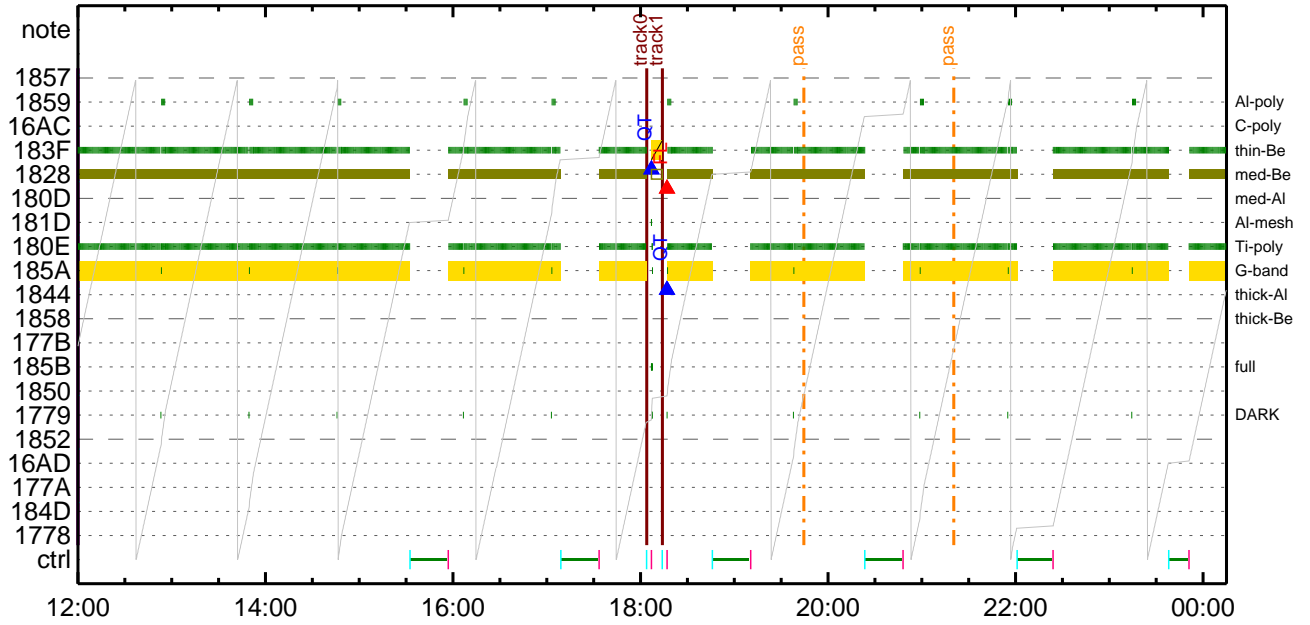
### CMDI #0776 2011/02/15



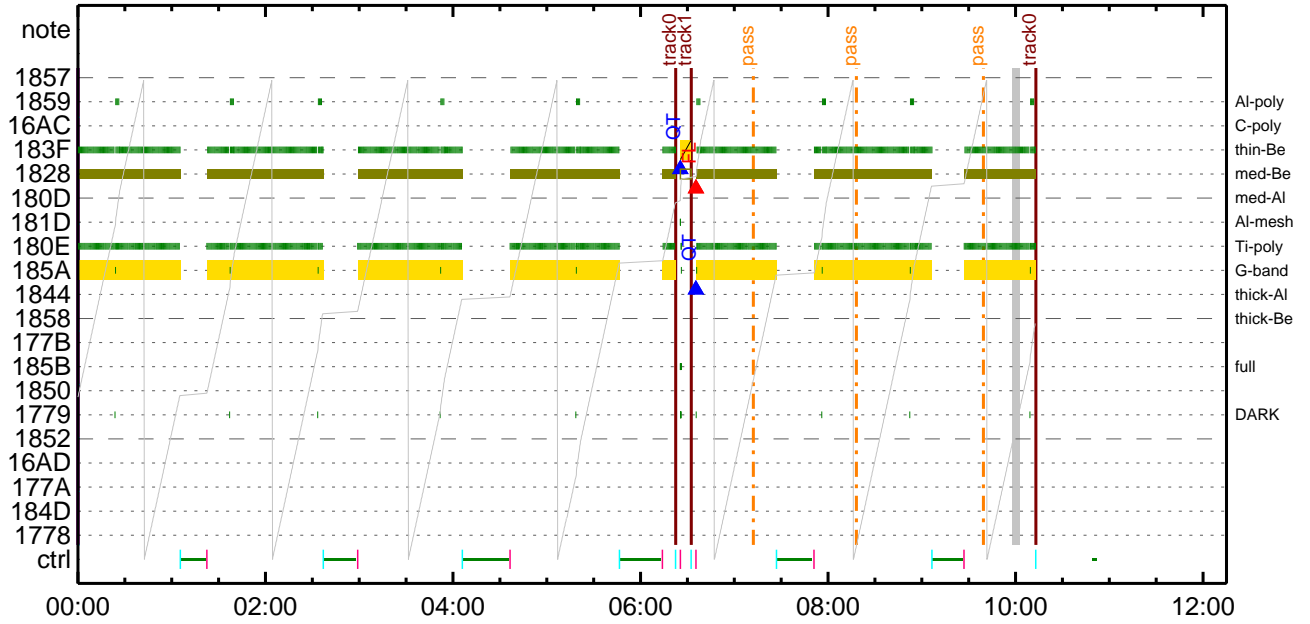
### CMDI #0776 2011/02/16



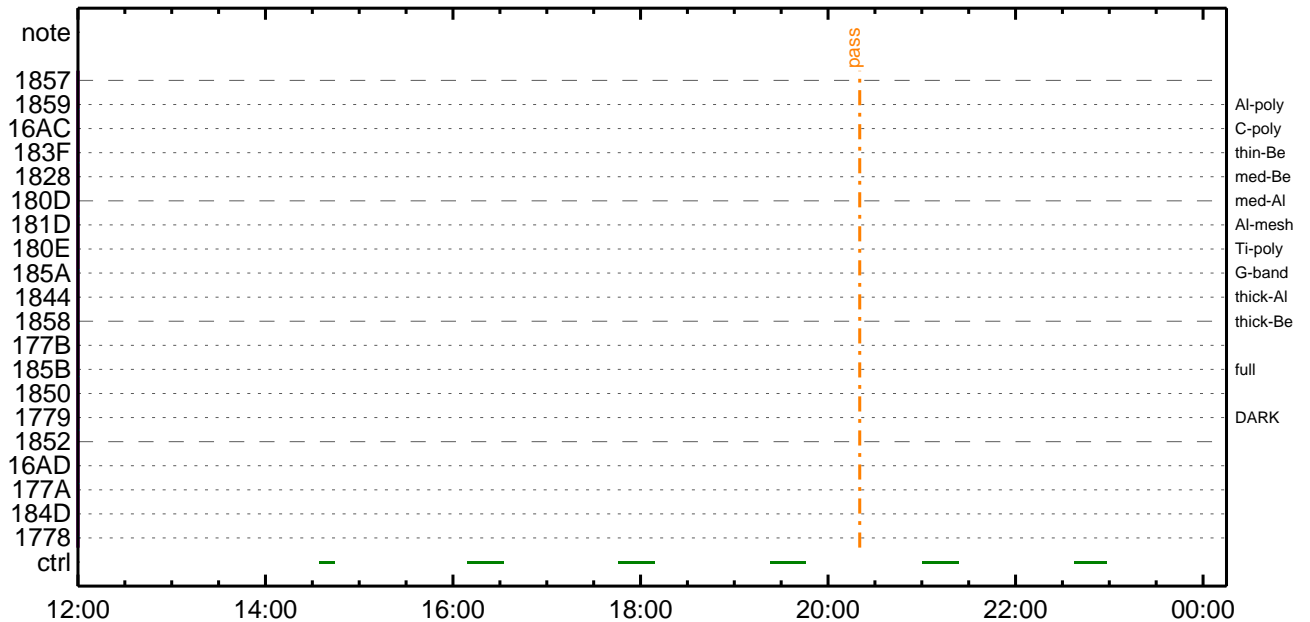
### CMDI #0776 2011/02/16



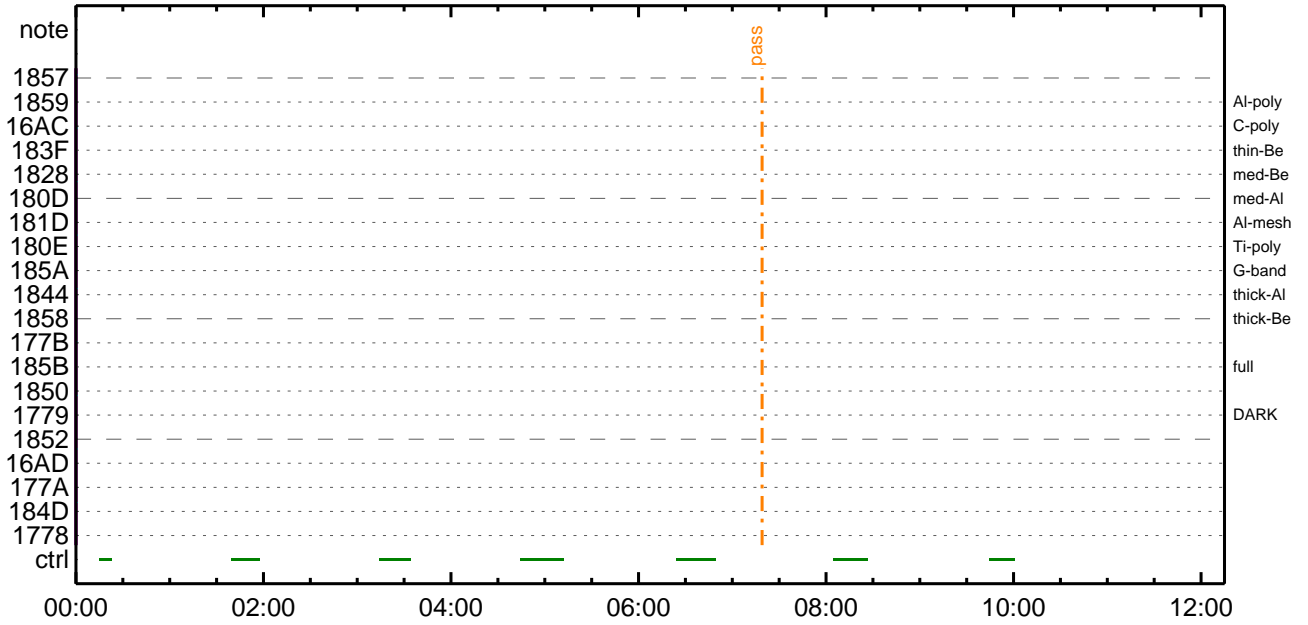
### CMDI #0776 2011/02/17



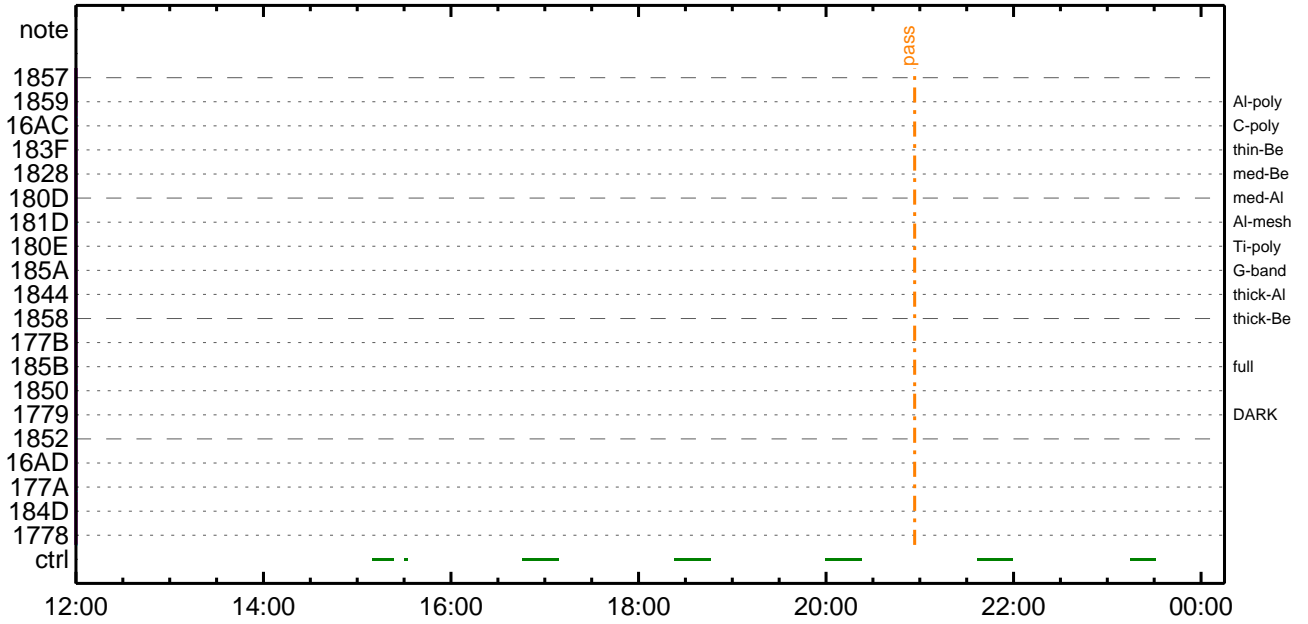
### CMDI #0776 2011/02/17



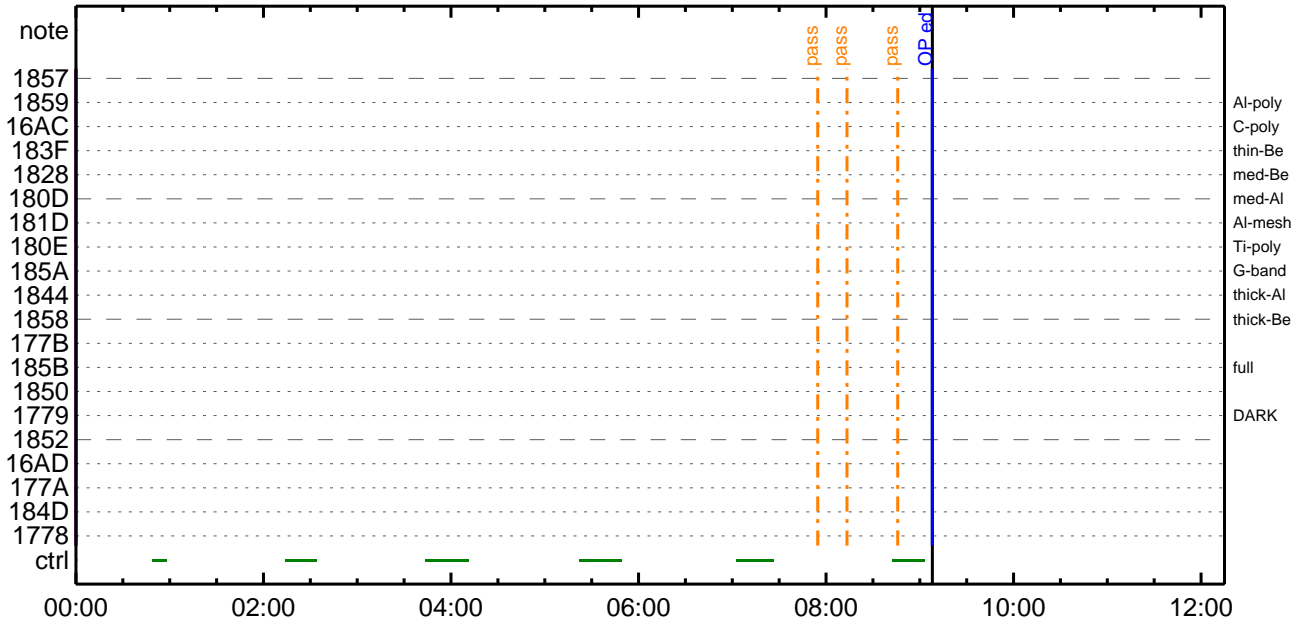
CMDI #0776 2011/02/18



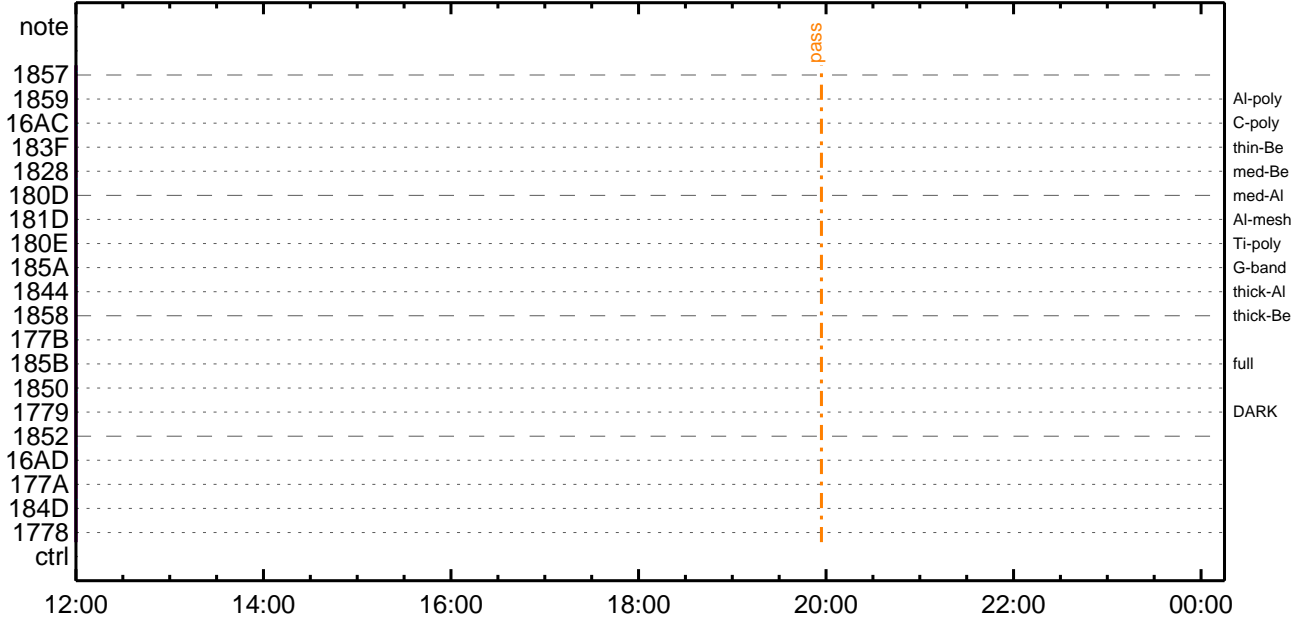
CMDI #0776 2011/02/18



CMDI #0776 2011/02/19



CMDI #0776 2011/02/19





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-896:OP
0104 ( )
0105 S. OG og-896:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½Ë¹ç•è² ÎOKò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½Ë¹ç•è² ÎOKò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½Ë¹ç•è² ÎOKò³ ÎÇ§
0165 C.
0166 C. ***** òË²¼ò Î½Ã´ ¶ÃºòËË-òºÃ÷¿® (½ãµ-YAYOXx½ª ê½çòðÃÓÃæç½ªªº òË½i¹çòçðª) *****
0167 C. DHUYã;½YE;Ë½Y½, Y;½YE;ËòðËã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Ã÷¿®NGUÎ½i¹ç;çºË²¼ò ÎTI-CMDÃ÷¿®ãî½Ë¹Ôªº òËòòò³ òË;f
0180 C. òËò¿;çSETòËDUMPAÎ±ºiYNY¹ç¹Ôª|ò³òË;f
0181 C.
0182 C. TIY³YpYóYEòðÃDË¿¿(UT)
0183 +. TI 2011-02-15 09:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-02-15 09:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-02-15 09:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0096 BC (cd 10 80 80 08 08)
0097 . C. ----- Success Verify ? OK / NG ____
0098 C.
0099 C.
0100 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0101 C.
0102 +. DC 07-F0 MDP_XRT_MODE_OBSV
0103 BC (c2)
0104 +. TI 2011-02-15 09:55:02.0
0105 DC 07-F0 MDP_XRT_MODE_OBSV
0106 BC (c2)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. ***** XRT END *****
0110 C.
0111 . C. ***** MDP `úÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0112 C. (¼á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñ¼ñ¼¼ñ¼»Ûñ¹ñè)
0113 . S. DC-BC dcbc-402:DCBC
0114 (MDP_known_event)
0115 C.
0116 C.
0117 . C. ***** ÿDÿ¹•Ï Daily±¿îññÊ´øñ¹ñèDCBC•x²è *****
0118 . S. DC-BC dcbc-153:DCBC
0119 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0120 C.
0121 C.
0122 . C. ¡ãLOSÿÁÿSÿÃÿ-¼Â»Û¡ä
0123 C.
0124 . C. ***** LOS *****
0125 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2011/02/15	10:05:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	10:05:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/02/15	10:06:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2011/02/15	10:06:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/02/15	10:06:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/02/15	10:06:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/02/15	10:06:22.0	XRT_ARS_DIS_437_OG [0x1b5]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/15	10:08:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/15	10:08:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2011/02/15	10:08:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2011/02/15	10:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/15	14:57:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	14:57:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/15	14:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/15	15:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/15	15:09:30.0	XRT_Custom_418_OG [0x1a2]						
2011/02/15	15:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/15	16:32:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	16:32:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/15	16:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/15	16:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/15	16:56:00.0	XRT_Custom_418_OG [0x1a2]						
2011/02/15	16:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/15	17:45:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	17:45:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/02/15	17:46:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2011/02/15	17:46:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/02/15	17:46:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/02/15	17:46:20.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/15	17:48:58.0	XRT_QT_PROG_SET_403_OG [0x193]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2011/02/15	17:49:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/15	17:55:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	17:55:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/02/15	17:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2011/02/15	17:56:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/02/15	17:56:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/02/15	17:56:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/02/15	17:56:22.0	XRT_ARS_DIS_437_OG [0x1b5]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/15	17:58:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/15	17:58:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2011/02/15	17:58:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2011/02/15	17:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/15	18:09:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/15	18:09:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/15	18:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/15	18:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/15	18:33:00.5	XRT_Custom_418_OG [0x1a2]						

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2011/02/15	18:34:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/15	19:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/15	19:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/15	19:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/15	19:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/15	20:10:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/15	20:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/15	21:24:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/15	21:24:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/15	21:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/15	21:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/15	21:47:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/15	21:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/15	23:01:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/15	23:01:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/15	23:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/15	23:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/15	23:21:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/15	23:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	00:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	00:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	00:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	00:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	00:45:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	00:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	02:01:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	02:01:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	02:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	02:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	02:21:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	02:22:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	03:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	03:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	03:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	03:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	03:58:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	03:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	05:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	05:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	05:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	05:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	05:36:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	05:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/16	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/16	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/16	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/16	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/16	06:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				

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2011/02/16	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/16	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/02/16	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/16	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/16	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/16	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/16	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	06:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/02/16	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/02/16	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	06:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	06:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	06:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	06:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	07:13:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	07:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	08:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	08:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	08:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	08:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	08:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	08:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	10:09:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	10:09:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	10:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	10:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	10:22:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	10:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	15:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	15:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	15:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	15:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	15:56:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	15:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	17:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	17:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/16	17:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/16	17:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/16	17:32:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/16	17:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/16	18:03:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/16	18:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/16	18:04:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/16	18:04:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/16	18:04:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/16	18:04:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/16	18:06:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				



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2011/02/16	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/16	18:13:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/16	18:13:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/16	18:14:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/02/16	18:14:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/16	18:14:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/02/16	18:14:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/16	18:14:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/16	18:16:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/16	18:16:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/02/16	18:16:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/02/16	18:17:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/16	18:46:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/16	18:46:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/16	18:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/16	18:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/16	19:09:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/16	19:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/16	20:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/16	20:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/16	20:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/16	20:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/16	20:47:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/16	20:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/16	22:01:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/16	22:01:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/16	22:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/16	22:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/16	22:23:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/16	22:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/16	23:38:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/16	23:38:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/16	23:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/16	23:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/16	23:50:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/16	23:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/17	01:05:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/17	01:05:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/17	01:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/17	01:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/17	01:21:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/17	01:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/17	02:37:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/17	02:37:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/17	02:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/17	02:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/17	02:58:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/17	02:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/17	04:06:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/17	04:06:02.0	XRT_FLD_RESET_412_OG [0x19c]			

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2011/02/17	04:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/17	04:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/17	04:35:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/17	04:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	05:46:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	05:46:32.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/17	05:46:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/17	05:49:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/17	06:13:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	06:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	06:22:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	06:22:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/02/17	06:22:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/02/17	06:22:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/02/17	06:22:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/02/17	06:22:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/17	06:25:28.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2011/02/17	06:25:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	06:32:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	06:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/02/17	06:32:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2011/02/17	06:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/02/17	06:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/02/17	06:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/02/17	06:32:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/17	06:35:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/17	06:35:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2011/02/17	06:35:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2011/02/17	06:35:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	07:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	07:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/17	07:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/17	07:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/17	07:50:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	07:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	09:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	09:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/17	09:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/17	09:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/17	09:26:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/17	09:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	10:12:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/17	10:13:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			