

XRT Timeline to be uploaded on 2011/02/19

Period: 2011/02/19 09:08:00 - 2011/02/23 09:25:00

* * * * *

Normal mode

* * * * *

XOB #185A: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad																								
Term	Pointing (x, y)				Comment																			
02/19 09:21:00 - 02/19 18:01:24	Track (157.4, 409.1)	[Ⓢ] 02/19 09:18:00	# OP start + 10min, Observe new flaring AR 11162.																					
02/19 18:14:30 - 02/20 05:32:24	Track (234.2, 407.2)	[Ⓢ] 02/19 18:11:30	# Continue tracking AR 11162.																					
02/20 05:45:30 - 02/20 17:59:00	Track (331.1, 403.5)	[Ⓢ] 02/20 05:42:30	# AR 11162.																					
02/20 18:35:00 - 02/21 05:53:54	Track (434.1, 397.9)	[Ⓢ] 02/20 18:32:00	# cont. AR obs.																					
02/21 06:11:00 - 02/21 10:31:54	Track (521.1, 391.5)	[Ⓢ] 02/21 06:04:00	# cont. AR obs.																					
PROG= 12 Inf-time(s)																								
└─ Subr= 1 1-time(s) 2.0sec																								
└─ Seqn= 19 1-time(s) 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec																								
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec																								
└─ Seqn= 10 4-time(s) 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																								
└─ Al-poly/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																								
└─ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																								
└─ Subr= 2 1-time(s) 2.0sec																								
└─ Seqn= 30 20-time(s) 2.0sec																								
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec																								
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec																								
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec																								
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec																								
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec																								
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec																								
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec																								
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 10%;">Default Filter</td> <td style="width: 10%;">Thicker Filter</td> <td style="width: 10%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 10%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table>													Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36																								
Term	Pointing (x, y)				Comment																			
02/19 18:04:30 - 02/19 18:11:24	Fixed (0.0, 0.0)		synoptic, shifted 1.5 min																					
02/20 05:35:30 - 02/20 05:42:24	Fixed (0.0, 0.0)		synoptic, shifted -27.5 min																					
02/20 18:25:00 - 02/20 18:31:54	Fixed (0.0, 0.0)		synoptic, shifted 22.0 min																					
02/21 05:57:00 - 02/21 06:03:54	Fixed (0.0, 0.0)		synoptic, shifted -6.0 min																					
PROG= 13 1-time(s)																								
└─ Subr= 1 1-time(s) 12.0sec																								
└─ Seqn= 7 1-time(s) 4.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 5 1-time(s) 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec																								
└─ Seqn= 8 1-time(s) 4.0sec																								
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 32 1-time(s) 2.0sec																								
└─ thin-Be/Open thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 4 1-time(s) 2.0sec																								
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 10%;">Default Filter</td> <td style="width: 10%;">Thicker Filter</td> <td style="width: 10%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 10%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table>													Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

* * * * *

Flare mode

* * * * *

XOB #185D: Flare Standard Obs. with eruptions mode-A (FW1=Open) + Thin-Be												
Term	Pointing (x, y)				Comment							
02/19 09:21:00 - 02/19 18:01:24	Track (157.4, 409.1)	[Ⓢ] 02/19 09:18:00	# OP start + 10min, Observe new flaring AR 11162.									
02/19 18:14:30 - 02/20 05:32:24	Track (234.2, 407.2)	[Ⓢ] 02/19 18:11:30	# Continue tracking AR 11162.									
02/20 05:45:30 - 02/20 17:59:00	Track (331.1, 403.5)	[Ⓢ] 02/20 05:42:30	# AR 11162.									
02/20 18:35:00 - 02/21 05:53:54	Track (434.1, 397.9)	[Ⓢ] 02/20 18:32:00	# cont. AR obs.									
02/21 06:11:00 - 02/21 10:31:54	Track (521.1, 391.5)	[Ⓢ] 02/21 06:04:00	# cont. AR obs.									
PROG= 20 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 25 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn= 60 1-time(s) 2.0sec												

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

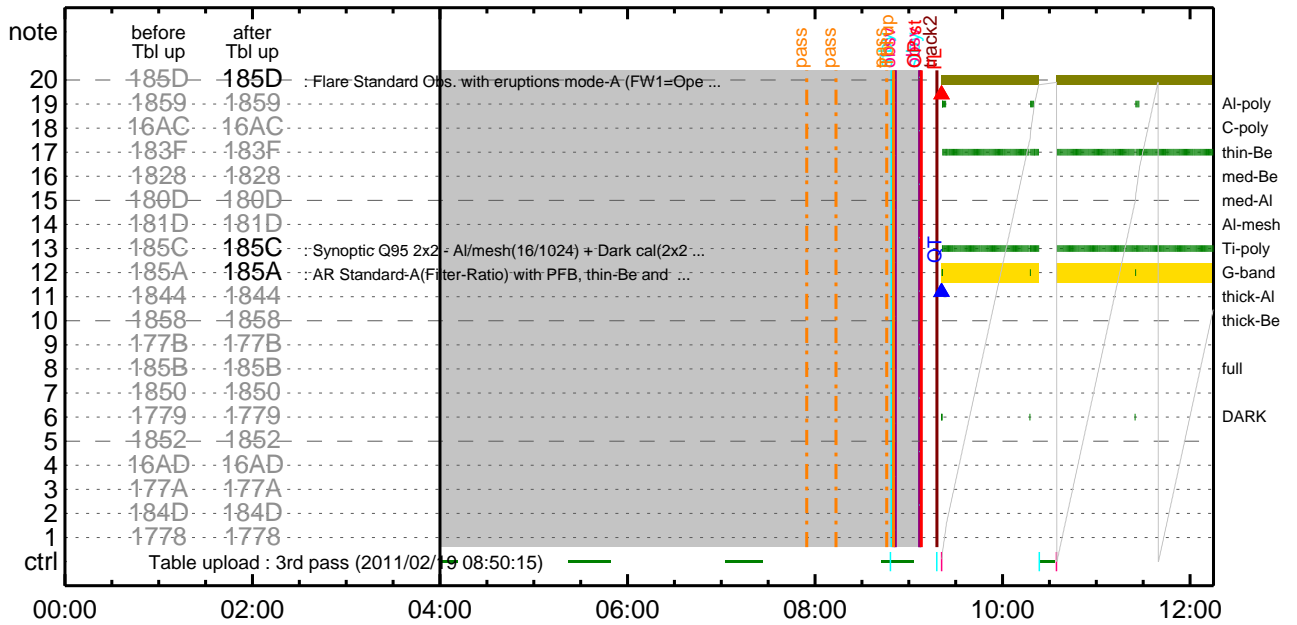
* * * * *

Flare Detection

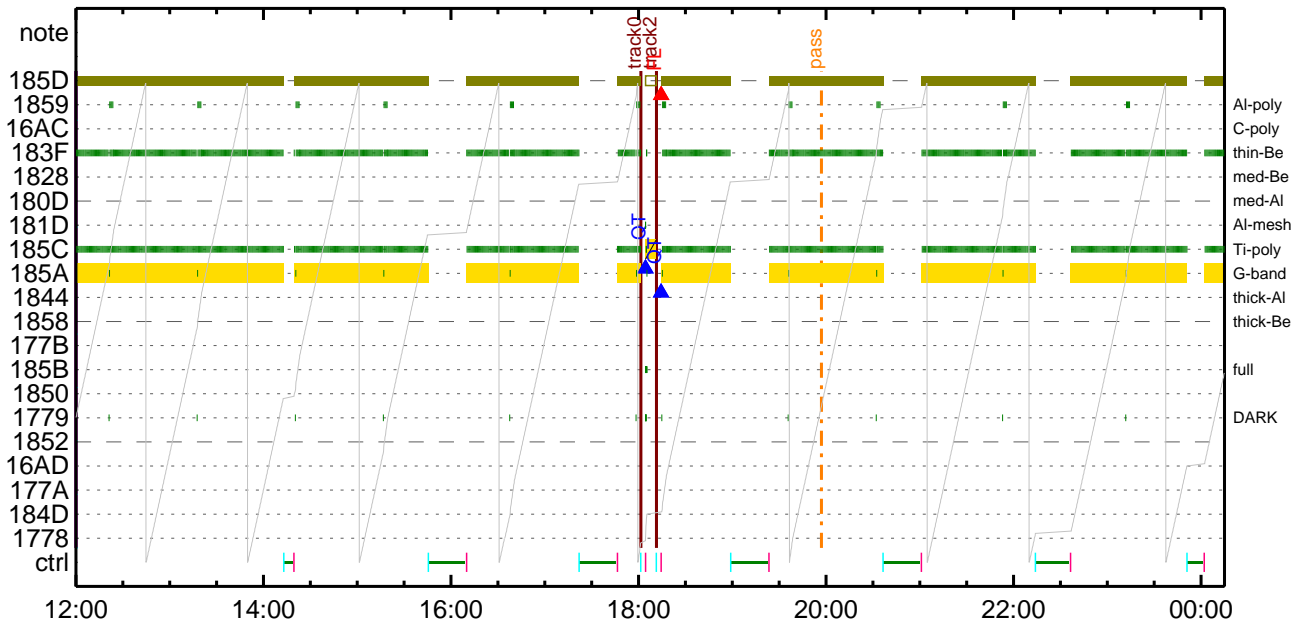
* * * * *

FLD Patrol												
Term	Pointing (x, y)								Comment			
02/19 09:18:16 - 02/19 18:01:46	Track (157.4,	409.1)	@ 02/19 09:18:00	# OP start + 10min, Observe new flaring AR 11162.							
02/19 18:11:46 - 02/20 05:32:46	Track (234.2,	407.2)	@ 02/19 18:11:30	# Continue tracking AR 11162.							
02/20 05:42:46 - 02/20 18:22:16	Track (331.1,	403.5)	@ 02/20 05:42:30	# AR 11162.							
02/20 18:32:16 - 02/21 05:54:16	Track (434.1,	397.9)	@ 02/20 18:32:00	# cont. AR obs.							
02/21 06:08:16 - 02/23 09:25:00	Track (521.1,	391.5)	@ 02/21 06:04:00	# cont. AR obs.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

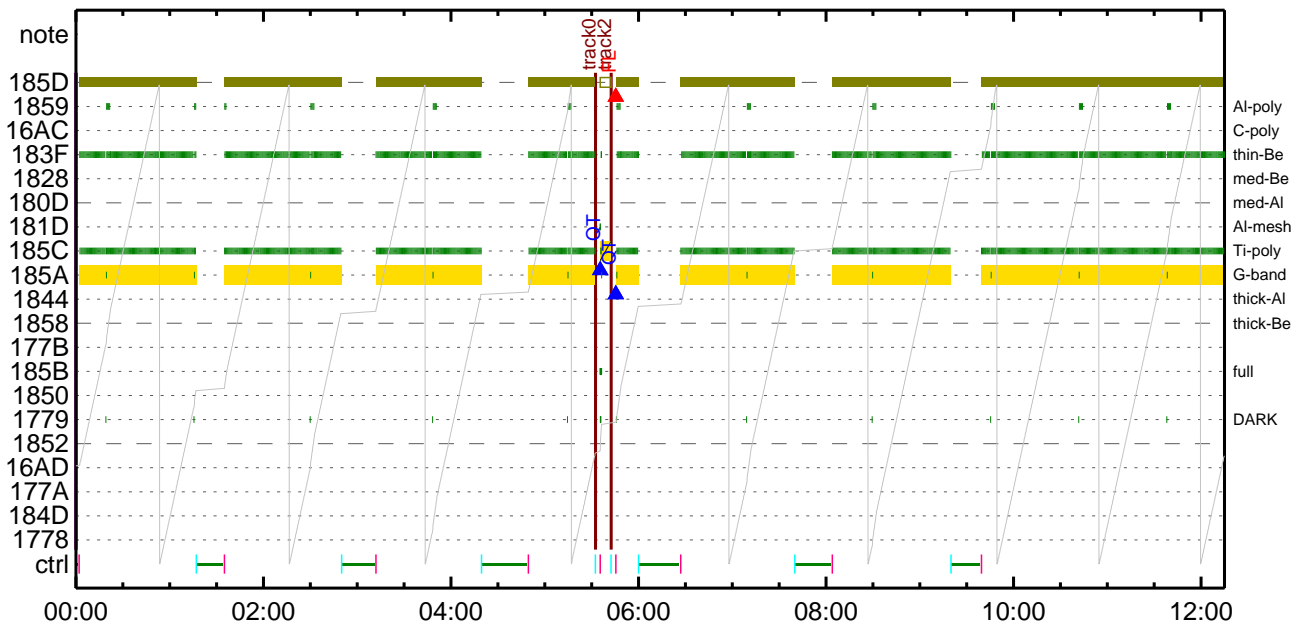
CMDI #0785 2011/02/19



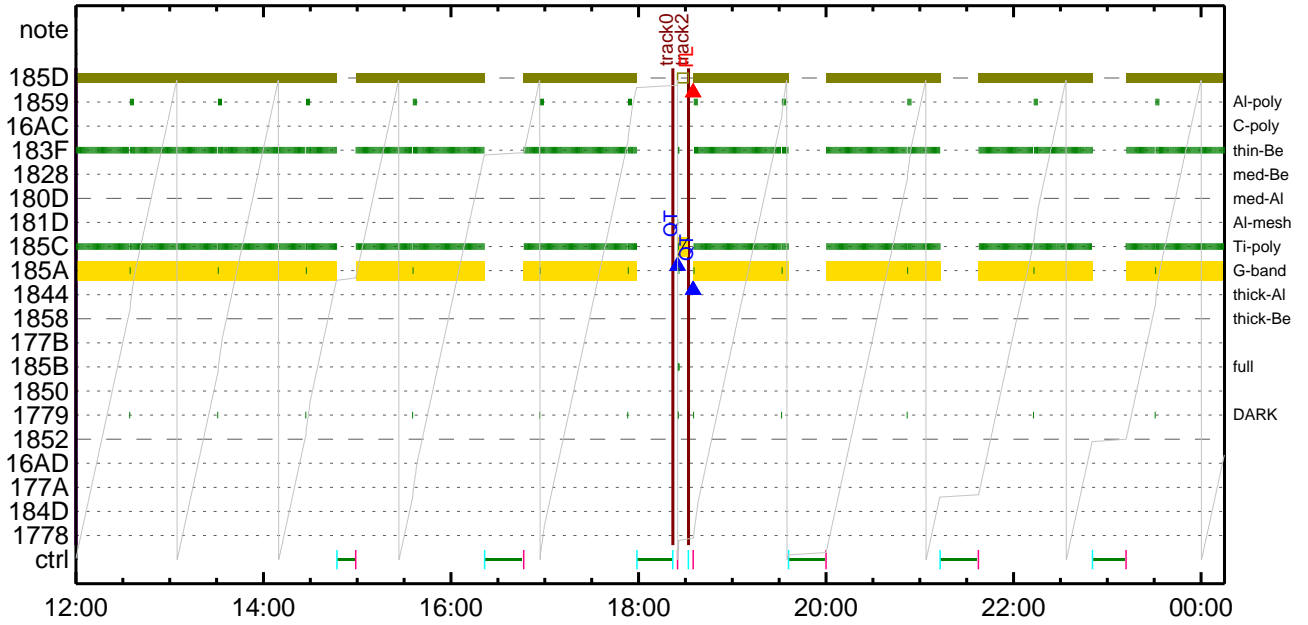
CMDI #0785 2011/02/19



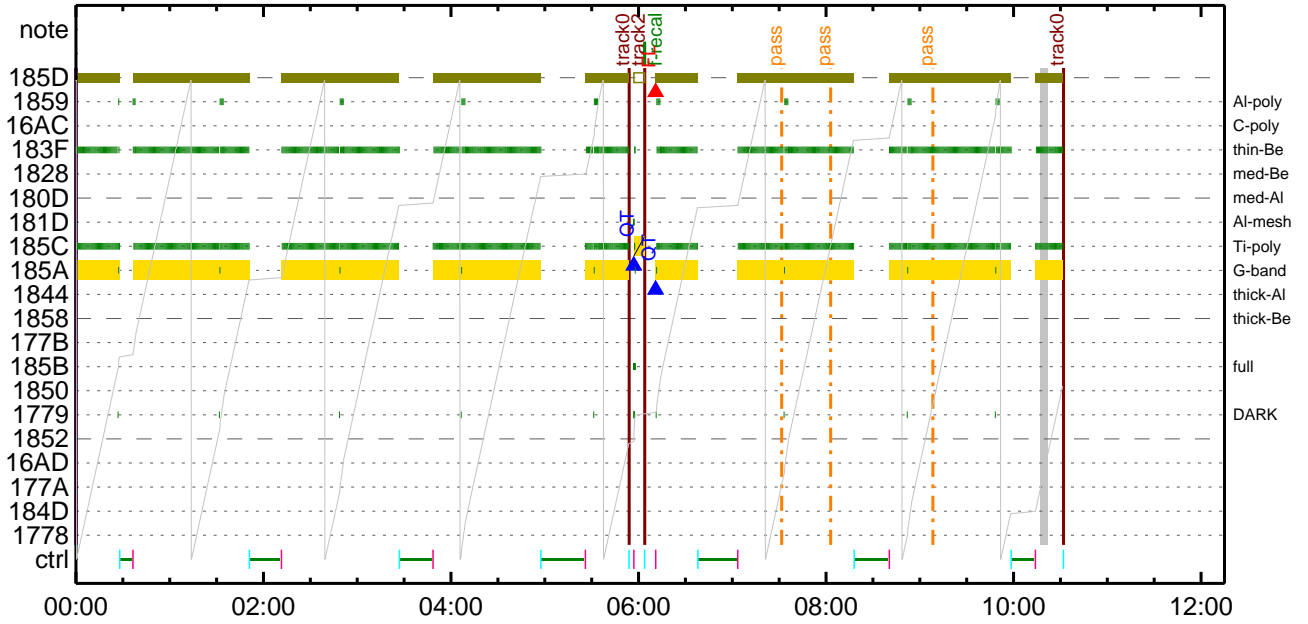
CMDI #0785 2011/02/20



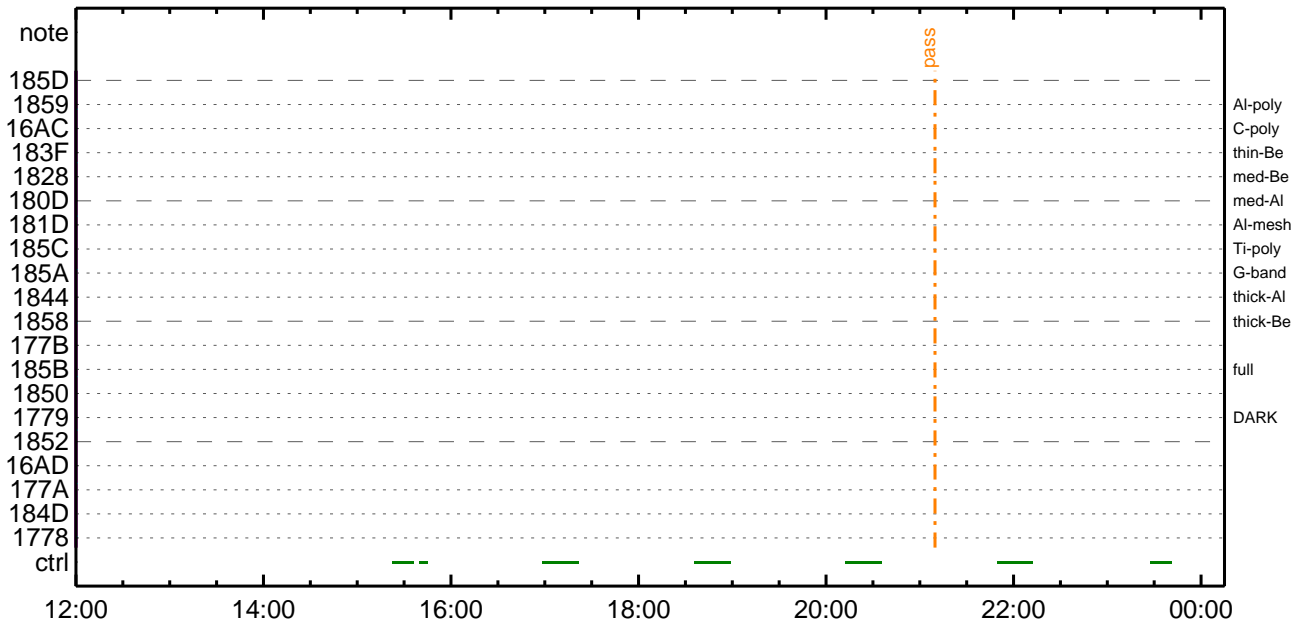
CMDI #0785 2011/02/20



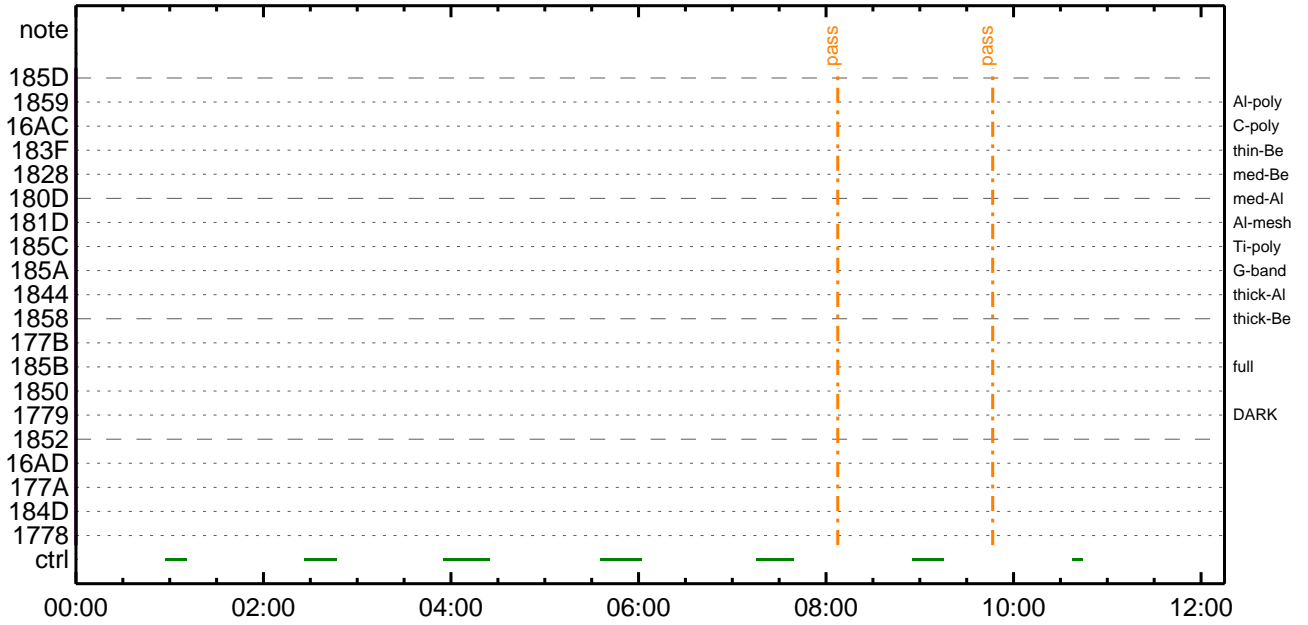
CMDI #0785 2011/02/21



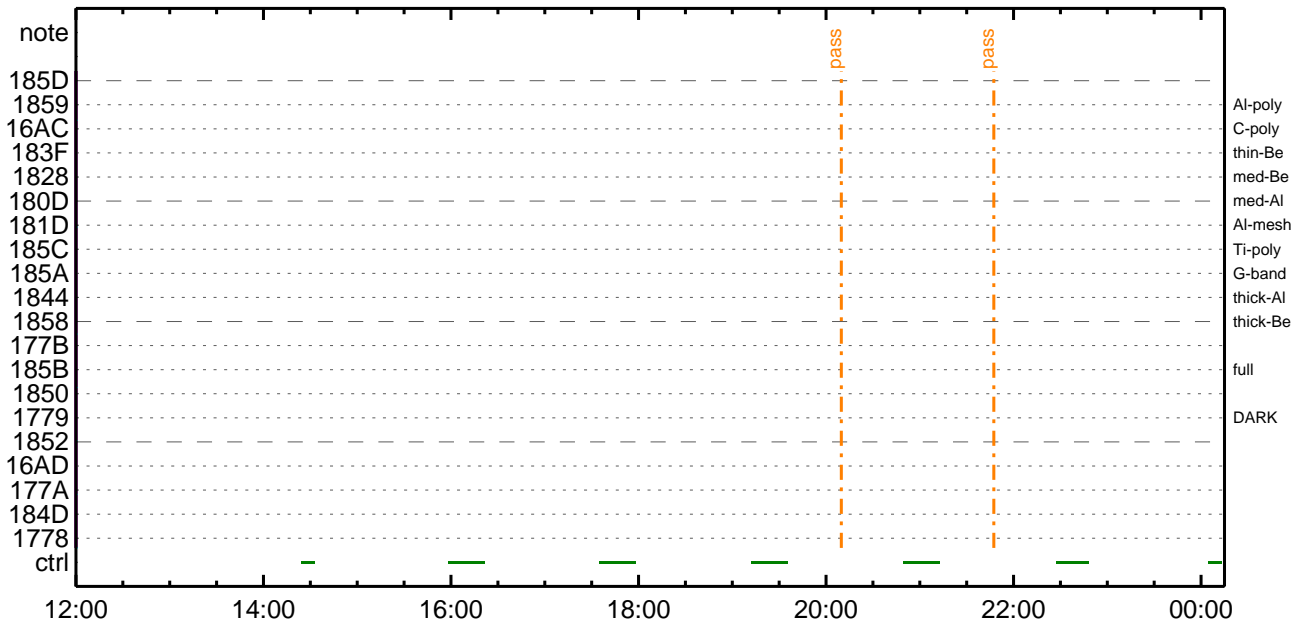
CMDI #0785 2011/02/21



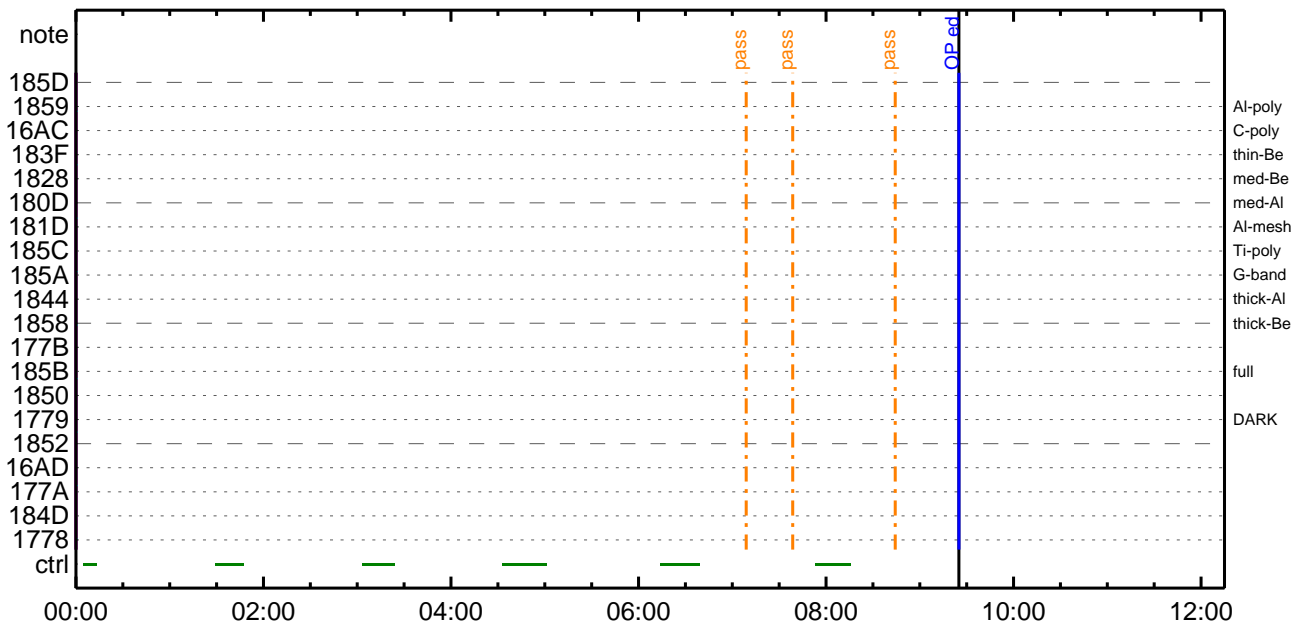
CMDI #0785 2011/02/22



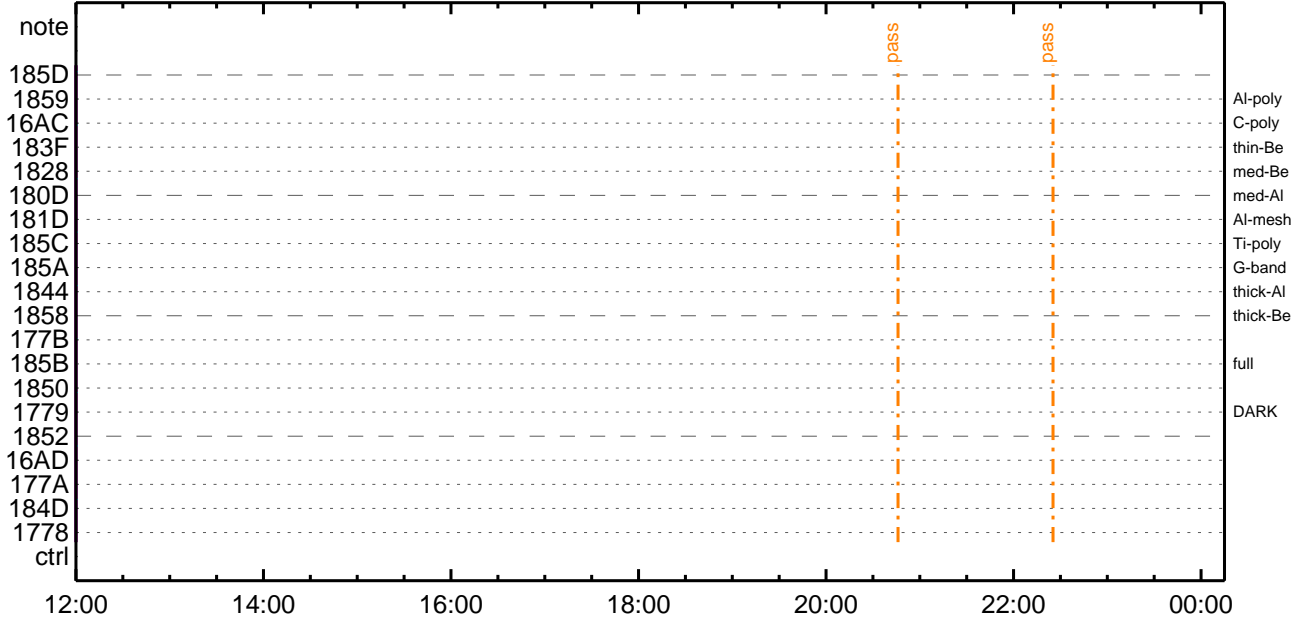
CMDI #0785 2011/02/22



CMDI #0785 2011/02/23



CMDI #0785 2011/02/23




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-905:OP
0104 ( )
0105 S. OG og-905:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½ª¹ç•è² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½ª¹ç•è² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½ª¹ç•è² îOKò³ îÇ§
0165 C.
0166 C. ***** òê²¼ò î½ª¹ ç¼ª òê²¼ò òª÷¿@ (%ãµ-YAYOXx½ª¹ç•è²çòðÀÕæòÇªª"òè¼i¹çòçòâ) *****
0167 C. DHUYã;4YE;ê½ª¹, Y;½YE;êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Á÷¿@NGUî½ª¹ç;ç°ê²¼ò îTI-CMDÁ÷¿@î½ª¹Òª.òêòòò³òè;f
0180 C. òèò¿;çSETòEDUMPAî½ª¹ç°î½ª¹ç¹Òª|ò³òè;f
0181 C.
0182 C. TIY³YpYóYêòðÀDî¿¿(UT)
0183 +. TI 2011-02-19 09:03:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-02-19 09:03:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-02-19 09:03:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


0194 C.
0195 +. TI 2011-02-19 09:07:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °È²¼íîÄë%îíñîîí¥Á¥§¥Á¥-¹àïÛ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TIíîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ¥Á¥Ö¥×½ªî»îð³îÇ§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLîîî¼è¹çªè²îOKîð³îÇ§
0229 C.
0230 . C. DHU¥à;¼¥É;è¼¥¼. ¥ì;¼¥È;Èîðîá^ñ
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-02-19 09:07:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-02-19 09:07:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2011-02-19 09:07:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-02-19 09:07:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´úÃîáî»ö¼ÝîèÉÀð¹îèDCBC·x²è *****
0276 C. (¼á°î¥Ö¥Á¥È¥¥¥¥¥¥¥ç¥èè¼¼î¼Á»Û¹îè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ¥Ð¥¹·Ï Daily±çîñîè'Ò¹îèDCBC·x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-906 2011-02-19 14:37:59 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁµ°Æ»Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÔµ•µ¿¼l¹çµÍ;çÀ®, ùµ¹µèµµµÇÁ+¿®µ•µÉµµµ³µÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 7s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2011-02-19 09:07:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîîî»ó¼YµÉÁµ¹µèDCBC•x²è *****
0069 C. (¼á°íYÓYÁYÉYBpYÉYáYçYèµÉ%¼µ¼Á»Ûµ¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDY¹.İ Daily±¿ÍÑµÉ`Øµ¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 +. DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 +. TI 2011-02-19 09:07:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP 'uãîaî»ö%ÿaëâa¹aëDCBC•x²è *****
0125 C. (%ã°îÿÓÿÃÿÈÿPÿËÿãÿçÿëaE%¼aa¼Ã»Ûa¹aè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹·ÿÿ Daily±;îÑaë'Øa¹aëDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿÿÃÿÿ¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
```

Feb 19, 11 14:38

XRT_OGLIST_0785.chk

Page 1/6

*** OP Sequence for XRT ***

2011/02/19	09:17:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	09:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/19	09:18:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/02/19	09:18:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/19	09:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/02/19	09:18:20.5	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/19	09:18:22.5	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/19	09:20:54.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	09:20:56.5	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/02/19	09:20:58.5	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14
2011/02/19	09:21:00.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	10:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	10:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	10:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	10:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	10:33:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/19	10:34:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	14:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	14:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	14:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	14:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	14:18:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/19	14:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	15:45:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	15:45:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	15:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	15:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	16:09:00.0	XRT_Custom_418_OG [0x1a2]			
2011/02/19	16:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	17:22:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	17:22:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	17:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	17:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	17:45:30.0	XRT_Custom_418_OG [0x1a2]			
2011/02/19	17:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	18:01:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	18:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/02/19	18:01:30.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/02/19	18:01:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/02/19	18:01:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/02/19	18:01:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/19	18:04:28.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/02/19	18:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	18:11:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	18:11:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/19	18:11:30.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/02/19	18:11:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/19	18:11:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			

Feb 19, 11 14:38

XRT_OGLIST_0785.chk

Page 2/6

2011/02/19	18:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/19	18:11:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/19	18:14:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	18:14:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/02/19	18:14:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 14
2011/02/19	18:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	18:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	18:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	18:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	19:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	19:22:30.0	XRT_Custom_418_OG [0x1a2]				
2011/02/19	19:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	20:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	20:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	20:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	20:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	21:00:00.0	XRT_Custom_418_OG [0x1a2]				
2011/02/19	21:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	22:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	22:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	22:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	22:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/19	22:35:30.5	XRT_Custom_418_OG [0x1a2]				
2011/02/19	22:36:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/19	23:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/19	23:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/19	23:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/19	23:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	00:01:01.0	XRT_Custom_418_OG [0x1a2]				
2011/02/20	00:02:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	01:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	01:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	01:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	01:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	01:34:00.0	XRT_Custom_418_OG [0x1a2]				
2011/02/20	01:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	02:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	02:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	02:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	02:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	03:11:00.0	XRT_Custom_418_OG [0x1a2]				
2011/02/20	03:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	04:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	04:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	04:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	04:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	04:48:30.0	XRT_Custom_418_OG [0x1a2]				
2011/02/20	04:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	05:32:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	05:32:26.0	XRT_FOCUS_POSITION_401_OG [0x191]				

Feb 19, 11 14:38

XRT_OGLIST_0785.chk

Page 3/6

2011/02/20	05:32:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
			AOCU_NM	5	02-76	00	00	00	00
2011/02/20	05:32:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/02/20	05:32:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/02/20	05:32:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/20	05:35:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/02/20	05:35:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	05:42:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	05:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/02/20	05:42:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00	00
2011/02/20	05:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/02/20	05:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/02/20	05:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/02/20	05:42:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/02/20	05:45:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	05:45:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2011/02/20	05:45:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	14		
2011/02/20	05:45:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	06:00:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	06:00:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	06:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/20	06:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/20	06:26:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/20	06:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	07:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	07:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	07:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/20	07:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/20	08:03:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/20	08:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	09:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	09:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	09:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/20	09:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/20	09:38:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/20	09:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	14:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	14:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	14:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/20	14:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/20	14:58:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/20	14:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	16:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	16:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/02/20	16:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/20	16:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/20	16:45:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/20	16:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/20	17:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/20	17:59:02.0	XRT_FLD_RESET_412_OG [0x19c]							

2011/02/20	17:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	18:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	18:21:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	18:21:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	18:22:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/02/20	18:22:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2011/02/20	18:22:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/02/20	18:22:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/02/20	18:24:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/20	18:25:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/02/20	18:31:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	18:31:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	18:32:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/02/20	18:32:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2011/02/20	18:32:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/02/20	18:32:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/02/20	18:32:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/02/20	18:34:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/02/20	18:34:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	18:34:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/02/20	18:35:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 14
2011/02/20	19:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	19:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	19:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	19:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	19:59:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	20:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/02/20	21:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/02/20	21:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	21:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	21:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	21:36:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/20	21:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/20	22:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2011/02/20	22:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/02/20	22:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/20	22:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/20	23:11:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/20	23:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/21	00:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/21	00:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_Custom_418_OG [0x1a2]			
2011/02/21	00:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/02/21	00:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/21	00:35:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/21	00:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/21	01:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/02/21	01:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/02/21	01:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_Custom_418_OG [0x1a2]			
2011/02/21			XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/02/21			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/02/21			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/02/21			MDP_XRT_FLD_RESET	1	07-F0	da
2011/02/21			MDP_XRT_PREFLR_STRT	1	07-F0	e8

Feb 19, 11 14:38

XRT_OGLIST_0785.chk

Page 5/6

2011/02/21	01:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/21	02:10:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	02:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	03:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	03:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/21	03:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/21	03:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/21	03:47:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	03:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	04:57:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	04:57:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/21	04:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/21	05:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/21	05:25:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	05:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	05:53:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	05:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/02/21	05:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/02/21	05:54:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/02/21	05:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/02/21	05:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/21	05:56:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/02/21	05:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	06:03:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	06:03:56.0	XRT_FOCUS_RECALIBRATE_432_OG [0x1b0]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/02/21	06:04:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/02/21	06:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/02/21	06:08:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/02/21	06:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/02/21	06:08:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/02/21	06:08:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/02/21	06:10:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/21	06:10:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/02/21	06:10:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2011/02/21	06:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	06:38:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	06:38:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/21	06:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/21	06:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/21	07:02:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	07:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	08:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	08:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/02/21	08:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/02/21	08:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/02/21	08:39:30.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	08:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/02/21	09:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/02/21	09:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

Feb 19, 11 14:38

XRT_OGLIST_0785.chk

Page 6/6

2011/02/21	09:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/02/21	10:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/02/21	10:13:00.0	XRT_Custom_418_OG [0x1a2]							
2011/02/21	10:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/02/21	10:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/02/21	10:32:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			