XRT Timeline to be uploaded on 2011/02/24

Period: 2011/02/24 10:03:00 - 2011/03/01 10:04:00

* * * * * **Normal mode** * * * * *

XOB #185B: AR (Filter-Ratio), thin-Be context, 384x384, 240s cad, offset

Term	Pointing (x, y)	Comment
02/24 10:25:30 - 02/24 13:49:54	Fixed (885.7, 328.8)	# OP start + 10min AR11162
02/25 06:28:00 - 02/25 09:29:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 16:03:00 - 02/25 17:48:00	Fixed (-411.0, 847.0)	#9
02/25 18:25:00 - 02/25 19:25:00	Fixed (-815.0, -471.0)	#15
02/25 19:49:30 - 02/25 21:02:30	Fixed (0.0, 0.0)	#3
02/25 21:26:30 - 02/25 22:40:00	Fixed (411.0, 847.0)	#7
02/25 23:02:00 - 02/26 00:17:30	Fixed (411.0, -847.0)	#12
02/26 00:26:00 - 02/26 05:58:54	Fixed (885.7, 328.8)	# AR11162
02/26 06:12:00 - 02/26 10:21:54	Fixed (885.7, 328.8)	# AR11162

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 82 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 47 20-time(s) 180.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	58.0sec

XOB #183F: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/4096) + G-band(16

Term	Pointing (x, y)	Comment
02/24 13:53:00 - 02/24 13:59:54	Fixed (0.0, 0.0)	synoptic, shifted manualy.
02/25 06:18:00 - 02/25 06:24:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 18:15:00 - 02/25 18:21:54	Fixed (-411.0, -847.0)	#14
02/26 06:02:00 - 02/26 06:08:54	Fixed (885.7, 328.8)	# AR11162

PROG= 17 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 69 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 28 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1860: HOP101 - Ti/poly 5.8s, G-band - 384x384-AEC0-ROI centered-60 sec

Term	Pointing (x, y)	Comment
02/24 14:03:00 - 02/25 05:49:00	Fixed (-16.0, -927.0)	# HOP101 helioseismology at S-pole for 16hr.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 46 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec

Subr= 2 15-time(s) 2.0sec

Seqn= 54 2-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

XOB #185A: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
02/25 09:33:00 - 02/25 11:59:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 13:33:00 - 02/25 14:37:00	Fixed (936.0, 0.0)	HOP130 #1
02/25 14:48:00 - 02/25 15:59:54	Fixed (-468.0, 0.0)	#4

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 10 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 30 20-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #185F: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 60s cad

Term	Pointing (x, y)	Comment
02/25 12:03:00 - 02/25 13:29:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.

PROG= 07 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 10 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 50 20-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

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Flare mode

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XOB #185D: Flare Standard Obs. with eruptions mode-A (FW1=Open) + Thin-Be

Term	Pointing (x, y)	Comment
02/24 10:25:30 - 02/24 13:49:54	Fixed (885.7, 328.8)	# OP start + 10min AR11162
02/24 14:03:00 - 02/25 05:49:00	Fixed (-16.0, -927.0)	# HOP101 helioseismology at S-pole for 16hr.
02/25 06:28:00 - 02/25 09:29:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 09:33:00 - 02/25 11:59:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 12:03:00 - 02/25 13:29:54	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 13:33:00 - 02/25 14:37:00	Fixed (936.0, 0.0)	HOP130 #1
02/25 14:48:00 - 02/25 15:59:54	Fixed (-468.0, 0.0)	#4
02/25 16:03:00 - 02/25 17:48:00	Fixed (-411.0, 847.0)	#9
02/25 18:25:00 - 02/25 19:25:00	Fixed (-815.0, -471.0)	#15
02/25 19:49:30 - 02/25 21:02:30	Fixed (0.0, 0.0)	#3
02/25 21:26:30 - 02/25 22:40:00	Fixed (411.0, 847.0)	#7
02/25 23:02:00 - 02/26 00:17:30	Fixed (411.0, -847.0)	#12
02/26 00:26:00 - 02/26 05:58:54	Fixed (885.7, 328.8)	# AR11162
02/26 06:12:00 - 02/26 10:21:54	Fixed (885.7, 328.8)	# AR11162

PROG= 20 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 25 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												

Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

Active Region Search

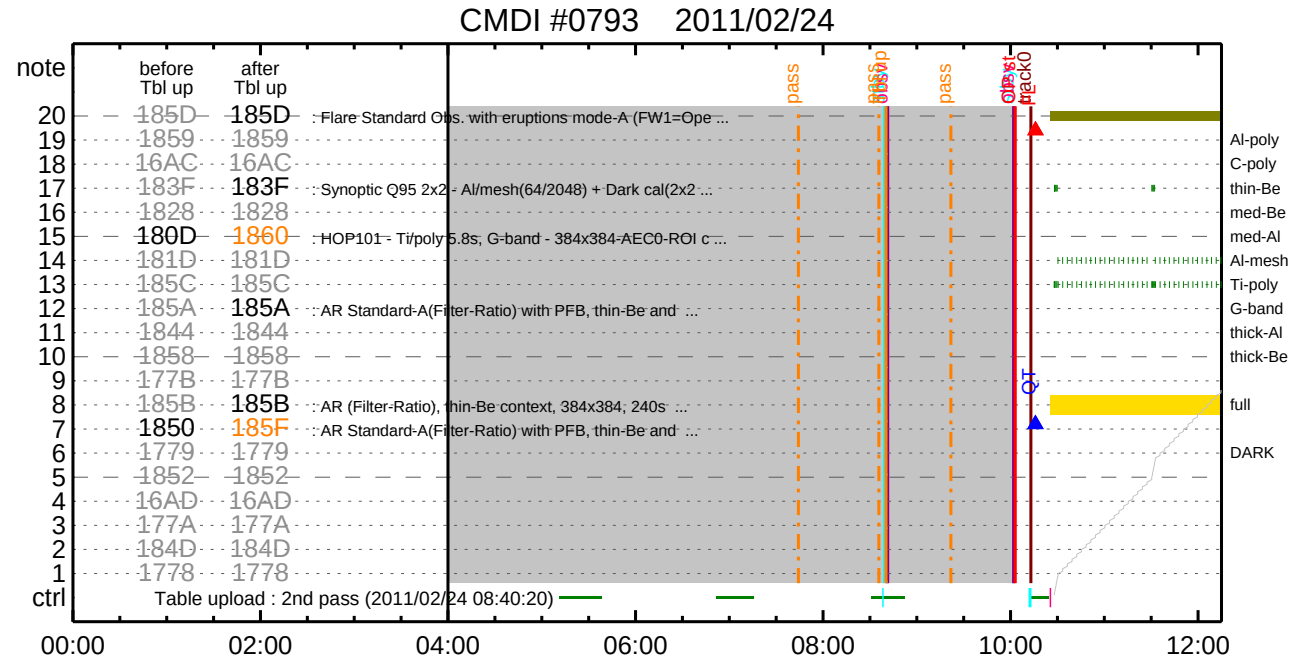
NOT USED

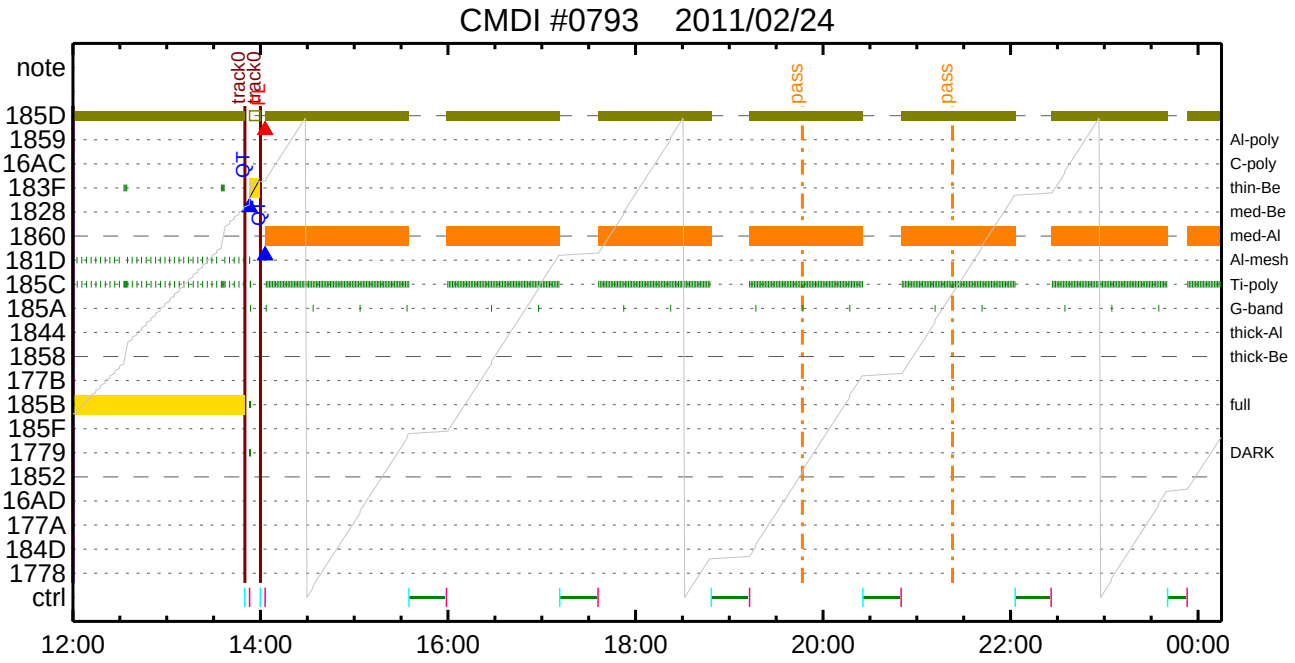
Flare Detection

FLD Patrol

Term	Pointing (x, y)	Comment
02/24 10:13:16 - 02/24 13:50:16	Fixed (885.7, 328.8)	# OP start + 10min AR11162
02/24 14:00:16 - 02/25 06:15:16	Fixed (-16.0, -927.0)	# HOP101 helioseismology at S-pole for 16hr.
02/25 06:25:16 - 02/25 18:12:16	Fixed (875.0, 315.0)	EIS observation with Stereo/Cluster.
02/25 18:22:16 - 02/26 05:59:16	Fixed (-815.0, -471.0)	#15
02/26 06:09:16 - 03/01 10:04:00	Fixed (885.7, 328.8)	# AR11162

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0793 2011/02/24
note
before Tbl up
after Tbl up
185D
1859
16AC
183F
1828
180D
181D
185C
185A
1844
1858
177B
185B
1850
1779
1852
16AD
177A
184D
1778
ctrl
: Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
: Synoptic Q95 2x2 - Al/mesh(64/2048) + Dark cal(2x2 ...
: HOP101 - Ti/poly 5.8s, G-band - 384x384-AEC0-ROI c ...
: AR Standard-A(Filter-Ratio) with PFB, thin-Be and ...
: AR (Filter-Ratio), thin-Be context, 384x384, 240s ...
: AR Standard-A(Filter-Ratio) with PFB, thin-Be and ...
Table upload : 2nd pass (2011/02/24 08:40:20)
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0793 2011/02/24
note
track0
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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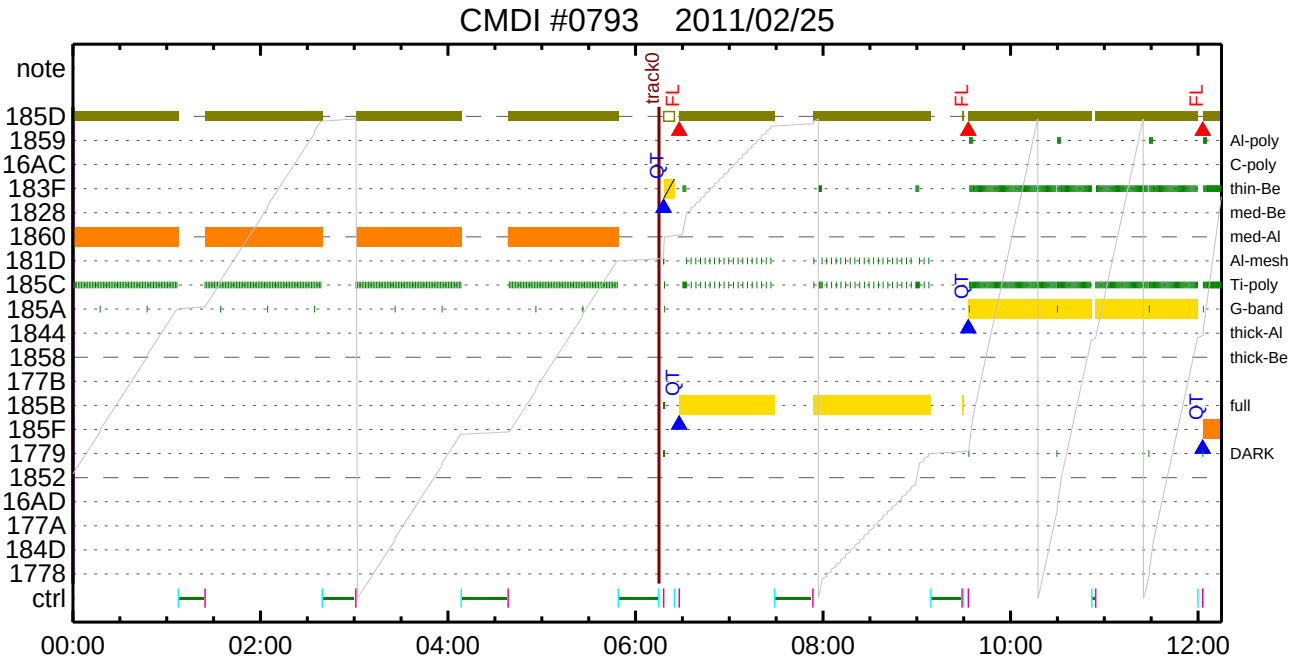
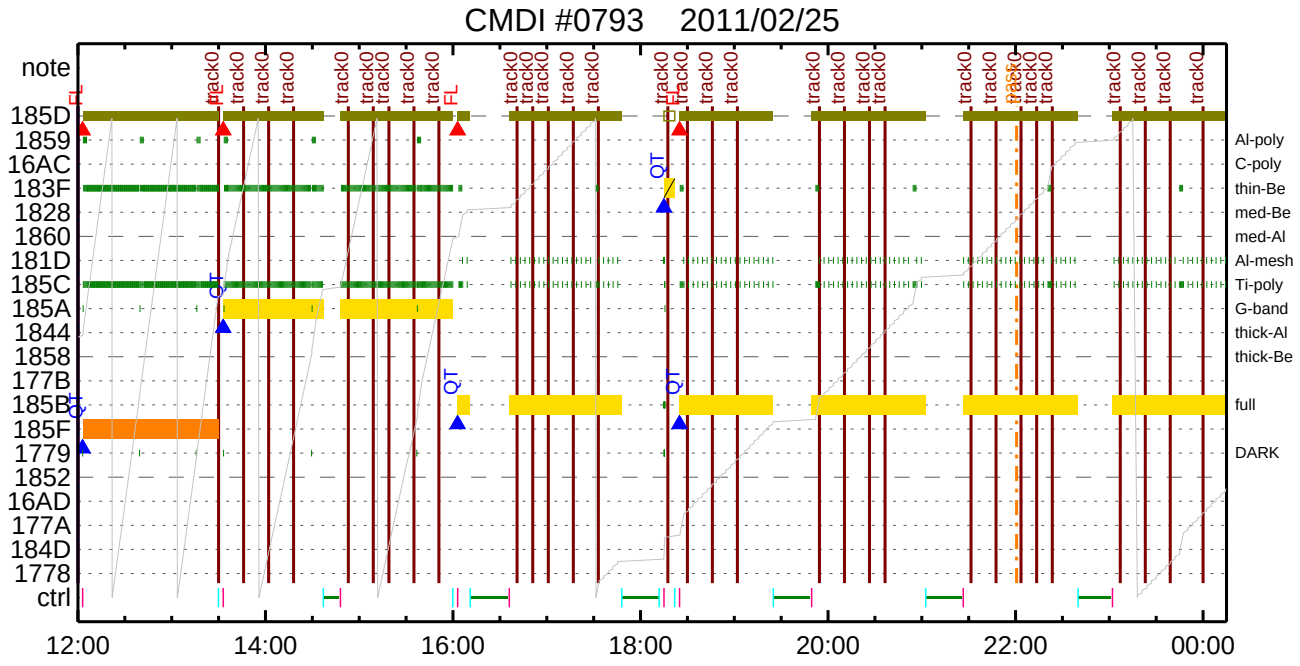
18:00
20:00
22:00
00:00


CMDI #0793 2011/02/25
note
track0
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0793 2011/02/25
note
185D
1859
16AC
183F
1828
1860
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185F
1779
1852
16AD
177A
184D
1778
ctrl
FL
QT
track0
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
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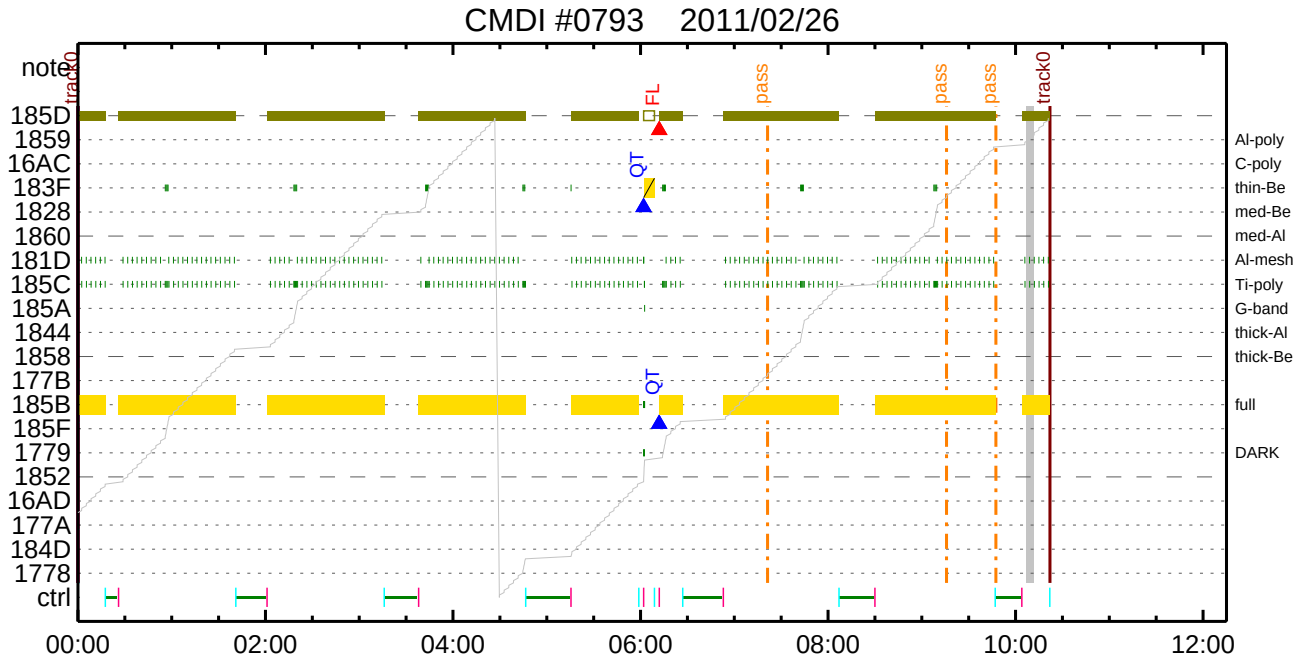
CMDI #0793 2011/02/26
note
185D
1859
16AC
183F
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181D
185C
185A
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177B
185B
185F
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1852
16AD
177A
184D
1778
ctrl
track0
FL
QT
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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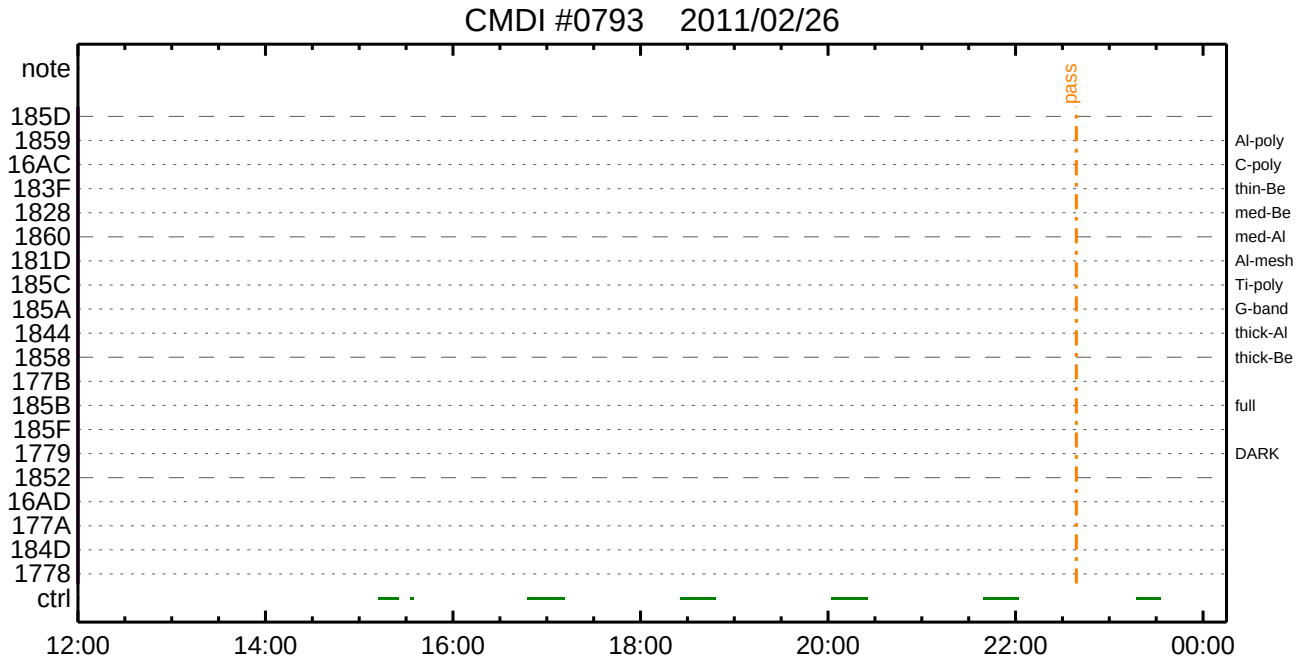
CMDI #0793 2011/02/26
note
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16AC
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1852
16AD
177A
184D
1778
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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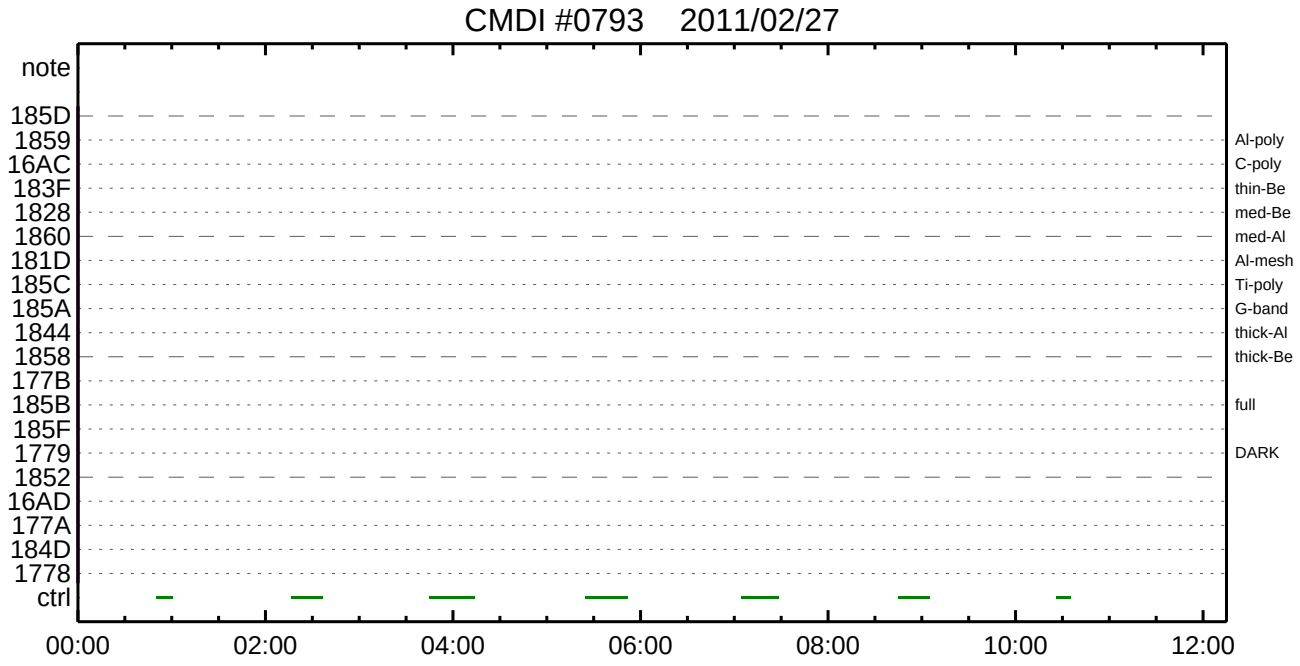
CMDI #0793 2011/02/27
note
185D
1859
16AC
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1860
181D
185C
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177B
185B
185F
1779
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16AD
177A
184D
1778
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
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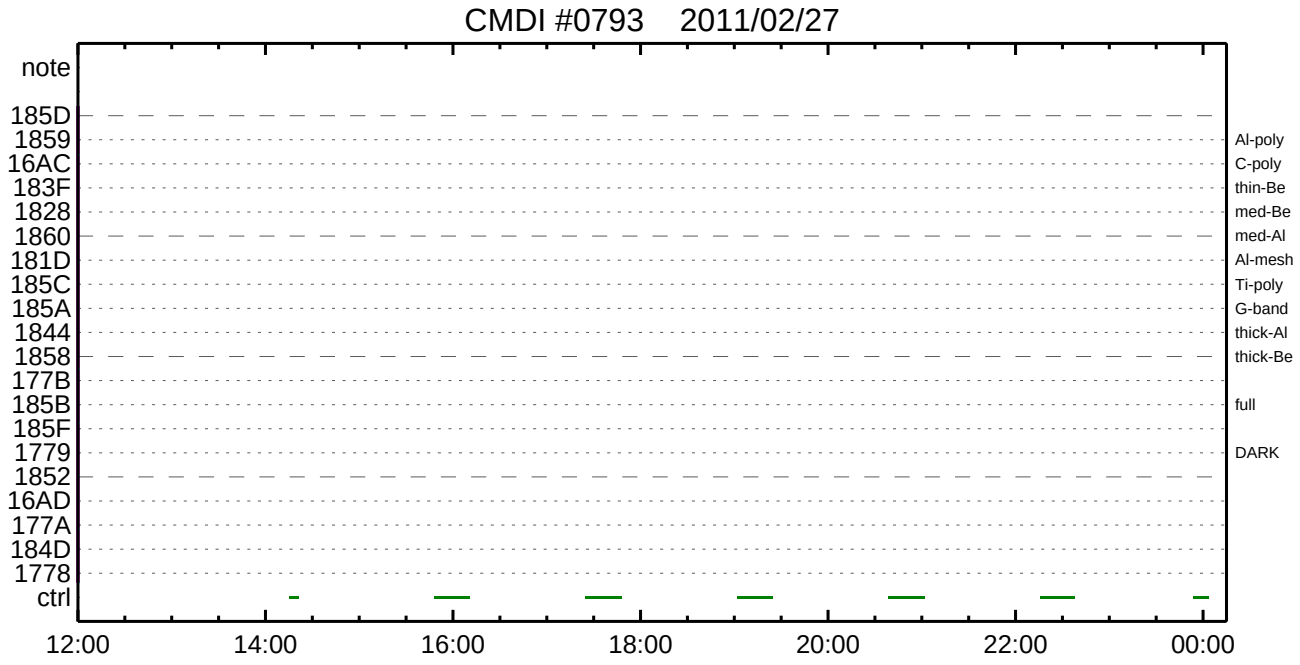
CMDI #0793 2011/02/27
note
185D
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16AC
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177B
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185F
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16AD
177A
184D
1778
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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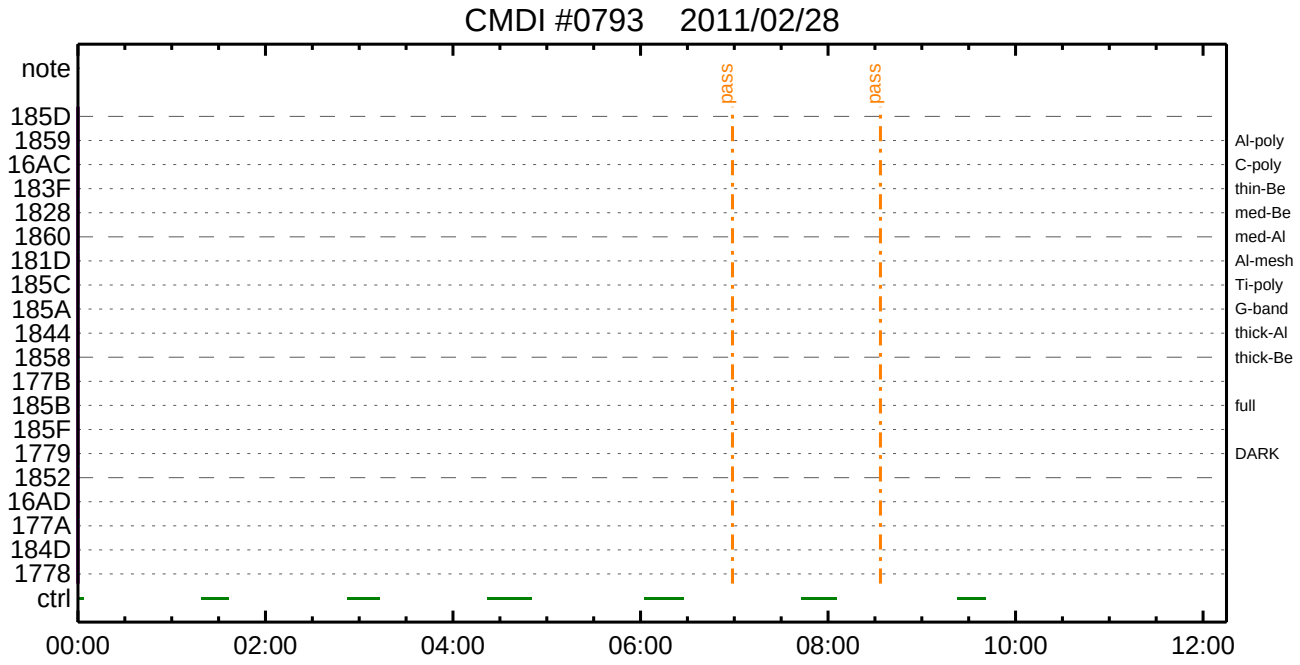
CMDI #0793 2011/02/28
note
pass
pass
185D
1859
16AC
183F
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181D
185C
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177B
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185F
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16AD
177A
184D
1778
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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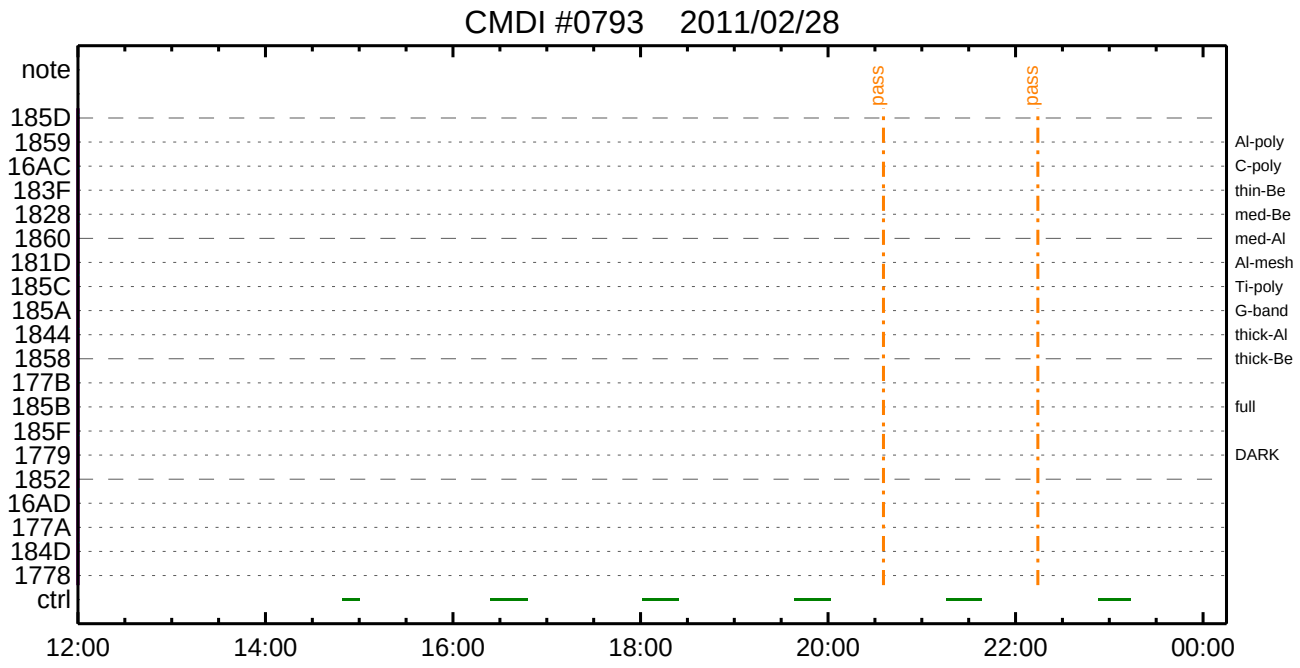
CMDI #0793 2011/02/28
note
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16AC
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16AD
177A
184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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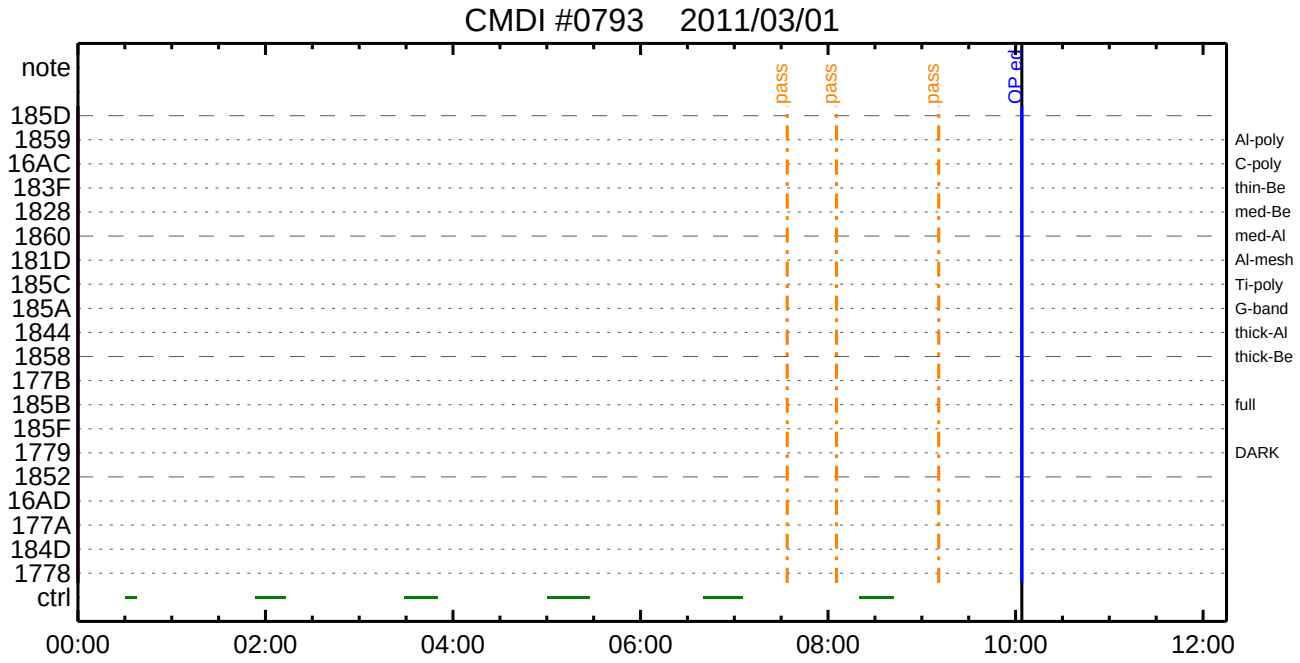
CMDI #0793 2011/03/01
note
185D
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ctrl
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pass
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med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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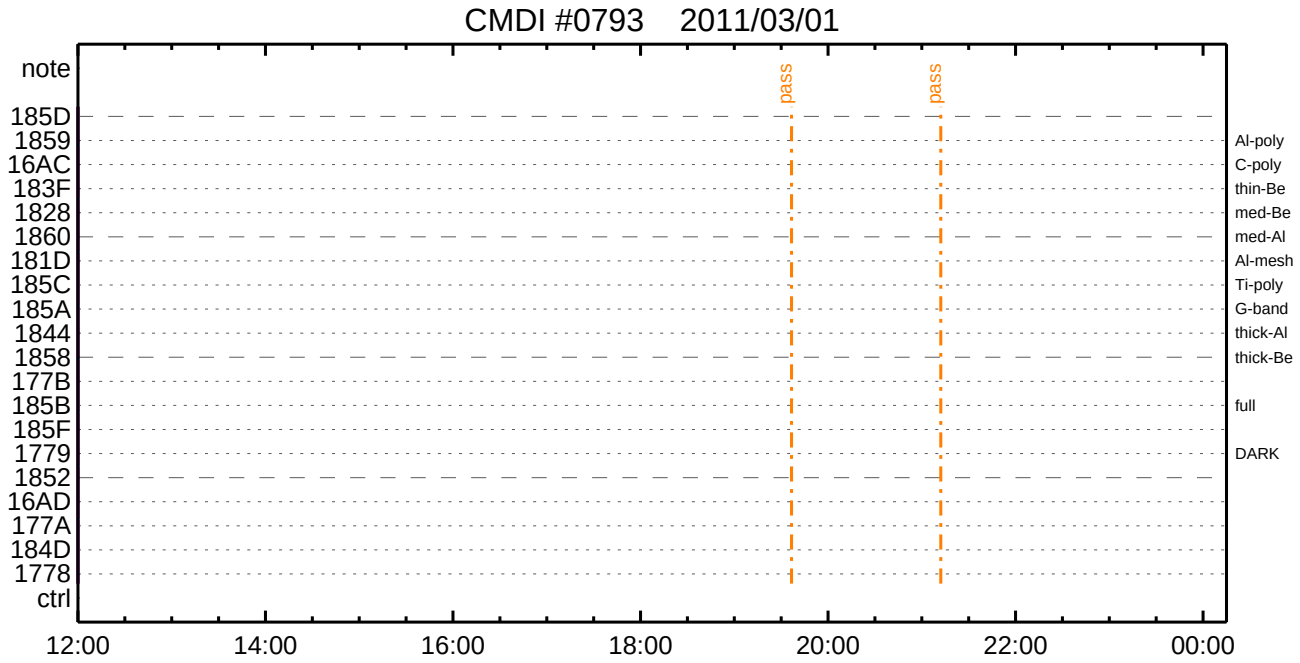
CMDI #0793 2011/03/01
note
185D
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184D
1778
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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```
(a) Spacecraft Operation Procedure (real-commands)

main-916 2011-02-24 14:08:28 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-916:OP
0104     ()
0105   . S. OG    og-916:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2011-02-24 09:58:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2011-02-24 09:58:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2011-02-24 09:58:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2011-02-24 10:02:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2011-02-24 10:02:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2011-02-24 10:02:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2011-02-24 10:02:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2011-02-24 10:02:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-917 2011-02-24 14:08:28 137 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¾Ê¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 1m33s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053   . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055   +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060   +. TI 2011-02-24 10:02:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068     C. ************ XRT START ************
0069     C.
0070   +. DC 07-F0 MDP_XRT_CTRL_MANU
0071     BC       (c1)
0072   +  DC 07-F0 MDP_XRT_MODE_STBY
0073     BC       (c3)
0074   . C. ------------ Success Verify ?              OK / NG____
0075     C.
0076     C. XRT Obs. Table Upload
0077   . S. RAM   ram-291:MDP_OBS_X
0078     ()
0079     C.
0080   +. DC 07-F0 MDP_DUMP_XRTTBL
0081     BC       (84 07 00 00 00 3a d4)
0082   . C. ------------ Comparison Check ?                        OK / ERR ____
0083     C.
0084     C.
0085   +. DC 07-F0 MDP_XRT_ROI_SET
0086     BC       (cd 01 b1 b1 04 04)
0087   +  DC 07-F0 MDP_XRT_ROI_SET
0088     BC       (cd 02 b1 b1 08 08)
0089   +  DC 07-F0 MDP_XRT_ROI_SET
0090     BC       (cd 03 b1 b1 08 08)
0091   +  DC 07-F0 MDP_XRT_ROI_SET
0092     BC       (cd 04 b1 b1 06 06)
0093   +  DC 07-F0 MDP_XRT_ROI_SET
0094     BC       (cd 05 85 83 06 06)
0095   +  DC 07-F0 MDP_XRT_ROI_SET
```

```
0096     BC       (cd 06 85 83 06 06)
0097   +  DC 07-F0 MDP_XRT_ROI_SET
0098     BC       (cd 07 80 80 20 20)
0099   +  DC 07-F0 MDP_XRT_ROI_SET
0100     BC       (cd 08 80 80 20 08)
0101   +  DC 07-F0 MDP_XRT_ROI_SET
0102     BC       (cd 09 80 80 08 20)
0103   +  DC 07-F0 MDP_XRT_ROI_SET
0104     BC       (cd 0a 80 80 06 06)
0105   +  DC 07-F0 MDP_XRT_ROI_SET
0106     BC       (cd 0f 80 80 06 06)
0107   +  DC 07-F0 MDP_XRT_ROI_SET
0108     BC       (cd 10 80 80 08 08)
0109    . C. ---------- Success Verify ?            OK / NG ____
0110     C.
0111     C.
0112    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0113     C.
0114   +. DC 07-F0 MDP_XRT_MODE_OBSV
0115     BC       (c2)
0116   +. TI 2011-02-24 10:02:02.0
0117     DC 07-F0 MDP_XRT_MODE_OBSV
0118     BC       (c2)
0119    . C. ---------- Success Verify ?            OK / NG ____
0120     C.
0121     C. ************ XRT END ***********
0122     C.
0123    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0124     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0125    . S. DC-BC dcbc-402:DCBC
0126     (MDP_known_event)
0127     C.
0128     C.
0129    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0130    . S. DC-BC dcbc-153:DCBC
0131     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0132     C.
0133     C.
0134    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0135     C.
0136    . C. *********** LOS ***********
0137     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-918 2011-02-24 14:08:28 145 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ******************************
0099     C.  SOT table upload
0100     C. ******************************
0101   . C. < Stop FG table >
0102  +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload FG Observation Table>
0109   . S. RAM   ram-269:MDP_OBS_F
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_F >
0113  +. DC 07-F0 MDP_DUMP_FGTBL
0114     BC       (82 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_F verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119     C. ******************************
0120     C.  SOT TI command set
0121     C. ******************************
0122     C. Execute, after the success of TBL upload.
0123  +. TI 2011-02-24 10:02:18.0
0124     DC 07-F0 MDP_SOT_MODE_OBSV
0125     BC       (40)
0126   . C. ------------------------------------
0127     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0128     C. ------------------------------------
0129     C.
0130     C.
0131   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0132     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0133   . S. DC-BC dcbc-402:DCBC
0134     (MDP_known_event)
0135     C.
0136     C.
0137   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0138   . S. DC-BC dcbc-153:DCBC
0139     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140     C.
0141     C.
0142   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0143     C.
0144   . C. ********** LOS **********
0145     C.
```

```
*** OP Sequence for XRT ***

2011/02/24  10:12:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  10:12:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  10:12:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  10:12:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  10:12:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/24  10:13:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 e2 c9 b1 48
2011/02/24  10:13:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/24  10:13:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/24  10:13:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/24  10:13:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/24  10:15:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  10:15:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  10:15:56.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2011/02/24  10:15:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/24  10:24:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  10:25:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  13:49:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  13:49:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/02/24  13:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/02/24  13:50:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/02/24  13:50:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/24  13:50:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/24  13:52:58.0  XRT_QT_PROG_SET_403_OG [0x193]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/24  13:53:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  13:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  13:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/24  14:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 52 66 01 68
2011/02/24  14:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/24  14:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/24  14:00:20.0  XRT_AEC_RESET_414_OG [0x19e]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/24  14:00:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/24  14:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  14:02:56.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2011/02/24  14:02:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/24  14:03:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  15:35:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  15:35:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  15:35:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  15:38:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  15:58:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  15:59:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  17:11:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  17:11:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  17:11:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  17:14:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  17:35:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  17:36:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

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2011/02/24  18:48:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  18:48:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  18:48:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  18:51:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  19:12:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  19:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  20:25:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  20:25:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  20:25:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  20:28:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  20:49:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  20:50:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  22:03:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  22:03:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  22:03:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  22:06:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  22:25:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  22:26:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/24  23:40:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/24  23:40:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/24  23:40:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/24  23:43:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/24  23:52:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/24  23:53:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  01:07:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  01:07:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  01:07:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  01:10:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  01:23:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  01:24:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  02:39:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  02:39:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  02:39:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  02:42:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  03:00:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  03:01:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  04:08:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  04:08:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  04:08:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  04:11:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  04:37:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  04:38:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  05:49:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  05:49:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  05:49:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  05:52:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  06:14:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  06:14:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/02/25  06:15:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 e4 01 b2 36
2011/02/25  06:15:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
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2011/02/25  06:15:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/25  06:15:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  06:17:58.0  XRT_QT_PROG_SET_403_OG [0x193]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/25  06:18:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  06:24:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  06:24:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  06:25:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  06:25:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  06:25:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  06:25:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  06:27:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  06:27:56.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2011/02/25  06:27:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  06:28:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  07:29:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  07:29:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  07:29:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  07:32:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  07:52:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  07:53:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  09:09:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  09:09:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  09:09:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  09:12:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  09:28:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  09:29:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  09:29:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  09:29:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  09:30:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  09:30:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  09:30:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  09:30:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  09:32:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  09:32:56.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2011/02/25  09:32:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  09:33:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  10:52:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  10:52:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  10:52:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  10:53:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  10:54:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  10:55:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  11:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  11:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  12:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  12:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  12:00:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  12:00:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
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2011/02/25  12:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  12:02:56.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2011/02/25  12:02:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  12:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  13:29:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  13:29:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  13:30:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 00 00 ac cd
2011/02/25  13:30:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  13:30:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  13:30:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  13:30:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  13:32:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  13:32:56.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2011/02/25  13:32:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  13:33:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  13:46:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 00 00 d6 67
2011/02/25  14:02:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/02/25  14:18:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 29 99
2011/02/25  14:37:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  14:37:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  14:37:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  14:40:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  14:47:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  14:48:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  14:53:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 00 00 53 33
2011/02/25  15:09:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 d6 36 b7 8e
2011/02/25  15:19:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 b4 b5 db 75
2011/02/25  15:35:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 ac 5b 00 00
2011/02/25  15:51:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 b4 b5 24 8b
2011/02/25  15:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  15:59:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  16:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  16:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  16:00:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  16:00:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  16:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  16:02:56.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2011/02/25  16:02:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  16:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  16:11:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  16:11:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  16:11:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  16:14:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  16:35:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  16:36:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  16:41:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 d6 36 48 72
2011/02/25  16:51:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 29 ca b7 8e
2011/02/25  17:01:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
```

```
                                    AOCU_NM              5 02-76 00 4b 4b db 75
2011/02/25  17:17:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                    AOCU_NM              5 02-76 00 53 a5 00 00
2011/02/25  17:33:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                    AOCU_NM              5 02-76 00 4b 4b 24 8b
2011/02/25  17:48:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  17:48:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  17:48:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  17:51:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  18:11:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  18:11:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/02/25  18:12:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/02/25  18:12:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/25  18:12:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  18:14:58.0  XRT_QT_PROG_SET_403_OG [0x193]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/25  18:15:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  18:17:30.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                    AOCU_NM              5 02-76 00 29 db 48 72
2011/02/25  18:21:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  18:21:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/25  18:22:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/25  18:22:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/25  18:22:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/25  18:22:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/25  18:24:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  18:24:56.0  XRT_QT_PROG_SET_404_OG [0x194]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2011/02/25  18:24:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/25  18:25:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  18:30:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 00 00 00 ac cd
2011/02/25  18:46:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 00 00 00 d6 67
2011/02/25  19:02:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2011/02/25  19:25:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  19:25:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  19:25:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  19:28:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  19:48:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  19:49:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  19:54:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 00 00 29 99
2011/02/25  20:10:30.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 00 00 00 53 33
2011/02/25  20:26:30.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM              5 02-76 00 d6 36 b7 8e
2011/02/25  20:36:30.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                    AOCU_NM              5 02-76 00 b4 b5 db 75
2011/02/25  21:02:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  21:02:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  21:02:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  21:05:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  21:25:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  21:26:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  21:31:30.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                    AOCU_NM              5 02-76 00 ac 5b 00 00
2011/02/25  21:47:30.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                    AOCU_NM              5 02-76 00 b4 b5 24 8b
2011/02/25  22:03:30.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                    AOCU_NM              5 02-76 00 d6 36 48 72
2011/02/25  22:13:30.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                    AOCU_NM              5 02-76 00 29 ca b7 8e
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2011/02/25  22:23:30.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 4b 4b db 75
2011/02/25  22:40:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/25  22:40:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/25  22:40:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/25  22:43:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/25  23:01:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/25  23:02:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/25  23:07:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 53 a5 00 00
2011/02/25  23:23:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 4b 4b 24 8b
2011/02/25  23:39:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 29 db 48 72
2011/02/26  00:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 e2 c9 b1 48
2011/02/26  00:17:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  00:17:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  00:17:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  00:20:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  00:25:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  00:26:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  01:41:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  01:41:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  01:41:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  01:44:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  02:00:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  02:01:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  03:16:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  03:16:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  03:16:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  03:19:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  03:37:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  03:38:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  04:46:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  04:46:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  04:46:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  04:49:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  05:14:30.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  05:15:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  05:58:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  05:58:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/02/26  05:59:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/02/26  05:59:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/02/26  05:59:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/26  06:01:58.0  XRT_QT_PROG_SET_403_OG [0x193]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/02/26  06:02:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  06:08:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  06:08:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/02/26  06:09:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/02/26  06:09:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/02/26  06:09:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/02/26  06:09:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/02/26  06:11:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
```

```
2011/02/26  06:11:56.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2011/02/26  06:11:58.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2011/02/26  06:12:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  06:27:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  06:27:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  06:27:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  06:30:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  06:52:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  06:53:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  08:07:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  08:07:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  08:07:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  08:10:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  08:29:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  08:30:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  09:47:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  09:47:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/02/26  09:47:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/02/26  09:50:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/02/26  10:03:00.0  XRT_Custom_418_OG [0x1a2]
2011/02/26  10:04:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/02/26  10:21:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/02/26  10:22:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```