

XRT Timeline to be uploaded on 2011/03/17

Period: 2011/03/17 22:48:00 - 2011/03/25 22:32:00

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Normal mode

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XOB #186A: AR Standard-A(Filter-Ratio) with PFB (384x384), thin-Be and Al/Poly context, 512x512 at 1064 1048, 120s cad-2																								
Term	Pointing (x, y)						Comment																	
03/17 23:05:00 - 03/18 06:18:54	Track (0.2, 340.6) <small>Ⓢ 03/17 22:58:00</small>						# OP start + 10min Track AR11172																	
03/18 06:32:00 - 03/18 17:42:00	Track (68.2, 340.2) <small>Ⓢ 03/18 06:29:00</small>						# Cont.																	
03/18 18:18:30 - 03/19 05:52:24	Track (173.7, 338.2) <small>Ⓢ 03/18 18:15:30</small>						# Cont.																	
03/19 06:05:30 - 03/19 17:55:24	Track (277.1, 334.8) <small>Ⓢ 03/19 06:02:30</small>						# Cont.																	
03/19 18:08:30 - 03/20 05:59:54	Track (379.0, 329.8) <small>Ⓢ 03/19 18:05:30</small>						# Cont.																	
03/20 06:13:00 - 03/20 17:58:54	Track (476.0, 323.4) <small>Ⓢ 03/20 06:10:00</small>						# Cont.																	
PROG= 07 Inf.-time(s)																								
┌ Subr= 1 1-time(s) 2.0sec																								
└─ Seqn= 17 1-time(s) 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec																								
└─└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec																								
└─ Seqn= 64 4-time(s) 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec																								
└─┬─ Al-poly/Open med-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec																								
└─└─ thin-Be/Open med-Be/Open close Safe Norm 4.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec																								
┌ Subr= 2 1-time(s) 2.0sec																								
└─ Seqn= 30 15-time(s) 2.0sec																								
└─┬─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec																								
└─┬─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec																								
└─┬─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec																								
└─┬─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec																								
└─└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec																								
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 10%;">Default Filter</td> <td style="width: 10%;">Thicker Filter</td> <td style="width: 10%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 10%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table>													Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36																								
Term	Pointing (x, y)						Comment																	
03/18 06:22:00 - 03/18 06:28:54	Fixed (0.0, 0.0)						synoptic, shifted 19.0 min																	
03/18 18:08:30 - 03/18 18:15:24	Fixed (0.0, 0.0)						synoptic, shifted 5.5 min																	
03/19 05:55:30 - 03/19 06:02:24	Fixed (0.0, 0.0)						synoptic, shifted -7.5 min																	
03/19 17:58:30 - 03/19 18:05:24	Fixed (0.0, 0.0)						synoptic, shifted -4.5 min																	
03/20 06:03:00 - 03/20 06:09:54	Fixed (0.0, 0.0)						synoptic																	
03/20 18:02:00 - 03/20 18:08:54	Fixed (0.0, 0.0)						synoptic, shifted -1.0 min																	
03/21 05:31:30 - 03/21 05:38:24	Fixed (0.0, 0.0)						synoptic, shifted -31.5 min																	
03/21 18:21:30 - 03/21 18:31:00	Fixed (0.0, 0.0)						synoptic, shifted 18.5 min																	
03/22 18:03:00 - 03/22 18:09:54	Fixed (0.0, 0.0)						synoptic																	
03/23 06:11:00 - 03/23 06:17:54	Fixed (0.0, 0.0)						synoptic, shifted 8.0 min																	
03/23 17:59:30 - 03/23 18:06:24	Fixed (0.0, 0.0)						synoptic, shifted -3.5 min																	
PROG= 13 1-time(s)																								
┌ Subr= 1 1-time(s) 12.0sec																								
└─ Seqn= 7 1-time(s) 4.0sec																								
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 5 1-time(s) 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec																								
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec																								
└─ Seqn= 8 1-time(s) 4.0sec																								
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 32 1-time(s) 2.0sec																								
└─┬─ thin-Be/Open thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─└─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 4 1-time(s) 2.0sec																								
└─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 10%;">Default Filter</td> <td style="width: 10%;">Thicker Filter</td> <td style="width: 10%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 10%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table>													Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #186B: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad-2												
Term	Pointing (x, y)						Comment					
03/20 18:12:00 - 03/21 05:28:24	Track (565.8, 315.7) <small>Ⓢ 03/20 18:09:00</small>						# Cont.					
03/21 05:41:30 - 03/21 17:55:00	Track (644.8, 307.1) <small>Ⓢ 03/21 05:38:30</small>						# Cont.					
03/22 12:34:06 - 03/22 17:59:54	Track (786.0, 285.7) <small>Ⓢ 03/22 06:14:30</small>						# Cont.					
03/22 18:13:00 - 03/23 06:07:54	Track (838.9, 274.1) <small>Ⓢ 03/22 18:10:00</small>						# Cont.					
03/23 06:21:00 - 03/23 08:51:30	Track (881.4, 261.7) <small>Ⓢ 03/23 06:18:00</small>						# Cont.					
03/23 09:15:06 - 03/23 17:31:00	Fixed (889.2, 258.8)						Obs AR11172 by fixed.					
03/23 18:09:30 - 03/23 22:22:00	Fixed (889.2, 258.8)						# Cont.					

PROG= 08 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 19		1-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 56		4-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 30		20-time(s)		2.0sec									
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
03/17 23:05:00 - 03/18 06:18:54	Track (0.2, 340.6) @ 03/17 22:58:00	# OP start + 10min Track AR11172
03/18 06:32:00 - 03/18 17:42:00	Track (68.2, 340.2) @ 03/18 06:29:00	# Cont.
03/18 18:18:30 - 03/19 05:52:24	Track (173.7, 338.2) @ 03/18 18:15:30	# Cont.
03/19 06:05:30 - 03/19 17:55:24	Track (277.1, 334.8) @ 03/19 06:02:30	# Cont.
03/19 18:08:30 - 03/20 05:59:54	Track (379.0, 329.8) @ 03/19 18:05:30	# Cont.
03/20 06:13:00 - 03/20 17:58:54	Track (476.0, 323.4) @ 03/20 06:10:00	# Cont.
03/20 18:12:00 - 03/21 05:28:24	Track (565.8, 315.7) @ 03/20 18:09:00	# Cont.
03/21 05:41:30 - 03/21 17:55:00	Track (644.8, 307.1) @ 03/21 05:38:30	# Cont.
03/22 12:34:06 - 03/22 17:59:54	Track (786.0, 285.7) @ 03/22 06:14:30	# Cont.
03/22 18:13:00 - 03/23 06:07:54	Track (838.9, 274.1) @ 03/22 18:10:00	# Cont.
03/23 06:21:00 - 03/23 08:51:30	Track (881.4, 261.7) @ 03/23 06:18:00	# Cont.
03/23 09:15:06 - 03/23 17:31:00	Fixed (889.2, 258.8)	Obs AR11172 by fixed.
03/23 18:09:30 - 03/23 22:22:00	Fixed (889.2, 258.8)	# Cont.

PROG= 15 1-time(s)

Subr= 1		4-time(s)		2.0sec									
Seqn= 55		45-time(s)		20.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 90		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)		600.0sec									
Seqn= 89		1-time(s)		2.0sec									
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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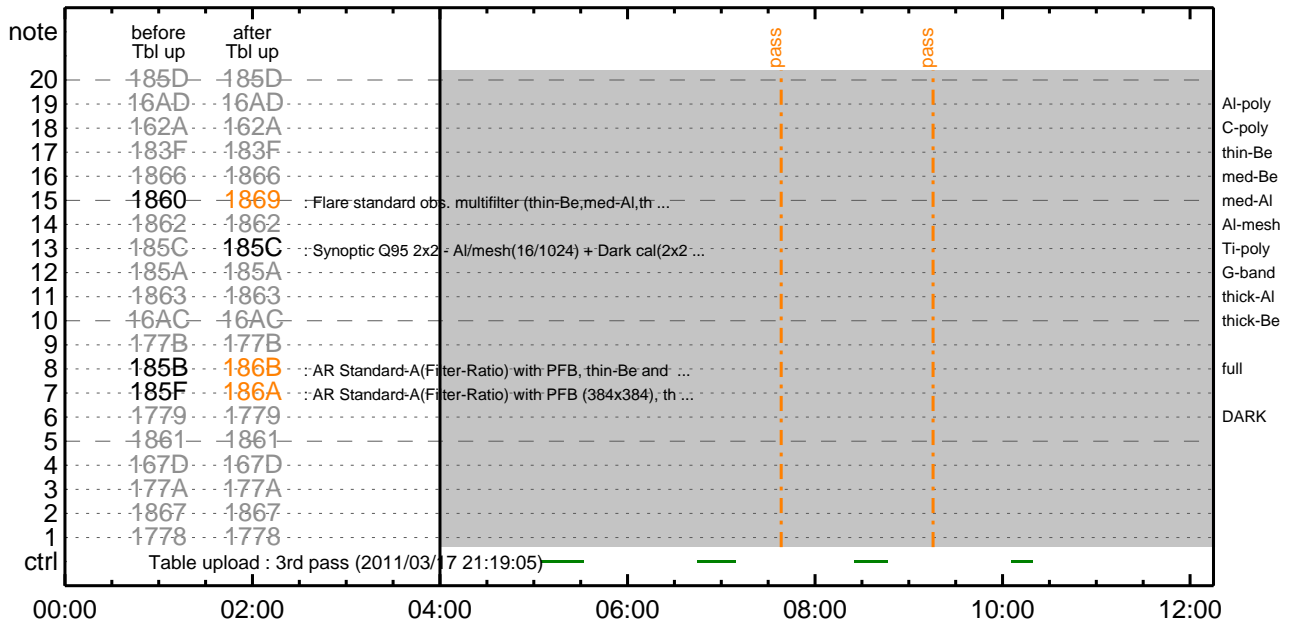
Flare Detection

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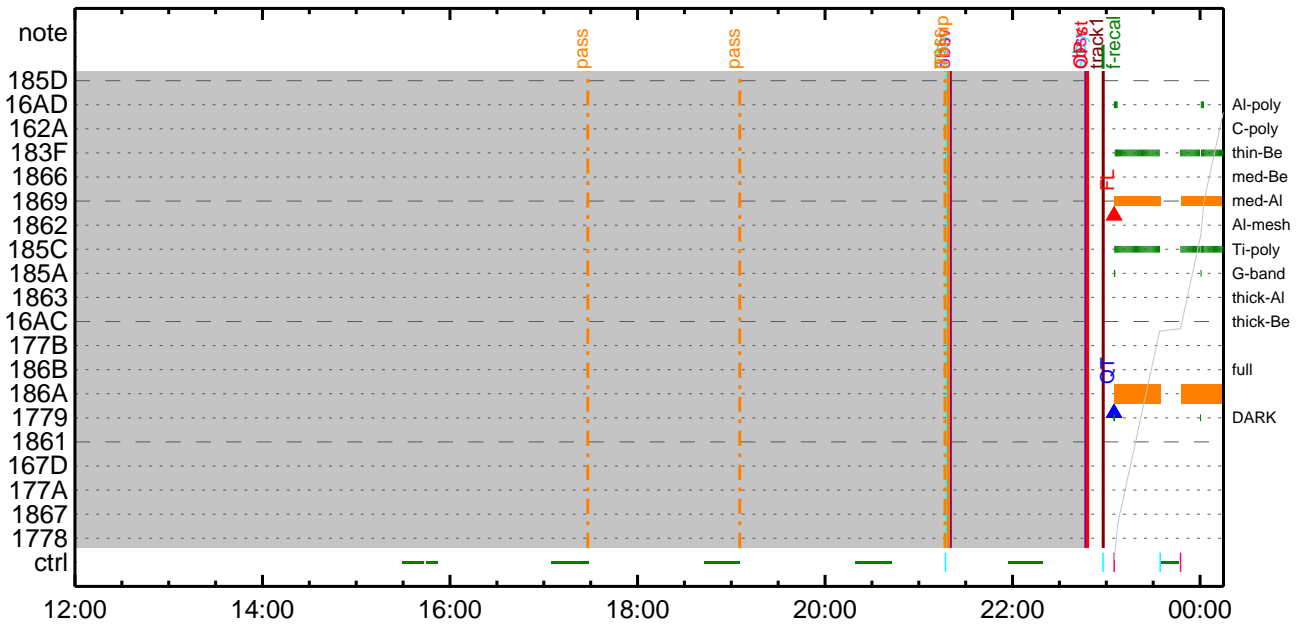
FLD Patrol

Term	Pointing (x, y)	Comment
03/17 23:02:16 - 03/18 06:19:16	Track (0.2, 340.6) @ 03/17 22:58:00	# OP start + 10min Track AR11172
03/18 06:29:16 - 03/18 18:05:46	Track (68.2, 340.2) @ 03/18 06:29:00	# Cont.
03/18 18:15:46 - 03/19 05:52:46	Track (173.7, 338.2) @ 03/18 18:15:30	# Cont.
03/19 06:02:46 - 03/19 17:55:46	Track (277.1, 334.8) @ 03/19 06:02:30	# Cont.
03/19 18:05:46 - 03/20 06:00:16	Track (379.0, 329.8) @ 03/19 18:05:30	# Cont.
03/20 06:10:16 - 03/20 17:59:16	Track (476.0, 323.4) @ 03/20 06:10:00	# Cont.
03/20 18:09:16 - 03/21 05:28:46	Track (565.8, 315.7) @ 03/20 18:09:00	# Cont.
03/21 05:38:46 - 03/21 18:18:46	Track (644.8, 307.1) @ 03/21 05:38:30	# Cont.
03/22 12:31:22 - 03/22 18:00:16	Track (786.0, 285.7) @ 03/22 06:14:30	# Cont.
03/22 18:10:16 - 03/23 06:08:16	Track (838.9, 274.1) @ 03/22 18:10:00	# Cont.
03/23 06:18:16 - 03/23 17:56:46	Track (881.4, 261.7) @ 03/23 06:18:00	# Cont.
03/23 18:06:46 - 03/25 22:32:00	Fixed (889.2, 258.8)	# Cont.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec

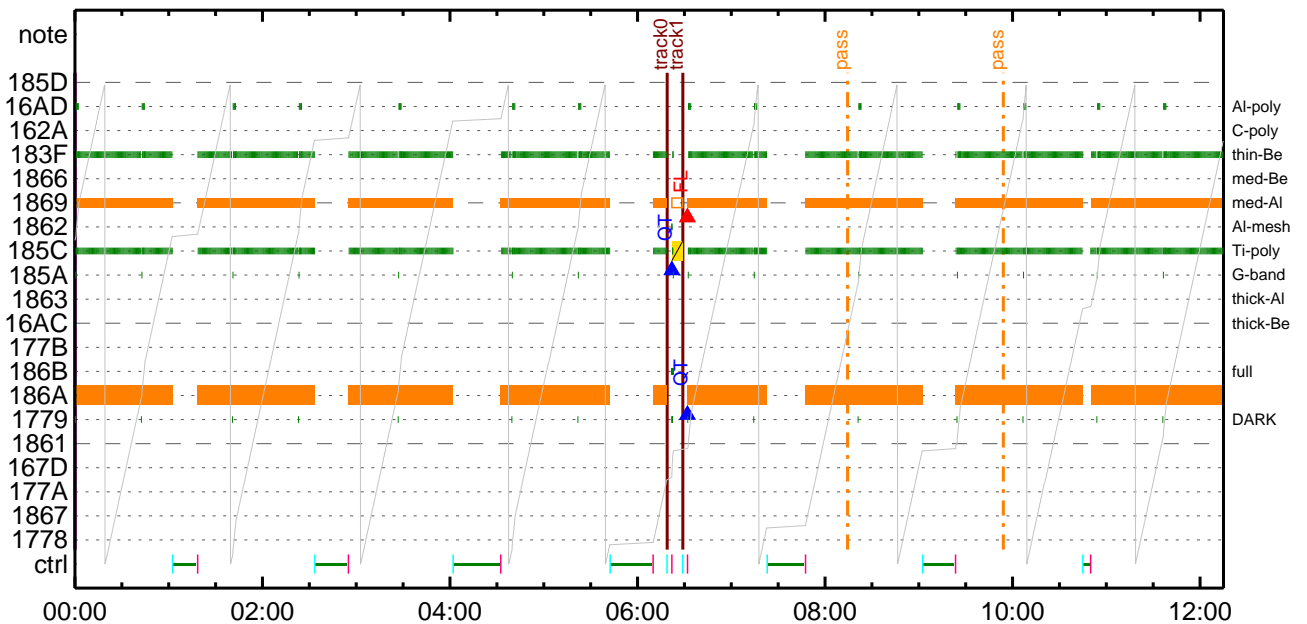
CMDI #0836 2011/03/17



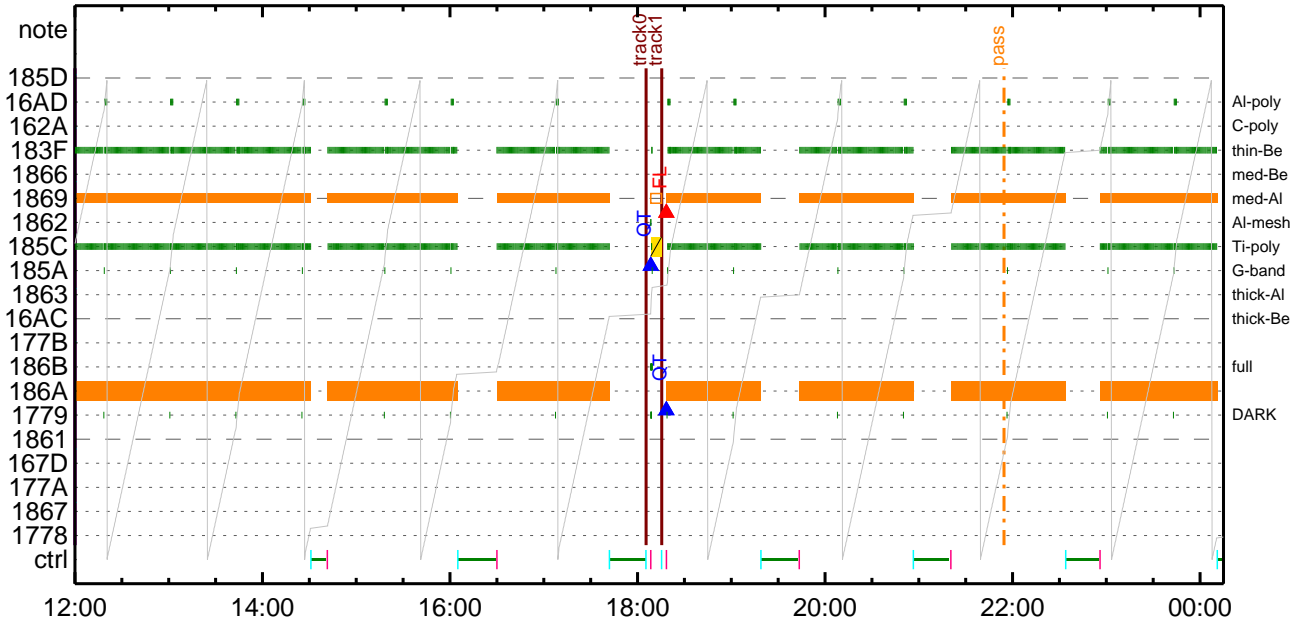
CMDI #0836 2011/03/17



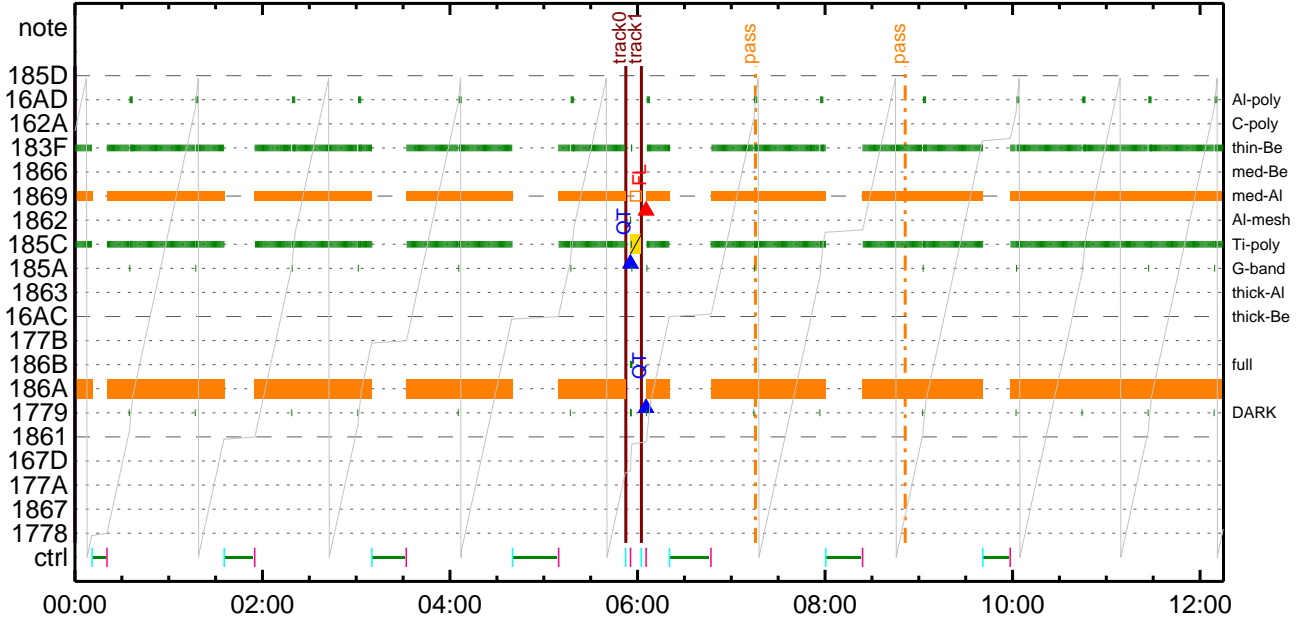
CMDI #0836 2011/03/18



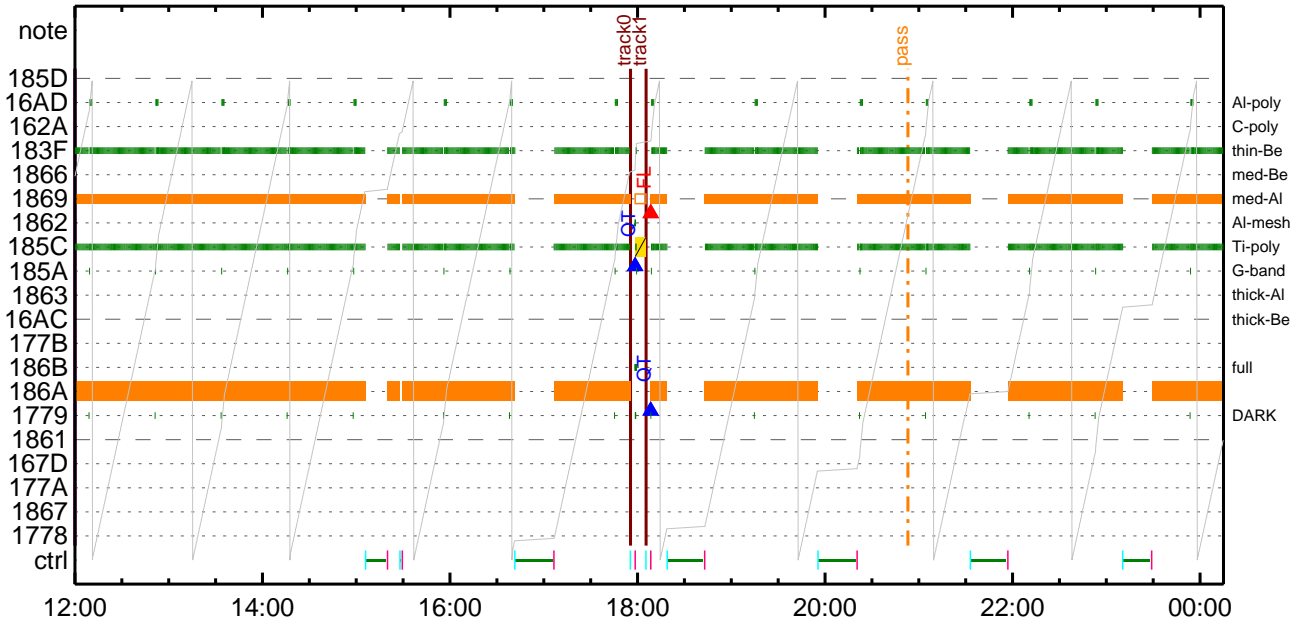
CMDI #0836 2011/03/18



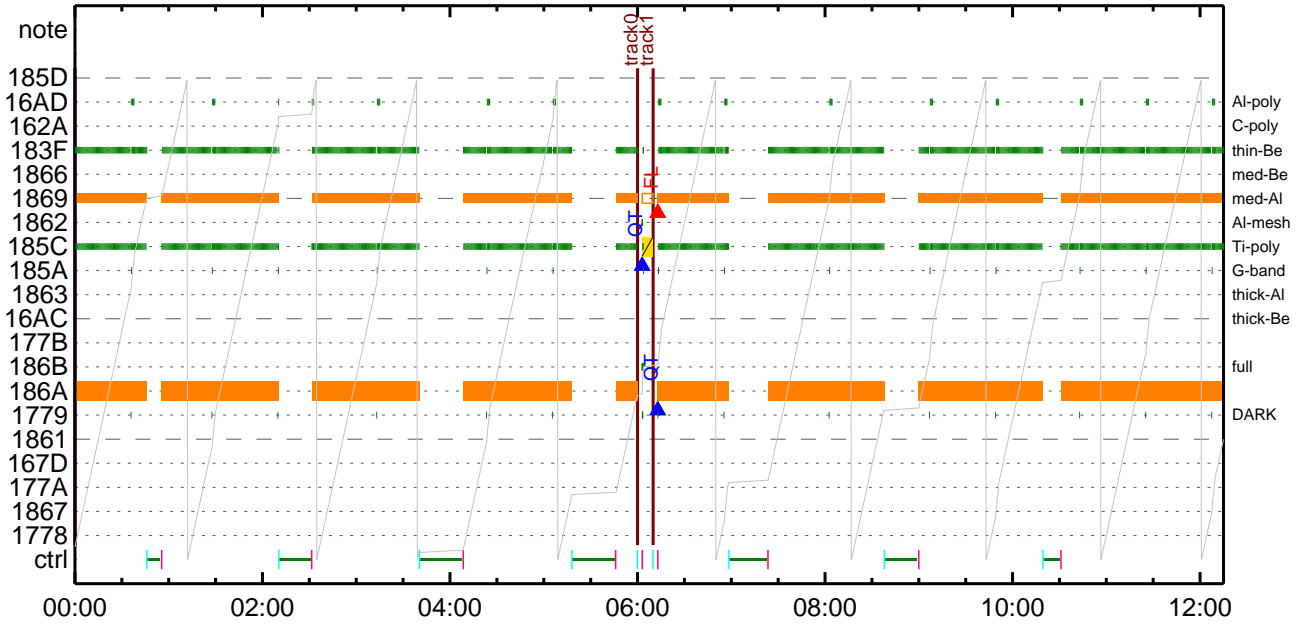
CMDI #0836 2011/03/19



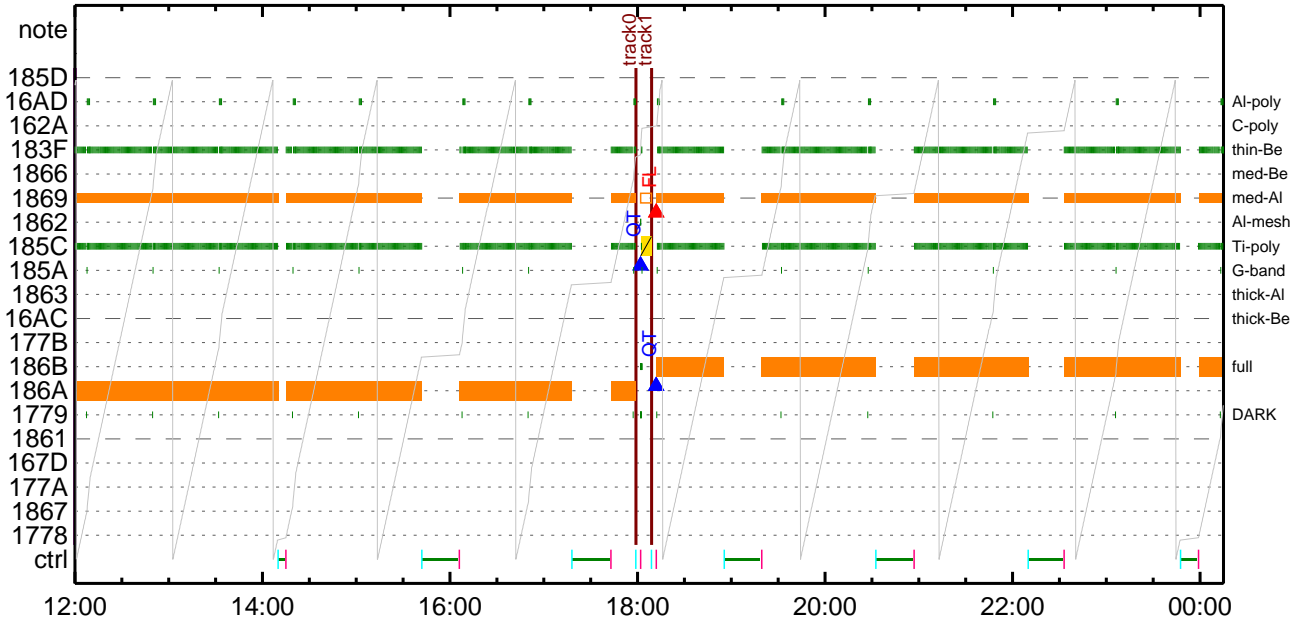
CMDI #0836 2011/03/19



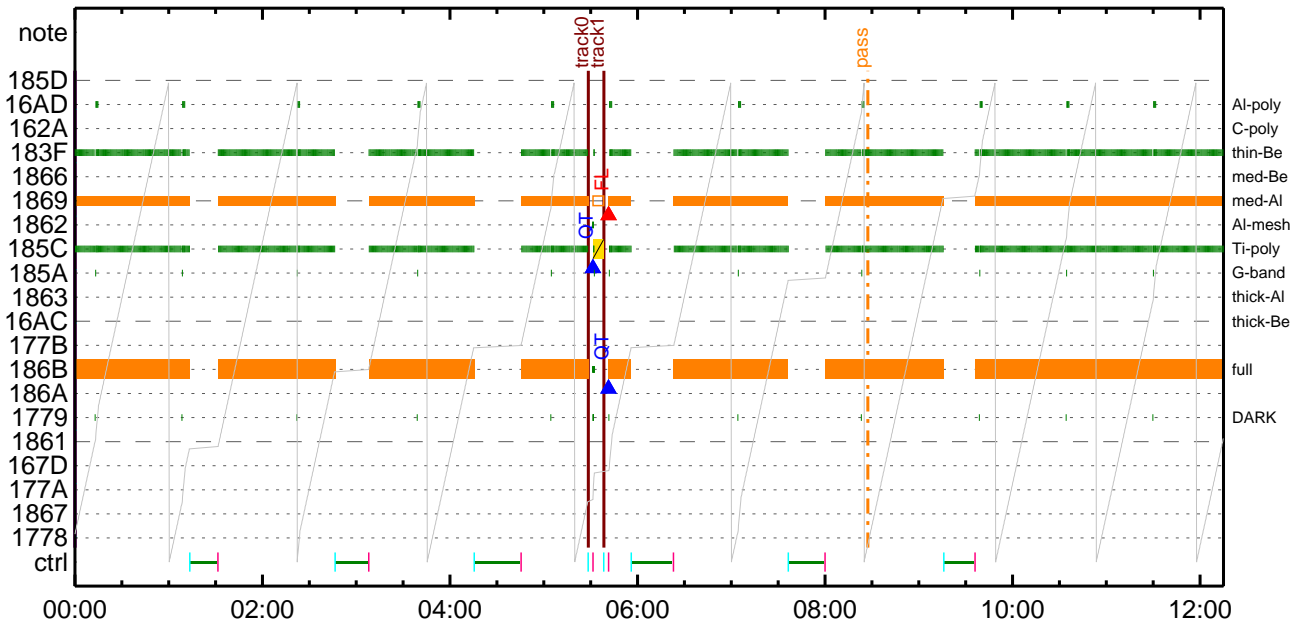
CMDI #0836 2011/03/20



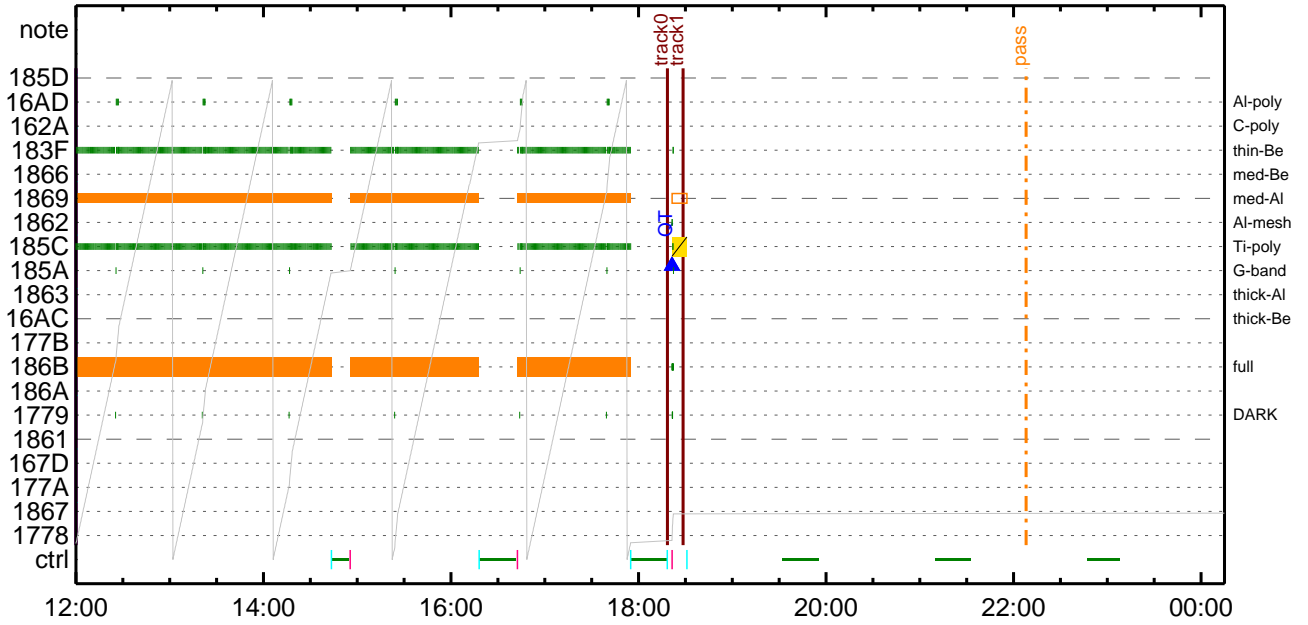
CMDI #0836 2011/03/20



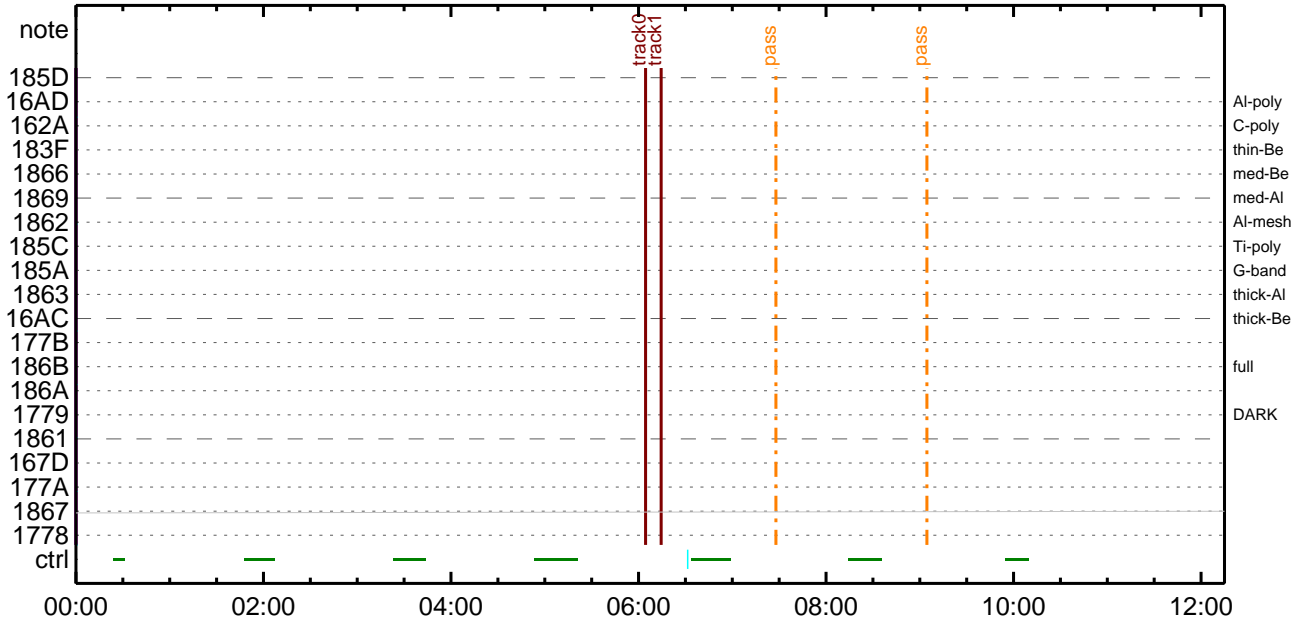
CMDI #0836 2011/03/21



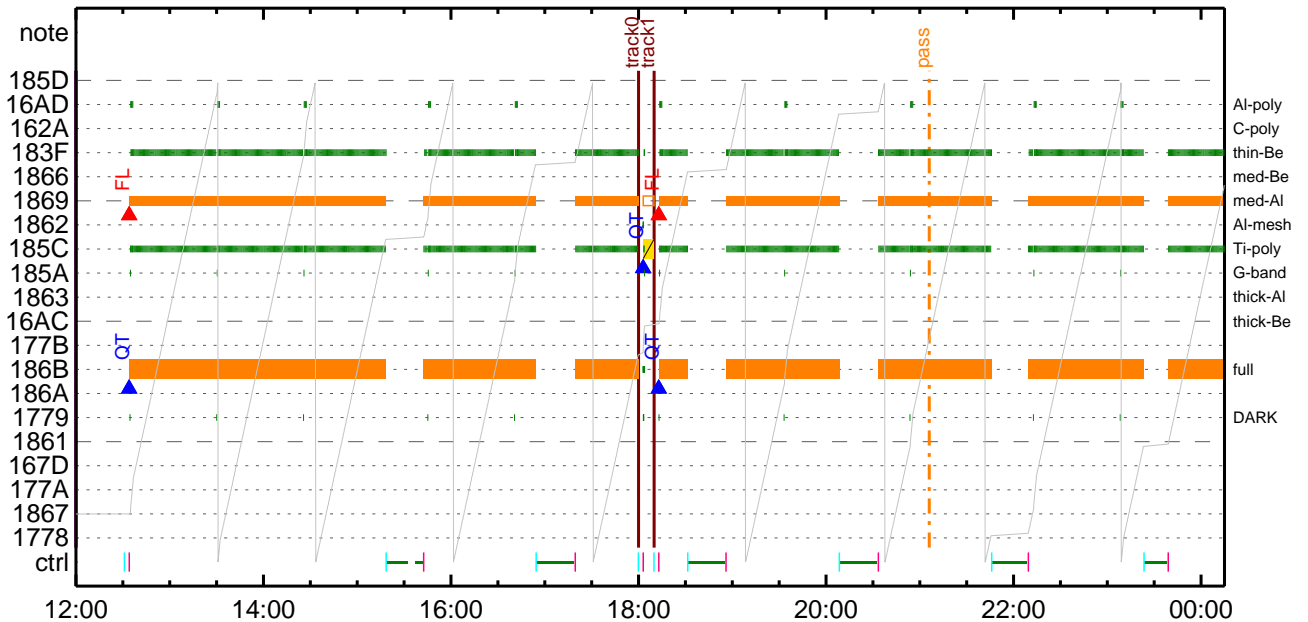
CMDI #0836 2011/03/21



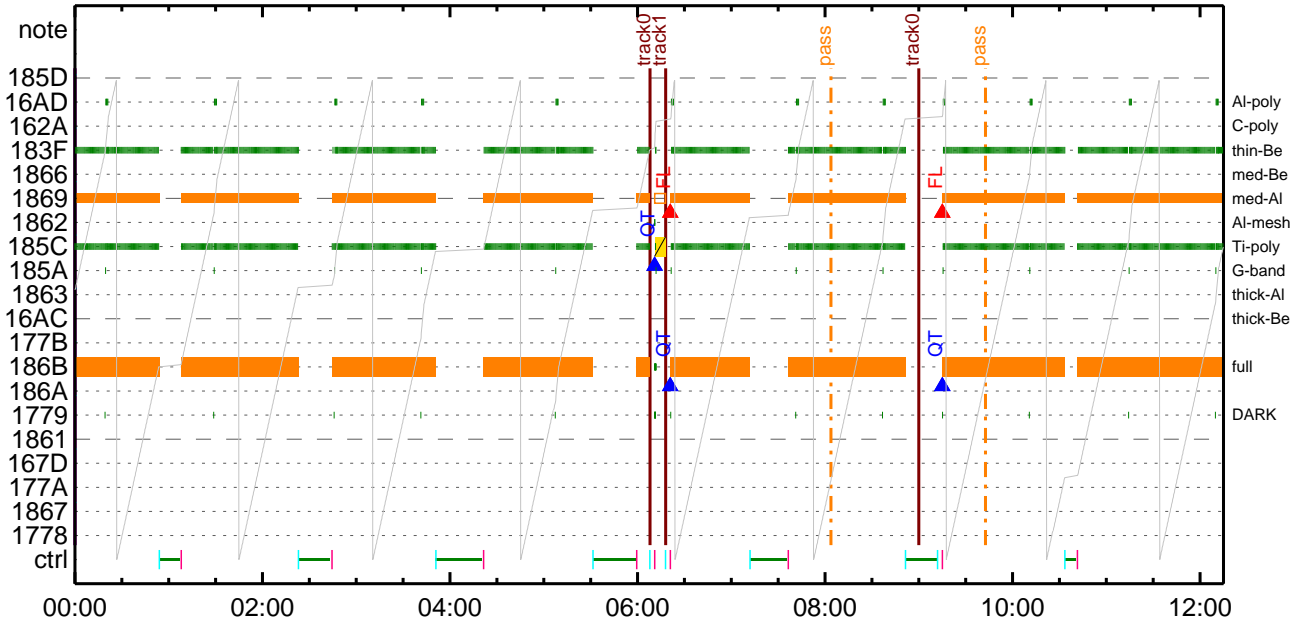
CMDI #0836 2011/03/22



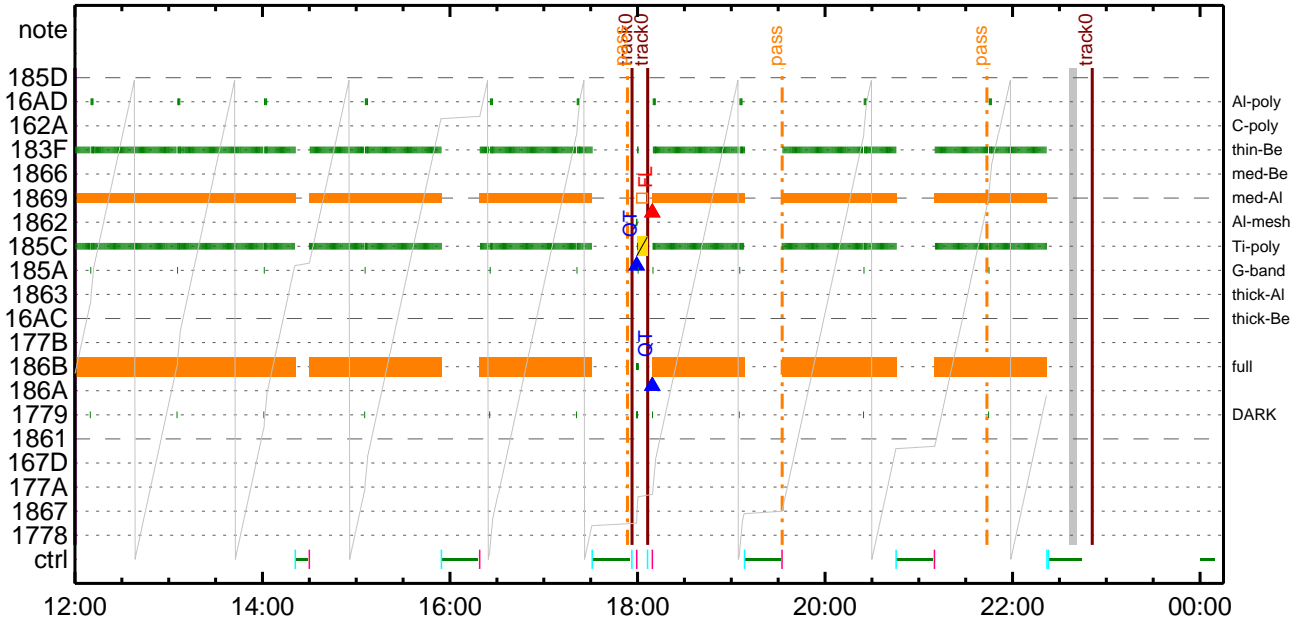
CMDI #0836 2011/03/22



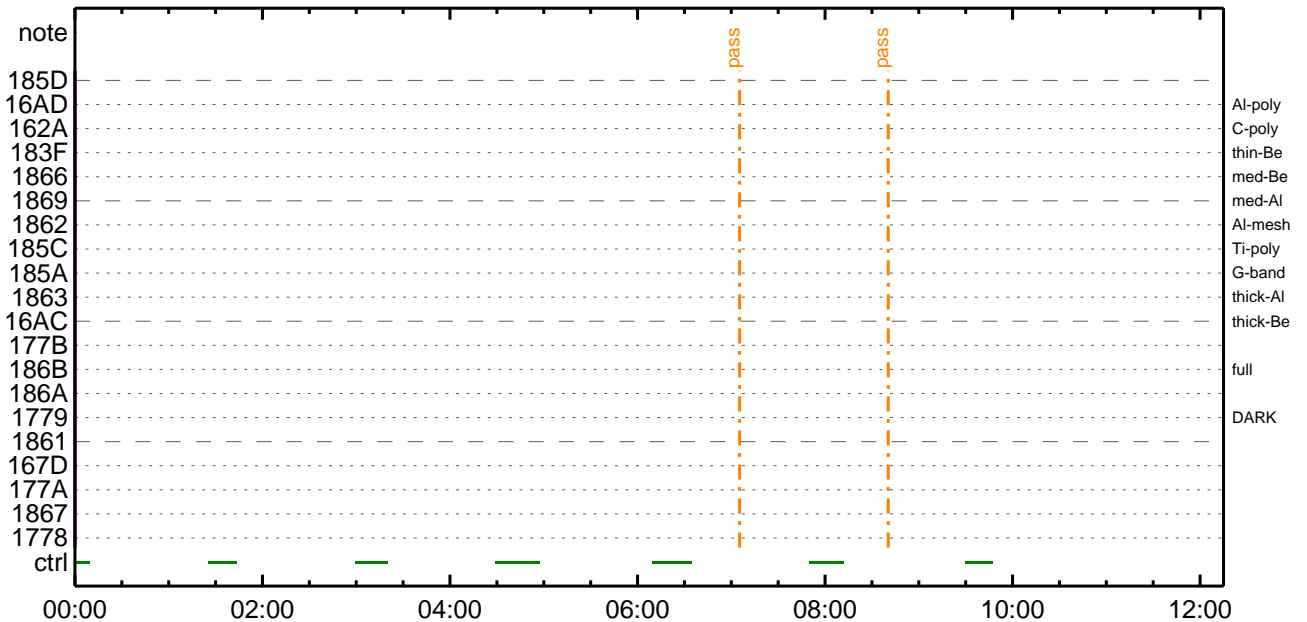
CMDI #0836 2011/03/23



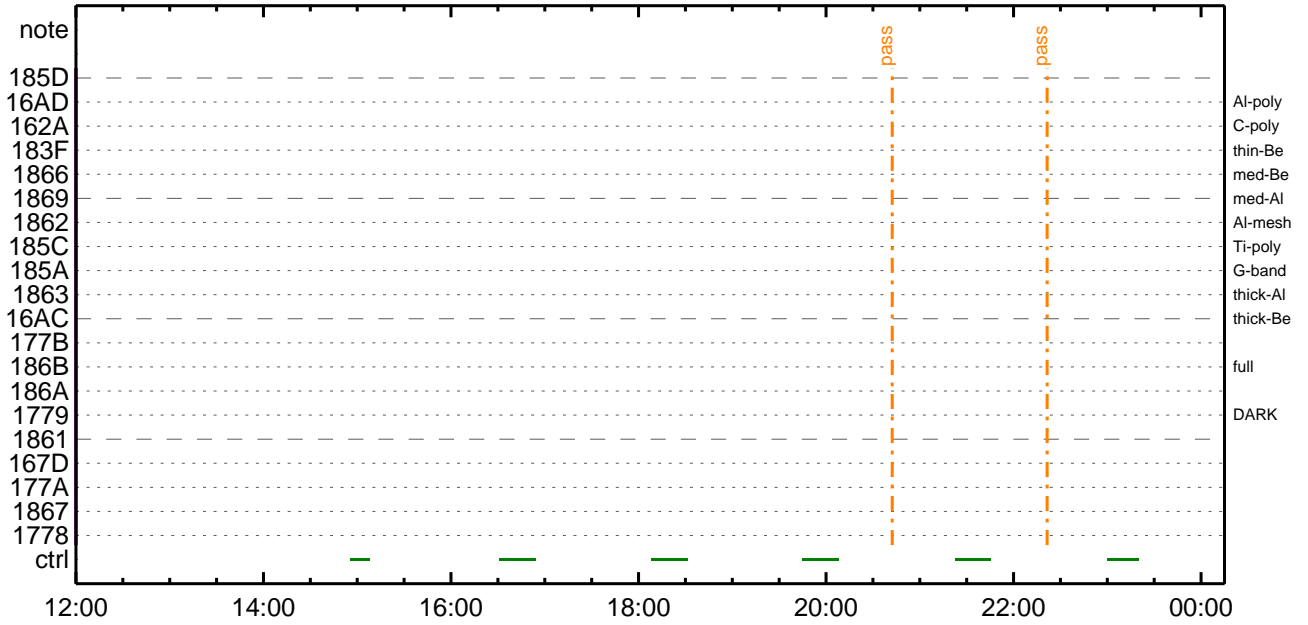
CMDI #0836 2011/03/23



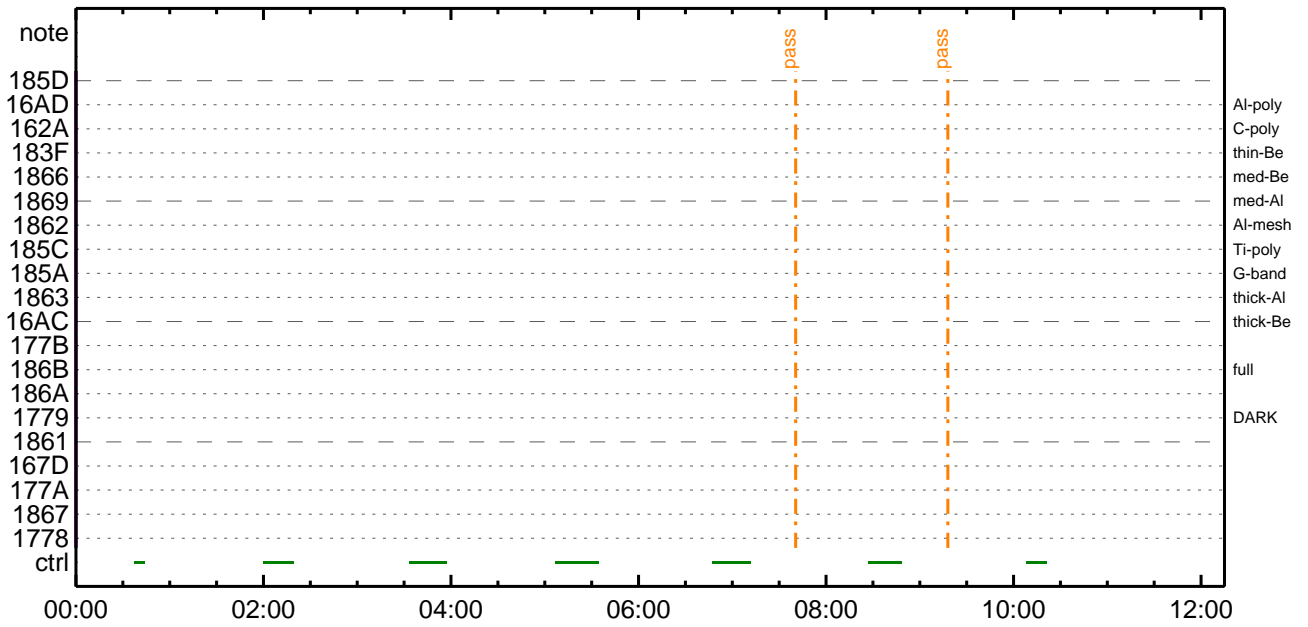
CMDI #0836 2011/03/24



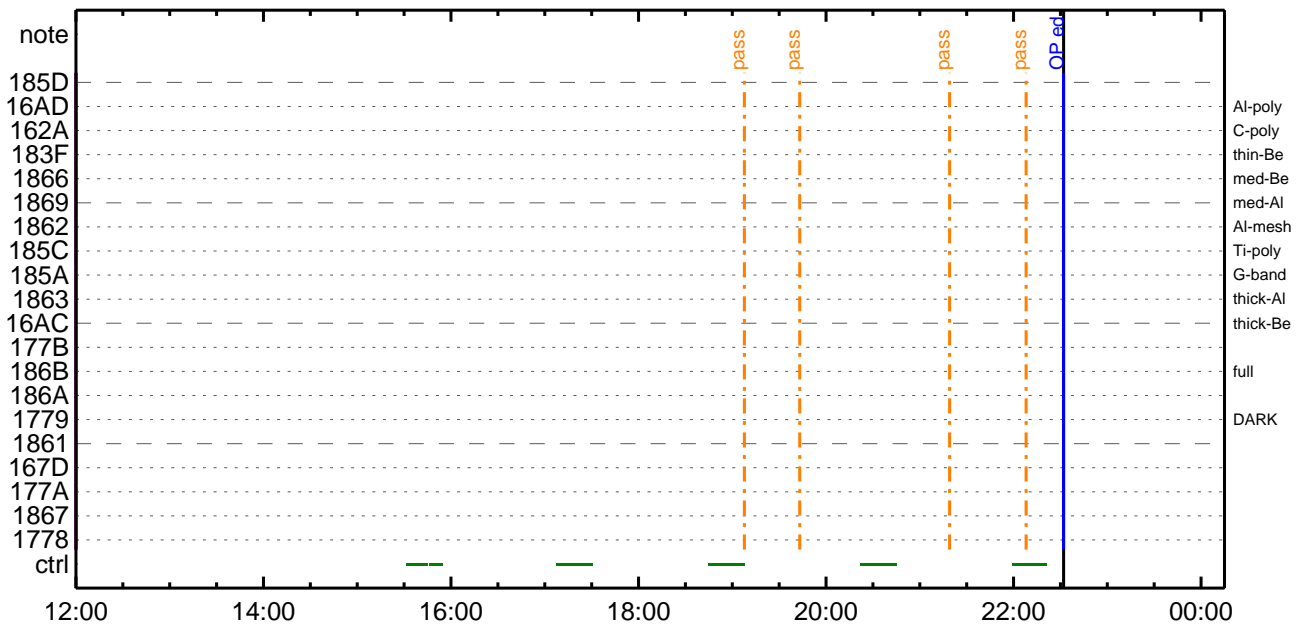
CMDI #0836 2011/03/24



CMDI #0836 2011/03/25



CMDI #0836 2011/03/25




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0096 C.                0302; SET0EDUMP01A±0iYNY¹0Ç¹00|0³0E;E
0097 C.
0098 . C. TIY³YFYYÖYÉ00dÄDİ¿(UT)
0099 +. TI 2011-03-17 22:43:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2011-03-17 22:43:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2011-03-17 22:43:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2011-03-17 22:47:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. 0E²¼0İÄê%îİÑ0İYÁY§YÁY-¹àİÜ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]            EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]           EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]              EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]              EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]            EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]           EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]              EQ        EXEC
0140 C.
0141 . C. YÄYÖY×½ªİ»0d³İÇ§
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]              EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y½, Yİ;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]              EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]            EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]           EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2011-03-17 22:47:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2011-03-17 22:47:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]            EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2011-03-17 22:47:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-03-17 22:47:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]            EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `úÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÄYÈY½YÉY¼YçYè0E¼¼0¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Upload DPL table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ãĚSTS_CHKãðOFFãĚã¹ãĚ
0109 C.
0110 . S. RAM ram-271:MDP_DPL
0111 ( )
0112 C.
0113 . C. < Dump RAMID=MDP_DPL >
0114 +. DC 07-F0 MDP_DUMP_FGTBL
0115 BC (82 07 00 38 b8 00 40)
0116 C. -----
0117 C. MDP_DPL verify = OK [ ]
0118 C. -----
0119 C.
0120 C. STS_CHKãðONãĚã¹ãĚ
0121 C.
0122 . C. < Update MDP DSC PAR1 >
0123 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124 BC (4c)
0125 C. MDP_CMD_CODE = F04C0700[ ]
0126 C. MDP_CMD_CNT (count-up 1) [ ]
0127 C. -----
0128 C.
0129 . C.
0130 C. *****
0131 C. SOT TI command set
0132 C. *****
0133 C. Execute, after the success of TBL upload.
0134 +. TI 2011-03-17 22:47:18.0
0135 DC 07-F0 MDP_SOT_MODE_OBSV
0136 BC (40)
0137 . C. -----
0138 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0139 C. -----
0140 C.
0141 C.
0142 C. ***** XRT START *****
0143 C.
0144 +. DC 07-F0 MDP_XRT_CTRL_MANU
0145 BC (c1)
0146 + DC 07-F0 MDP_XRT_MODE_STBY
0147 BC (c3)
0148 . C. ----- Success Verify ? OK / NG____
0149 C.
0150 C. XRT Obs. Table Upload
0151 . S. RAM ram-291:MDP_OBS_X
0152 ( )
0153 C.
0154 +. DC 07-F0 MDP_DUMP_XRTTBL
0155 BC (84 07 00 00 00 3a d4)
0156 . C. ----- Comparison Check ? OK / ERR ____
0157 C.
0158 C.
0159 +. DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 01 b1 b1 04 04)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 02 b1 b1 08 08)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 03 b1 b1 08 08)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 04 b1 b1 06 06)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 05 85 83 06 06)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 06 85 83 08 08)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 07 85 83 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 08 80 80 20 20)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 09 80 80 20 08)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 0a 80 80 08 20)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 0f 80 80 06 06)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 10 80 80 08 08)
0183 . C. ----- Success Verify ? OK / NG ____
0184 C.
0185 C.
0186 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0187 C.
0188 +. DC 07-F0 MDP_XRT_MODE_OBSV
0189 BC (c2)
0190 +. TI 2011-03-17 22:47:02.0
0191 DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 . C. ----- Success Verify ? OK / NG ____

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*** OP Sequence for XRT ***

2011/03/17	22:57:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/17	22:57:56.0	XRT_FOCUS_RECALIBRATE_432_OG [0x1b0]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2011/03/17	22:58:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/03/17	23:01:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/03/17	23:02:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/03/17	23:02:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/03/17	23:02:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/03/17	23:02:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/03/17	23:04:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/17	23:04:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2011/03/17	23:04:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f
2011/03/17	23:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/17	23:34:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/17	23:34:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/17	23:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/17	23:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/17	23:46:30.0	XRT_Custom_418_OG [0x1a2]			
2011/03/17	23:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/18	01:02:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/18	01:02:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/18	01:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/18	01:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/18	01:17:30.0	XRT_Custom_418_OG [0x1a2]			
2011/03/18	01:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/18	02:33:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/18	02:33:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/18	02:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/18	02:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/18	02:54:00.0	XRT_Custom_418_OG [0x1a2]			
2011/03/18	02:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/18	04:02:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/18	04:02:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/18	04:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/18	04:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/18	04:31:30.0	XRT_Custom_418_OG [0x1a2]			
2011/03/18	04:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/18	05:42:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/18	05:42:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/18	05:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/18	05:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/18	06:09:00.0	XRT_Custom_418_OG [0x1a2]			
2011/03/18	06:10:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/18	06:18:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/18	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/03/18	06:19:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/03/18	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/03/18	06:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/03/18	06:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5

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2011/03/18	06:21:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2011/03/18	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	06:28:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	06:28:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/03/18	06:29:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/03/18	06:29:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/03/18	06:29:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/03/18	06:29:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/03/18	06:29:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/18	06:31:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	06:31:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2011/03/18	06:31:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/03/18	06:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	07:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	07:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	07:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	07:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	07:46:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/18	07:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	09:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	09:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	09:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	09:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	09:22:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/18	09:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	10:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	10:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	10:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	10:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	10:49:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/18	10:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	14:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	14:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	14:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	14:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	14:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/18	14:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	16:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	16:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	16:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	16:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	16:29:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/18	16:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/18	17:42:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	17:42:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/18	17:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/18	17:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/18	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/18	18:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	

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2011/03/19	03:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/19	03:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/19	03:31:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/19	03:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	04:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	04:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/19	04:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/19	04:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/19	05:08:30.0	XRT_Custom_418_OG [0x1a2]					
2011/03/19	05:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	05:52:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	05:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/03/19	05:52:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/03/19	05:52:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/03/19	05:52:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/03/19	05:52:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/03/19	05:55:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2011/03/19	05:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	06:02:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	06:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/03/19	06:02:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/03/19	06:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/03/19	06:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/03/19	06:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/03/19	06:02:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/03/19	06:05:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/19	06:05:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2011/03/19	06:05:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f	
2011/03/19	06:05:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	06:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	06:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/19	06:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/19	06:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/19	06:46:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/19	06:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	08:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	08:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/19	08:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/19	08:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/19	08:23:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/19	08:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	09:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	09:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/19	09:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/19	09:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/19	09:57:30.0	XRT_Custom_418_OG [0x1a2]					
2011/03/19	09:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/19	15:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/19	15:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2011/03/19	15:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	15:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	15:19:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	15:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	15:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	15:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	15:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	15:28:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	15:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	15:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	16:41:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	16:41:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	16:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	16:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	17:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	17:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/03/19	17:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/03/19	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/03/19	17:55:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/03/19	17:55:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/19	17:58:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/03/19	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	18:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/03/19	18:05:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/03/19	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/03/19	18:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/03/19	18:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/03/19	18:05:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/19	18:08:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	18:08:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/03/19	18:08:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/03/19	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	18:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	18:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	18:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	18:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	18:42:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	18:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	19:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	19:55:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	19:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	19:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	20:19:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	20:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	21:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	21:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/03/19	21:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	21:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	21:56:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	21:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/19	23:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/19	23:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/19	23:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/19	23:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/19	23:28:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/19	23:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	00:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	00:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	00:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	00:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	00:54:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/20	00:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	02:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	02:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	02:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	02:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	02:30:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/20	02:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	03:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	03:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	03:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	03:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	04:07:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/20	04:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	05:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	05:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	05:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	05:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	05:45:00.0	XRT_Custom_418_OG [0x1a2]							
2011/03/20	05:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/03/20	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/03/20	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/03/20	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/03/20	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/20	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/03/20	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/03/20	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/03/20	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/03/20	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/03/20	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/03/20	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/20	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							

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2011/03/20	06:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2011/03/20	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/03/20	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	06:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	06:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	06:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	07:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	07:22:30.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	07:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	08:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	08:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	08:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	08:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	08:59:00.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	09:00:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	10:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	10:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	10:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	10:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	10:30:00.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	10:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	14:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	14:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	14:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	14:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	14:14:00.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	14:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	15:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	15:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	15:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	15:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	16:05:00.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	16:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	17:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	17:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/20	17:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/20	17:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/20	17:42:00.0	XRT_Custom_418_OG [0x1a2]								
2011/03/20	17:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	17:58:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	17:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/03/20	17:59:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2011/03/20	17:59:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/03/20	17:59:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/03/20	17:59:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/20	18:01:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2011/03/20	18:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/20	18:08:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/20	18:08:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	

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2011/03/21	04:45:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	05:28:24.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	05:28:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/03/21	05:28:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/03/21	05:28:46.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/03/21	05:28:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/03/21	05:28:50.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/03/21	05:31:28.0	XRT_QT_PROG_SET_407_OG [0x197]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2011/03/21	05:31:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	05:38:24.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	05:38:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/03/21	05:38:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2011/03/21	05:38:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/03/21	05:38:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/03/21	05:38:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/03/21	05:38:52.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/03/21	05:41:24.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	05:41:26.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2011/03/21	05:41:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f		
2011/03/21	05:41:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	05:56:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	05:56:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	05:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/03/21	05:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/03/21	06:22:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/21	06:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	07:36:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	07:36:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	07:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/03/21	07:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/03/21	07:59:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/21	08:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	09:16:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	09:16:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	09:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/03/21	09:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/03/21	09:35:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/21	09:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	14:43:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	14:43:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	14:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/03/21	14:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/03/21	14:54:30.0	XRT_Custom_418_OG [0x1a2]					
2011/03/21	14:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/03/21	16:18:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/03/21	16:18:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/03/21	16:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/03/21	16:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/03/21	16:41:30.0	XRT_Custom_418_OG [0x1a2]					

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2011/03/21	16:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/21	17:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/21	17:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/21	17:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/21	17:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/21	18:18:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/21	18:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/03/21	18:18:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/03/21	18:18:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/03/21	18:18:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/03/21	18:18:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/21	18:21:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/03/21	18:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/21	18:28:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/03/21	18:31:00.0	XRT_CTRL_MANU_440_OG [0x1b8]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/21	18:31:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/03/21	20:31:30.0	XRT_Custom_444_OG [0x1bc]							
2011/03/21	22:31:30.0	XRT_Custom_444_OG [0x1bc]							
2011/03/22	00:31:30.0	XRT_Custom_444_OG [0x1bc]							
2011/03/22	02:31:30.0	XRT_Custom_447_OG [0x1bf]							
2011/03/22	02:31:40.0	XRT_Custom_444_OG [0x1bc]							
2011/03/22	04:31:40.0	XRT_Custom_448_OG [0x1c0]							
2011/03/22	06:04:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/03/22	06:14:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/03/22	06:31:18.0	XRT_Custom_447_OG [0x1bf]							
2011/03/22	06:31:28.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/22	06:31:30.0	XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2011/03/22	12:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/22	12:31:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/03/22	12:31:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/03/22	12:31:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/03/22	12:31:26.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/03/22	12:31:28.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/22	12:34:00.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/22	12:34:02.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/03/22	12:34:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/03/22	12:34:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/22	15:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/22	15:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/22	15:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/22	15:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/22	15:41:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/22	15:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/22	16:54:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/22	16:54:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/22	16:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/22	16:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/22	17:18:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/22	17:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/22	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/22	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				

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2011/03/23	02:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/23	02:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/23	02:43:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/23	02:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	03:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	03:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/23	03:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/23	03:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/23	04:20:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/23	04:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	05:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	05:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/23	05:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/23	05:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/23	05:58:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/23	05:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	06:07:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	06:07:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/03/23	06:08:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/03/23	06:08:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/03/23	06:08:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/03/23	06:08:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/23	06:10:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/03/23	06:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	06:17:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	06:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/03/23	06:18:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/03/23	06:18:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/03/23	06:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/03/23	06:18:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/03/23	06:18:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/03/23	06:20:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/23	06:20:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/03/23	06:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/03/23	06:21:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	07:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	07:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/23	07:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/23	07:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/23	07:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/03/23	07:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/03/23	08:51:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	08:51:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/03/23	08:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/03/23	08:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/03/23	09:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 e8 fe b0 f6				
2011/03/23	09:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/03/23	09:12:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/03/23	09:12:22.0	XRT_FLD_ENA_411_OG [0x19b]							

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2011/03/23	09:12:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/03/23	09:12:26.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/03/23	09:12:28.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/03/23	09:15:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	09:15:02.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2011/03/23	09:15:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f
2011/03/23	09:15:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/23	10:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	10:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	10:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/23	10:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/23	10:40:30.0	XRT_Custom_418_OG [0x1a2]					
2011/03/23	10:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/23	14:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	14:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	14:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/23	14:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/23	14:29:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/23	14:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/23	15:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	15:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	15:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/23	15:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/23	16:18:00.0	XRT_Custom_418_OG [0x1a2]					
2011/03/23	16:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/23	17:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	17:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	17:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/03/23	17:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/03/23	17:56:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	17:56:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/03/23	17:56:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2011/03/23	17:56:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/03/23	17:56:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/03/23	17:56:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/03/23	17:59:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2011/03/23	17:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/03/23	18:06:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/03/23	18:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2011/03/23	18:06:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 e8 fe b0	f6
2011/03/23	18:06:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/03/23	18:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/03/23	18:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/03/23	18:06:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/03/23	18:09:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/03/23	18:09:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2011/03/23	18:09:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f
2011/03/23	18:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]					

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2011/03/23	19:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/23	19:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/23	19:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/23	19:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/23	19:31:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/23	19:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/03/23	20:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/23	20:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/23	20:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/23	20:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/23	21:09:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/03/23	21:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/03/23	22:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/03/23	22:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/23	22:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/03/23	22:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/03/23	22:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/03/23	22:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00