

XRT Timeline to be uploaded on 2011/04/02

Period: 2011/04/02 22:35:00 - 2011/04/12 22:12:00

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Normal mode

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XOB #186D: AR dynamics 3 min cadence long exp (512FOV) short exp (384FOV) Thin-Be Ti-poly												
Term	Pointing (x, y)	Comment										
04/02 22:48:00 - 04/03 06:23:24	Track (852.1, -274.0) ^{Ⓜ 04/02 22:45:00}	# OP start + 10min, AR 11176										
04/03 06:36:30 - 04/03 17:46:30	Track (872.9, -281.2) ^{Ⓜ 04/03 06:33:30}	# AR 11176										
04/03 18:23:00 - 04/04 05:56:54	Fixed (887.0, -292.0)	# AR 11176										
PROG= 06 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 48 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ Subr= 1 15-time(s) 180.0sec												
└─┬─┬─ Seqn= 95 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 3 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 2.00s Obs 1x1 512x512 (1064, 1048) Q=90 2 0 2.0sec												
└─┬─┬─ Seqn= 31 1-time(s) 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 12ms Obs 1x1 384x384 (1064, 1048) Q=90 3 0 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 16ms Obs 1x1 512x512 (1064, 1048) Q=90 2 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 1 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 12ms Obs 1x1 384x384 (1064, 1048) Q=90 0 1 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 2 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 12ms Obs 1x1 384x384 (1064, 1048) Q=90 0 2 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 3 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 12ms Obs 1x1 384x384 (1064, 1048) Q=90 0 3 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36												
Term	Pointing (x, y)	Comment										
04/03 06:26:30 - 04/03 06:33:24	Fixed (0.0, 0.0)	synoptic, shifted 23.5 min										
04/03 18:13:00 - 04/03 18:19:54	Fixed (0.0, 0.0)	synoptic, shifted 10.0 min										
04/04 06:00:00 - 04/04 06:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min										
04/04 18:03:00 - 04/04 18:09:54	Fixed (0.0, 0.0)	synoptic										
04/05 06:03:00 - 04/05 06:09:54	Fixed (0.0, 0.0)	synoptic										
04/05 20:23:00 - 04/05 20:29:54	Fixed (0.0, 0.0)	synoptic, shifted										
04/06 05:36:00 - 04/06 05:42:54	Fixed (0.0, 0.0)	synoptic, shifted -27.0 min										
04/06 18:26:00 - 04/06 18:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min										
04/07 05:57:30 - 04/07 06:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min										
04/07 18:03:00 - 04/07 18:36:00	Fixed (0.0, 0.0)	synoptic extended										
PROG= 13 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 7 1-time(s) 4.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Seqn= 5 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Seqn= 8 1-time(s) 4.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Seqn= 32 1-time(s) 2.0sec												
└─┬─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Seqn= 4 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #186E: AR dynamics 5 min cadence long exp (512FOV) short exp (384FOV) Thin-Be Ti-poly												
Term	Pointing (x, y)	Comment										
04/04 06:14:00 - 04/04 17:59:54	Fixed (887.0, -292.0)	# AR 11176										
04/04 18:13:00 - 04/05 05:59:54	Fixed (887.0, -292.0)	# AR 11176										
04/05 12:03:00 - 04/05 20:19:54	Fixed (822.0, 425.0)	# AR 11180										
04/06 00:03:00 - 04/06 05:32:54	Track (765.4, 278.9) ^{Ⓜ 04/06 00:00:00}	# AR 11183										
04/06 05:46:00 - 04/06 17:59:30	Track (793.6, 274.0) ^{Ⓜ 04/06 05:43:00}	# AR 11183										
04/06 21:43:00 - 04/07 05:54:24	Track (859.0, 259.7) ^{Ⓜ 04/06 21:40:00}	# AR 11183										
04/07 06:07:30 - 04/07 17:59:54	Fixed (892.0, 252.0)	# AR 11183										
PROG= 03 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 48 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ Subr= 1 8-time(s) 300.0sec												
└─┬─┬─ Seqn= 95 1-time(s) 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1064, 1048)	Q=90	2	0	2.0sec
Seqn= 31 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=90	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16ms	Obs	1x1	512x512 (1064, 1048)	Q=90	2	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	3	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #186F: Full Sun 6 Filter- 1x1 Q98 -FW1/FW2-short short exposures

Term	Pointing (x, y)	Comment
04/05 06:45:00 - 04/05 07:03:00	Fixed (0.0, 0.0)	# HOP 130 3
PROG= 09 1-time(s)		
Subr= 1 1-time(s) 180.0sec		
Seqn= 52 1-time(s) 30.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 20 1-time(s) 30.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 125ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 27 1-time(s) 30.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 63 1-time(s) 30.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 77 1-time(s) 2.0sec		
med-Al/Open	med-Al/Open	close Safe Norm 16.0s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open	close Safe Norm 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be	close Safe Norm 32.0s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 360.0sec		
Seqn= 83 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Dark 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 4 1-time(s) 4.0sec		
Open/G-band	Open/G-band	open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1869: Flare standard obs. multfilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
04/02 22:48:00 - 04/03 06:23:24	Track (852.1, -274.0) @ 04/02 22:45:00	# OP start + 10min, AR 11176
04/03 06:36:30 - 04/03 17:46:30	Track (872.9, -281.2) @ 04/03 06:33:30	# AR 11176
04/03 18:23:00 - 04/04 05:56:54	Fixed (887.0, -292.0)	# AR 11176
04/04 06:14:00 - 04/04 17:59:54	Fixed (887.0, -292.0)	# AR 11176
04/04 18:13:00 - 04/05 05:59:54	Fixed (887.0, -292.0)	# AR 11176
04/05 12:03:00 - 04/05 20:19:54	Fixed (822.0, 425.0)	# AR 11180
04/06 00:03:00 - 04/06 05:32:54	Track (765.4, 278.9) @ 04/06 00:00:00	# AR 11183
04/06 05:46:00 - 04/06 17:59:30	Track (793.6, 274.0) @ 04/06 05:43:00	# AR 11183
04/06 21:43:00 - 04/07 05:54:24	Track (859.0, 259.7) @ 04/06 21:40:00	# AR 11183
04/07 06:07:30 - 04/07 17:59:54	Fixed (892.0, 252.0)	# AR 11183
PROG= 15 1-time(s)		
Subr= 1 4-time(s) 2.0sec		
Seqn= 55 45-time(s) 20.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 90 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 600.0sec		
Seqn= 89 1-time(s) 2.0sec		
Open/Al-mesh	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

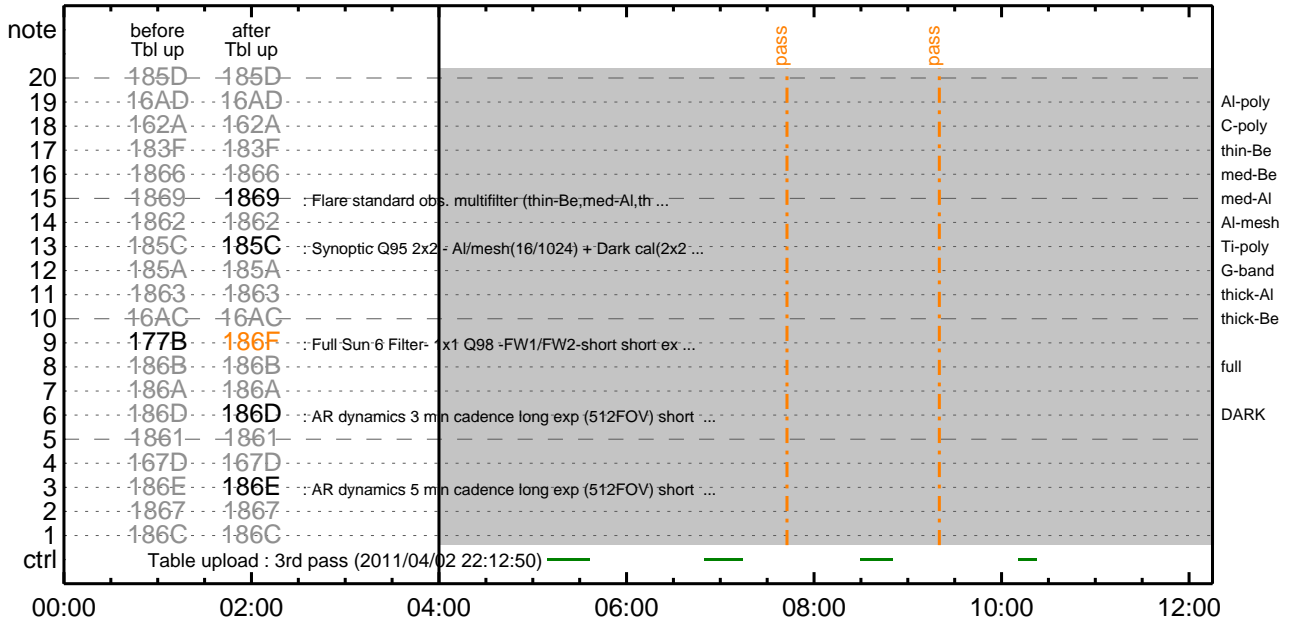
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Flare Detection

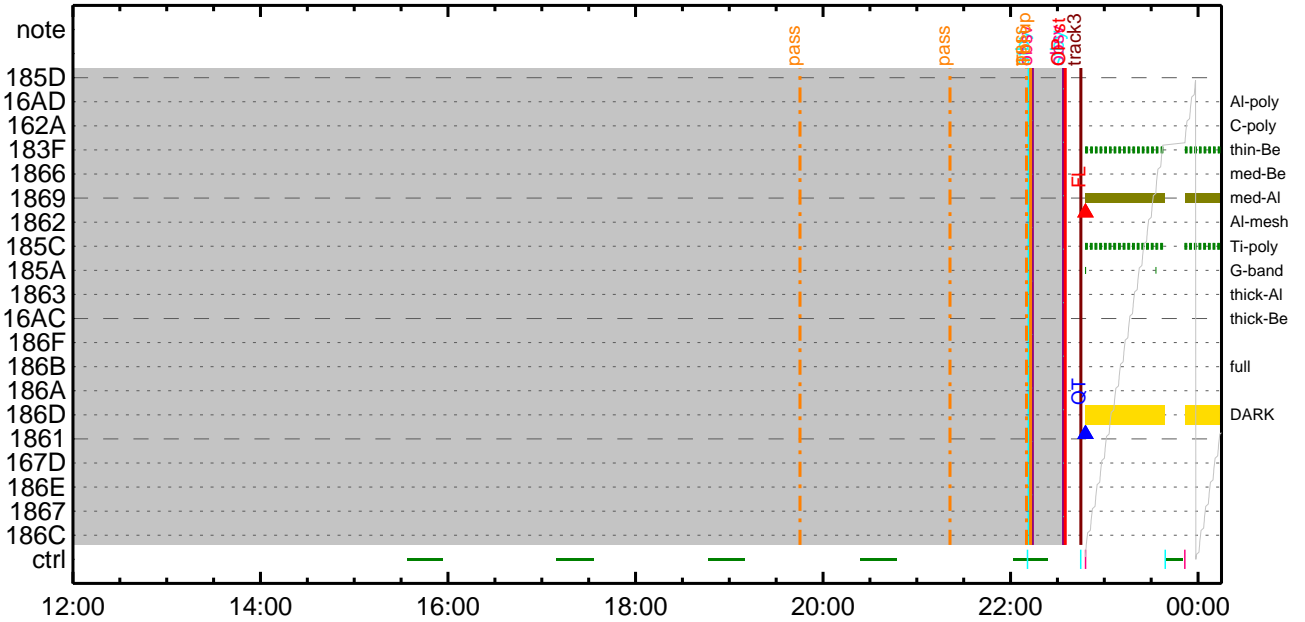
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FLD Patrol									
Term		Pointing (x, y)				Comment			
04/02 22:45:16 - 04/03 06:23:46		Track (852.1, -274.0)		④ 04/02 22:45:00		# OP start + 10min, AR 11176			
04/03 06:33:46 - 04/03 18:10:16		Track (872.9, -281.2)		④ 04/03 06:33:30		# AR 11176			
04/03 18:20:16 - 04/04 05:57:16		Fixed (887.0, -292.0)				# AR 11176			
04/04 06:11:16 - 04/04 18:00:16		Fixed (887.0, -292.0)				# AR 11176			
04/04 18:10:16 - 04/05 06:00:16		Fixed (887.0, -292.0)				# AR 11176			
04/05 12:00:16 - 04/05 20:20:16		Fixed (822.0, 425.0)				# AR 11180			
04/06 00:00:16 - 04/06 05:33:16		Track (765.4, 278.9)		④ 04/06 00:00:00		# AR 11183			
04/06 05:43:16 - 04/06 18:23:16		Track (793.6, 274.0)		④ 04/06 05:43:00		# AR 11183			
04/06 21:40:16 - 04/07 05:54:46		Track (859.0, 259.7)		④ 04/06 21:40:00		# AR 11183			
04/07 06:04:46 - 04/07 18:00:16		Fixed (892.0, 252.0)				# AR 11183			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

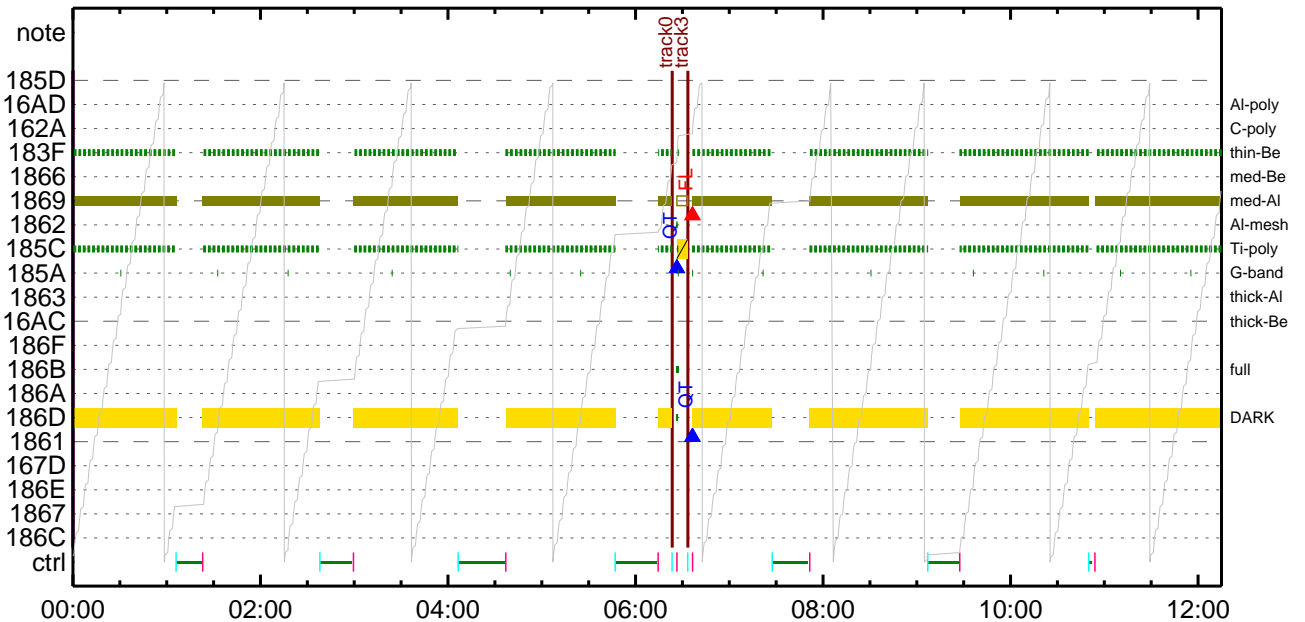
CMDI #0860 2011/04/02



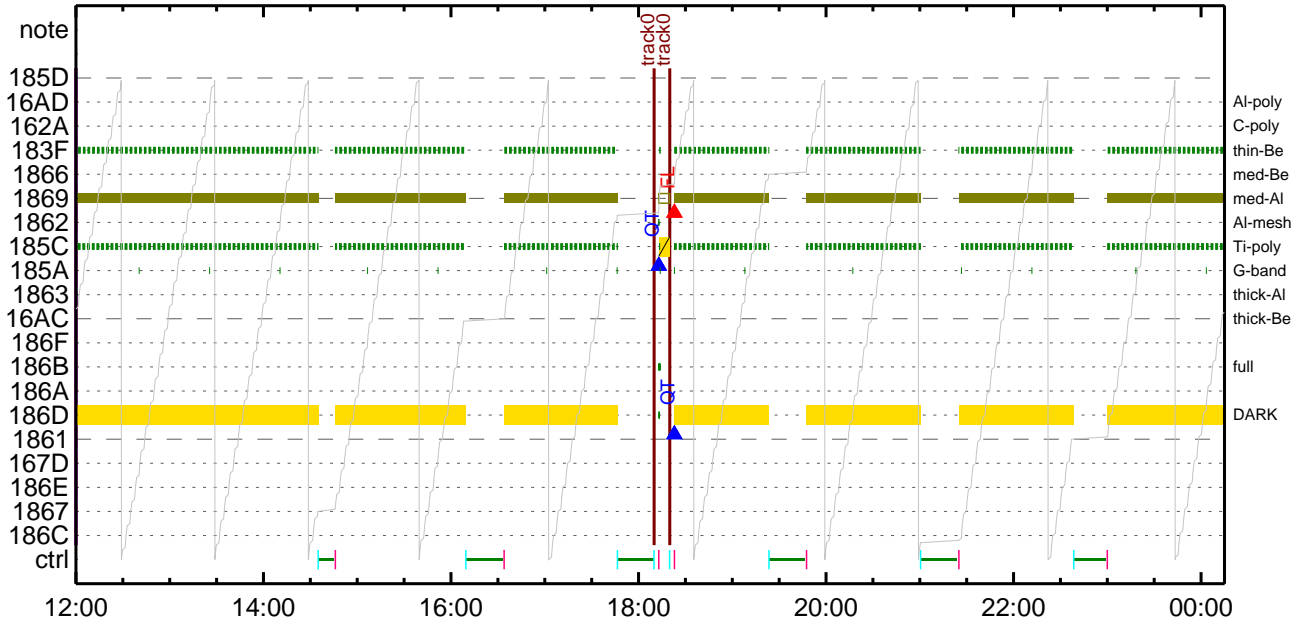
CMDI #0860 2011/04/02



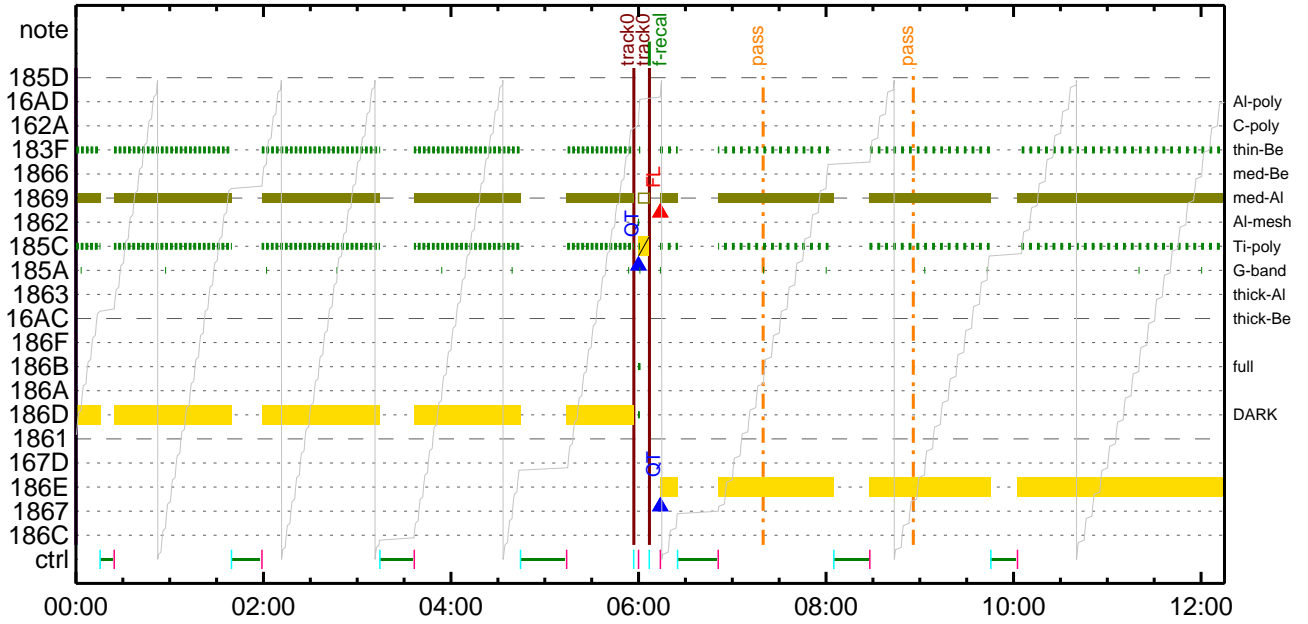
CMDI #0860 2011/04/03



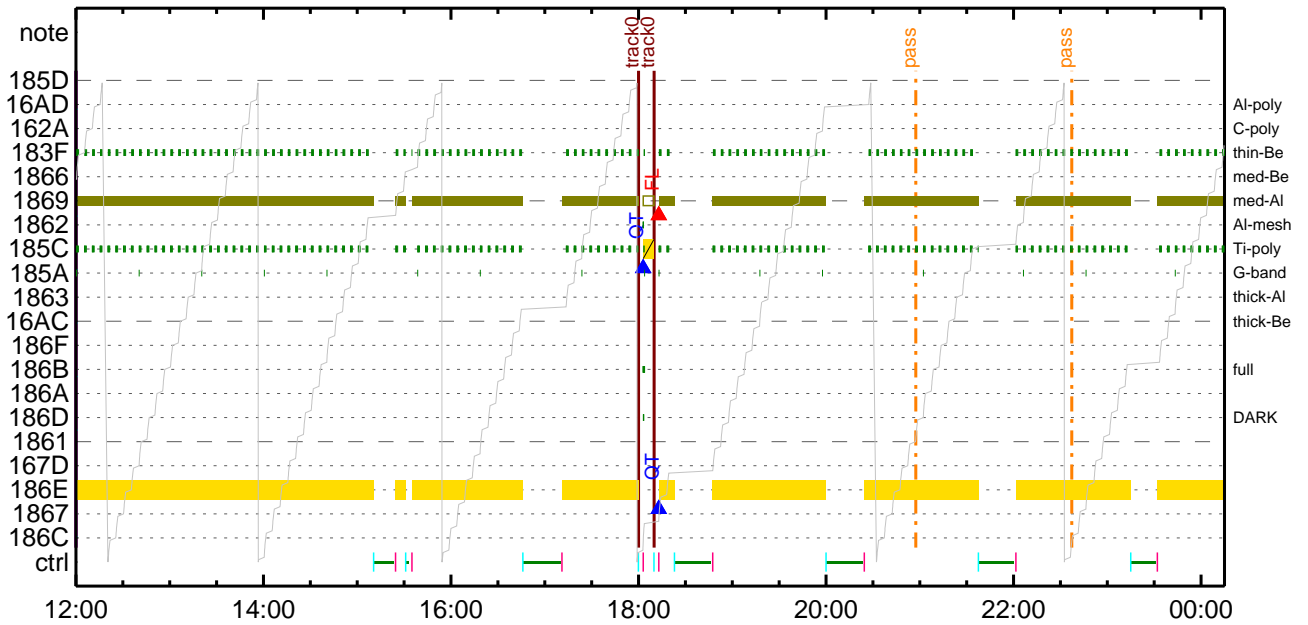
CMDI #0860 2011/04/03



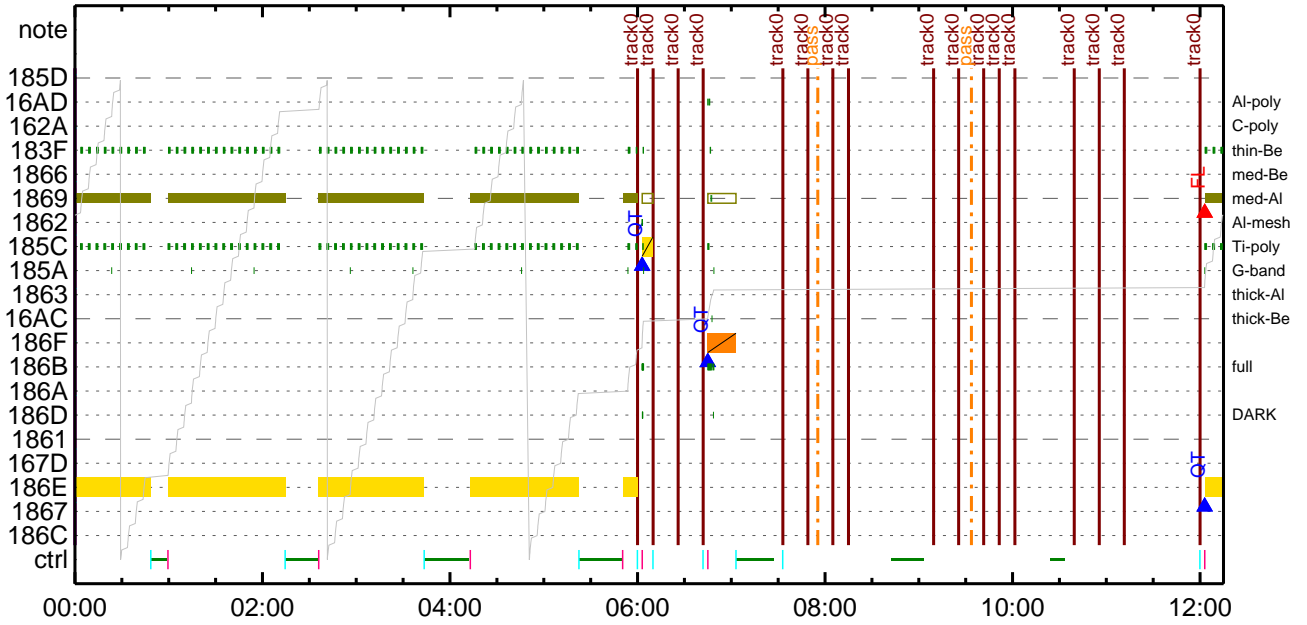
CMDI #0860 2011/04/04



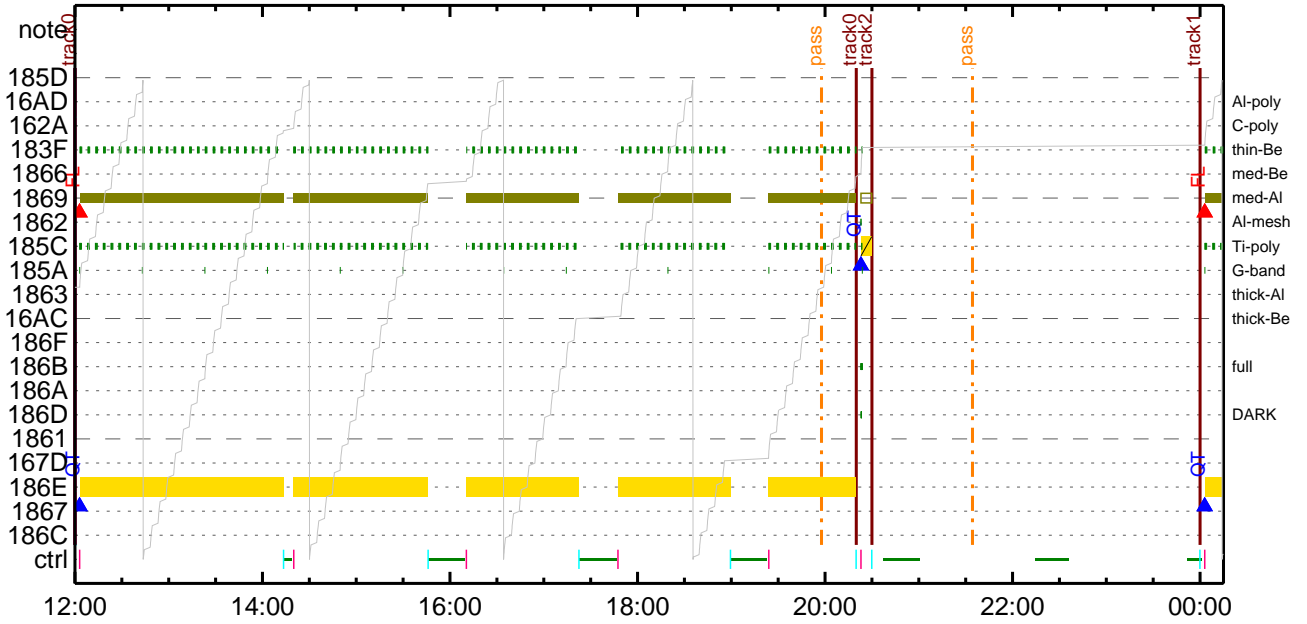
CMDI #0860 2011/04/04



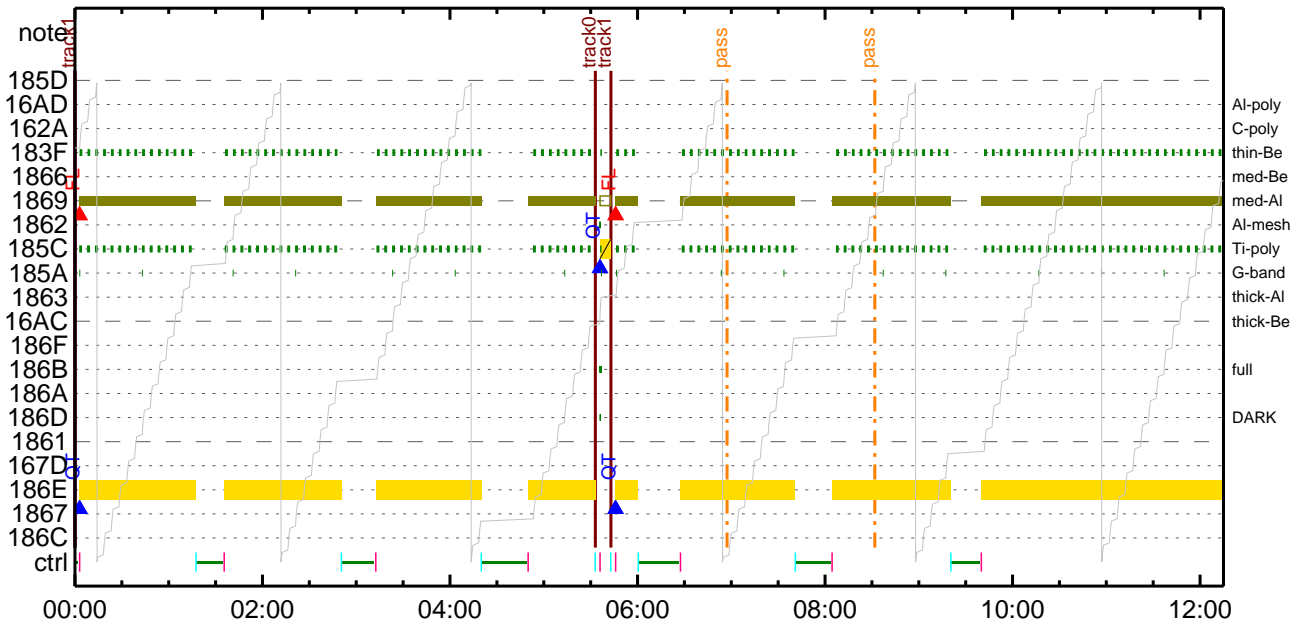
CMDI #0860 2011/04/05



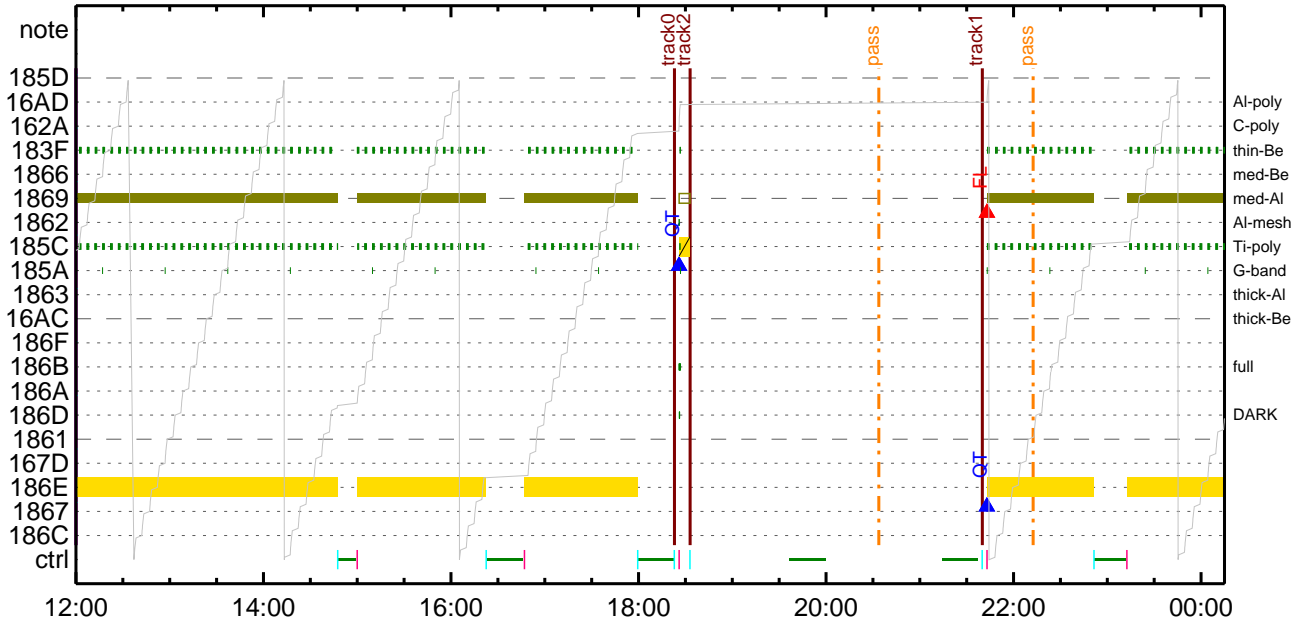
CMDI #0860 2011/04/05



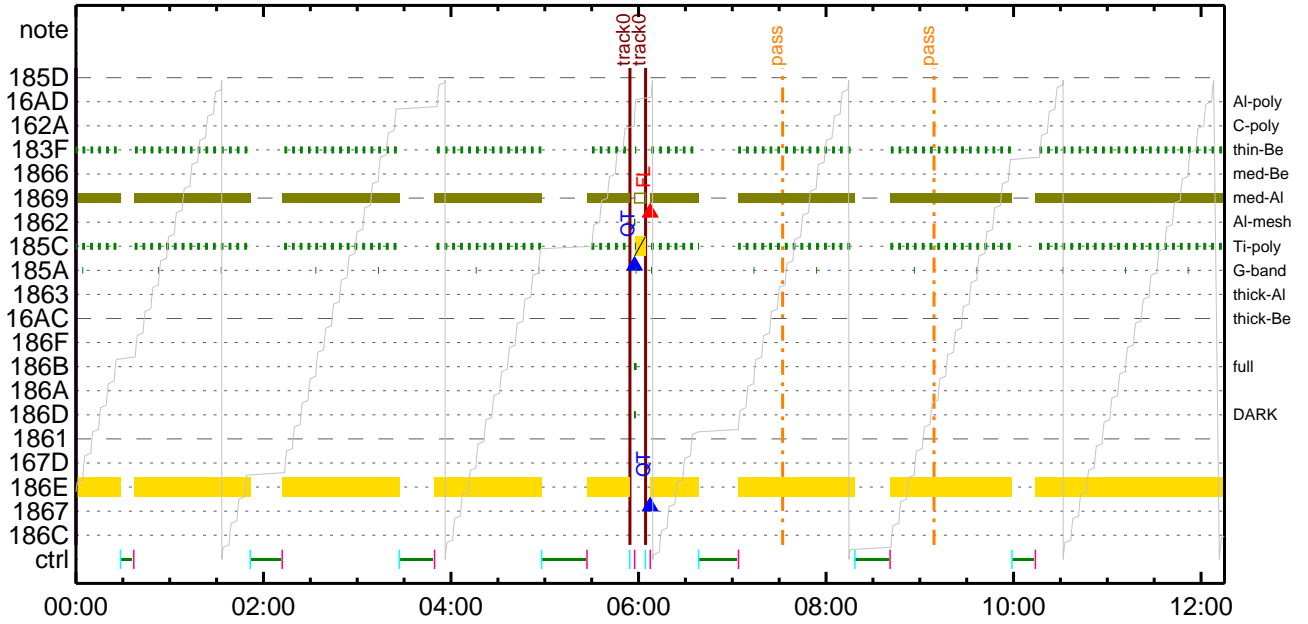
CMDI #0860 2011/04/06



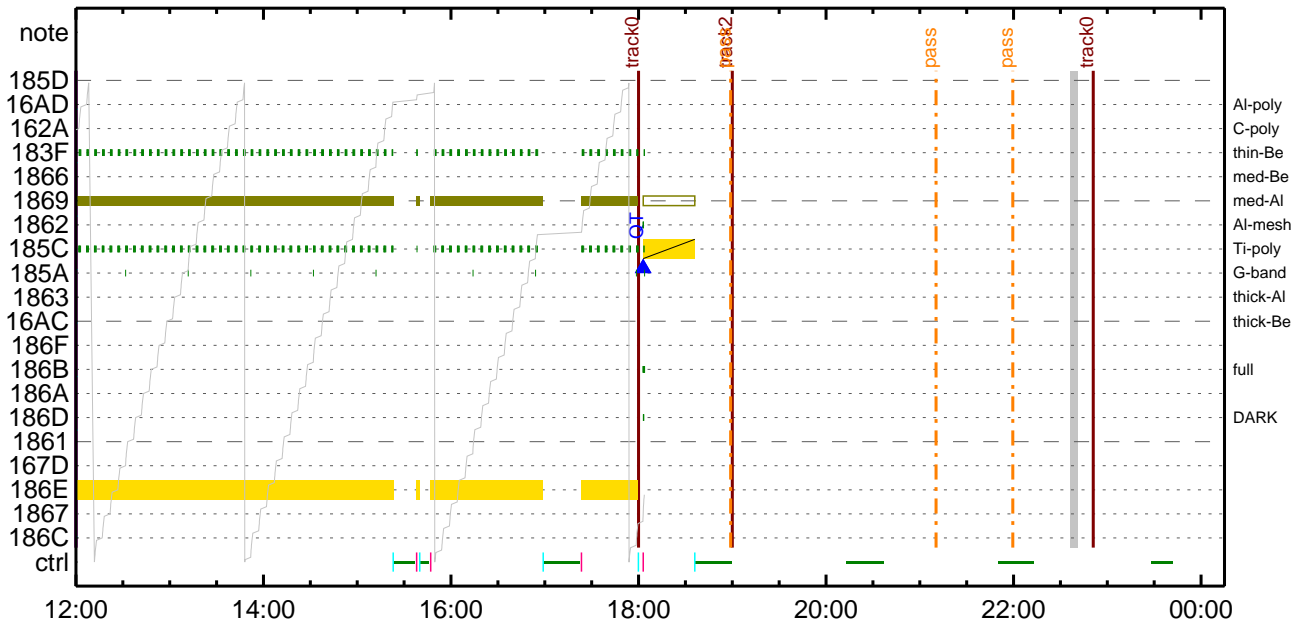
CMDI #0860 2011/04/06



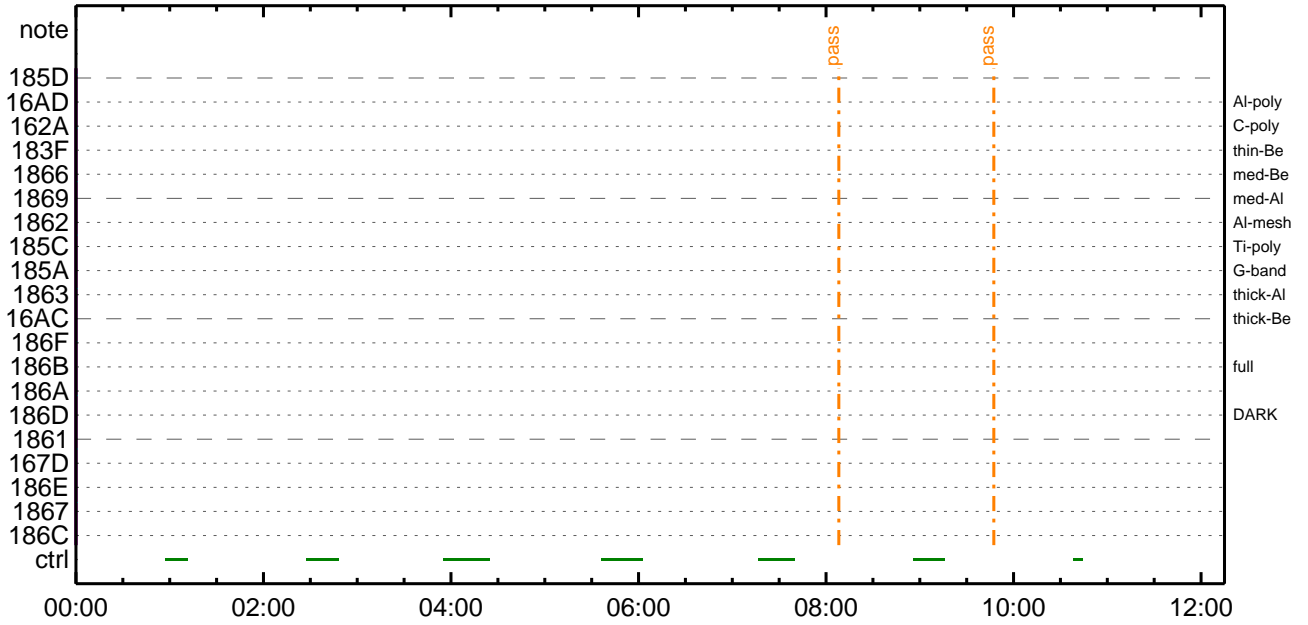
CMDI #0860 2011/04/07



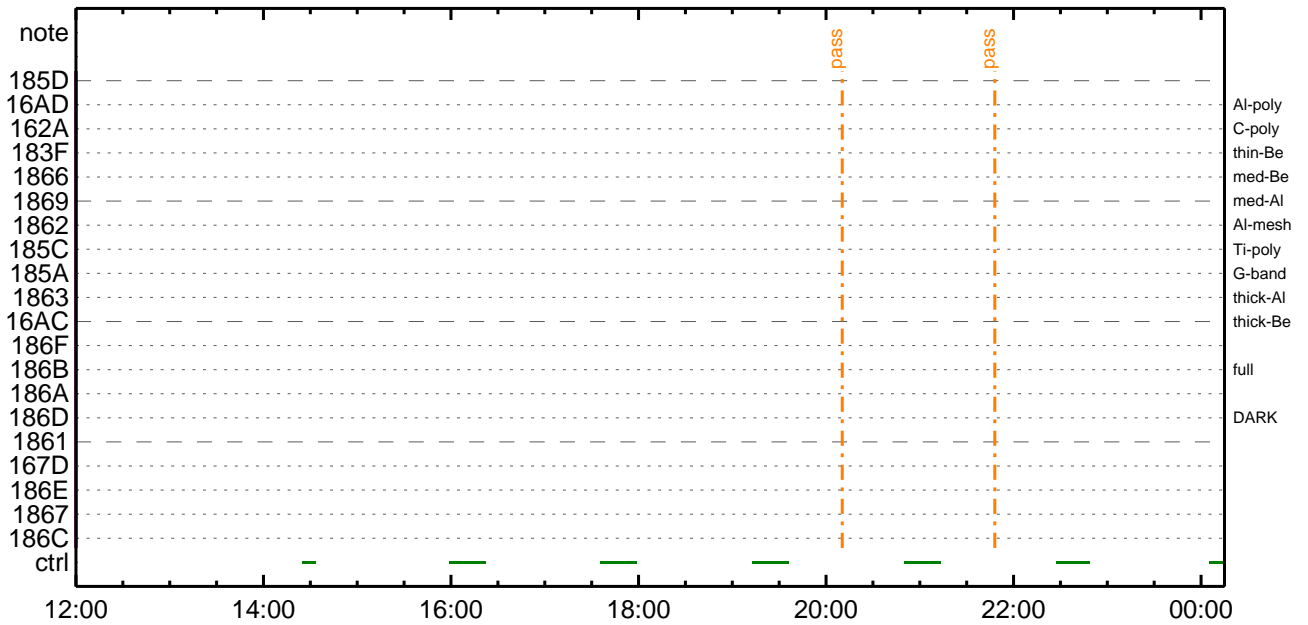
CMDI #0860 2011/04/07



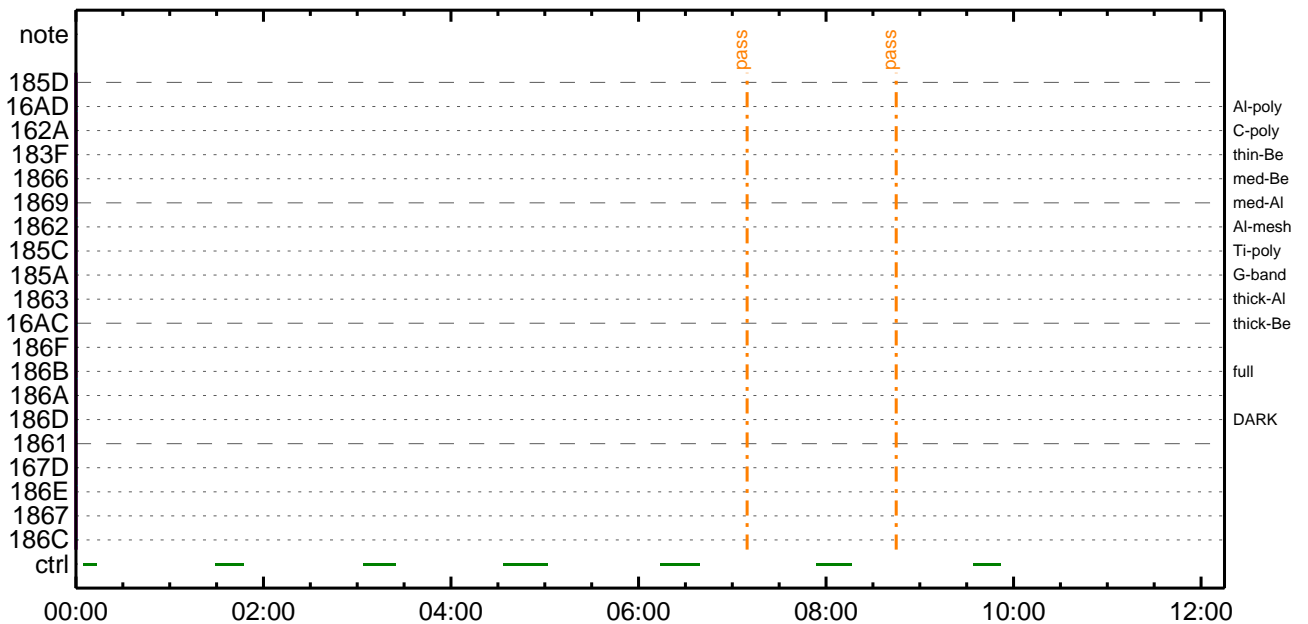
CMDI #0860 2011/04/08



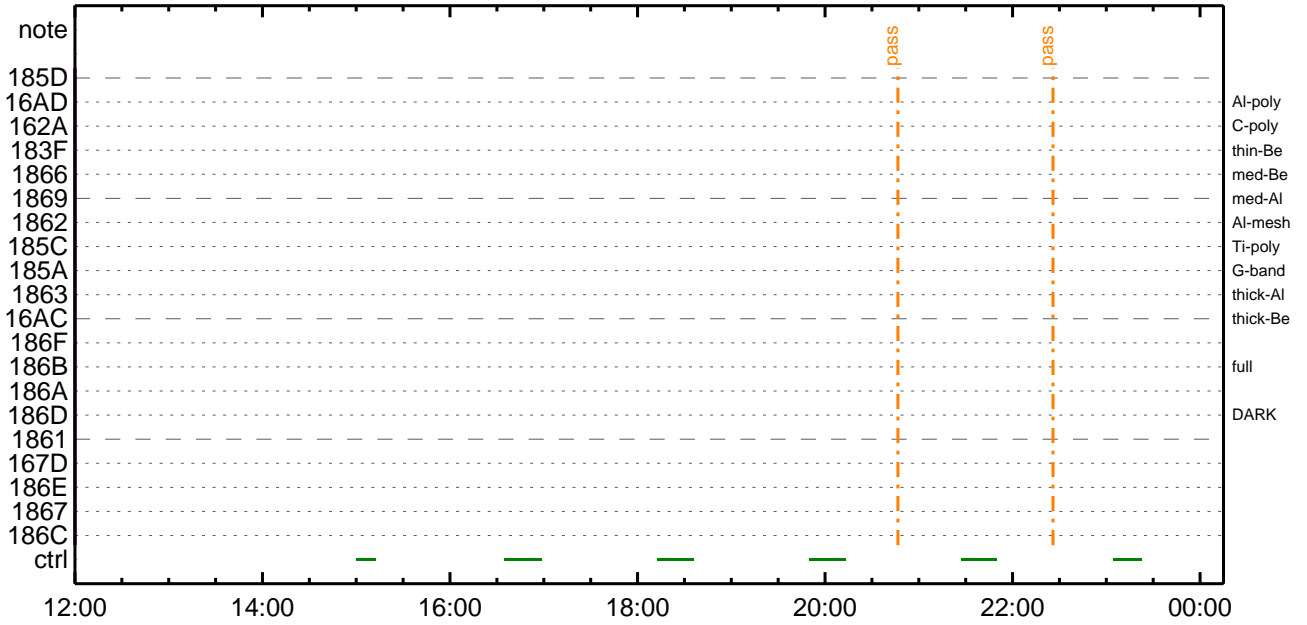
CMDI #0860 2011/04/08



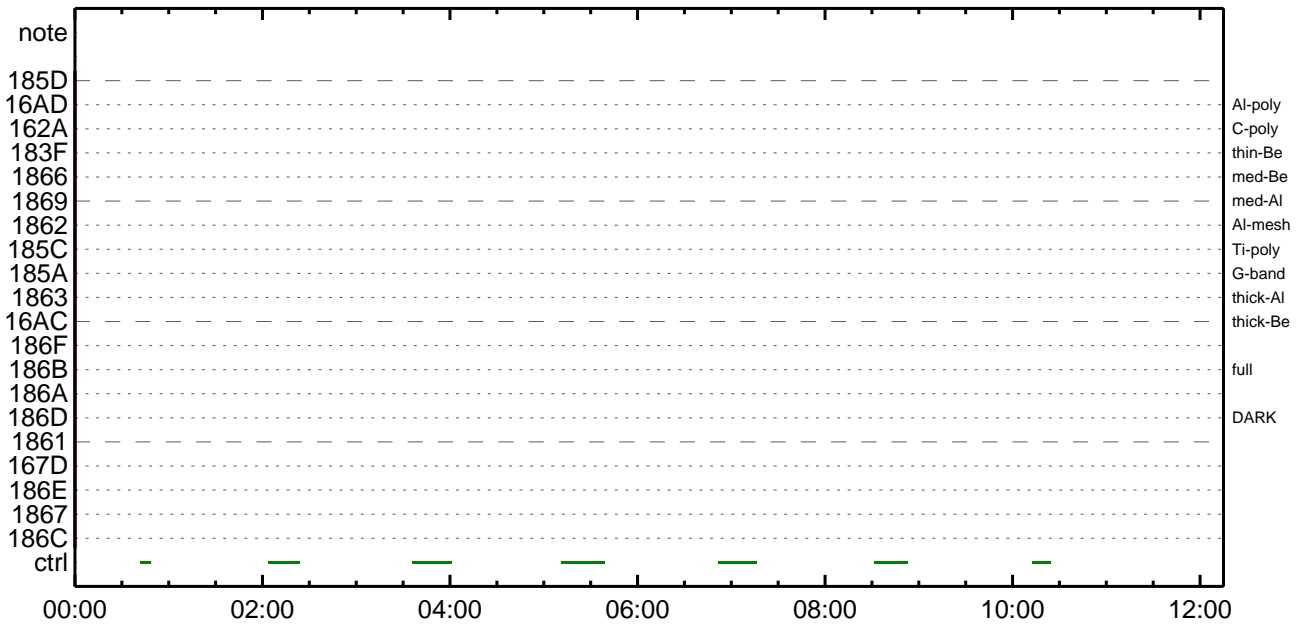
CMDI #0860 2011/04/09



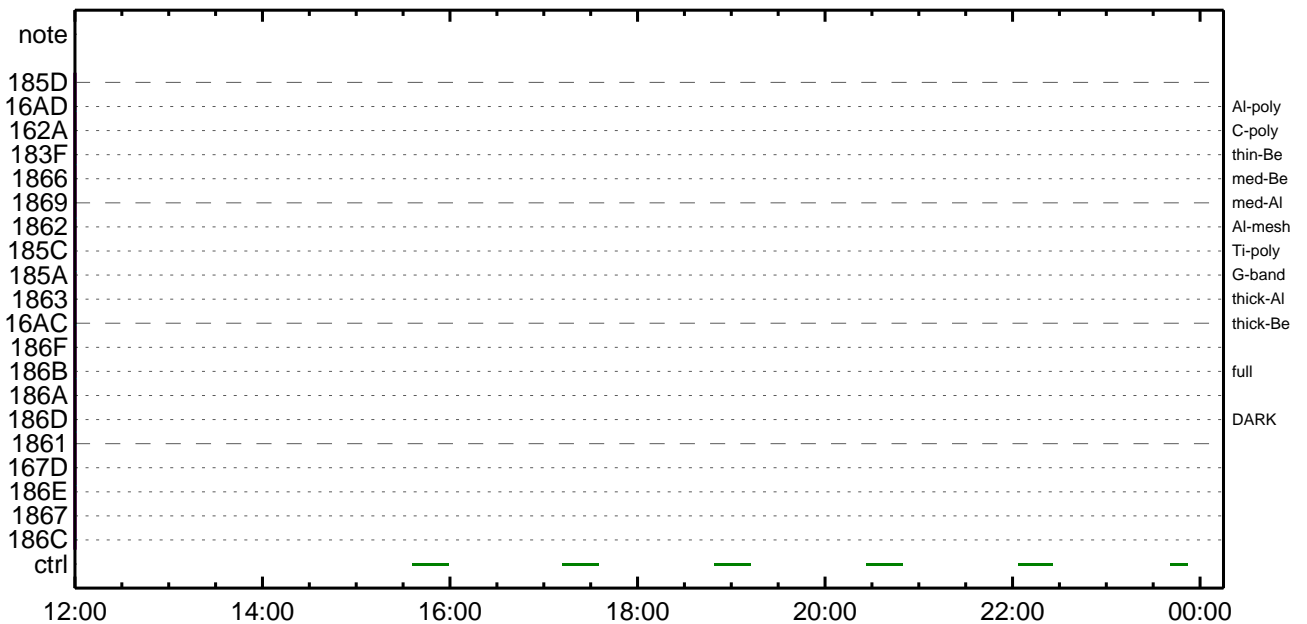
CMDI #0860 2011/04/09



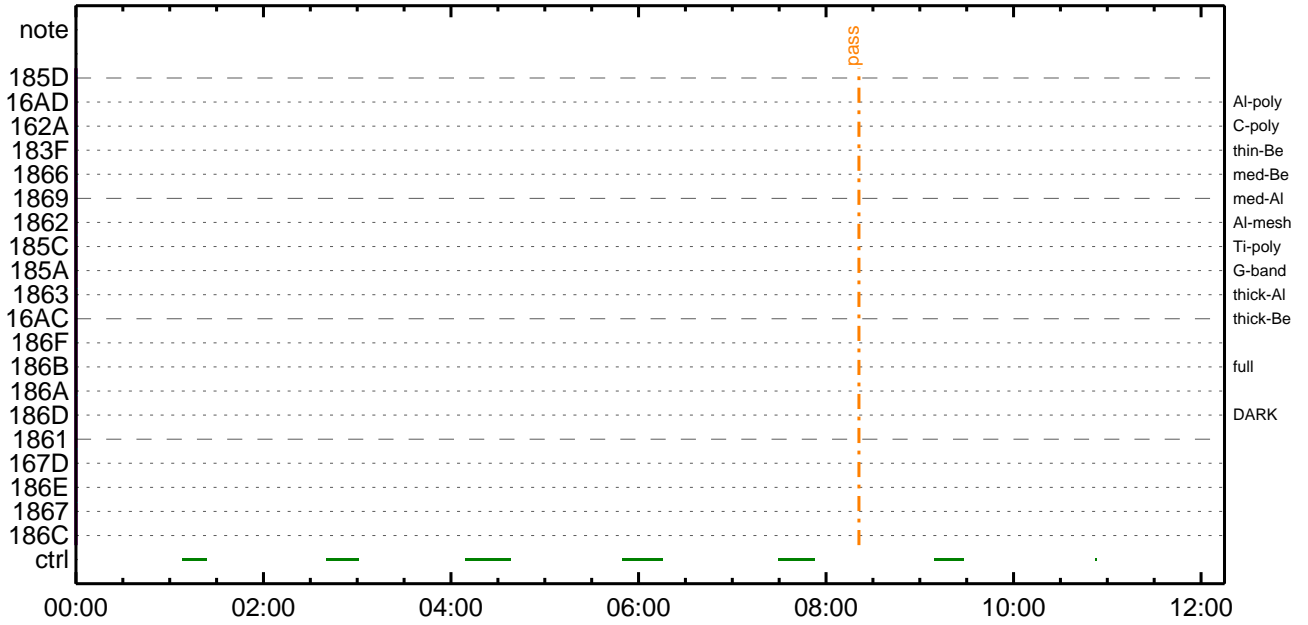
CMDI #0860 2011/04/10



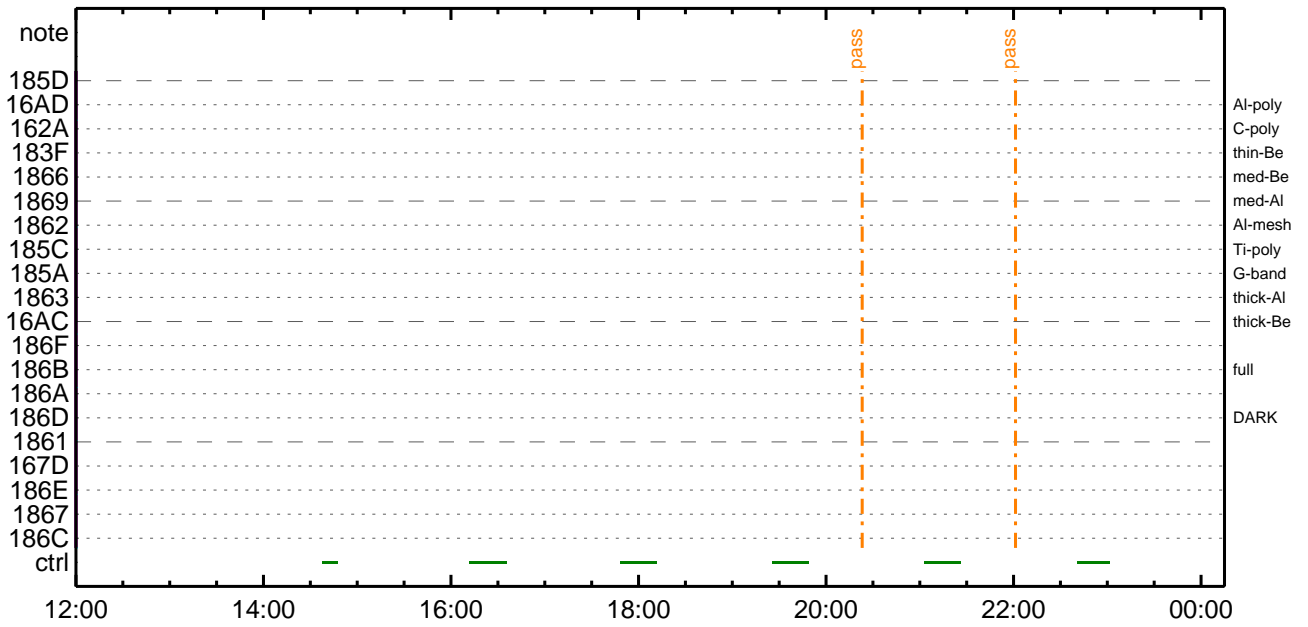
CMDI #0860 2011/04/10



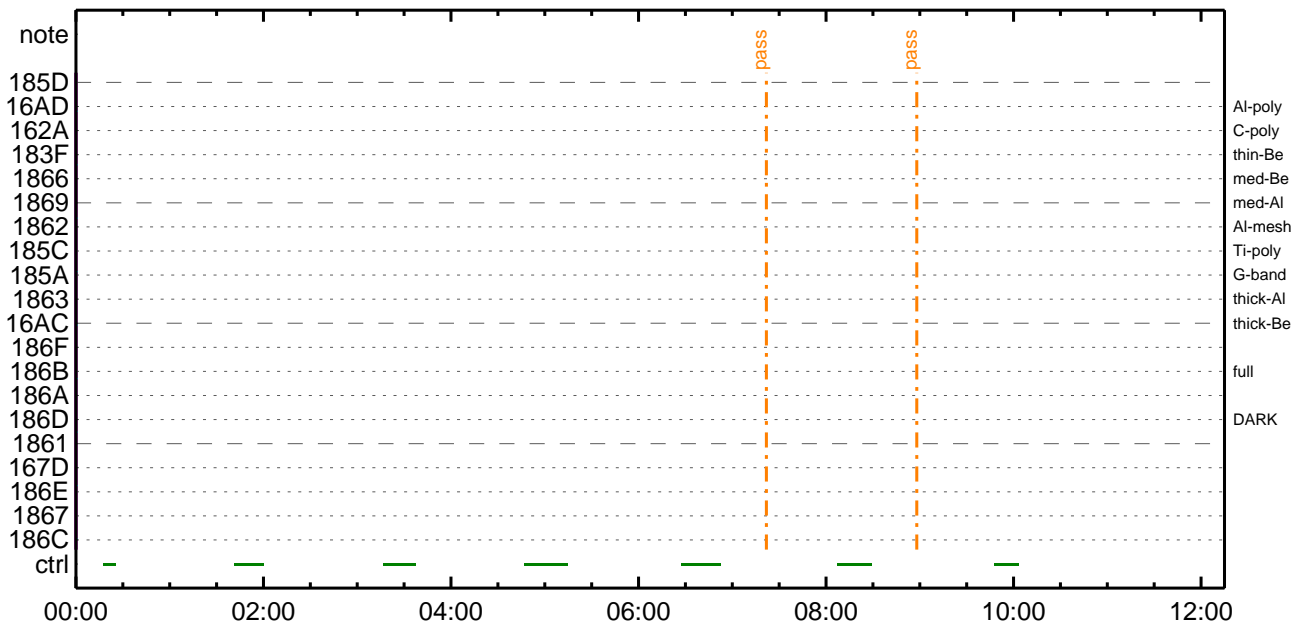
CMDI #0860 2011/04/11



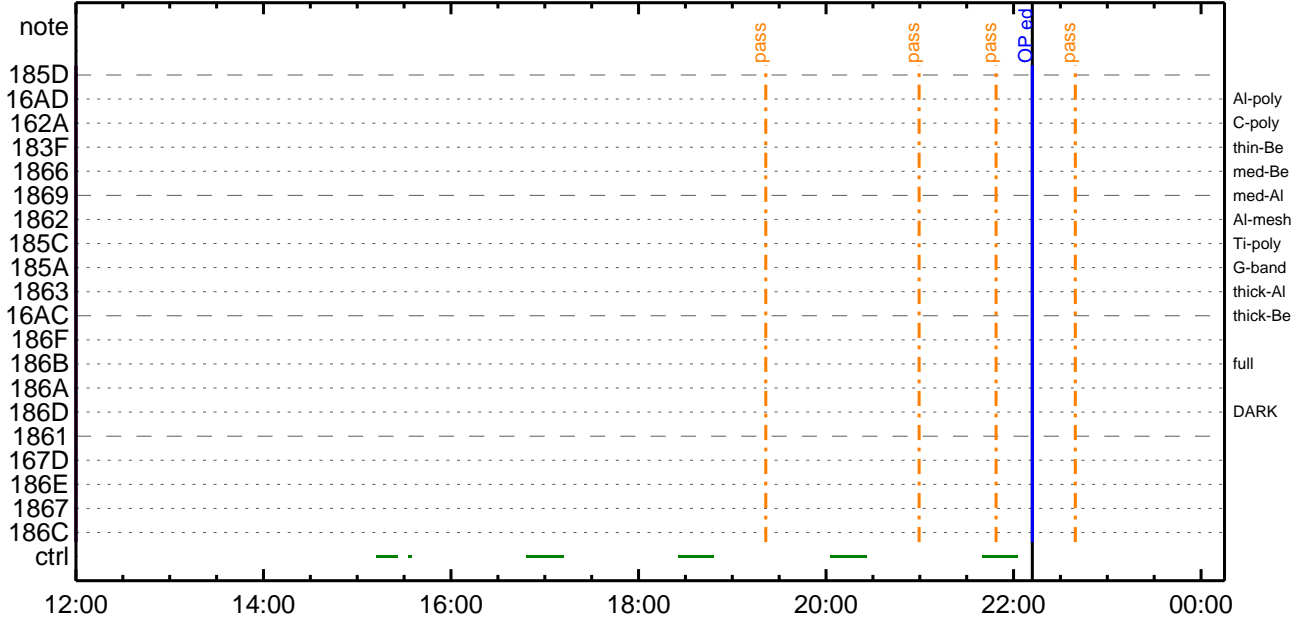
CMDI #0860 2011/04/11



CMDI #0860 2011/04/12



CMDI #0860 2011/04/12



0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGYI;WYE;WYEX
0100 C. *****
0101 C.
0102 C. iãOP/OGYI;WYE;ã
0103 S. OP op-983:OP
0104 (
0105 S. OG og-983:OG
0106 (
0107 C.
0108 C. iãNMOG&OPíî°eWYEX;ã
0109 C. NMOG(0x200000-0x207FFF;S 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. WYEXWYEXãî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGáî¼È¹ç•ê²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;S 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. WYEXWYEXãî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGáî¼È¹ç•ê²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;S 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. WYEXWYEXãî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPáî¼È¹ç•ê²îOKð³îÇ§
0165 C.
0166 C. ***** °È²¼ðî¼Ã´¶Á°°ÈÈ-ð°Á÷¿@ (¼áµ-WYEXWYEXê¼çððÁÔÁæç¼ª°°ðè¼¹¹çççá) *****
0167 C. DHUYã;WYE;È¼Y½;Yì;¼YE;Èððíã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |S OPOG UPLOADá-Á÷¿@NGáî¼¹¹ç;ç°È²¼ðî¼TI-CMDÁ÷¿@á¼Á¹Ôª°°Èðð³ðÈ;F
0180 C. áðç¿;çSETáÈDUMPáÍÆ±°iWY¹ç¹Ôª|³³ðÈ;F
0181 C.
0182 C. TIY³WYEXWYEXêððÁDí¿(UT)
0183 +. TI 2011-04-02 22:30:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-04-02 22:30:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-04-02 22:30:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-04-02 22:34:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄê%İİñαİŷÄŷ§ŷÄŷ-¹àİŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»αò³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαİŷÈ¹ç•è²İOKαò³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷİ;¼ŷÈ;Èαòİāα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-04-02 22:34:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-04-02 22:34:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2011-04-02 22:34:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-04-02 22:34:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃİαİ»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼ª°İŷÖŷÄŷÈŷŷŷÄŷçŷèè¼αα¼Ä»Űα¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-984 2011-04-02 13:32:35 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YBÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;É¸µâµ°µ»Í×ÁÇóÍYçYÁY×YÍ;¼YÉ;ÉÈÈµ•ííÉ;ÈÈµ°ÇÖ¸µ•¿¼í¹Ç¸Í;çÀ®.Ù¸¹¸È¸µÇÁ+¿®µ•Èµ¸¸È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ° ºÀ, í×ÈY¸¸åLOSµÇ¸Í»¹Ö¸¸¹íí, µ. ; çÉÖÍ×µÈXÁÖONµí¹Ö¸ÈµíµÈ¸¸¸µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÖYÉYÍYÁY-¾ÔÖ¸µ•¿µé; ç°É²¼µí° ºÀ, ¼É¾Çµð¼Á¹Öµíµé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í° ºÀ,
0033 C. *****
0034 C. ° RESTART;ÈPT1;Èµ•¿µ¼¼í¹Ç¸Í; ç°É²¼µí° ºÀ»¸; çDCBC-150µØ¿Éµà;f
0035 C.
0036 . C. ;ãPT1° ºÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÁYÈÀÛÔ;ÈÁ•Á°²öÈð;È. åµí° ºÀ, ° º«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1° ºÀ, µ-¼«ºÀ»µµ»µ¿µ¿;ã; ç°É²¼µð¼Á¹Öµíµé;f
0055 C. YçYÖYÁYÈÀÛÔ»µÁ•Á°²öÈðµ-¼µµ¼¼í¹Ç¸Í °í»¸¹¸È¸µÇÁÖ¸Á;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í° ºÀ,
0059 C. *****
0060 C. ° RESTART;ÈPT2;Èµ•¿µ¼¼í¹Ç¸Í; ç°É²¼µí° ºÀ»¸; çDCBC-151µØ¿Éµà;f
0061 C.
0062 . C. ;ãPT2° ºÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÁYÈÀÛÔ;ÈÁ•Á°²öÈð;È. åµí° ºÀ, ° º«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR° ºÀ, Áã»µ; çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR° ºÀ, Áã»µ;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 31s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-04-02 22:34:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0153 C. (%ã°îÿÓÿÄÿËÿPÿËÿÄÿçÿèñ¼¼¼¼»Û¹ñè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿDÿ¹•î Daily±¿îÑñÉ´Ø¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÄÿSÿÿÄÿ-¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-985 2011-04-02 13:32:35 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀã•µ°È»Í×ÁÇãíYçYÁY×Yí;¼YÉ;ÈÈ¿µ•ííÈ;ÈãÈ¼°ÇÒã•ã¿¼í¹çãí;çÁ®, ùã¹ãÈãããÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-265:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãíÁ°ãÈSTS_CHKãðOFFãÈã¹ãÈ
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãÈ
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700 [ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2011-04-02 22:34:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 06 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 07 85 83 08 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 08 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 09 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0a 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 +. DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 +. TI 2011-04-02 22:34:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP `uãîaî»ö%ÿaëâða¹aëDCBC•x²è *****
0125 C. (%ã°îÿÓÿÃÿÈÿPÿËÿãÿçÿëaE%¼aa¼Ã»Ûa¹aè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹·Ï Daily±;îÑaë'Øa¹aëDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿËÿÿ¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
```

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*** OP Sequence for XRT ***

2011/04/02	22:44:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/02	22:44:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/02	22:45:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/04/02	22:45:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/02	22:45:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/02	22:45:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/02	22:45:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/02	22:47:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/02	22:47:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2011/04/02	22:47:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f
2011/04/02	22:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/02	23:39:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/02	23:39:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/02	23:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/02	23:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/02	23:50:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/02	23:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	01:06:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	01:06:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	01:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	01:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	01:22:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	01:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	02:38:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	02:38:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	02:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	02:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	02:58:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	02:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	04:06:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	04:06:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	04:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	04:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	04:36:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	04:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	05:47:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	05:47:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	05:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	05:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	06:13:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	06:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	06:23:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	06:23:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/04/03	06:23:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/04/03	06:23:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/04/03	06:23:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/04/03	06:23:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/03	06:26:28.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d

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2011/04/03	06:26:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	06:33:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	06:33:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/03	06:33:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/04/03	06:33:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/03	06:33:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/03	06:33:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/03	06:33:52.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/03	06:36:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	06:36:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2011/04/03	06:36:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f
2011/04/03	06:36:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	07:27:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	07:27:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	07:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	07:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	07:50:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	07:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	09:07:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	09:07:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	09:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	09:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	09:26:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	09:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	10:50:01.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	10:50:03.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	10:50:05.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	10:53:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	10:53:15.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	10:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	14:35:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	14:35:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	14:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	14:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	14:45:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	14:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	16:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	16:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	16:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	16:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	16:33:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/03	16:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/03	17:46:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	17:46:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/03	17:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/03	17:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/03	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/03	18:09:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/04/03	18:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00

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2011/04/03	18:10:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/03	18:10:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/03	18:10:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/03	18:12:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2011/04/03	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/03	18:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/03	18:19:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/03	18:20:00.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	19	f3	b1	27
2011/04/03	18:20:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/03	18:20:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/03	18:20:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/03	18:20:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/03	18:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/03	18:22:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2011/04/03	18:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/04/03	18:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/03	19:23:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/03	19:23:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/03	19:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/03	19:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/03	19:46:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/03	19:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/03	21:00:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/03	21:00:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/03	21:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/03	21:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/03	21:24:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/03	21:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/03	22:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/03	22:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/03	22:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/03	22:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/03	22:59:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/03	23:00:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	00:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	00:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	00:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	00:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	00:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	00:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	01:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	01:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	01:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	01:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	01:58:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	01:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	03:14:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	03:14:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	03:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2011/04/04	03:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	03:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	03:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	04:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	04:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	04:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	04:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	05:13:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	05:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	05:56:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	05:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/04	05:57:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/04	05:57:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/04	05:57:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/04	05:57:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/04	05:59:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/04	06:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	06:06:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	06:06:56.0	XRT_FOCUS_RECALIBRATE_432_OG [0x1b0]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/04/04	06:07:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 19 f3 b1 27				
2011/04/04	06:10:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/04	06:11:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/04	06:11:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/04	06:11:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/04	06:11:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/04	06:13:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	06:13:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/04/04	06:13:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/04	06:14:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	06:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	06:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	06:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	06:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	06:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	06:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	08:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	08:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	08:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	08:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	08:27:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	08:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	09:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	09:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	09:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	09:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	10:01:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	10:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	15:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	15:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/04/04	15:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	15:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	15:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	15:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	15:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	15:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	15:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	15:34:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	15:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	15:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	16:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	16:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	16:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	16:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	17:10:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	17:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/04	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/04	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/04	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/04	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/04	18:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/04	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/04	18:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 19 f3 b1 27				
2011/04/04	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/04	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/04	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/04	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/04	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/04/04	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/04	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	18:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	18:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	18:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	18:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	18:46:30.5	XRT_Custom_418_OG [0x1a2]							
2011/04/04	18:47:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	20:00:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	20:00:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	20:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	20:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	20:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	20:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	21:37:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	21:37:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/04/04	21:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	21:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	22:00:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	22:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/04	23:15:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/04	23:15:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/04	23:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/04	23:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/04	23:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/04	23:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	00:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	00:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/05	00:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/05	00:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/05	00:58:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/05	00:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	02:14:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	02:14:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/05	02:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/05	02:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/05	02:35:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/05	02:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	03:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	03:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/05	03:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/05	03:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/05	04:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/05	04:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	05:22:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	05:22:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/05	05:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/05	05:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/05	05:49:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/05	05:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/05	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/05	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/05	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/05	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/05	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/05	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 ac cd				
2011/04/05	06:26:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 00 d6 67				
2011/04/05	06:41:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	06:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/05	06:42:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00 00				
2011/04/05	06:42:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/05	06:42:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							

2011/04/05	06:42:20.0	XRT_ARS_DIS_438	MDP_XRT_FLRCTRL_DIS OG [0x1b6]	1	07-F0	c9						
2011/04/05	06:44:58.0	XRT_QT_PROG_SET_415	OG [0x19f] MDP_XRT_ARS_DIS	1	07-F0	d5						
2011/04/05	06:45:00.0	XRT_CTRL_AUTO_406	OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0	c4	09					
2011/04/05	07:03:00.0	XRT_CTRL_MANU_408	OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/05	07:03:02.0	XRT_FLD_RESET_412	OG [0x19c] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	07:03:04.0	XRT_PREFLR_STRT_422	OG [0x1a6] MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/05	07:06:14.0	XRT_PREFLR_STOP_424	OG [0x1a8] MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/05	07:32:54.0	XRT_CTRL_MANU_400	OG [0x190] MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/05	07:33:00.0	AOCS_Ore-point_Start_6	OG [0x09c] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	07:49:00.0	AOCS_Ore-point_Start_7	OG [0x09d] AOCU_NM	5	02-76	00	00	00	29	99		
2011/04/05	08:05:00.0	AOCS_Ore-point_Start_8	OG [0x09e] AOCU_NM	5	02-76	00	00	00	53	33		
2011/04/05	08:15:00.0	AOCS_Ore-point_Start_9	OG [0x09f] AOCU_NM	5	02-76	00	d6	36	b7	8e		
2011/04/05	09:09:30.0	AOCS_Ore-point_Start_10	OG [0x0a0] AOCU_NM	5	02-76	00	b4	b5	db	75		
2011/04/05	09:25:30.0	AOCS_Ore-point_Start_11	OG [0x0a1] AOCU_NM	5	02-76	00	ac	5b	00	00		
2011/04/05	09:41:30.0	AOCS_Ore-point_Start_12	OG [0x0a2] AOCU_NM	5	02-76	00	d6	36	48	72		
2011/04/05	09:51:30.0	AOCS_Ore-point_Start_13	OG [0x0a3] AOCU_NM	5	02-76	00	29	ca	b7	8e		
2011/04/05	10:01:30.5	AOCS_Ore-point_Start_14	OG [0x0a4] AOCU_NM	5	02-76	00	4b	4b	db	75		
2011/04/05	10:39:30.0	AOCS_Ore-point_Start_15	OG [0x0a5] AOCU_NM	5	02-76	00	53	a5	00	00		
2011/04/05	10:55:30.0	AOCS_Ore-point_Start_16	OG [0x0a6] AOCU_NM	5	02-76	00	4b	4b	24	8b		
2011/04/05	11:11:30.0	AOCS_Ore-point_Start_17	OG [0x0a7] AOCU_NM	5	02-76	00	29	db	48	72		
2011/04/05	11:59:54.0	XRT_CTRL_MANU_400	OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	11:59:56.0	XRT_FOCUS_POSITION_409	OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00			
2011/04/05	12:00:00.0	AOCS_Ore-point_Start_18	OG [0x0a8] AOCU_NM	5	02-76	00	da	36	b6	f2		
2011/04/05	12:00:16.0	XRT_FLD_ENA_411	OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8						
2011/04/05	12:00:18.0	XRT_FLRCTRL_ENA_413	OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2011/04/05	12:00:20.0	XRT_AEC_RESET_443	OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0						
2011/04/05	12:00:22.0	XRT_ARS_DIS_437	OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5						
2011/04/05	12:02:54.0	XRT_FLD_RESET_412	OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/05	12:02:56.0	XRT_QT_PROG_SET_434	OG [0x1b2] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03					
2011/04/05	12:02:58.0	XRT_FL_PROG_SET_436	OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f					
2011/04/05	12:03:00.0	XRT_CTRL_AUTO_406	OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/05	14:13:30.0	XRT_CTRL_MANU_408	OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	14:13:32.0	XRT_FLD_RESET_412	OG [0x19c] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	14:13:34.0	XRT_PREFLR_STRT_422	OG [0x1a6] MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/05	14:16:44.0	XRT_PREFLR_STOP_424	OG [0x1a8] MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/05	14:19:00.0	XRT_CUSTOM_418	OG [0x1a2] MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/05	14:20:00.0	XRT_CTRL_AUTO_419	OG [0x1a3] MDP_XRT_CUSTOM_418	1	07-F0	c0						
2011/04/05	15:46:00.0	XRT_CTRL_MANU_408	OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/05	15:46:02.0	XRT_FLD_RESET_412	OG [0x19c] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	15:46:04.0	XRT_PREFLR_STRT_422	OG [0x1a6] MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/05	15:49:14.0	XRT_PREFLR_STOP_424	OG [0x1a8] MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/05	16:09:30.0	XRT_CUSTOM_418	OG [0x1a2] MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/05	16:10:30.0	XRT_CTRL_AUTO_419	OG [0x1a3] MDP_XRT_CUSTOM_418	1	07-F0	c0						
2011/04/05	17:22:30.0	XRT_CTRL_MANU_408	OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/05	17:22:32.0	XRT_FLD_RESET_412	OG [0x19c] MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/05	17:22:34.0	XRT_PREFLR_STRT_422	OG [0x1a6] MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/05	17:25:44.0	XRT_PREFLR_STOP_424	OG [0x1a8] MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/05			MDP_XRT_PREFLR_STOP	1	07-F0	e9						

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2011/04/05	17:46:30.5	XRT_Custom_418_OG [0x1a2]							
2011/04/05	17:47:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	18:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	18:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/05	18:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/05	19:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/05	19:23:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/05	19:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	20:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	20:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/05	20:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/05	20:20:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/05	20:20:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/05	20:20:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/05	20:22:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/05	20:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/05	20:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	20:30:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	02 00 00 01 ca				
2011/04/05	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/05	23:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/06	00:00:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/04/06	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/06	00:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/06	00:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/06	00:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/06	00:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	00:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/04/06	00:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/06	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	01:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	01:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	01:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	01:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	01:34:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	01:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	02:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	02:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	02:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	02:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	03:11:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	03:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	04:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	04:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	04:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	04:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	04:49:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	04:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	05:32:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	05:32:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				

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2011/04/06	05:33:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2011/04/06	05:33:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/06	05:33:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/06	05:33:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/06	05:35:58.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2011/04/06	05:36:00.5	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	05:42:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	05:42:56.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/06	05:43:00.0	AOCS_ORe-point_Start_20_OG [0x0aa] AOCU_NM	5	02-76	01	00	00	00	00
2011/04/06	05:43:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/06	05:43:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/06	05:43:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/06	05:43:22.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/06	05:45:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	05:45:56.0	XRT_QT_PROG_SET_434_OG [0x1b2] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2011/04/06	05:45:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/04/06	05:46:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	06:00:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	06:00:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	06:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	06:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	06:26:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	06:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	07:41:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	07:41:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	07:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	07:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	08:03:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	08:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	09:20:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	09:20:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	09:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	09:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	09:39:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	09:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	14:47:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	14:47:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	14:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	14:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	14:59:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	15:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	16:22:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	16:22:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/06	16:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/06	16:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/06	16:46:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/06	16:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/06	17:59:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/06	17:59:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/04/06	17:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/06	18:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/06	18:22:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/06	18:22:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/04/06	18:23:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/04/06	18:23:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/04/06	18:23:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/04/06	18:23:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/06	18:25:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2011/04/06	18:26:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/06	18:32:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/06	18:33:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/04/06	21:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/06	21:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/04/06	21:40:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/04/06	21:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/04/06	21:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/04/06	21:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/04/06	21:40:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/06	21:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/06	21:42:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2011/04/06	21:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f	
2011/04/06	21:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/06	22:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/06	22:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/06	22:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/06	22:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/06	23:11:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/06	23:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/07	00:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/07	00:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/07	00:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/07	00:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/07	00:36:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/07	00:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/07	01:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/07	01:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/07	01:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/07	01:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/07	02:11:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/07	02:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/07	03:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/07	03:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/07	03:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/07	03:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/07	03:48:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/07	03:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/07	04:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/07	04:58:02.0	XRT_FLD_RESET_412_OG [0x19c]					

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2011/04/07	04:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/07	05:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/07	05:26:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/07	05:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	05:54:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	05:54:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/04/07	05:54:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2011/04/07	05:54:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/04/07	05:54:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/04/07	05:54:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/07	05:57:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/04/07	05:57:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	06:04:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	06:04:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/07	06:04:30.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 e9 9a b0 b5
2011/04/07	06:04:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/07	06:04:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/07	06:04:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/07	06:04:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/07	06:07:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/07	06:07:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2011/04/07	06:07:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f
2011/04/07	06:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	06:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	06:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/07	06:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/07	06:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/07	07:03:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/07	07:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	08:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	08:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/07	08:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/07	08:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/07	08:40:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/07	08:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	09:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	09:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/07	09:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/07	10:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/07	10:13:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/07	10:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	15:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	15:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/07	15:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/07	15:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/07	15:37:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/07	15:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/07	15:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/07	15:40:02.0	XRT_FLD_RESET_412_OG [0x19c]				

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2011/04/07	15:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/07	15:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/07	15:46:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/07	15:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/07	16:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/07	16:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/07	16:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/07	17:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/07	17:22:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/07	17:23:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/07	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/04/07	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/04/07	18:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/04/07	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/04/07	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/04/07	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/04/07	18:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2011/04/07	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/07	18:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/07	18:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/07	18:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/07	18:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/07	19:00:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	02 00 72 fd 59		
2011/04/07	22:51:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		