

# XRT Timeline to be uploaded on 2011/04/12

Period: 2011/04/12 22:30:00 - 2011/04/18 10:38:00

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Normal mode

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XOB #1844: AR Standard-A(Filter-Ratio) with PFB, thin-Be context, 384x384 at 1064 1048, 120s cad													
Term	Pointing (x, y)		Comment										
04/12 22:43:00 - 04/13 06:01:24	Track (	-311.9, 303.6)	@ 04/12 22:40:00	# OP start + 10min AR tracking									
04/13 06:14:30 - 04/13 09:59:54	Track (	-247.2, 305.3)	@ 04/13 06:11:30	# AR tracking									
04/14 07:23:00 - 04/14 17:37:54	Track (	-23.8, 307.3)	@ 04/14 07:20:00	AR tracking									
04/14 17:58:00 - 04/15 05:56:24	Track (	70.4, 306.5)	@ 04/14 17:48:00	#AR Tracking									
04/15 06:09:30 - 04/15 17:59:54	Track (	180.1, 304.3)	@ 04/15 06:06:30	# AR tracking									
04/15 18:13:00 - 04/15 21:43:00	Track (	285.1, 301.0)	@ 04/15 18:10:00	#AR tracking									
<b>PROG= 05 Inf.-time(s)</b>													
Subr= 1 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 82 4-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 15 20-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec	
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec	
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec	
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1871: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/2048) + Thin-Be(36/2048)												
Term	Pointing (x, y)		Comment									
04/13 06:04:30 - 04/13 06:11:24	Fixed (	0.0, 0.0)	synoptic, shifted 1.5 min									
04/14 06:33:00 - 04/14 06:39:54	Fixed (	0.0, 0.0)	synoptic, shifted +30 min manually									
04/14 17:41:00 - 04/14 17:47:54	Fixed (	0.0, 0.0)	Synoptic									
04/15 05:59:30 - 04/15 06:06:24	Fixed (	0.0, 0.0)	synoptic, shifted -3.5 min									
04/15 18:03:00 - 04/15 18:09:54	Fixed (	0.0, 0.0)	synoptic									
<b>PROG= 04 1-time(s)</b>												
Subr= 1 1-time(s) 12.0sec												
Seqn= 98 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 74 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 32 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long												
Term	Pointing (x, y)		Comment									
04/14 06:43:00 - 04/14 06:49:54	Fixed (	-528.4, -528.4)	# XRT Quadrant pointing 1									
<b>PROG= 20 1-time(s)</b>												
Subr= 1 1-time(s) 12.0sec												
Seqn= 38 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long												
Term		Pointing (x, y)					Comment					
04/14 06:53:00 - 04/14 06:59:54		Fixed ( 528.4, -528.4)					XRT QUADRANT 2					
PROG= 11 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 36 1-time(s) 12.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 93 2-time(s) 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long												
Term		Pointing (x, y)					Comment					
04/14 07:03:00 - 04/14 07:09:54		Fixed ( 528.4, 528.4)					# XRT Quadrant pointing 3					
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 39 1-time(s) 12.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 93 2-time(s) 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long												
Term		Pointing (x, y)					Comment					
04/14 07:13:00 - 04/14 07:19:54		Fixed ( -528.4, 528.4)					XRT QUADRANT 4					
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 40 1-time(s) 12.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 93 2-time(s) 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

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### Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term		Pointing (x, y)					Comment					
04/12 22:43:00 - 04/13 06:01:24		Track ( -311.9, 303.6) <sup>Ⓜ 04/12 22:40:00</sup>					# OP start + 10min AR tracking					
04/13 06:14:30 - 04/13 09:59:54		Track ( -247.2, 305.3) <sup>Ⓜ 04/13 06:11:30</sup>					# AR tracking					
04/14 07:23:00 - 04/14 17:37:54		Track ( -23.8, 307.3) <sup>Ⓜ 04/14 07:20:00</sup>					AR tracking					
04/14 17:58:00 - 04/15 05:56:24		Track ( 70.4, 306.5) <sup>Ⓜ 04/14 17:48:00</sup>					#AR Tracking					
04/15 06:09:30 - 04/15 17:59:54		Track ( 180.1, 304.3) <sup>Ⓜ 04/15 06:06:30</sup>					# AR tracking					
04/15 18:13:00 - 04/15 21:43:00		Track ( 285.1, 301.0) <sup>Ⓜ 04/15 18:10:00</sup>					#AR tracking					
PROG= 16 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─┬─ Seqn= 87 1-time(s) 2.0sec												
└─┬─┬─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Seqn= 60 1-time(s) 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 90 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└─ Subr= 3 30-time(s) 60.0sec												
└─┬─ Seqn= 87 1-time(s) 2.0sec												
└─┬─┬─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Seqn= 88 1-time(s) 2.0sec												

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 90</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 90</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4</b>	<b>24-time(s)</b>	<b>600.0sec</b>											
	<b>Seqn= 89</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

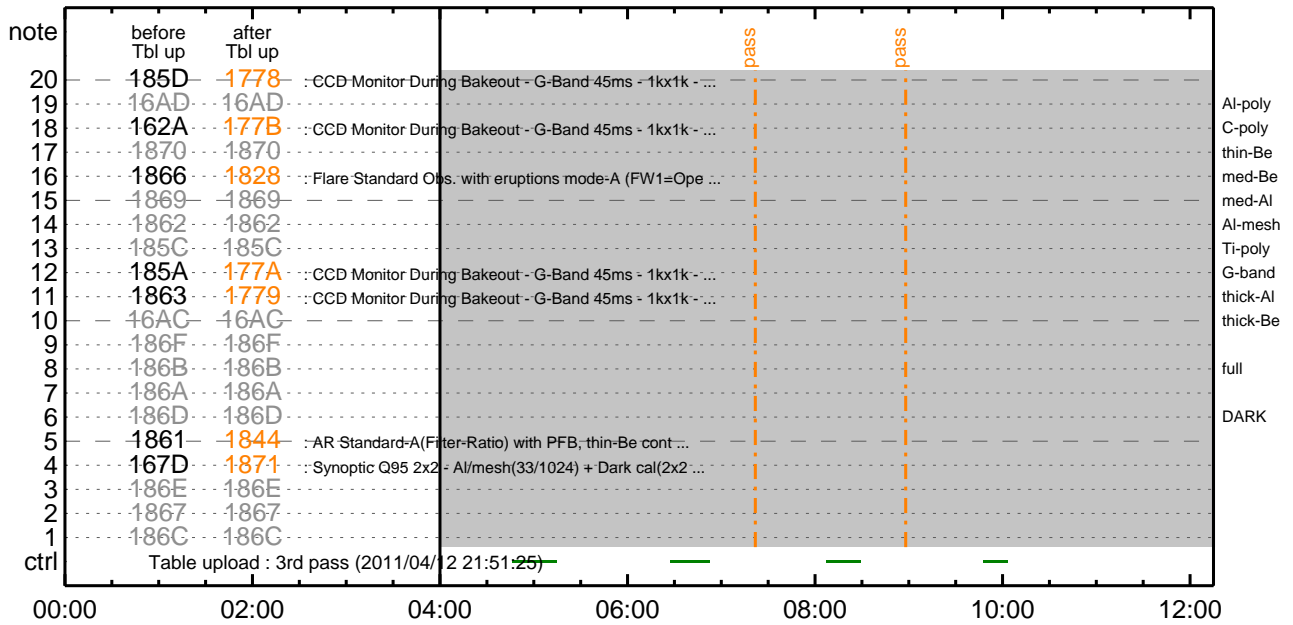
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### Flare Detection

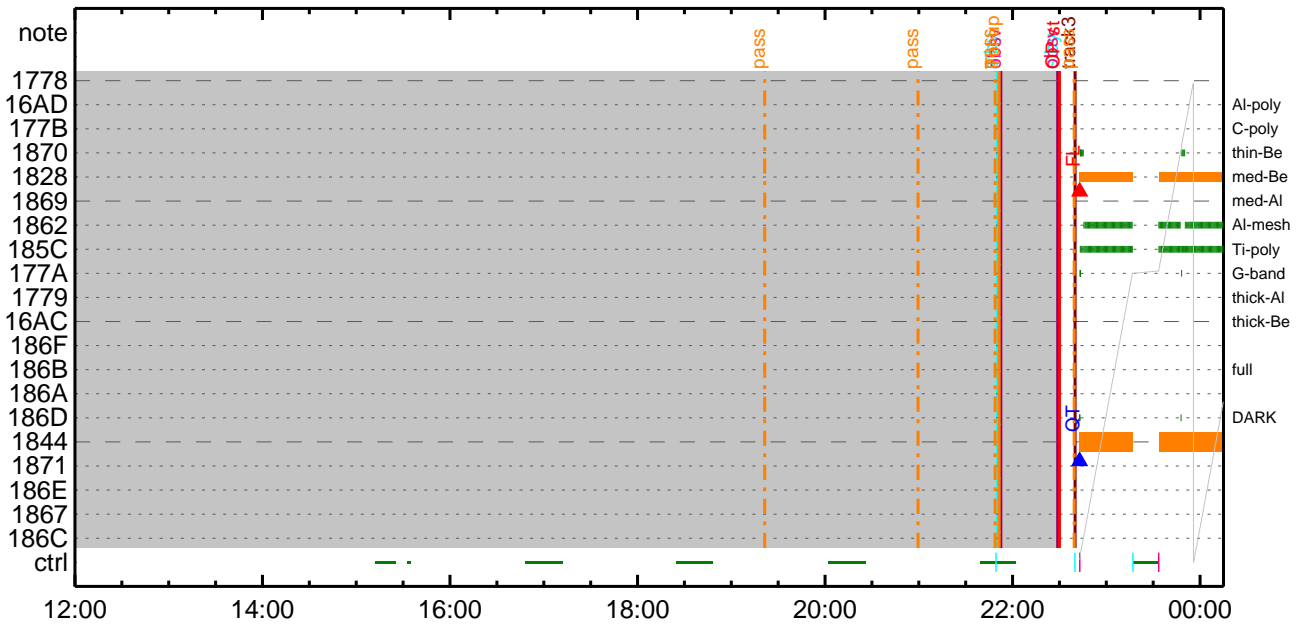
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FLD Patrol												
Term	Pointing (x, y)						Comment					
04/12 22:40:16 - 04/13 06:01:46	Track (	-311.9,	303.6)	@ 04/12 22:40:00	# OP start + 10min AR tracking							
04/13 06:11:46 - 04/14 06:30:16	Track (	-247.2,	305.3)	@ 04/13 06:11:30	# AR tracking							
04/14 07:20:16 - 04/14 17:38:16	Track (	-23.8,	307.3)	@ 04/14 07:20:00	AR tracking							
04/14 17:48:16 - 04/15 05:56:46	Track (	70.4,	306.5)	@ 04/14 17:48:00	#AR Tracking							
04/15 06:06:46 - 04/15 18:00:16	Track (	180.1,	304.3)	@ 04/15 06:06:30	# AR tracking							
04/15 18:10:16 - 04/18 10:38:00	Track (	285.1,	301.0)	@ 04/15 18:10:00	#AR tracking							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

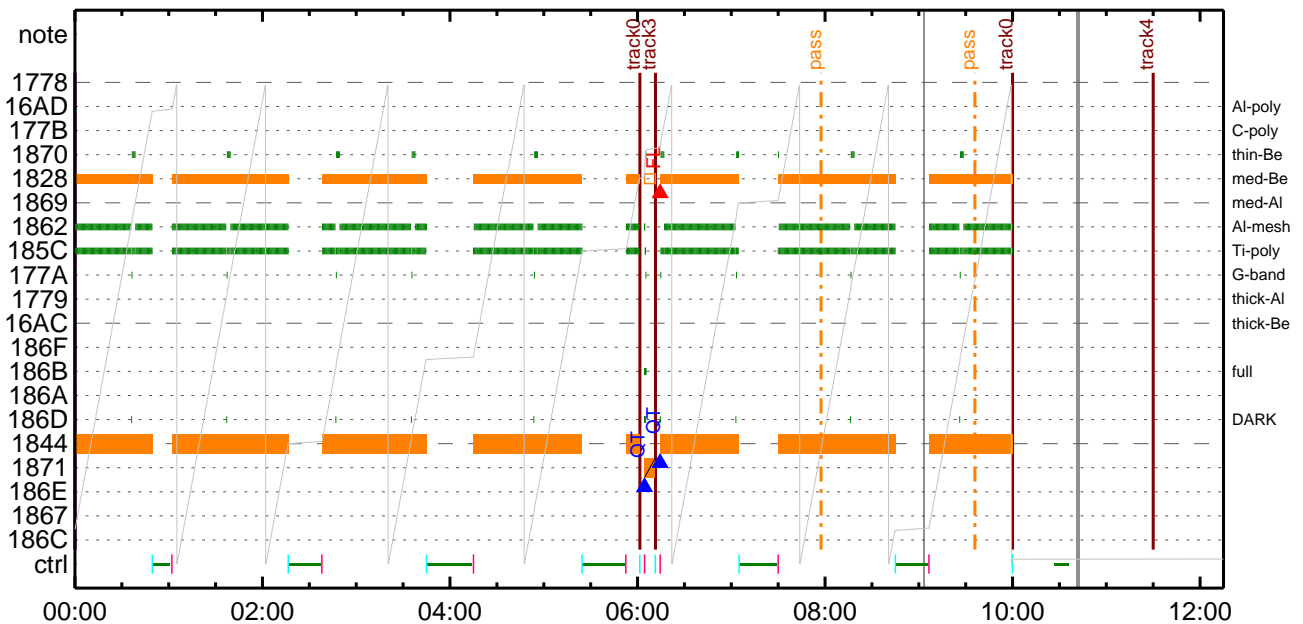
### CMDI #0867 2011/04/12



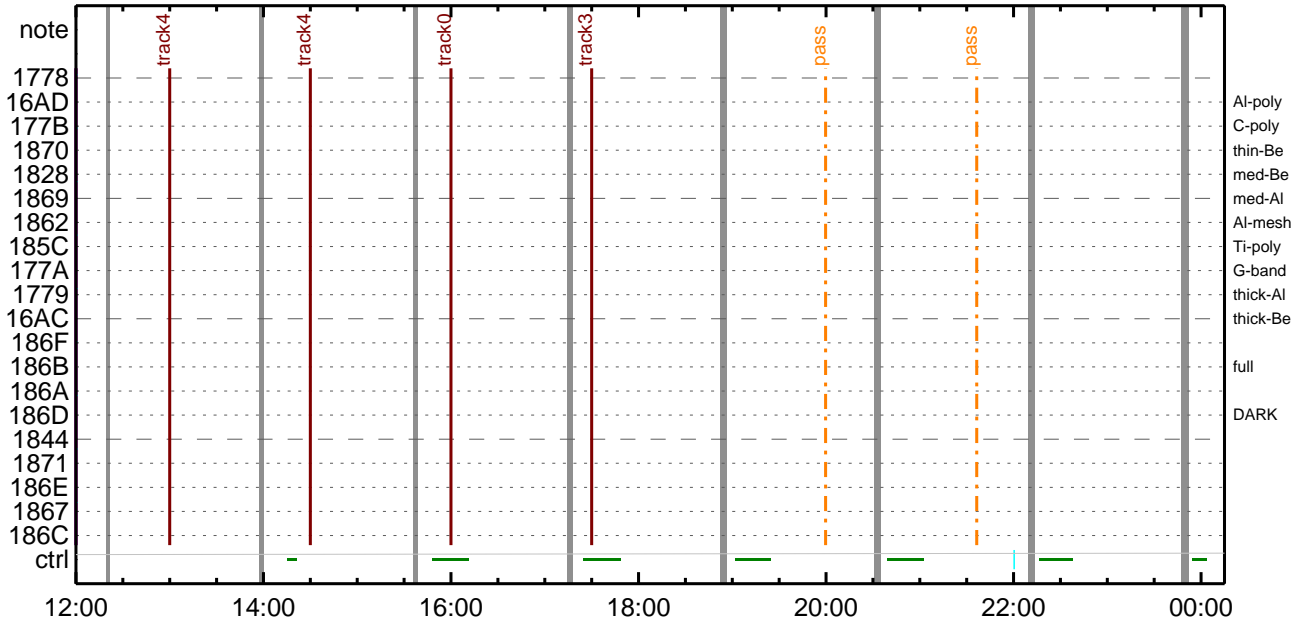
### CMDI #0867 2011/04/12



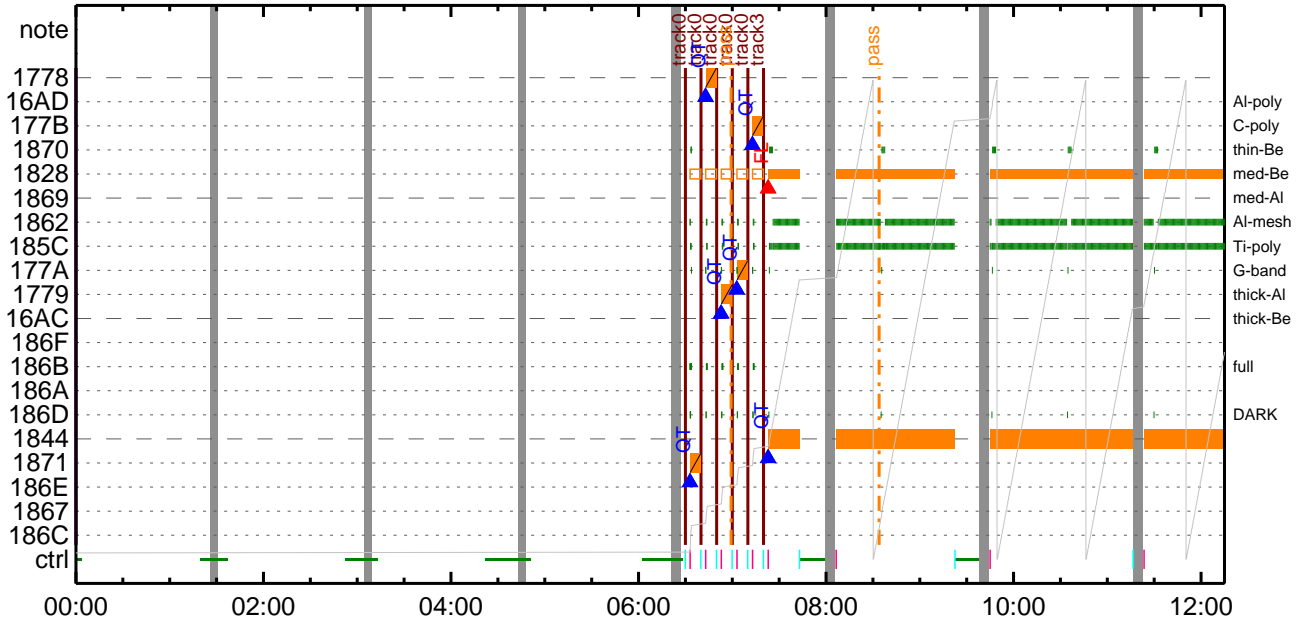
### CMDI #0867 2011/04/13



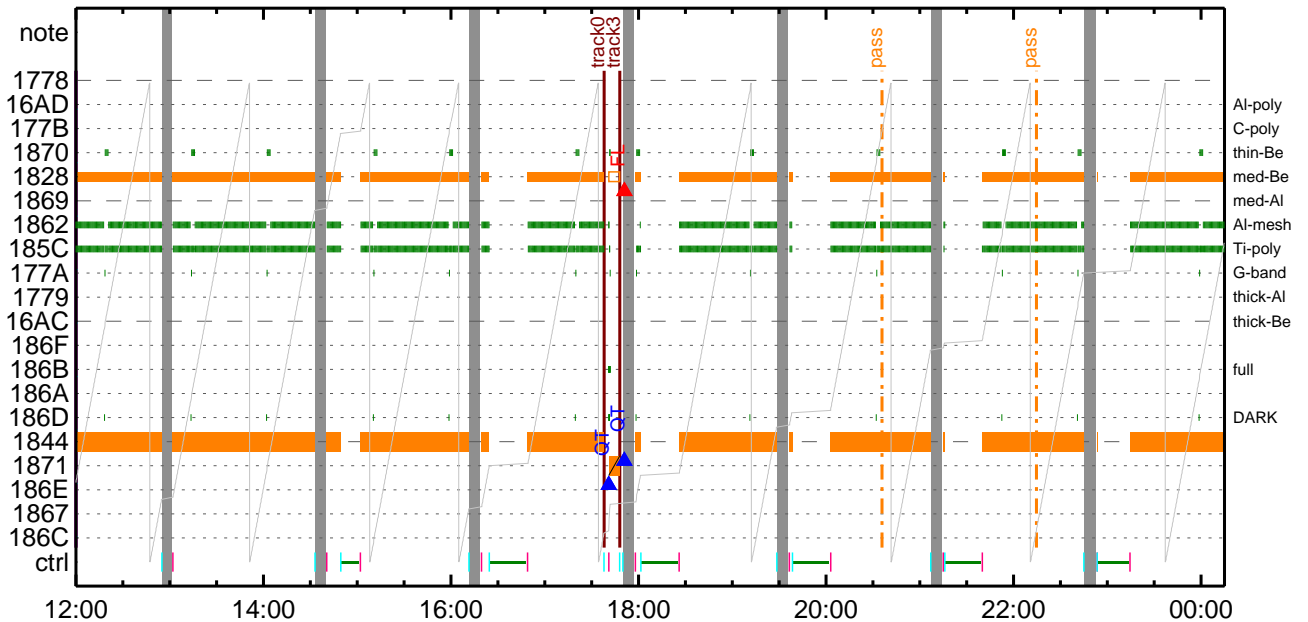
CMDI #0867 2011/04/13



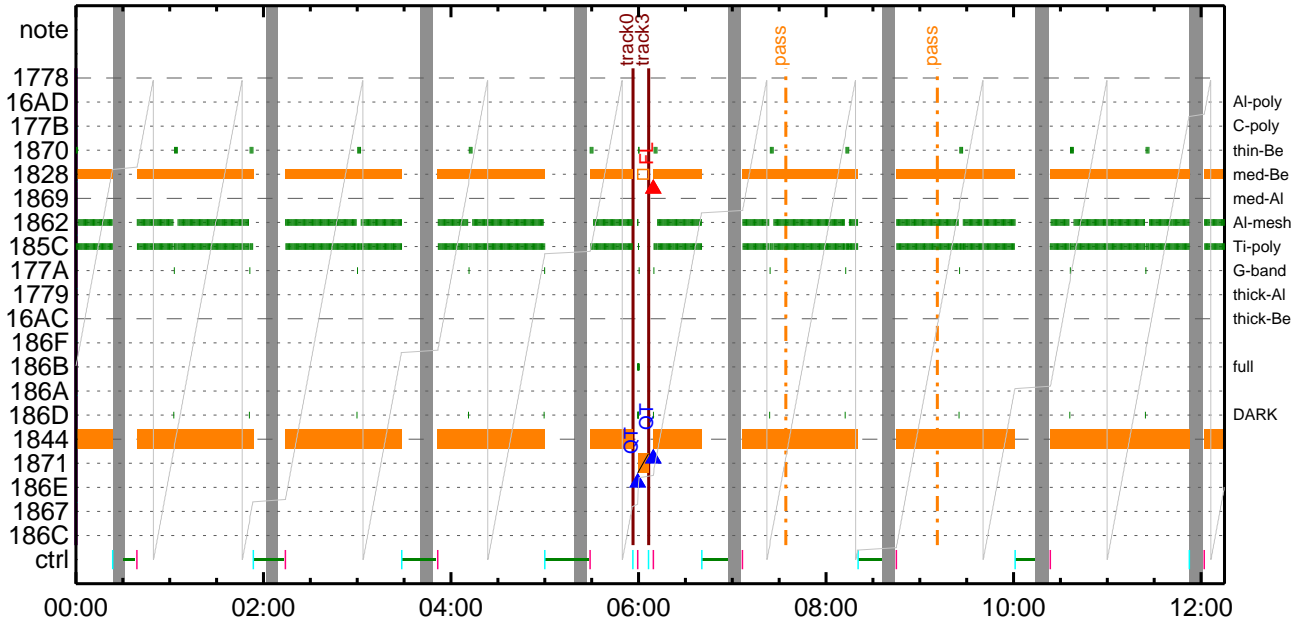
CMDI #0867 2011/04/14



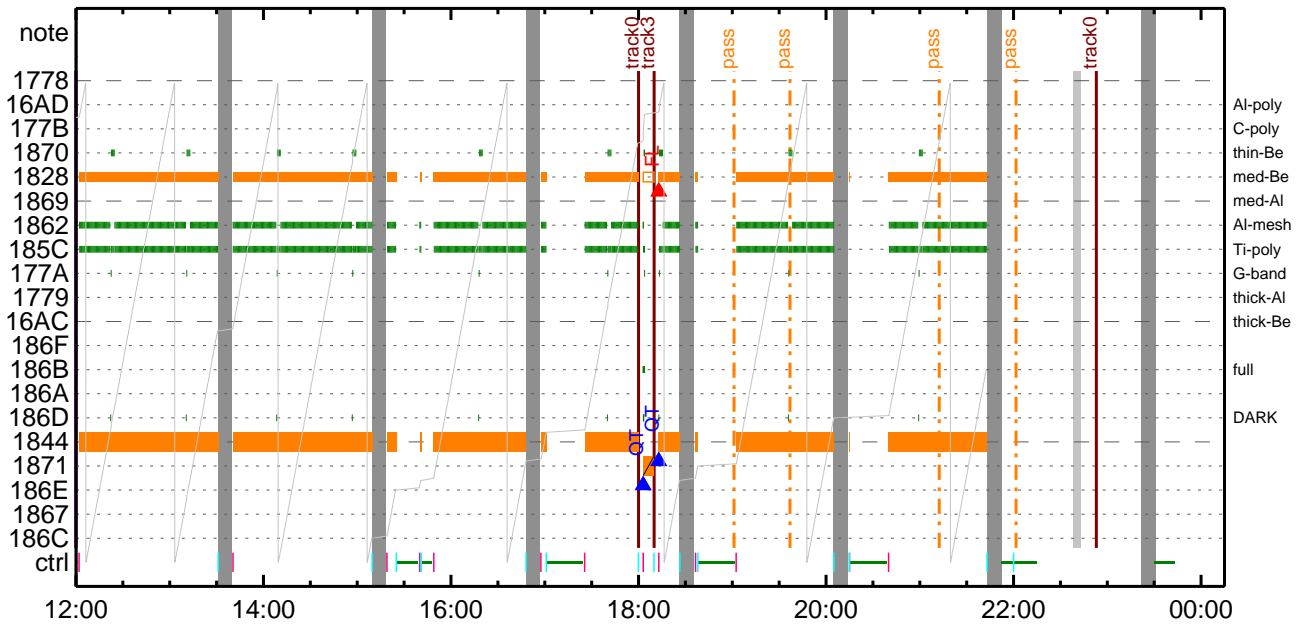
CMDI #0867 2011/04/14



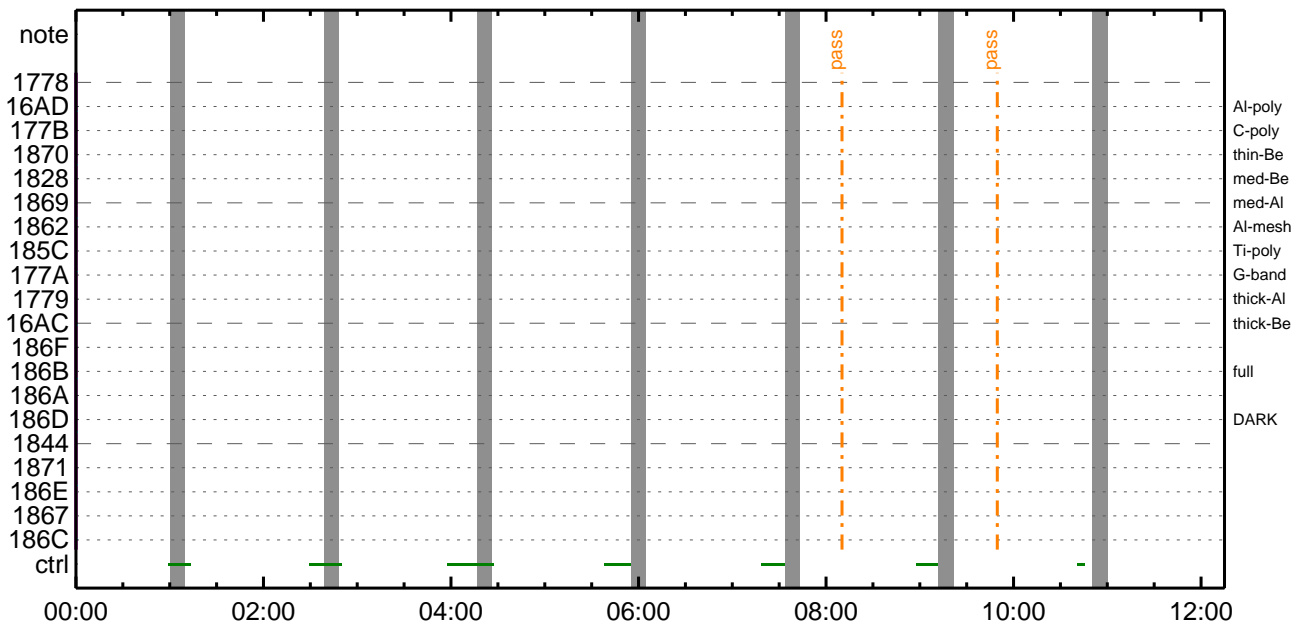
CMDI #0867 2011/04/15



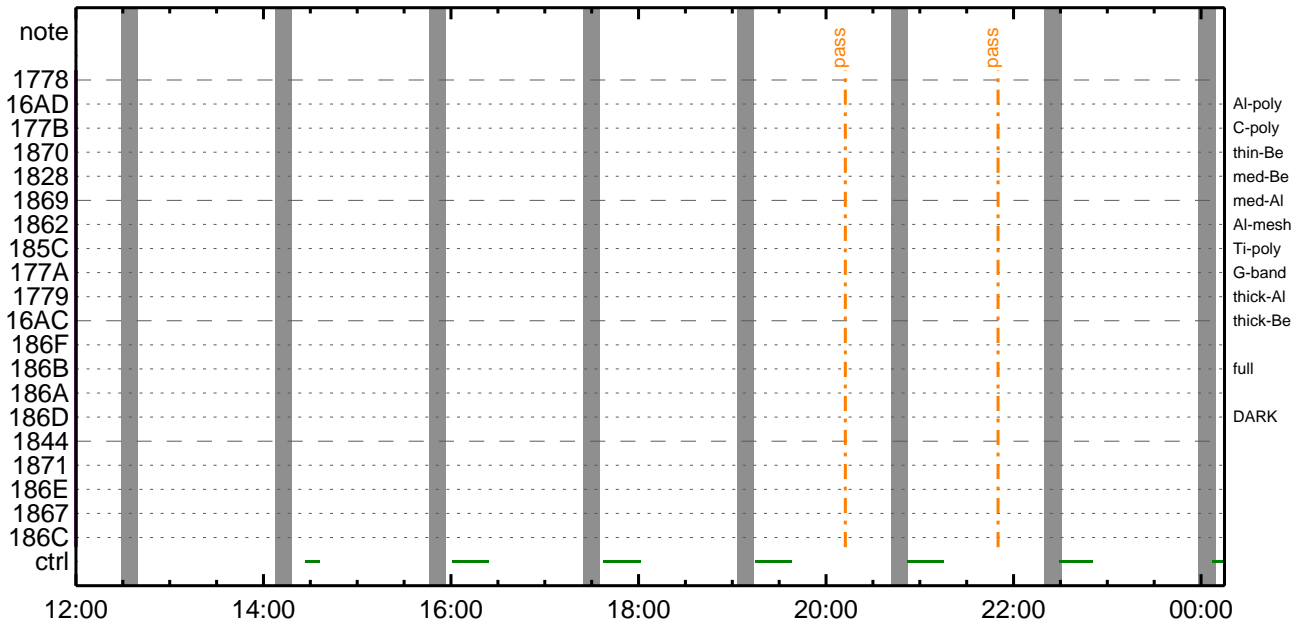
CMDI #0867 2011/04/15



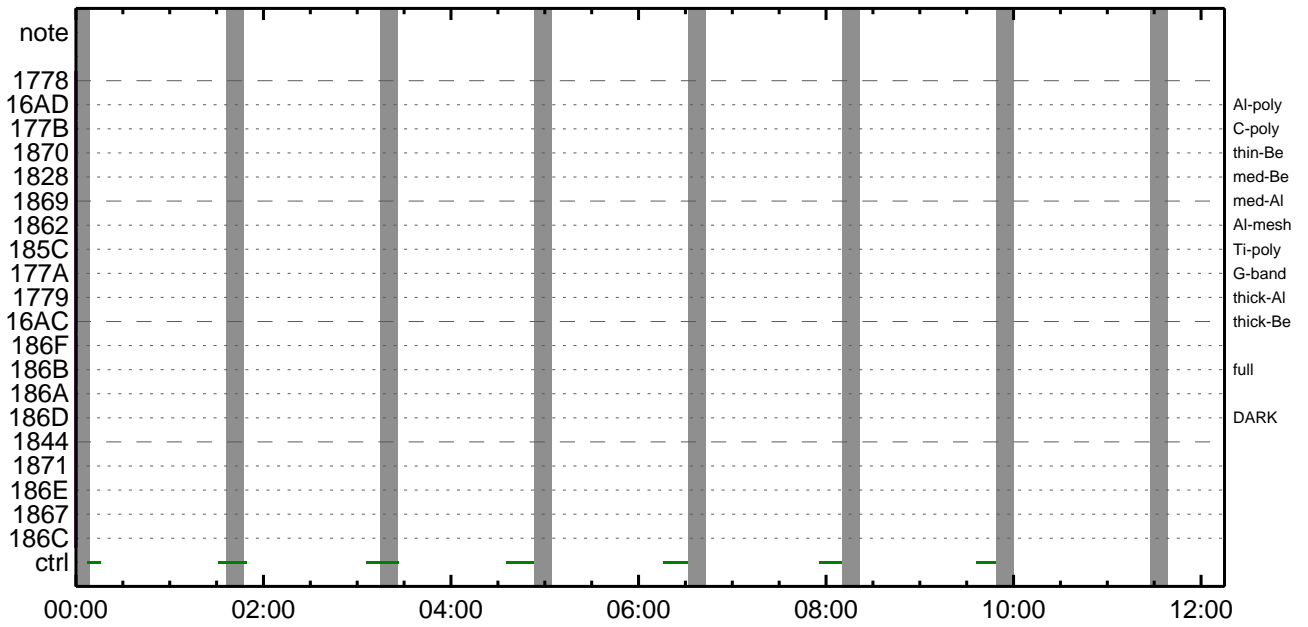
CMDI #0867 2011/04/16



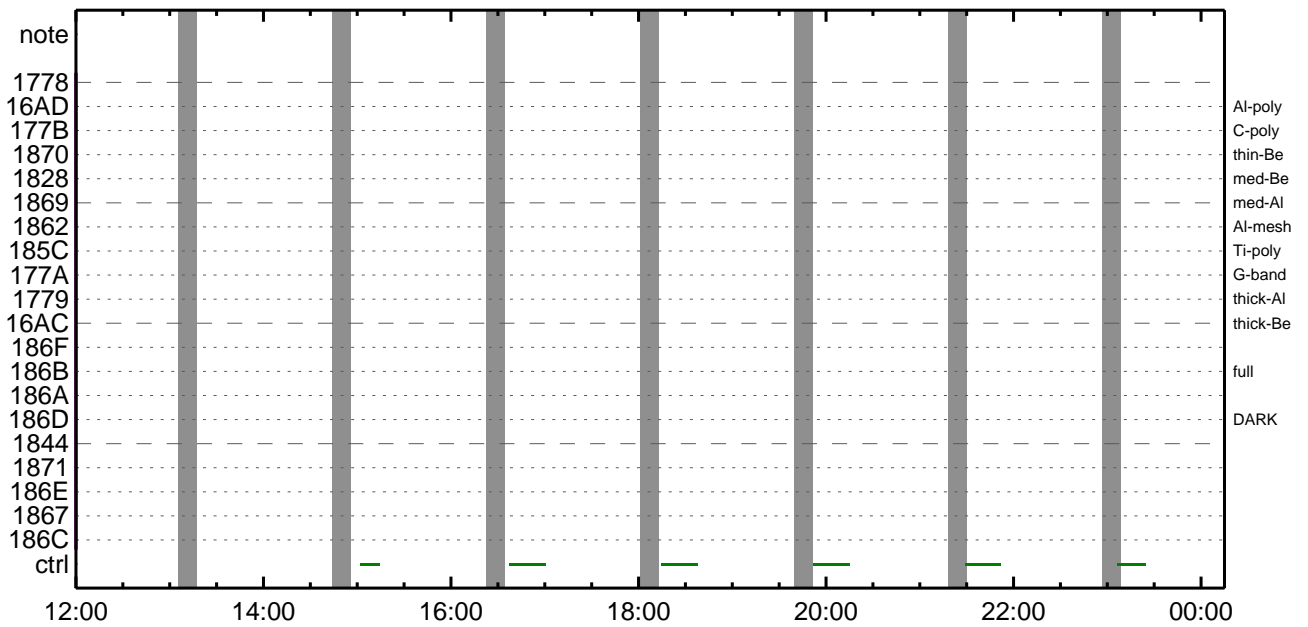
CMDI #0867 2011/04/16



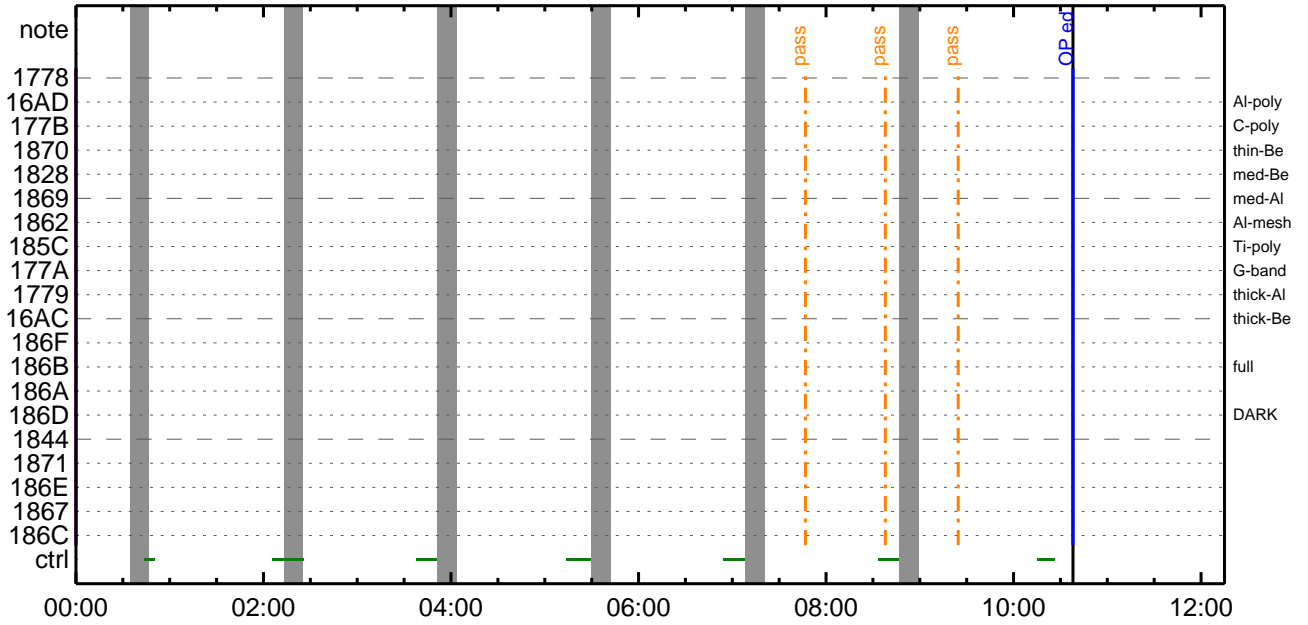
CMDI #0867 2011/04/17



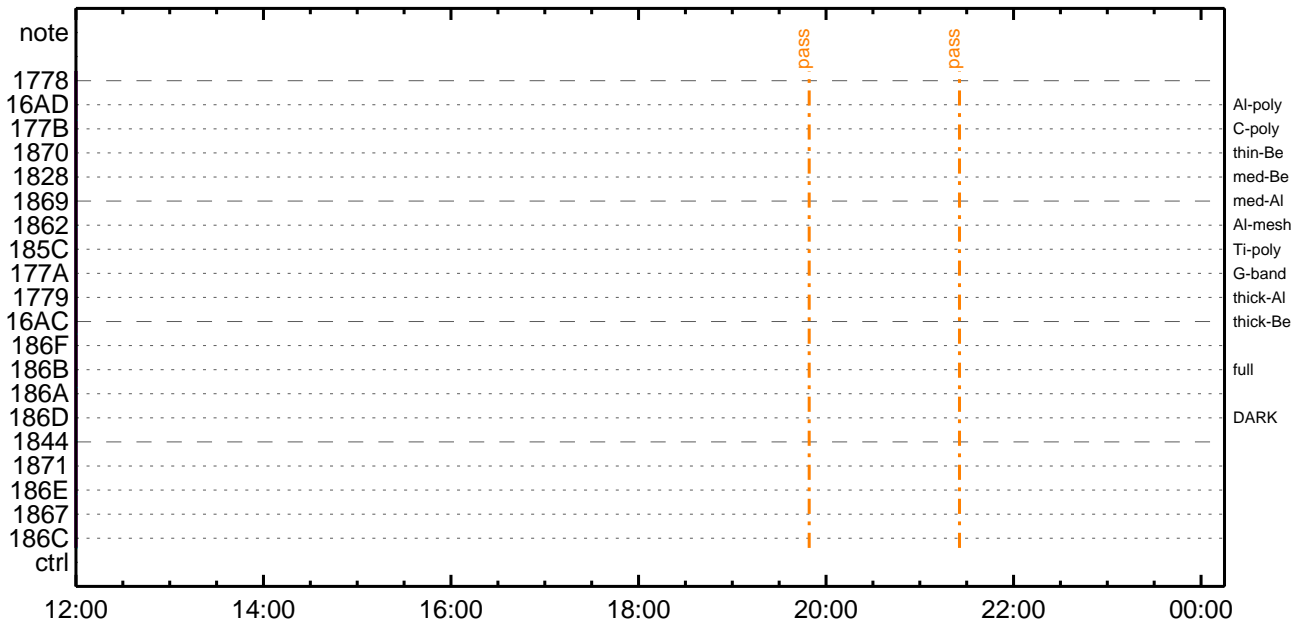
CMDI #0867 2011/04/17



CMDI #0867 2011/04/18



CMDI #0867 2011/04/18









```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 664546.3 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 . C. ***** MDP 'ûÃîñî»ò%ÿñÈÃð¹ñèDCBC•x²è *****
0140 C. (%ã°îÿÓÿÃÿÈÿPÿËÿÿãÿçÿèñÈ%¼ñ¼Ã»Ûñ¹ñè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÃÿSÿÿÃÿ~¼Ã»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-995 2011-04-12 10:26:10 151 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøøøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a c0 c0 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 40 c0 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 40 40 10 10)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d c0 40 10 10)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 . C. ----- Success Verify ? OK / NG ____
0063 C.
0064 C.
0065 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0066 C.
0067 +. DC 07-F0 MDP_XRT_MODE_OBSV
0068 BC (c2)
0069 +. TI 2011-04-12 22:29:02.0
0070 DC 07-F0 MDP_XRT_MODE_OBSV
0071 BC (c2)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C. ***** XRT END *****
0075 . C. *****
0076 C. SOT table upload
0077 C. *****
0078 . C. < Stop SP table >
0079 +. DC 07-F0 MDP_SP_CTRL_MANU
0080 BC (61)
0081 C. -----
0082 C. MDP_SP_CTRL_MODE = MANU [ ]
0083 C. -----
0084 C.
0085 . C. <Upload SP Observation Table>
0086 . S. RAM ram-284:MDP_OBS_S
0087 ( )
0088 C.
0089 . C. < Dump RAMID=MDP_OBS_S >
0090 +. DC 07-F0 MDP_DUMP_SPTBL
0091 BC (83 07 00 00 00 38 b8)
0092 C. -----
0093 C. MDP_OBS_S verify = OK/NG [ ]
0094 C. -----
0095 C.
```

```

0096 . C. < Upload DPL table >
0097 +. DC 07-F0 MDP_FG_CTRL_MANU
0098 BC (51)
0099 . C. -----
0100 C. MDP_FG_CTRL_MODE = MANU [ ]
0101 C. -----
0102 C.
0103 C. ¶çYÃY×Yí;¼YÉºîÁºªÈSTS_CHKºðOFFºÈª¹ªë
0104 C.
0105 . S. RAM ram-271:MDP_DPL
0106 ( )
0107 C.
0108 . C. < Dump RAMID=MDP_DPL >
0109 +. DC 07-F0 MDP_DUMP_FGTBL
0110 BC (82 07 00 38 b8 00 40)
0111 . C. -----
0112 C. MDP_DPL verify = OK [ ]
0113 C. -----
0114 C.
0115 C. STS_CHKºðONºÈª¹ªë
0116 C.
0117 . C. < Update MDP DSC PAR1 >
0118 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0119 BC (4c)
0120 C. MDP_CMD_CODE = F04C0700[ ]
0121 C. MDP_CMD_CNT (count-up 1) [ ]
0122 C. -----
0123 C.
0124 . C.
0125 C. *****
0126 C. SOT TI command set
0127 C. *****
0128 C. Execute, after the success of TBL upload.
0129 +. TI 2011-04-12 22:29:18.0
0130 DC 07-F0 MDP_SOT_MODE_OBSV
0131 BC (40)
0132 . C. -----
0133 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0134 C. -----
0135 C.
0136 C.
0137 . C. ***** MDP `úÃîªî»ö¼YºÈÂªª¹ªëDCBC•x²è *****
0138 C. (%áºîYÔYÃYÈY²YÈYáYçYèºÈ¼ºª¼Ã»Ûª¹ªë)
0139 . S. DC-BC dcbc-402:DCBC
0140 (MDP_known_event)
0141 C.
0142 C.
0143 . C. ***** ¶D¶¹•î Daily±¿îÑºÈ´Øª¹ªëDCBC•x²è *****
0144 . S. DC-BC dcbc-153:DCBC
0145 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0146 C.
0147 C.
0148 . C. ;ãLOS¶ÃY¶YÃY-¼Ã»Û;ã
0149 C.
0150 . C. ***** LOS *****
0151 C.

```

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\*\*\* OP Sequence for XRT \*\*\*

2011/04/12	22:39:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/12	22:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/12	22:40:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/04/12	22:40:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/12	22:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/12	22:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/12	22:40:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/12	22:42:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/12	22:42:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2011/04/12	22:42:58.0	XRT_FL_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/04/12	22:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/12	23:17:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/12	23:17:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/12	23:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/12	23:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/12	23:32:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/12	23:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/13	00:49:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/13	00:49:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/13	00:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/13	00:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/13	01:01:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/13	01:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/13	02:16:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/13	02:16:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/13	02:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/13	02:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/13	02:37:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/13	02:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/13	03:45:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/13	03:45:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/13	03:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/13	03:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/13	04:14:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/13	04:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/13	05:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/13	05:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/13	05:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/13	05:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/13	05:51:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/13	05:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/13	06:01:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/13	06:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/04/13	06:01:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/04/13	06:01:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/04/13	06:01:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/04/13	06:01:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/13	06:04:28.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04

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2011/04/13	06:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/13	06:11:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/13	06:11:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/13	06:11:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/04/13	06:11:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/13	06:11:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/13	06:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/13	06:11:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/13	06:14:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/13	06:14:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2011/04/13	06:14:28.0	XRT_FL_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2011/04/13	06:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/13	07:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/13	07:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/13	07:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/13	07:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/13	07:29:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/13	07:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/13	08:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/13	08:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/13	08:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/13	08:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/13	09:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/13	09:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/13	09:59:54.0	XRT_CTRL_MANU_440_OG [0x1b8]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/13	10:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 3c 72				
2011/04/13	10:00:24.0	XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/04/13	11:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 23 8d				
2011/04/13	12:00:24.0	XRT_Custom_444_OG [0x1bc]							
2011/04/13	13:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/04/13	14:00:24.0	XRT_Custom_444_OG [0x1bc]							
2011/04/13	14:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04 00 00 dc 73				
2011/04/13	16:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 c3 8e				
2011/04/13	16:00:24.0	XRT_Custom_444_OG [0x1bc]							
2011/04/13	17:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/04/13	18:00:24.0	XRT_Custom_447_OG [0x1bf]							
2011/04/13	18:00:34.0	XRT_Custom_444_OG [0x1bc]							
2011/04/13	20:00:34.0	XRT_Custom_448_OG [0x1c0]							
2011/04/13	22:00:12.0	XRT_Custom_447_OG [0x1bf]							
2011/04/13	22:00:22.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/13	22:00:24.0	XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2011/04/14	06:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	06:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/14	06:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/14	06:30:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	06:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	06:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	06:32:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/04/14	06:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	06:39:54.0	XRT_CTRL_MANU_441_OG [0x1b9]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	06:40:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				

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2011/04/14	06:42:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/04/14	06:42:52.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2011/04/14	06:42:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	06:42:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	06:42:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	06:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	06:49:54.0	XRT_CTRL_MANU_441_OG [0x1b9]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	06:50:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2011/04/14	06:52:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/04/14	06:52:52.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/04/14	06:52:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	06:52:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	06:52:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	06:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	06:59:54.0	XRT_CTRL_MANU_441_OG [0x1b9]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	07:00:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2011/04/14	07:02:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/04/14	07:02:52.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2011/04/14	07:02:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	07:02:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	07:02:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	07:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	07:09:54.0	XRT_CTRL_MANU_441_OG [0x1b9]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	07:10:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2011/04/14	07:12:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/04/14	07:12:52.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2011/04/14	07:12:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	07:12:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	07:12:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	07:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	07:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	07:19:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/14	07:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2011/04/14	07:20:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/14	07:20:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/14	07:20:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/14	07:20:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	07:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	07:22:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/04/14	07:22:58.0	XRT_FL_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2011/04/14	07:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	07:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	07:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	07:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	07:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	08:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	08:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/04/14	09:22:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	09:22:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	09:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	09:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	09:44:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	09:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	11:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	11:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	11:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	11:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	11:22:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	11:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	12:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	12:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	12:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	12:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	13:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	13:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	14:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	14:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	14:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	14:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	14:39:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	14:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	14:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	14:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	14:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	14:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	15:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	15:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	16:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	16:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	16:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	16:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	16:18:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	16:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	16:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	16:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/14	16:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/14	16:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/14	16:48:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/14	16:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/14	17:37:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/14	17:37:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/14	17:38:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/14	17:38:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/14	17:38:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/14	17:38:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/14	17:40:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/04/14	17:41:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/04/14	17:47:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	17:47:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/14	17:48:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/04/14	17:48:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/14	17:48:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/14	17:48:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/14	17:48:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/14	17:50:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	17:50:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	17:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	17:50:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	17:50:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2011/04/14	17:50:58.0	XRT_FL_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/04/14	17:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	17:57:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/14	17:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	18:01:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	18:01:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	18:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	18:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	18:25:00.5	XRT_Custom_418_OG [0x1a2]			
2011/04/14	18:26:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	19:28:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	19:28:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	19:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	19:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	19:35:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/14	19:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	19:38:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	19:38:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	19:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	19:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	20:02:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/14	20:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	21:07:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	21:07:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	21:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	21:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	21:14:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/14	21:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	21:16:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	21:16:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	21:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	21:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/14	21:39:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/14	21:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/14	22:45:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/14	22:45:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/14	22:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/14	22:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			

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2011/04/14	22:52:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/14	22:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/14	22:53:30.5	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/14	22:53:32.5	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/14	22:53:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/14	22:56:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/14	23:13:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/14	23:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	00:23:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	00:23:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/15	00:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/15	00:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/15	00:38:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/15	00:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	01:53:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	01:53:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/15	01:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/15	01:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/15	02:13:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/15	02:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	03:28:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	03:28:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/15	03:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/15	03:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/15	03:50:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/15	03:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	05:00:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	05:00:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/15	05:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/15	05:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/15	05:28:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/15	05:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	05:56:24.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	05:56:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/04/15	05:56:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2011/04/15	05:56:46.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/04/15	05:56:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/04/15	05:56:50.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/15	05:59:28.0	XRT_QT_PROG_SET_404_OG [0x194]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/04/15	05:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/15	06:06:24.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/15	06:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/04/15	06:06:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2011/04/15	06:06:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/04/15	06:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/04/15	06:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/04/15	06:06:52.0	XRT_ARS_DIS_437_OG [0x1b5]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/15	06:09:24.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/15	06:09:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	

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2011/04/15	06:09:28.0	XRT_FL_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2011/04/15	06:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	06:40:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	06:40:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	06:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	06:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	07:05:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	07:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	08:20:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	08:20:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	08:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	08:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	08:44:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	08:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	10:01:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	10:01:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	10:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	10:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	10:22:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	10:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	11:52:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	11:52:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	11:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	11:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	12:01:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	12:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	13:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	13:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	13:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	13:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	13:39:30.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	13:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	15:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	15:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	15:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	15:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	15:18:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	15:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	15:25:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	15:25:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	15:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	15:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	15:39:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	15:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	15:41:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	15:41:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	15:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	15:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	15:48:00.0	XRT_Custom_418_OG [0x1a2]			
2011/04/15	15:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	16:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			

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2011/04/15	16:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	16:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/15	16:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/15	16:56:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/15	16:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	17:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	17:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	17:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/15	17:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/15	17:24:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/15	17:25:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/04/15	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/04/15	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/04/15	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/04/15	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/04/15	18:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2011/04/15	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/04/15	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2011/04/15	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/04/15	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/04/15	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/04/15	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/04/15	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	18:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2011/04/15	18:12:58.0	XRT_FL_PROG_SET_426_OG [0x1aa]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2011/04/15	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	18:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	18:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	18:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/15	18:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/15	18:35:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/15	18:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	18:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	18:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	18:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/15	18:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/15	19:01:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/15	19:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	20:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/04/15	20:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/04/15	20:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/04/15	20:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/04/15	20:14:00.0	XRT_Custom_418_OG [0x1a2]						
2011/04/15	20:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/04/15	20:15:00.5	XRT_CTRL_MANU_408_OG [0x198]						

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2011/04/15	20:15:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	20:15:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	20:18:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	20:39:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	20:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/15	21:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	21:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/15	21:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/15	21:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/15	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/15	22:53:00.0	AOCS_Re-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00