

# XRT Timeline to be uploaded on 2011/04/23

Period: 2011/04/23 10:06:00 - 2011/04/28 09:42:00

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**Normal mode**

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<b>XOB #1873: AR Band during South Polar Monitor - 2048x512 - offset 504 - Ti/Poly+thin-Be - 2x2 - AEC 2-3</b>												
Term	Pointing (x, y)		Comment									
04/23 10:33:06 - 04/23 17:59:54	Fixed ( -16.0, -913.0)		# OP start + 10min, HOP81									
<b>PROG= 02 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 76 1-time(s) 180.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x512 (1024, 1528)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x512 (1024, 1528)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x512 (1024, 1528)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x512 (1024, 1528)	Q=95	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36</b>											
Term	Pointing (x, y)		Comment								
04/23 18:03:00 - 04/23 18:12:00	Fixed ( 0.0, 0.0)		synoptic								
04/24 06:19:30 - 04/24 06:26:24	Fixed ( 0.0, 0.0)		synoptic, shifted 16.5 min								
04/24 18:06:30 - 04/24 18:13:24	Fixed ( 0.0, 0.0)		synoptic, shifted 3.5 min								
04/25 05:53:00 - 04/25 06:02:00	Fixed ( 0.0, 0.0)		synoptic, shifted -10.0 min								
04/25 17:53:30 - 04/25 18:00:30	Fixed ( 0.0, 0.0)		synoptic, shifted -9.5 min								
04/26 06:04:24 - 04/26 06:09:54	Fixed ( 0.0, 0.0)		synoptic								

<b>PROG= 13 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 32 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #186B: AR Standard-A(Filter-Ratio) with PFB, thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad-2</b>											
Term	Pointing (x, y)		Comment								
04/24 00:13:00 - 04/24 06:16:24	Track ( -251.5, -208.0) <sup>Ⓜ 04/24 00:10:00</sup>		AR11195								
04/26 06:13:00 - 04/26 08:35:30	Track ( 215.1, -210.3) <sup>Ⓜ 04/26 06:10:00</sup>		#AR11195								

<b>PROG= 08 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 56 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 30 20-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #186A: AR Standard-A(Filter-Ratio) with PFB (384x384), thin-Be and Al/Poly context, 512x512 at 1064 1048, 120s cad-2</b>											
Term	Pointing (x, y)		Comment								
04/24 06:29:30 - 04/24 17:23:30	Track ( -198.3, -207.2) <sup>Ⓜ 04/24 06:26:30</sup>		#AR11195								
04/24 18:16:30 - 04/25 05:49:54	Track ( -96.7, -206.5) <sup>Ⓜ 04/24 18:13:30</sup>		#AR11195								
04/25 08:29:16 - 04/25 17:50:24	Track ( 23.5, -206.9) <sup>Ⓜ 04/25 08:00:00</sup>		AR11195								

**PROG= 07 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn= 17</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 64</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 30</b>	<b>15-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1874: Full Sun 6 Filter- 1x1 Q98 -FW1/FW2-short exposures-2**

Term	Pointing (x, y)	Comment
04/25 06:53:06 - 04/25 07:10:00	Track ( 0.0, 0.0) @ 04/25 06:00:00	#HOP172 DC

**PROG= 09 1-time(s)**

Subr= 1	1-time(s)	180.0sec										
<b>Seqn= 26</b>	<b>1-time(s)</b>	<b>30.0sec</b>										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 12</b>	<b>1-time(s)</b>	<b>30.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 35</b>	<b>1-time(s)</b>	<b>30.0sec</b>										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 43</b>	<b>1-time(s)</b>	<b>30.0sec</b>										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 44</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
med-Al/Open	med-Al/Open	close	Safe	Norm	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 11</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>360.0sec</b>										
<b>Seqn= 83</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 4</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)**

Term	Pointing (x, y)	Comment
04/23 10:33:06 - 04/23 17:59:54	Fixed ( -16.0, -913.0)	# OP start + 10min, HOP81
04/24 00:13:00 - 04/24 06:16:24	Track ( -251.5, -208.0) @ 04/24 00:10:00	AR11195
04/24 06:29:30 - 04/24 17:23:30	Track ( -198.3, -207.2) @ 04/24 06:26:30	#AR11195
04/24 18:16:30 - 04/25 05:49:54	Track ( -96.7, -206.5) @ 04/24 18:13:30	#AR11195
04/25 08:29:16 - 04/25 17:50:24	Track ( 23.5, -206.9) @ 04/25 08:00:00	AR11195
04/25 18:40:46 - 04/26 05:59:54	Track ( 110.7, -208.1) @ 04/25 18:00:30	#AR11195
04/26 06:13:00 - 04/26 08:35:30	Track ( 215.1, -210.3) @ 04/26 06:10:00	#AR11195

**PROG= 15 1-time(s)**

Subr= 1	4-time(s)	2.0sec										
<b>Seqn= 55</b>	<b>45-time(s)</b>	<b>20.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 90</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>600.0sec</b>										
<b>Seqn= 89</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

L Open/Al-mesh Open/Al-mesh close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec  
 Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

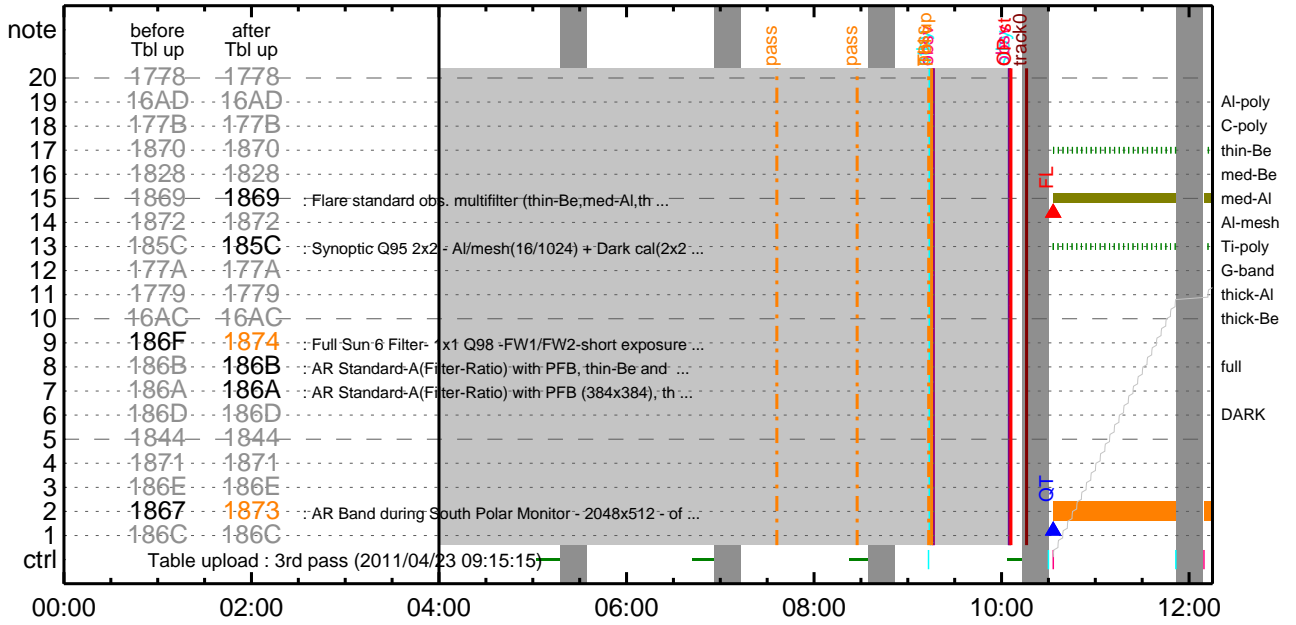
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**Flare Detection**

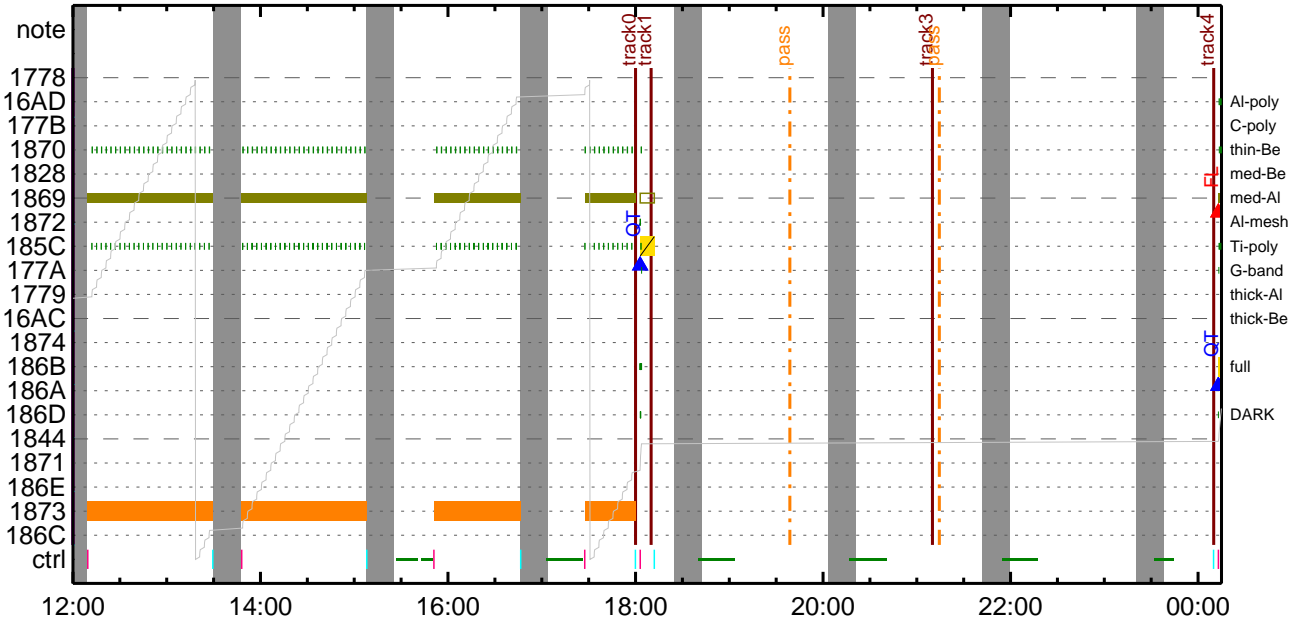
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<b>FLD Patrol</b>										
Term	Pointing (x, y)		Comment							
04/23 10:30:22 - 04/23 18:00:16	Fixed ( -16.0, -913.0)		# OP start + 10min, HOP81							
04/24 00:10:16 - 04/24 06:16:46	Track ( -251.5, -208.0) <sup>Ⓞ</sup> 04/24 00:10:00		AR11195							
04/24 06:26:46 - 04/24 18:03:46	Track ( -198.3, -207.2) <sup>Ⓞ</sup> 04/24 06:26:30		#AR11195							
04/24 18:13:46 - 04/25 05:50:16	Track ( -96.7, -206.5) <sup>Ⓞ</sup> 04/24 18:13:30		#AR11195							
04/25 08:29:02 - 04/25 17:50:46	Track ( 23.5, -206.9) <sup>Ⓞ</sup> 04/25 08:00:00		AR11195							
04/25 18:40:32 - 04/26 06:04:16	Track ( 110.7, -208.1) <sup>Ⓞ</sup> 04/25 18:00:30		#AR11195							
04/26 06:10:16 - 04/28 09:42:00	Track ( 215.1, -210.3) <sup>Ⓞ</sup> 04/26 06:10:00		#AR11195							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

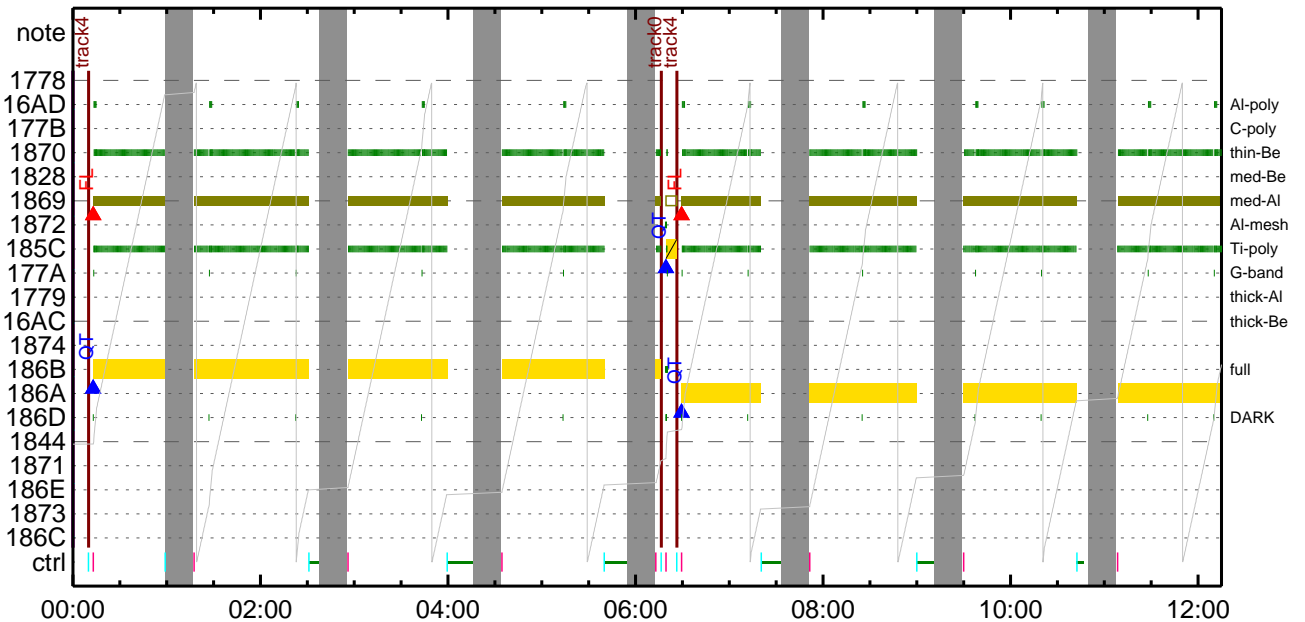
### CMDI #0888 2011/04/23



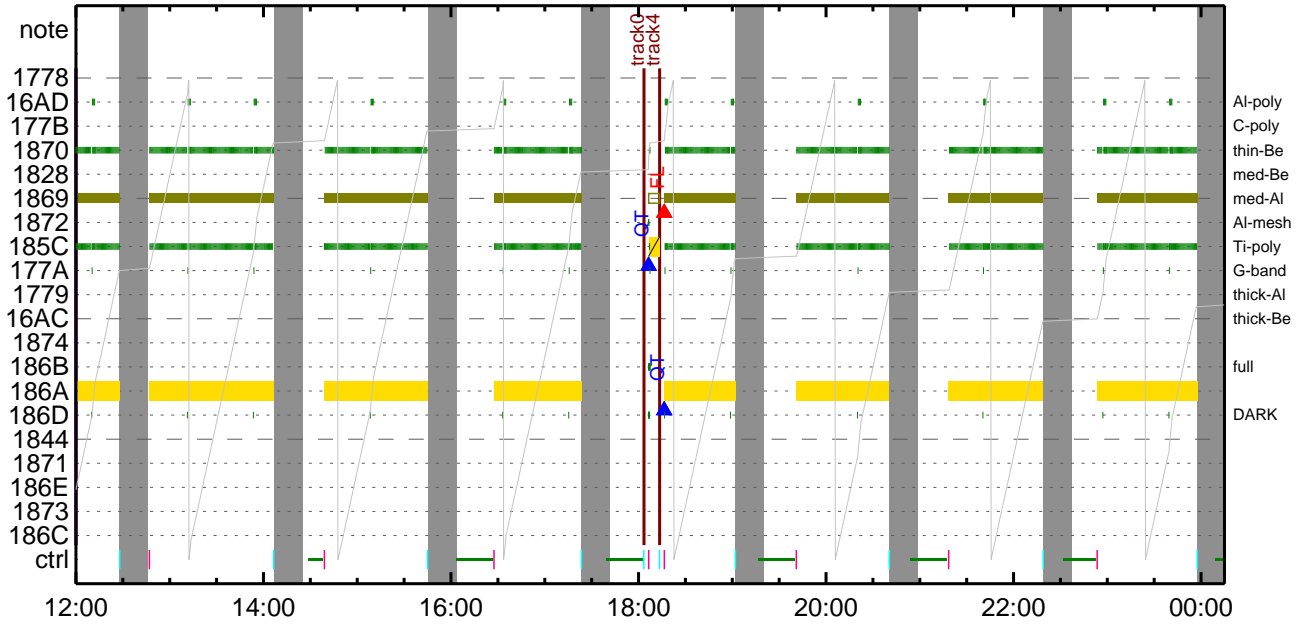
### CMDI #0888 2011/04/23



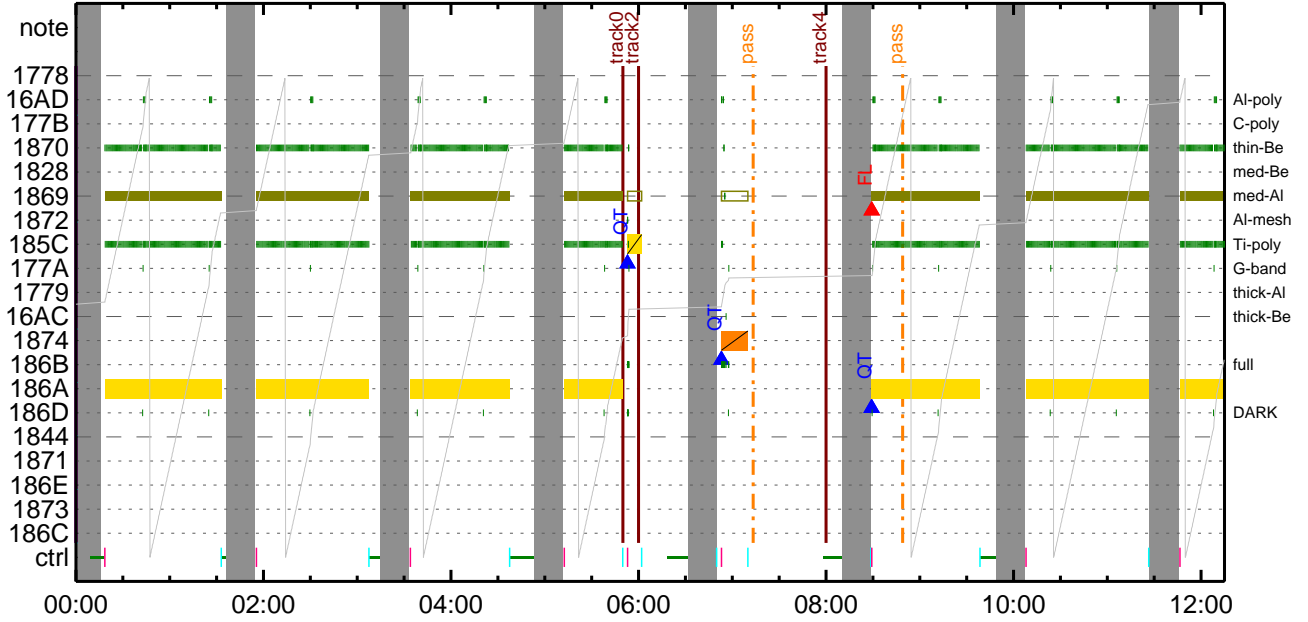
### CMDI #0888 2011/04/24



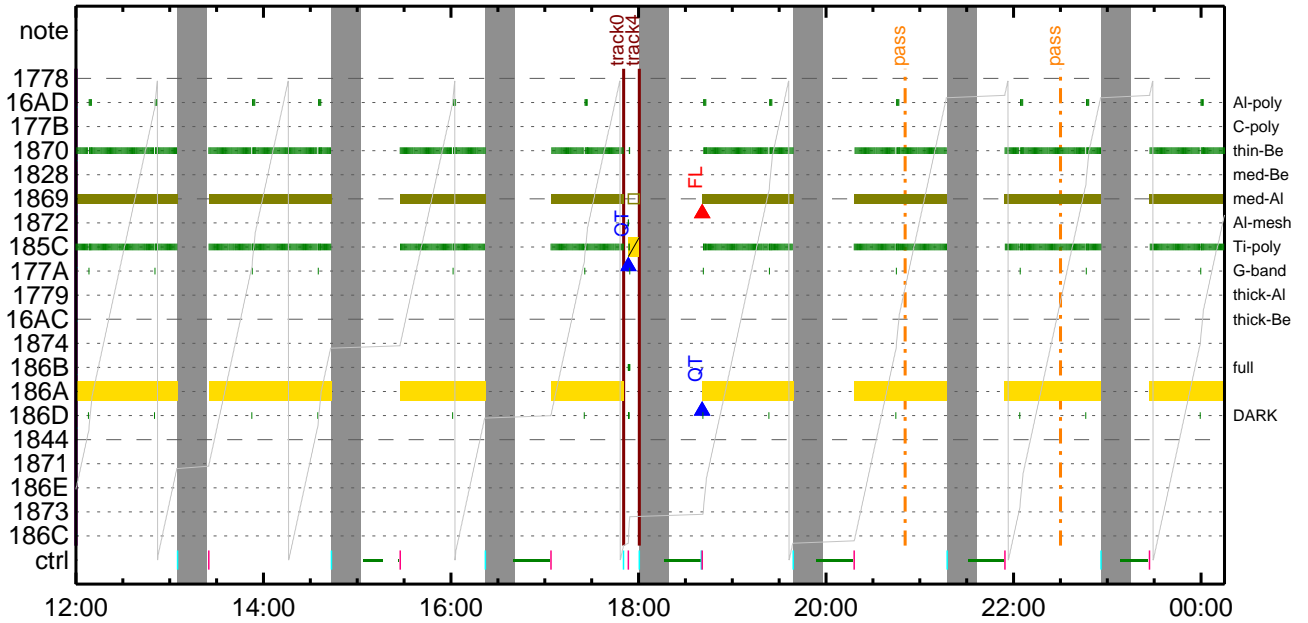
CMDI #0888 2011/04/24



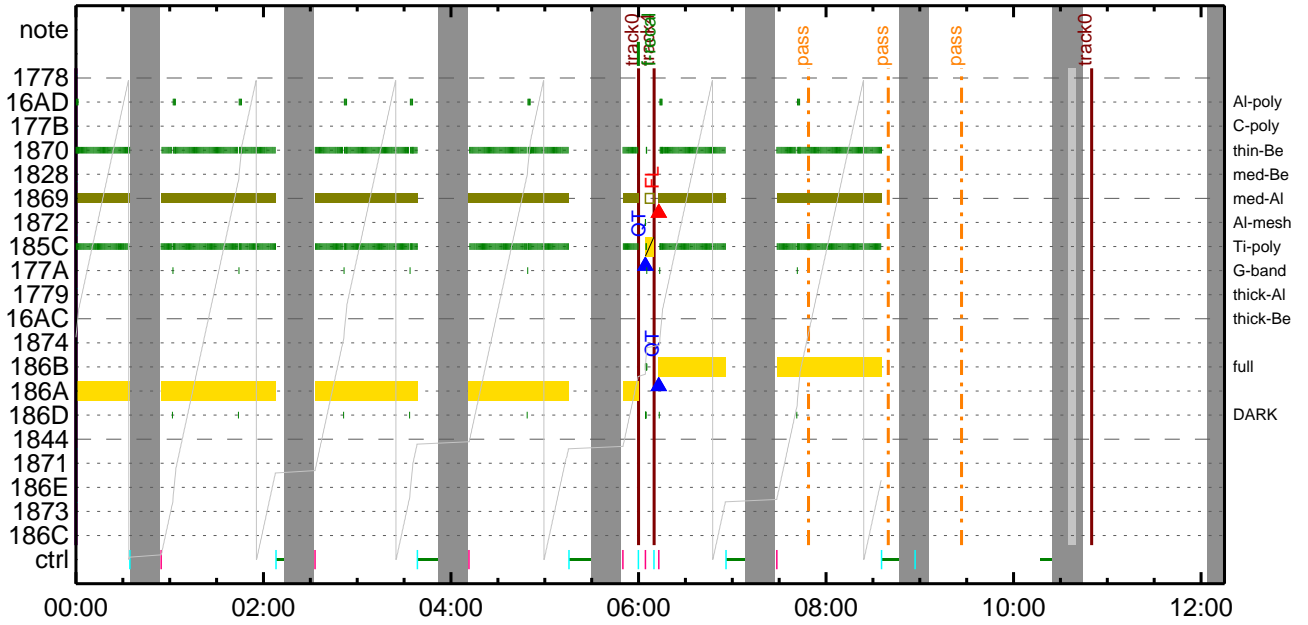
CMDI #0888 2011/04/25



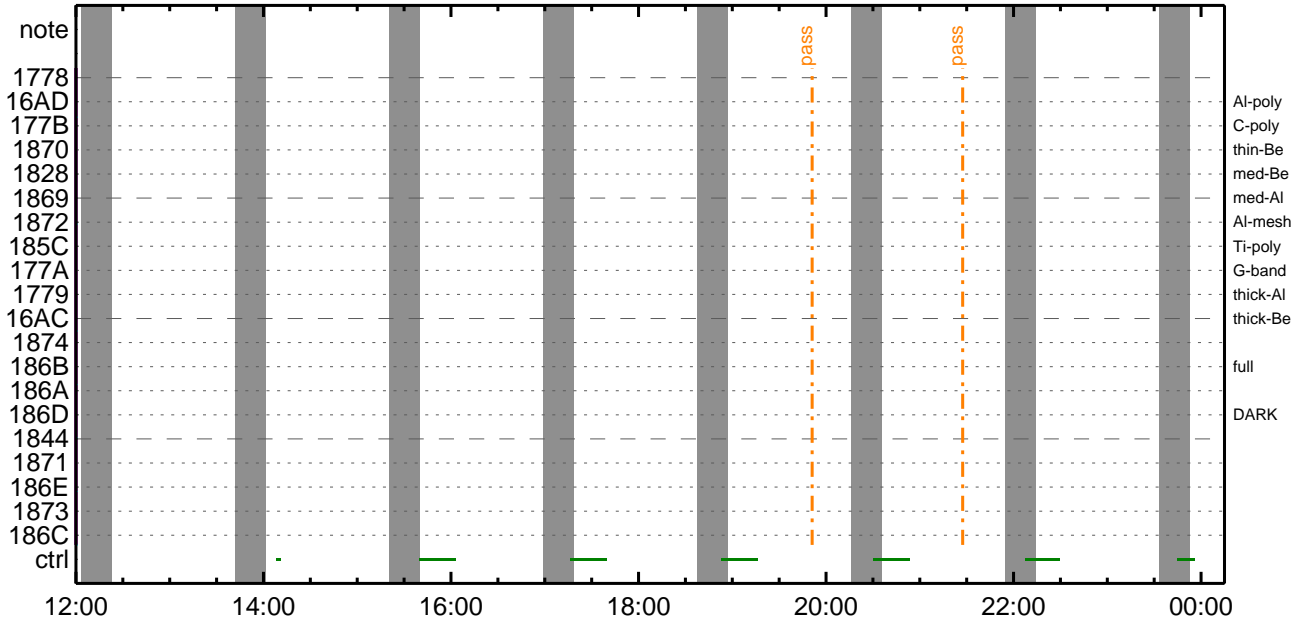
CMDI #0888 2011/04/25



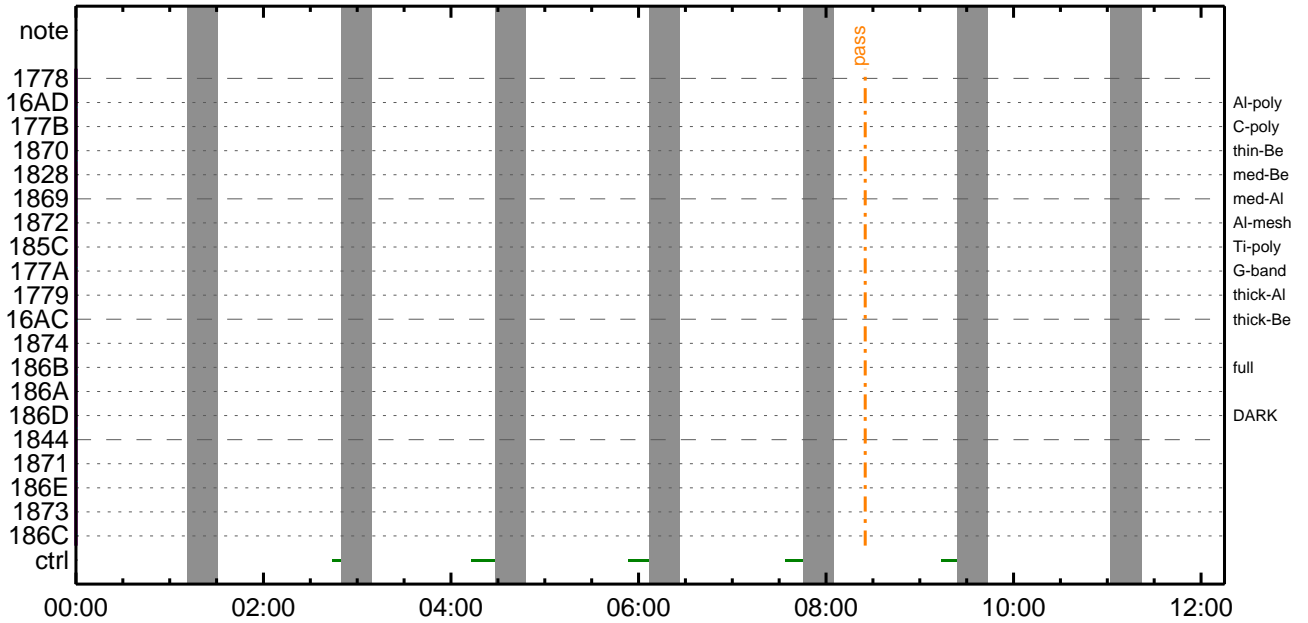
CMDI #0888 2011/04/26



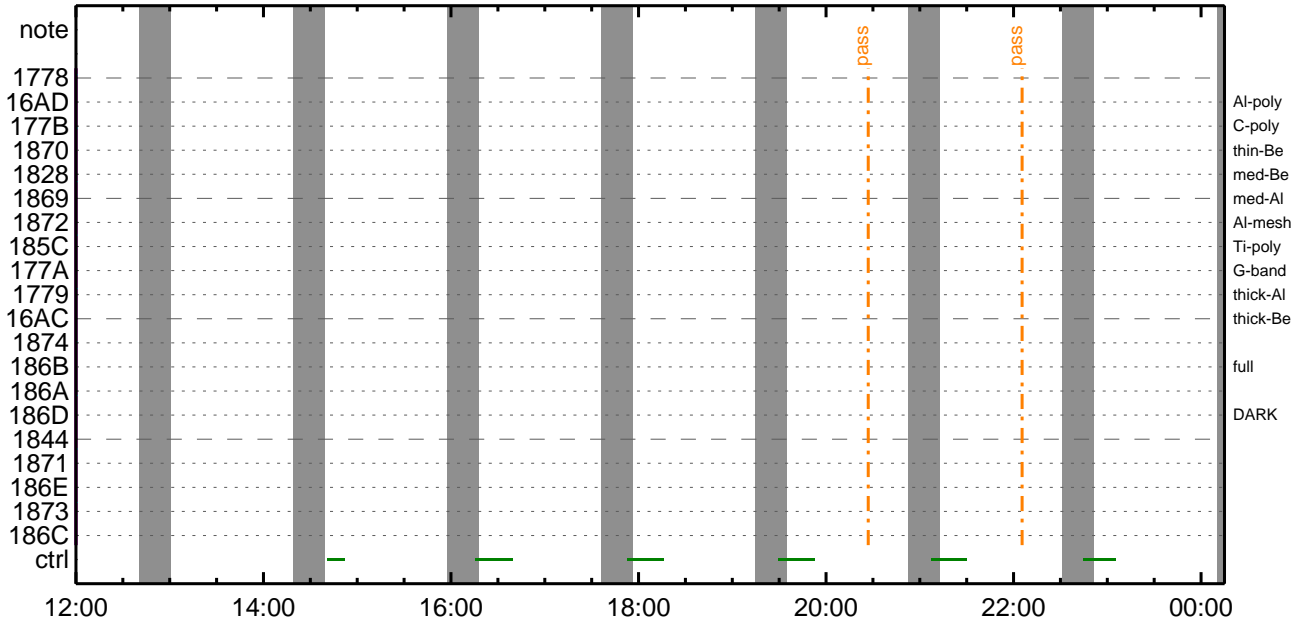
CMDI #0888 2011/04/26



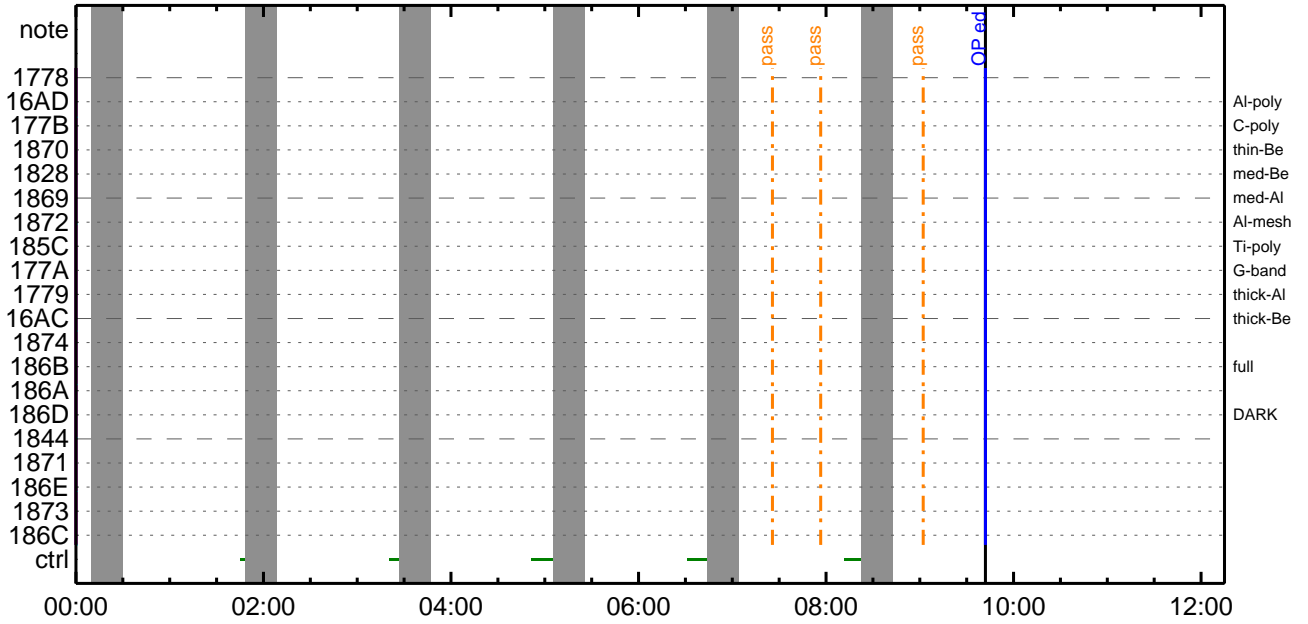
CMDI #0888 2011/04/27



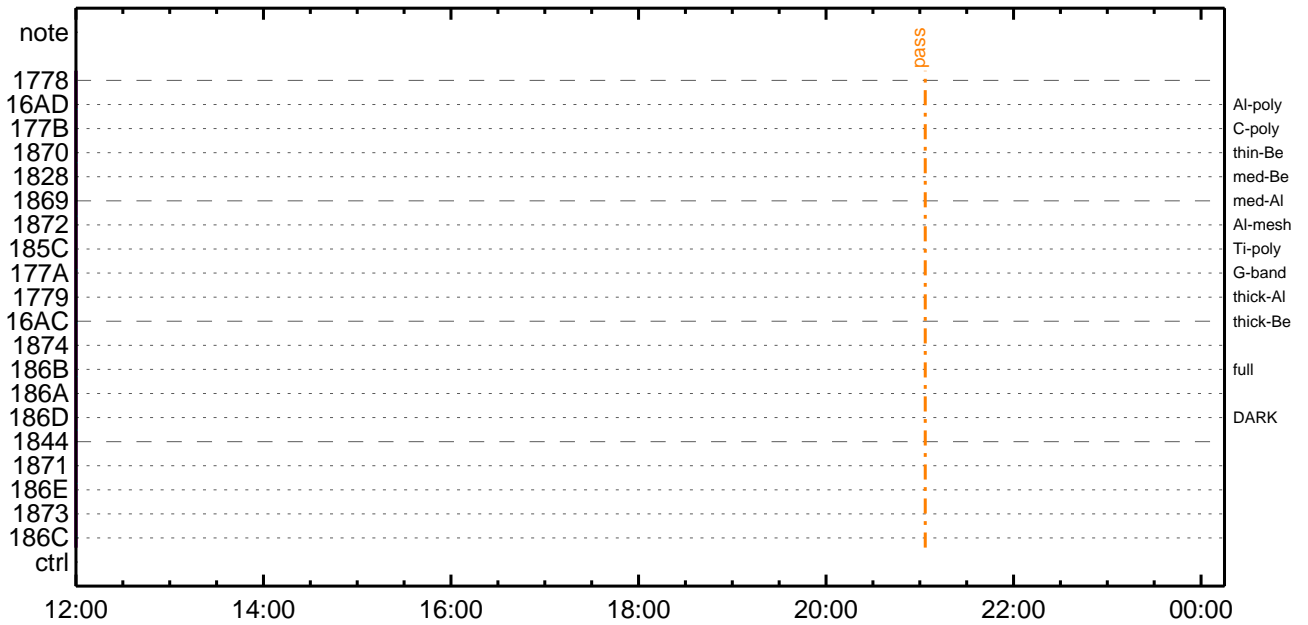
CMDI #0888 2011/04/27



CMDI #0888 2011/04/28



CMDI #0888 2011/04/28















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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-261:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-285:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2011-04-23 10:05:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 C. ***** XRT START *****
0150 C.
0151 +. DC 07-F0 MDP_XRT_CTRL_MANU
0152 BC (c1)
0153 + DC 07-F0 MDP_XRT_MODE_STBY
0154 BC (c3)
0155 . C. ----- Success Verify ? OK / NG____
0156 C.
0157 C. XRT Obs. Table Upload
0158 . S. RAM ram-291:MDP_OBS_X
0159 ( )
0160 C.
0161 +. DC 07-F0 MDP_DUMP_XRTTBL
0162 BC (84 07 00 00 00 3a d4)
0163 . C. ----- Comparison Check ? OK / ERR ____
0164 C.
0165 C.
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 01 b1 b1 04 04)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 02 b1 b1 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 03 b1 b1 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 04 b1 b1 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 05 85 83 06 06)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 06 80 bf 20 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 07 80 80 20 20)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 08 80 80 20 08)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 09 80 80 08 20)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 0a 85 83 06 06)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 0b 85 83 08 08)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 0f 80 80 06 06)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 10 80 80 08 08)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

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0194 C.
0195 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0196 C.
0197 +. DC 07-F0 MDP_XRT_MODE_OBSV
0198 BC (c2)
0199 +. TI 2011-04-23 10:05:02.0
0200 DC 07-F0 MDP_XRT_MODE_OBSV
0201 BC (c2)
0202 . C. ----- Success Verify ? OK / NG ____
0203 C.
0204 C. ***** XRT END *****
0205 C.
0206 . C. ***** MDP `úÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0207 C. (¼á°îÿÓÿÃÿÈÿËÿËÿÁÿçÿèñÊ¼ññ¼Á»Ûñ¹ñè)
0208 . S. DC-BC dcbc-402:DCBC
0209 (MDP_known_event)
0210 C.
0211 C.
0212 . C. ***** ÿDÿ!•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0213 . S. DC-BC dcbc-153:DCBC
0214 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0215 C.
0216 C.
0217 . C. ¡ãLOSÿÁÿSÿÃÿ¼Á»Ûñ¹ñè
0218 C.
0219 . C. ***** LOS *****
0220 C.
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\*\*\* OP Sequence for XRT \*\*\*

2011/04/23	10:16:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	51	26	01	68
2011/04/23	10:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	10:30:02.0	XRT_FOCUS_POSITION_415_OG [0x19f]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	af	00	
2011/04/23	10:30:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/04/23	10:30:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/04/23	10:30:26.0	XRT_AEC_RESET_414_OG [0x19e]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/04/23	10:30:28.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/04/23	10:33:00.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/04/23	10:33:02.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	02		
2011/04/23	10:33:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	0f		
2011/04/23	10:33:06.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	11:51:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	11:51:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/04/23	11:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/04/23	11:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/04/23	12:08:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/23	12:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	13:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	13:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/04/23	13:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/04/23	13:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/04/23	13:47:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/23	13:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	15:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	15:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/04/23	15:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/04/23	15:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/04/23	15:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/23	15:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	16:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	16:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/04/23	16:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/04/23	16:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/04/23	17:26:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/23	17:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/04/23	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2011/04/23	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2011/04/23	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2011/04/23	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/04/23	18:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d		
2011/04/23	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/04/23	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/04/23	18:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/23	21:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03	00	00	00	00
2011/04/24	00:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/04/24	00:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							

2011/04/24	00:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	04	00	00	00
2011/04/24	00:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/24	00:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/24	00:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/24	00:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/24	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/24	00:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2011/04/24	00:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f		
2011/04/24	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	00:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	00:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/24	00:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/24	01:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/24	01:16:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/24	01:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	02:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	02:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/24	02:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/24	02:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/24	02:55:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/24	02:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	03:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	03:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/24	03:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/24	04:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/24	04:33:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/24	04:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	05:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	05:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/24	05:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/24	05:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/24	06:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/24	06:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	06:16:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	06:16:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/04/24	06:16:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2011/04/24	06:16:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/04/24	06:16:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/04/24	06:16:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/24	06:19:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/04/24	06:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/24	06:26:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/24	06:26:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/04/24	06:26:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04	00	00	00
2011/04/24	06:26:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/24	06:26:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/24	06:26:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/24	06:26:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			



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2011/04/24	06:29:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	06:29:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2011/04/24	06:29:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f
2011/04/24	06:29:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	07:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	07:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	07:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	07:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	07:50:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	07:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	09:00:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	09:00:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	09:00:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	09:03:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	09:29:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	09:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	10:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	10:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	10:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	10:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	11:07:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	11:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	12:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	12:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	12:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	12:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	12:46:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	12:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	14:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	14:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	14:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	14:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	14:38:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	14:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	15:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	15:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	15:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	15:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	16:26:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/24	16:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/24	17:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	17:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/24	17:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/24	17:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/24	18:03:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/24	18:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/04/24	18:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2011/04/24	18:03:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/04/24	18:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/04/24	18:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]					

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2011/04/24	18:06:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/04/24	18:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/24	18:13:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/24	18:13:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/04/24	18:13:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00
2011/04/24	18:13:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/04/24	18:13:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/04/24	18:13:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/04/24	18:13:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/04/24	18:16:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/24	18:16:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2011/04/24	18:16:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f
2011/04/24	18:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/24	19:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/24	19:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/24	19:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/24	19:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/24	19:40:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/24	19:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/24	20:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/24	20:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/24	20:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/24	20:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/24	21:17:30.0	XRT_Custom_418_OG [0x1a2]				
2011/04/24	21:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/24	22:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/24	22:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/24	22:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/24	22:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/24	22:52:30.0	XRT_Custom_418_OG [0x1a2]				
2011/04/24	22:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/24	23:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/24	23:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/24	23:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/25	00:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/25	00:17:30.0	XRT_Custom_418_OG [0x1a2]				
2011/04/25	00:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/25	01:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/25	01:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/25	01:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/25	01:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/25	01:54:30.0	XRT_Custom_418_OG [0x1a2]				
2011/04/25	01:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/04/25	03:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/04/25	03:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/04/25	03:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/04/25	03:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/04/25	03:33:00.0	XRT_Custom_418_OG [0x1a2]				
2011/04/25	03:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2011/04/25	04:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	04:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/25	04:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/25	04:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/25	05:11:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/25	05:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	05:49:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	05:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/04/25	05:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/04/25	05:50:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/04/25	05:50:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/04/25	05:50:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/25	05:52:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2011/04/25	05:53:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	06:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/04/25	06:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	06:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	06:50:02.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/04/25	06:50:22.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/04/25	06:50:24.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/04/25	06:50:26.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/25	06:53:04.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2011/04/25	06:53:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	07:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	08:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00	
2011/04/25	08:28:40.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	08:28:42.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/04/25	08:29:02.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/04/25	08:29:04.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/04/25	08:29:06.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/04/25	08:29:08.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/04/25	08:29:10.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/25	08:29:12.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2011/04/25	08:29:14.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f	
2011/04/25	08:29:16.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	09:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	09:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/25	09:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/25	09:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/25	10:07:00.0	XRT_Custom_418_OG [0x1a2]					
2011/04/25	10:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	11:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/04/25	11:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/04/25	11:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/04/25	11:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/04/25	11:45:30.0	XRT_Custom_418_OG [0x1a2]					
2011/04/25	11:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/04/25	13:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/04/25	13:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	13:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	13:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	13:24:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/25	13:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	14:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	14:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	14:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	14:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	15:26:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/25	15:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	16:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	16:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	16:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	16:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	17:03:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/25	17:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	17:50:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	17:50:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/25	17:50:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/25	17:50:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/25	17:50:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/25	17:50:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/25	17:53:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/25	17:53:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	18:00:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/04/25	18:00:30.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	18:00:32.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	18:00:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	18:03:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	18:40:10.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	18:40:12.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/25	18:40:32.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/25	18:40:34.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/25	18:40:36.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/25	18:40:38.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/25	18:40:40.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	18:40:42.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/04/25	18:40:44.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/25	18:40:46.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	19:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	19:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	19:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	19:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	20:17:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/25	20:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	21:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	21:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	21:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2011/04/25	21:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	21:53:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/25	21:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/25	22:56:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/25	22:56:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/25	22:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/25	22:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/25	23:26:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/25	23:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	00:34:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	00:34:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	00:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	00:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	00:53:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/26	00:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	02:08:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	02:08:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	02:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	02:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	02:32:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/26	02:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	03:38:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	03:38:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	03:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	03:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	04:10:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/26	04:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	05:15:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	05:15:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	05:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	05:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	05:49:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/26	05:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	05:59:56.0	XRT_FOCUS_RECALIBRATE_432_OG [0x1b0]								
			XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/04/26	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/26	06:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/26	06:04:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/26	06:04:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/26	06:04:20.0	XRT_ARS_DIS_431_OG [0x1af]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/26	06:04:22.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/26	06:04:24.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/26	06:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]								
			AOCU_NM	5	02-76	04 00 00 00 00				
2011/04/26	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/26	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/26	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/26	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				

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2011/04/26	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	06:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/04/26	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/26	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	06:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	06:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	06:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	06:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	07:27:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/26	07:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/26	08:35:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	08:35:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/26	08:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/26	08:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/26	08:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/26	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				