

XRT Timeline to be uploaded on 2011/04/28

Period: 2011/04/28 09:42:00 - 2011/05/03 09:32:00

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Normal mode

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XOB #1877: AR Dynamics-HOP 185 Ti/Poly-Med-Be (384FOV) AEC3 Q95, G-band(512FOV) - 1min cadence													
Term	Pointing (x, y)		Comment										
04/28 10:21:36 - 04/28 11:39:00	Track (658.2, -231.6) ^{@ 04/28 09:52:00}		# OP start + 10min, HOP185										
04/29 10:00:36 - 04/29 11:59:54	Track (775.6, -244.0) ^{@ 04/29 08:00:00}		HOP185										
04/30 08:19:36 - 04/30 08:30:00	Track (863.4, -258.7) ^{@ 04/30 08:00:00}		HOP185										
PROG= 03 Inf.-time(s)													
└─ Subr= 1 1-time(s) 30.0sec													
└─ Seqn= 45 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 15-time(s) 60.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #186A: AR Standard-A(Filter-Ratio) with PFB (384x384), thin-Be and Al/Poly context, 512x512 at 1064 1048, 120s cad-2													
Term	Pointing (x, y)		Comment										
04/28 12:00:06 - 04/28 13:59:54	Track (658.2, -231.6) ^{@ 04/28 09:52:00}		# OP start + 10min, HOP185										
04/28 14:03:00 - 04/28 17:59:54	Track (650.9, -233.0) ^{@ 04/28 14:00:00}		AR11195										
04/28 18:53:06 - 04/29 05:29:00	Track (675.8, -235.2) ^{@ 04/28 18:10:00}		# AR11195										
04/29 06:18:30 - 04/29 07:59:54	Track (741.5, -241.9) ^{@ 04/29 06:15:30}		# AR11195										
04/29 12:03:00 - 04/29 17:11:30	Track (769.3, -245.2) ^{@ 04/29 12:00:00}		AR11195										
04/29 18:07:00 - 04/30 05:38:54	Track (796.1, -248.8) ^{@ 04/29 18:04:00}		# AR11195										
04/30 05:52:00 - 04/30 07:47:00	Track (840.3, -255.9) ^{@ 04/30 05:49:00}		# AR11195										
PROG= 07 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 64 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 30 15-time(s) 2.0sec													
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36													
Term	Pointing (x, y)		Comment										
04/28 18:03:00 - 04/28 18:13:00	Fixed (0.0, 0.0)		synoptic										
04/29 06:08:30 - 04/29 06:15:24	Fixed (0.0, 0.0)		synoptic, shifted 5.5 min										
04/29 17:57:00 - 04/29 18:03:54	Fixed (0.0, 0.0)		synoptic, shifted -6.0 min										
04/30 05:42:00 - 04/30 05:48:54	Fixed (0.0, 0.0)		synoptic, shifted -21.0 min										
PROG= 13 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 32 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1876: AR Dynamics-HOP 185 Ti/Poly-Med-Be (384FOV) AEC3 Q95, G-band(512FOV) - 3min cadence

Term	Pointing (x, y)	Comment
04/29 08:03:00 - 04/29 10:00:00	Track (775.6, -244.0) ^{Ⓞ 04/29 08:00:00}	HOP185
PROG= 19 Inf.-time(s)		
Subr= 1	1-time(s) 30.0sec	
Seqn= 45	1-time(s) 30.0sec	
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	15-time(s) 180.0sec	
Seqn= 61	1-time(s) 2.0sec	
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
04/28 10:21:36 - 04/28 11:39:00	Track (658.2, -231.6) ^{Ⓞ 04/28 09:52:00}	# OP start + 10min, HOP185
04/28 12:00:06 - 04/28 13:59:54	Track (658.2, -231.6) ^{Ⓞ 04/28 09:52:00}	# OP start + 10min, HOP185
04/28 14:03:00 - 04/28 17:59:54	Track (650.9, -233.0) ^{Ⓞ 04/28 14:00:00}	AR11195
04/28 18:53:06 - 04/29 05:29:00	Track (675.8, -235.2) ^{Ⓞ 04/28 18:10:00}	# AR11195
04/29 06:18:30 - 04/29 07:59:54	Track (741.5, -241.9) ^{Ⓞ 04/29 06:15:30}	# AR11195
04/29 08:03:00 - 04/29 10:00:00	Track (775.6, -244.0) ^{Ⓞ 04/29 08:00:00}	HOP185
04/29 10:00:36 - 04/29 11:59:54	Track (775.6, -244.0) ^{Ⓞ 04/29 08:00:00}	HOP185
04/29 12:03:00 - 04/29 17:11:30	Track (769.3, -245.2) ^{Ⓞ 04/29 12:00:00}	AR11195
04/29 18:07:00 - 04/30 05:38:54	Track (796.1, -248.8) ^{Ⓞ 04/29 18:04:00}	# AR11195
04/30 05:52:00 - 04/30 07:47:00	Track (840.3, -255.9) ^{Ⓞ 04/30 05:49:00}	# AR11195
04/30 08:19:36 - 04/30 08:30:00	Track (863.4, -258.7) ^{Ⓞ 04/30 08:00:00}	HOP185
PROG= 15 1-time(s)		
Subr= 1	4-time(s) 2.0sec	
Seqn= 55	45-time(s) 20.0sec	
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 90	1-time(s) 2.0sec	
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2	1-time(s) 600.0sec	
Seqn= 89	1-time(s) 2.0sec	
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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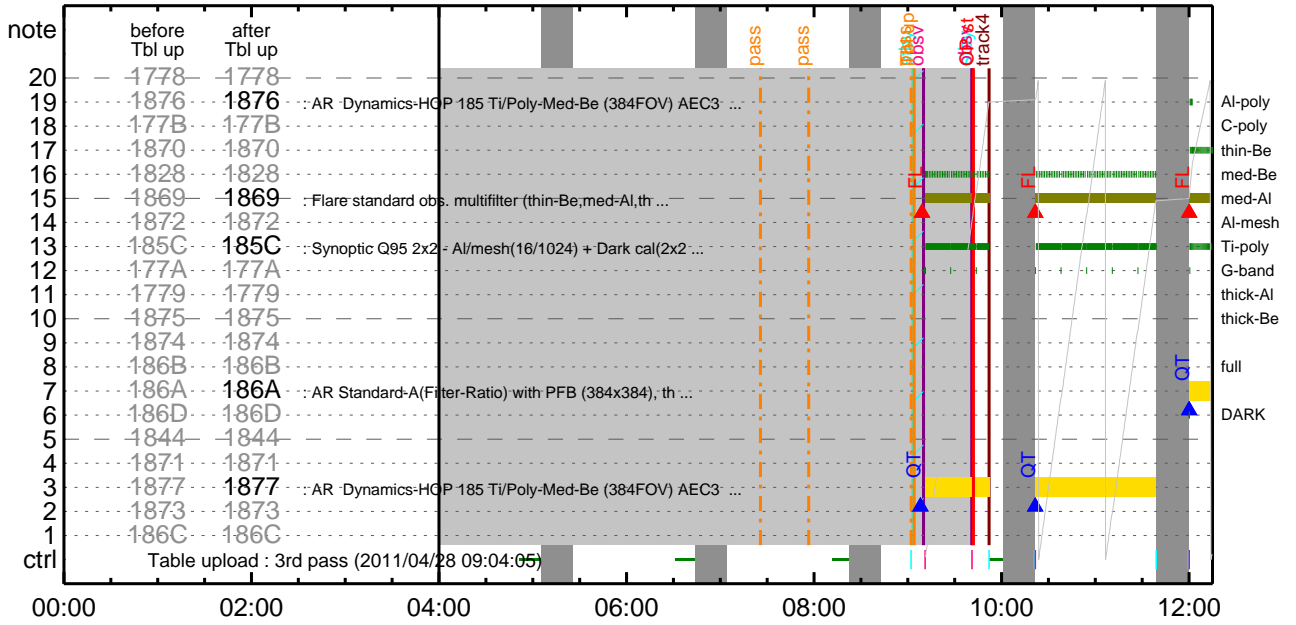
Flare Detection

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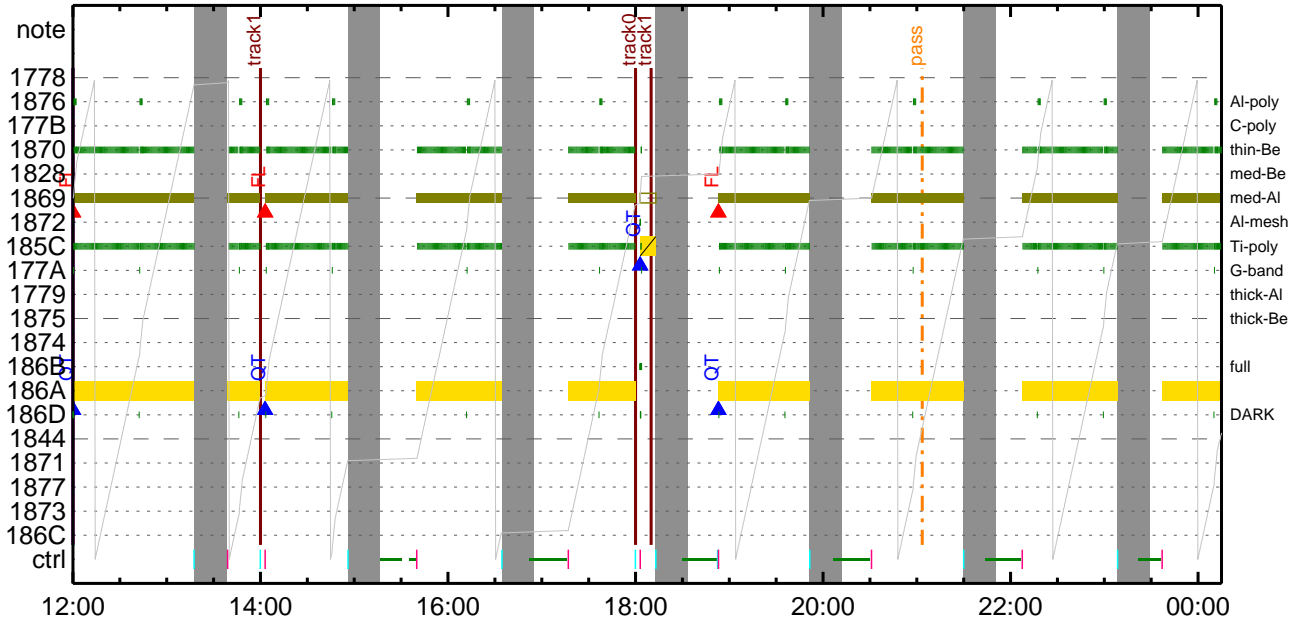
FLD Patrol

Term	Pointing (x, y)	Comment
04/28 18:52:52 - 04/29 06:05:46	Track (675.8, -235.2) ^{Ⓞ 04/28 18:10:00}	# AR11195
04/29 06:15:46 - 04/29 17:54:16	Track (741.5, -241.9) ^{Ⓞ 04/29 06:15:30}	# AR11195
04/29 18:04:16 - 04/30 05:39:16	Track (796.1, -248.8) ^{Ⓞ 04/29 18:04:00}	# AR11195
04/30 05:49:16 - 05/03 09:32:00	Track (840.3, -255.9) ^{Ⓞ 04/30 05:49:00}	# AR11195
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

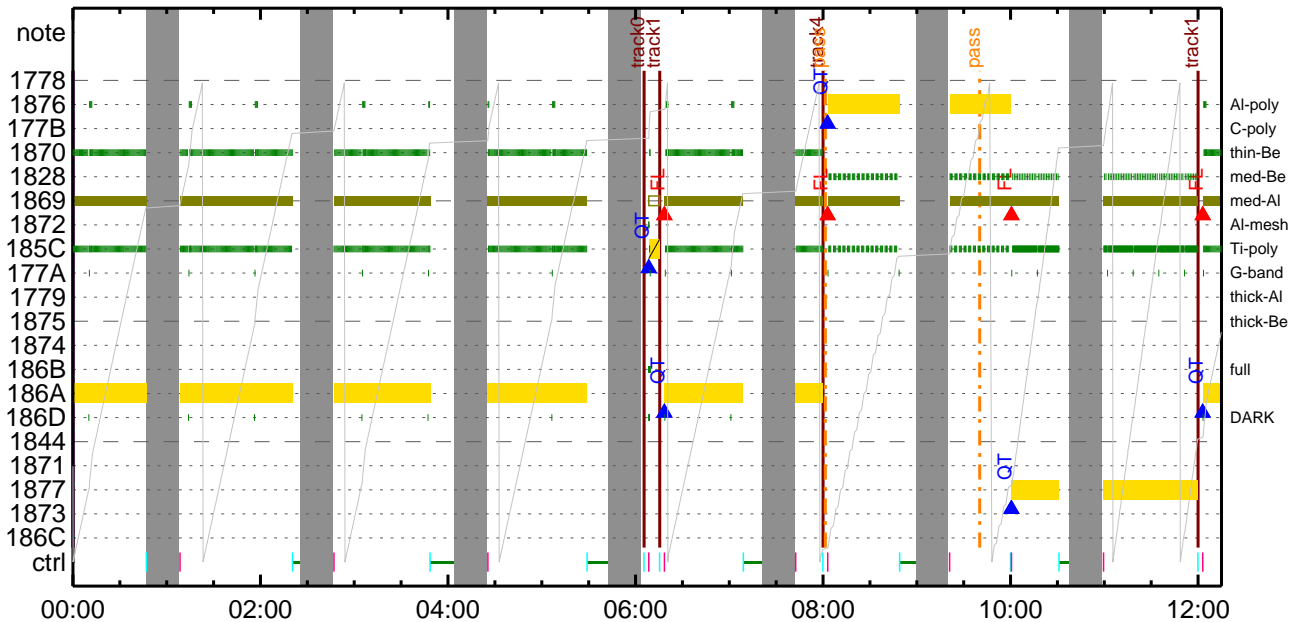
CMDI #0895 2011/04/28



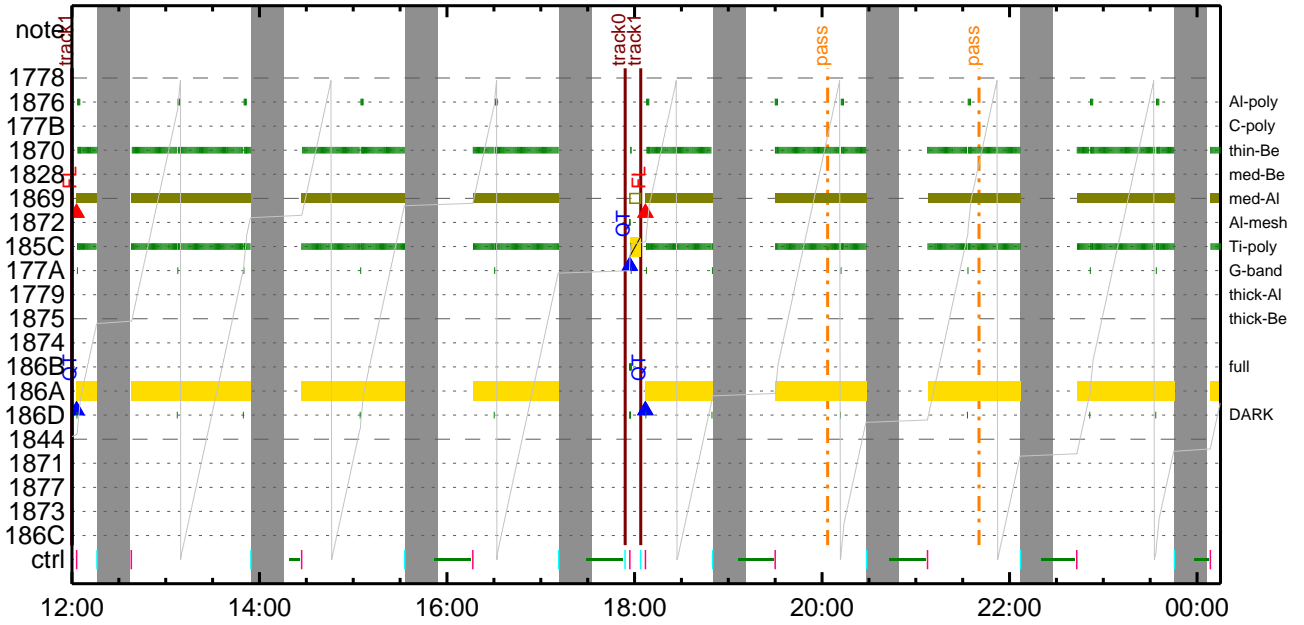
CMDI #0895 2011/04/28



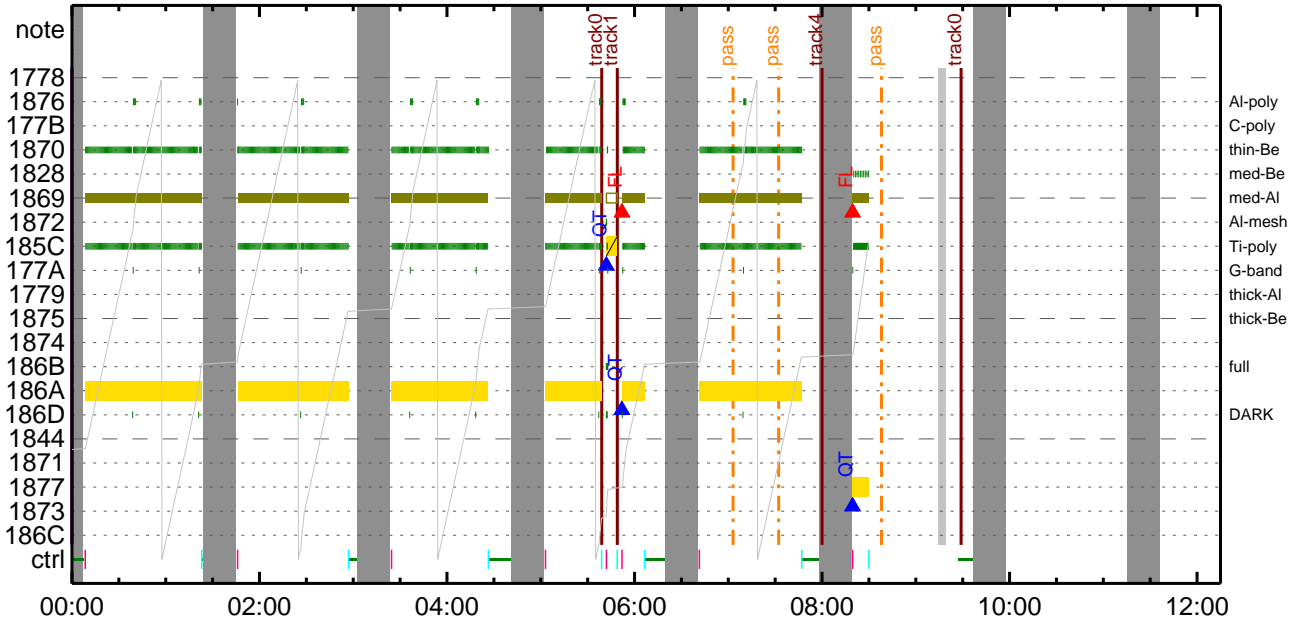
CMDI #0895 2011/04/29



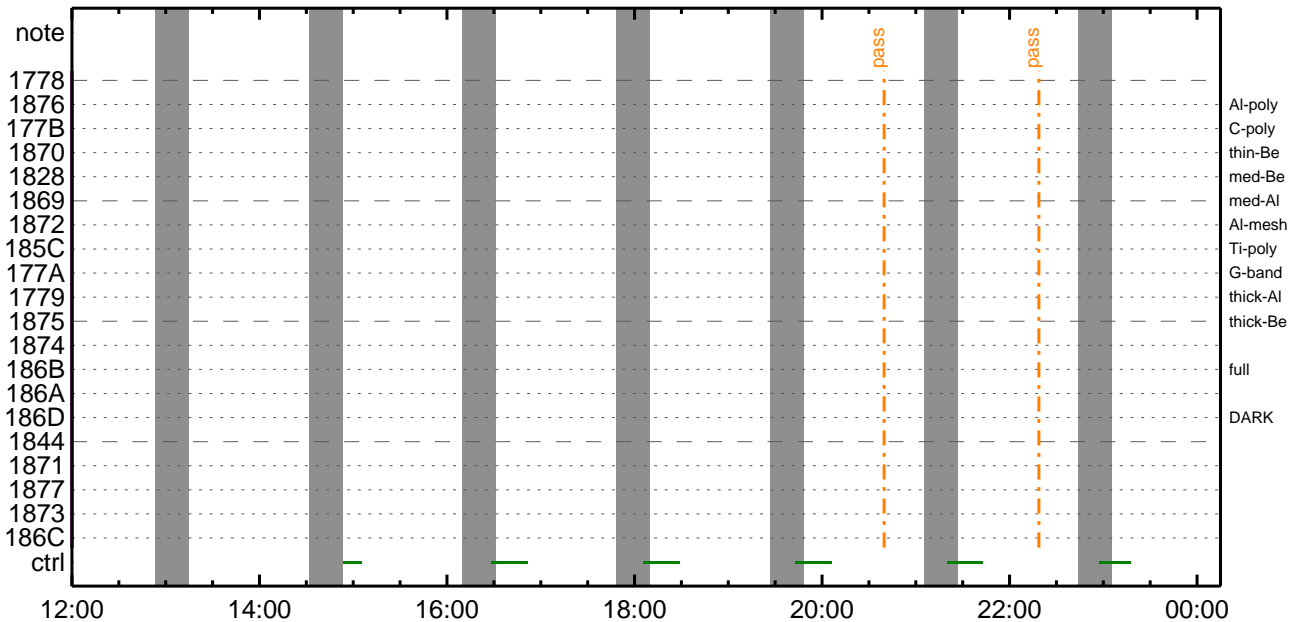
CMDI #0895 2011/04/29



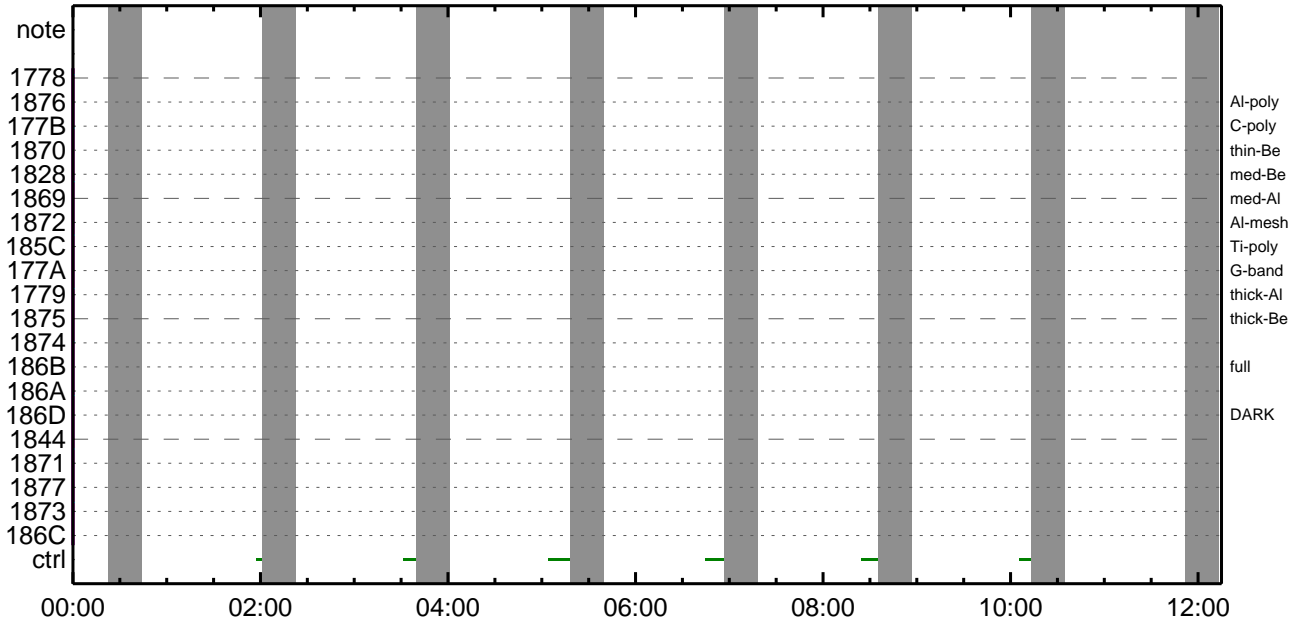
CMDI #0895 2011/04/30



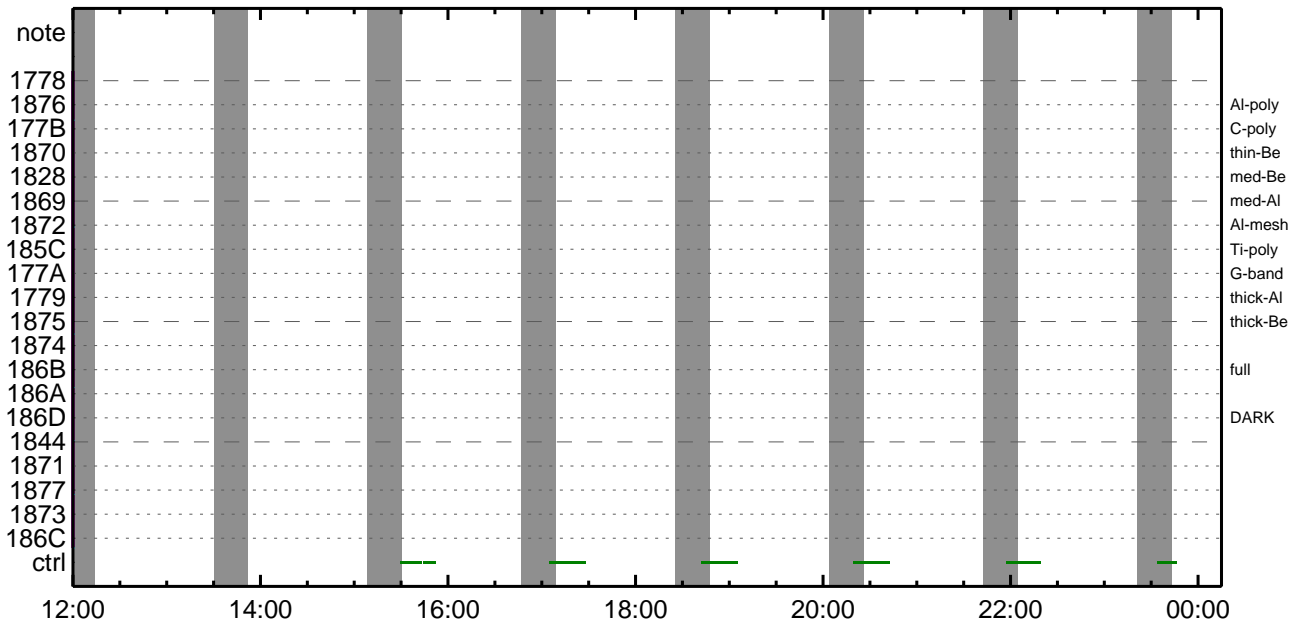
CMDI #0895 2011/04/30



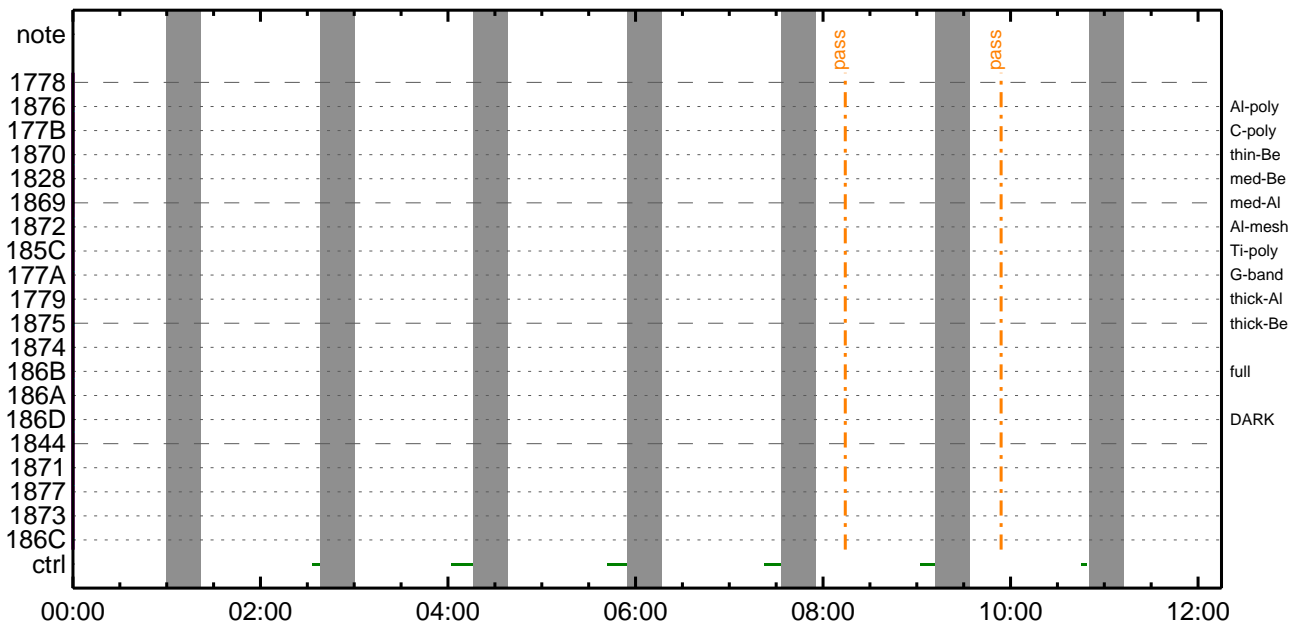
CMDI #0895 2011/05/01



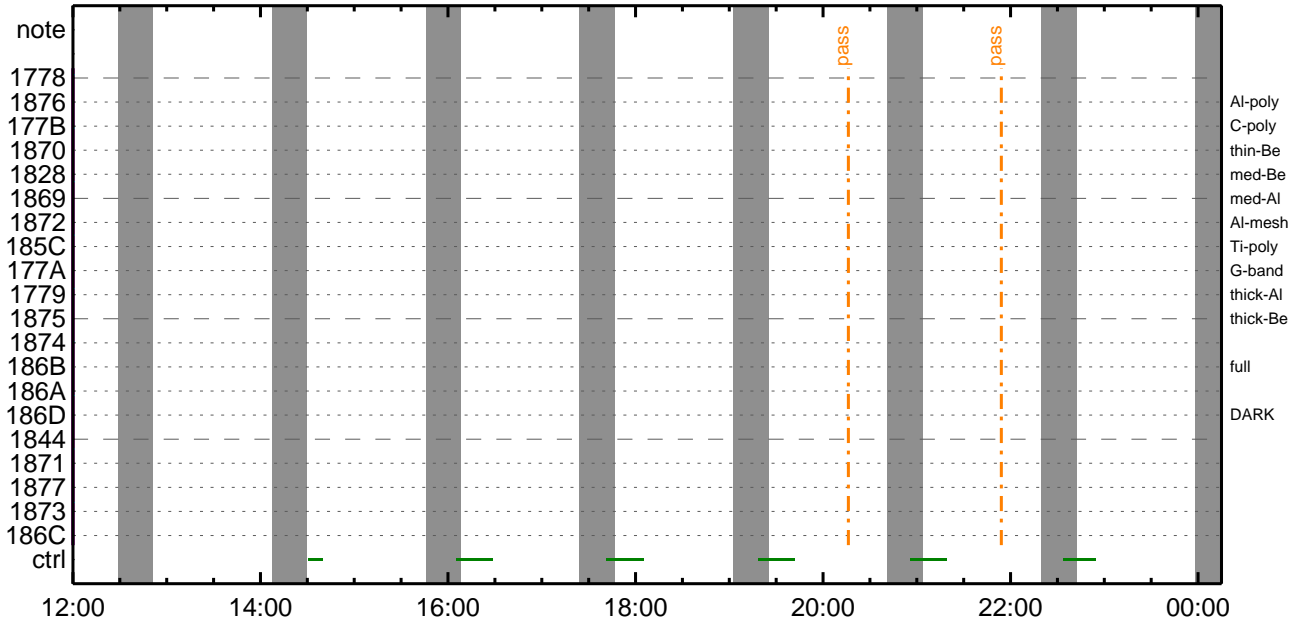
CMDI #0895 2011/05/01



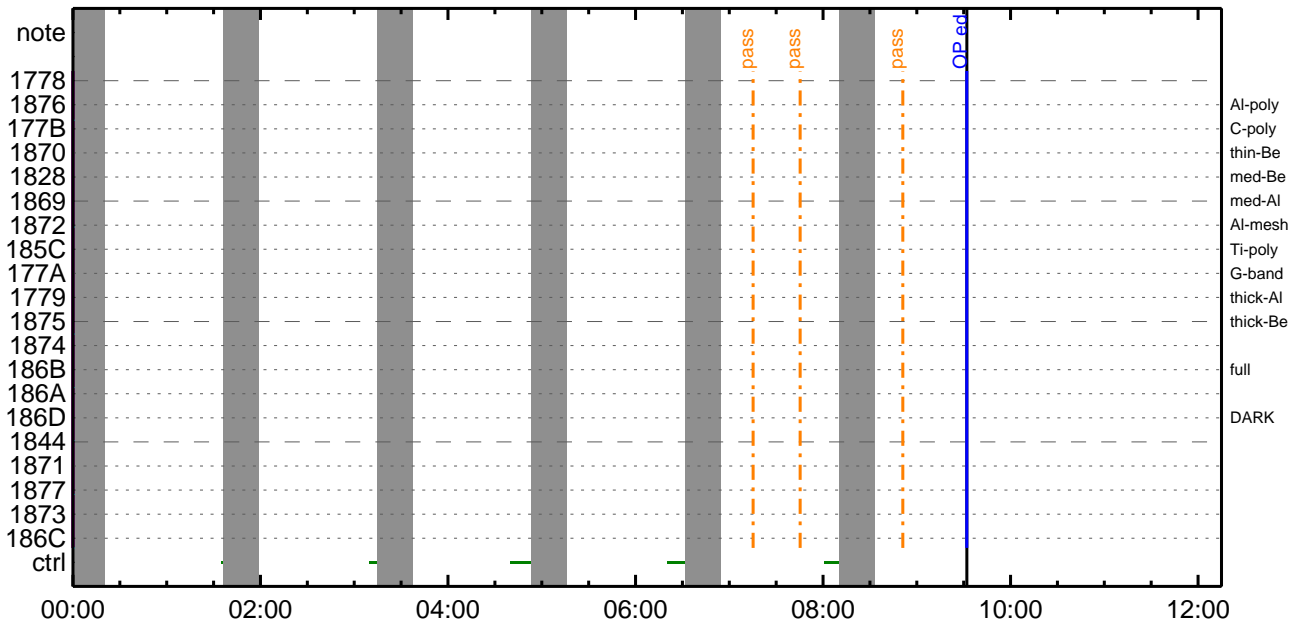
CMDI #0895 2011/05/02



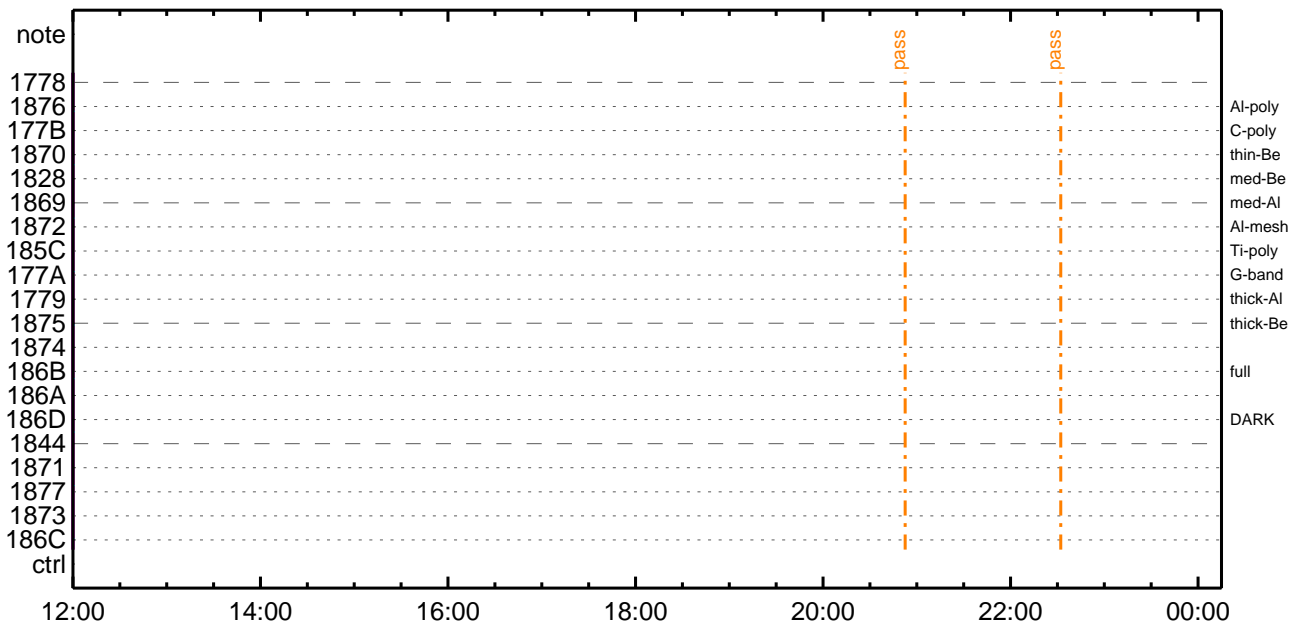
CMDI #0895 2011/05/02



CMDI #0895 2011/05/03



CMDI #0895 2011/05/03




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-034:OP
0104 ( )
0105 S. OG og-034:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYôYx½ªî»ò³îÇ§²¼òî½A´¶Á°òEÉ-ò°Á÷¿@) *****
0167 C. DHUYâ;½YE;E½Y½;Yi;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½E¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½A´¶Á°òEÉ-ò°Á÷¿@²¼òî½E¹ç;
0180 C. ²¼òî½E¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½A´¶Á°òEÉ-ò°Á÷¿@²¼òî½E¹ç;
0181 C.
0182 C. TIY³Y½YôYÉòðÁDî¿(UT)
0183 +. TI 2011-04-28 09:37:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-04-28 09:37:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-04-28 09:37:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-04-28 09:41:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷ°èŷÄŷÖŷ×½ªî»αò³îç§
0229 C.
0230 C. DHUŷä;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Êαòîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-04-28 09:41:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-04-28 09:41:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-04-28 09:41:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-04-28 09:41:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄîαî»ö¼ŷαÊÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷèè¼αα¼Ä»Ûα¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.î Daily±çîñαè´Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ä
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Resume FG table (auto mode) >
0120 +. DC 07-F0 MDP_FG_CTRL_AUTO
0121 BC (50)
0122 . C. -----
0123 C. MDP_FG_CTRL_MODE = AUTO [ ]
0124 C. -----
0125 C.
0126 C. *****
0127 C. SOT TI command set
0128 C. *****
0129 C. Execute, after the success of TBL upload.
0130 +. TI 2011-04-28 09:41:18.0
0131 DC 07-F0 MDP_SOT_MODE_OBSV
0132 BC (40)
0133 . C. -----
0134 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0135 C. -----
0136 C.
0137 C. Only when FG_CTRL_AUTO is used in RT.
0138 +. TI 2011-04-28 09:41:20.0
0139 DC 07-F0 MDP_FG_CTRL_AUTO
0140 BC (50)
0141 . C. -----
0142 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0143 C. -----
0144 C. ***** SOT END *****
0145 C.
0146 C. ***** XRT START *****
0147 C.
0148 +. DC 07-F0 MDP_XRT_CTRL_MANU
0149 BC (c1)
0150 + DC 07-F0 MDP_XRT_MODE_STBY
0151 BC (c3)
0152 . C. ----- Success Verify ? OK / NG____
0153 C.
0154 C. XRT Obs. Table Upload
0155 . S. RAM ram-291:MDP_OBS_X
0156 ( )
0157 C.
0158 +. DC 07-F0 MDP_DUMP_XRTTBL
0159 BC (84 07 00 00 00 3a d4)
0160 . C. ----- Comparison Check ? OK / ERR ____
0161 C.
0162 C.
0163 +. DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 01 b1 b1 04 04)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 02 b1 b1 08 08)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 03 b1 b1 08 08)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 04 b1 b1 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 05 85 83 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 06 85 83 08 08)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 07 85 83 06 06)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 08 80 80 20 20)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 09 80 80 20 08)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 0a 80 80 08 20)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 0f 80 80 06 06)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 10 80 80 08 08)
0187 + DC 07-F0 MDP_XRT_FLD_ENA
0188 BC (d8)
0189 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0190 BC (c8)
0191 + DC 07-F0 MDP_XRT_ARS_DIS
0192 BC (d5)
0193 +. DC 07-F0 MDP_XRT_QT_PROG_SET

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0194 BC (c4 03)
0195 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0196 BC (c5 0f)
0197 . C. ----- Success Verify ? OK / NG ____
0198 C.
0199 C.
0200 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0201 C.
0202 +. DC 07-F0 MDP_XRT_MODE_OBSV
0203 BC (c2)
0204 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0205 BC (c0)
0206 +. TI 2011-04-28 09:41:02.0
0207 DC 07-F0 MDP_XRT_MODE_OBSV
0208 BC (c2)
0209 +. TI 2011-04-28 09:41:04.0
0210 DC 07-F0 MDP_XRT_CTRL_AUTO
0211 BC (c0)
0212 . C. ----- Success Verify ? OK / NG ____
0213 C.
0214 C. ***** XRT END *****
0215 C.
0216 . C. ***** MDP `uAÎaî»ö%ÿoEÂÐa¹aèDCBC•x²è *****
0217 C. (%â°iÿÓYÄYÈYpYÈYâYçYèaE%¼aa¼A»Üa¹aè)
0218 . S. DC-BC dcbc-402:DCBC
0219 (MDP_known_event)
0220 C.
0221 C.
0222 . C. ***** YDÿ¹•İ Daily±;İÑaE'Øa¹aèDCBC•x²è *****
0223 . S. DC-BC dcbc-153:DCBC
0224 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0225 C.
0226 C.
0227 . C. ;ãLOS¥Á¥S¥Ã¥-¼A»Ü;ã
0228 C.
0229 . C. ***** LOS *****
0230 C.
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*** OP Sequence for XRT ***

2011/04/28	09:52:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2011/04/28	09:52:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	09:52:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	09:52:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/28	09:55:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/28	10:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	10:21:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/28	10:21:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/28	10:21:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/28	10:21:26.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/28	10:21:28.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/28	10:21:30.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	10:21:32.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2011/04/28	10:21:34.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/04/28	10:21:36.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/28	11:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	11:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	11:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/28	11:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/28	11:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	11:59:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/28	11:59:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/28	11:59:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/28	11:59:56.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/28	11:59:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/28	12:00:00.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	12:00:02.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2011/04/28	12:00:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/04/28	12:00:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/28	13:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	13:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	13:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/28	13:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/28	13:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/28	13:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/28	13:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/28	13:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/04/28	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/04/28	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/28	14:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/28	14:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/28	14:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/28	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/28	14:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2011/04/28	14:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0f			
2011/04/28	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/04/28	14:56:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	14:56:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	14:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/28	14:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/28	15:39:00.0	XRT_Custom_418_OG [0x1a2]									
2011/04/28	15:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	16:34:30.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	16:34:32.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	16:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/28	16:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/28	17:16:00.0	XRT_Custom_418_OG [0x1a2]									
2011/04/28	17:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]									
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00						
2011/04/28	18:00:00.0	AOCs_Ore-point_Start_3_OG [0x099]									
		AOCU_NM	5	02-76	00 00 00 00 00						
2011/04/28	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]									
		MDP_XRT_FLD_DIS	1	07-F0	d9						
2011/04/28	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]									
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2011/04/28	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2011/04/28	18:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d						
2011/04/28	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	18:10:00.0	AOCs_Ore-point_Start_2_OG [0x098]									
		AOCU_NM	5	02-76	01 00 00 00 00						
2011/04/28	18:13:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	18:13:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	18:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/28	18:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/28	18:52:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	18:52:32.0	XRT_FOCUS_POSITION_409_OG [0x199]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2011/04/28	18:52:52.0	XRT_FLD_ENA_411_OG [0x19b]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2011/04/28	18:52:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2011/04/28	18:52:56.0	XRT_AEC_RESET_443_OG [0x1bb]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2011/04/28	18:52:58.0	XRT_ARS_DIS_431_OG [0x1af]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2011/04/28	18:53:00.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	18:53:02.0	XRT_QT_PROG_SET_425_OG [0x1a9]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07						
2011/04/28	18:53:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f						
2011/04/28	18:53:06.0	XRT_CTRL_AUTO_406_OG [0x196]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	19:51:30.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	19:51:32.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	19:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/28	19:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/28	20:30:00.0	XRT_Custom_418_OG [0x1a2]									
2011/04/28	20:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	21:30:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	21:30:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2011/04/28	21:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2011/04/28	21:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2011/04/28	22:06:30.0	XRT_Custom_418_OG [0x1a2]									
2011/04/28	22:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2011/04/28	23:08:30.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2011/04/28	23:08:32.0	XRT_FLD_RESET_412_OG [0x19c]									

2011/04/28	23:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/28	23:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/28	23:36:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/28	23:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	00:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	00:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	00:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	00:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	01:07:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	01:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	02:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	02:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	02:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	02:46:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	02:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	03:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	03:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	03:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	03:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	04:24:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	04:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	05:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	05:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	05:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	05:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	06:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/04/29	06:05:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/04/29	06:05:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/04/29	06:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/04/29	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/29	06:08:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2011/04/29	06:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	06:15:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/04/29	06:15:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2011/04/29	06:15:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/29	06:15:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/29	06:15:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/29	06:15:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/29	06:18:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	06:18:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2011/04/29	06:18:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f			
2011/04/29	06:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	07:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	07:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	07:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2011/04/29	07:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	07:41:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/29	07:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	07:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/04/29	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2011/04/29	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/29	08:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/29	08:00:20.5	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/29	08:00:22.5	XRT_ARS_DIS_437_OG [0x1b5]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/29	08:02:54.5	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	08:02:56.5	XRT_QT_PROG_SET_445_OG [0x1bd]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2011/04/29	08:02:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f			
2011/04/29	08:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	08:49:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	08:49:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	08:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	08:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	09:20:00.0	XRT_Custom_418_OG [0x1a2]						
2011/04/29	09:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	10:00:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	10:00:02.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/04/29	10:00:22.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/29	10:00:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/29	10:00:26.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/29	10:00:28.0	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/29	10:00:30.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	10:00:32.0	XRT_QT_PROG_SET_434_OG [0x1b2]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2011/04/29	10:00:34.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f			
2011/04/29	10:00:36.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	10:31:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	10:31:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	10:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/04/29	10:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/04/29	10:58:30.0	XRT_Custom_418_OG [0x1a2]						
2011/04/29	10:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/04/29	11:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/04/29	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2011/04/29	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/04/29	12:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/04/29	12:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/04/29	12:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/04/29	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/04/29	12:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2011/04/29	12:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f			
2011/04/29	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/04/29	12:16:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2011/04/29	12:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	12:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	12:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	12:37:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/29	12:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	13:54:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	13:54:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	13:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	13:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	14:26:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/29	14:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	15:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	15:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	15:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	15:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	16:15:30.0	XRT_Custom_418_OG [0x1a2]							
2011/04/29	16:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	17:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	17:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	17:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	17:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/29	17:54:00.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/29	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/29	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/29	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/29	17:56:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/29	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	18:03:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	18:03:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/29	18:04:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/04/29	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/29	18:04:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/29	18:04:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/29	18:04:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/29	18:06:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	18:06:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/04/29	18:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2011/04/29	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	18:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	18:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	18:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	18:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	19:29:00.0	XRT_Custom_418_OG [0x1a2]							
2011/04/29	19:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	20:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	20:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	20:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2011/04/29	20:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	21:06:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/29	21:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	22:07:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	22:07:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	22:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	22:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/29	22:42:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/29	22:43:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/29	23:45:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/29	23:45:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/29	23:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/29	23:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/30	00:07:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/30	00:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/30	01:23:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/30	01:23:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/30	01:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/30	01:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/30	01:45:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/30	01:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/30	02:57:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/30	02:57:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/30	02:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/30	03:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/30	03:23:30.0	XRT_Custom_418_OG [0x1a2]								
2011/04/30	03:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/30	04:26:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/30	04:26:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/04/30	04:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/04/30	04:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/04/30	05:02:00.0	XRT_Custom_418_OG [0x1a2]								
2011/04/30	05:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/30	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/30	05:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/04/30	05:39:00.0	AOCs_Orе-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2011/04/30	05:39:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/04/30	05:39:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/04/30	05:39:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/30	05:41:58.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/04/30	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/04/30	05:48:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/04/30	05:48:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/04/30	05:49:00.0	AOCs_Orе-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	01 00 00 00 00				
2011/04/30	05:49:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/04/30	05:49:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/04/30	05:49:20.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/04/30	05:49:22.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/04/30	05:51:54.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				

