XRT Timeline to be uploaded on 2011/05/03

Period: 2011/05/03 09:32:00 - 2011/05/07 10:55:00

* * * * * **Normal mode** * * * * *

XOB #1879: Special HOP 186 Al/mesh (128/4096ms) + Al/Poly (181/5796ms) + Synoptic Q95 2x2 - Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1

Term	Pointing (x, y)	Comment
05/03 10:23:00 - 05/03 10:37:24	Fixed (0.0, 0.0)	# OP start + 10min - HOP 186 + synoptic
05/04 10:03:00 - 05/04 10:14:54	Fixed (0.0, 0.0)	HOP 186 + synoptic circa 10UT

PROG= 16 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 32 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 42 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/G-band	close	Safe	Dark	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 34 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1865: AR Standard-A(Filter-Ratio) with PFB (512x512), thin-Be and Al/Poly context, 384x384 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
05/04 07:33:05 - 05/04 09:13:54	Track (-276.4, 328.0) @ 05/04 03:10:00	AR 11203 tracking
05/04 10:57:00 - 05/04 23:59:54	Track (-216.6, 328.6) @ 05/04 10:15:00	AR 11203 tracking cont.
05/05 05:03:00 - 05/05 10:21:54	Track (-54.2, 329.0) @ 05/05 05:00:00	AR 11203 tracking cont.

PROG= 04 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 10 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 16 20-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
05/04 09:17:00 - 05/04 09:23:54	Fixed (-528.4, -528.4)	XRT Quadrant Pointing 1

PROG= 20 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 38 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long

Term	Pointing (x, y)	Comment
05/04 09:27:00 - 05/04 09:33:54	Fixed (528.4, -528.4)	XRT Quadrant 2

PROG= 11 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 36 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
05/04 09:37:00 - 05/04 09:43:54	Fixed (528.4, 528.4)	XRt Quadrant 3

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 39 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
05/04 09:47:00 - 05/04 09:53:54	Fixed (-528.4, 528.4)	XRT Quadrant 4

PROG= 18 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 40 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #185C: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be*36

Term	Pointing (x, y)	Comment
05/05 00:07:00 - 05/05 00:16:54	Fixed (0.0, 0.0)	synoptic and SOT engineering and science monitor

PROG= 13 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 32 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1874: Full Sun 6 Filter- 1x1 Q98 -FW1/FW2-short exposures-2

Term	Pointing (x, y)	Comment
05/05 00:20:00 - 05/05 00:59:54	Fixed (0.0, 0.0)	synoptic and SOT engineering and science monitor

PROG= 09 1-time(s)

Subr= 1 1-time(s) 180.0sec

Seqn= 26 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 12 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 35 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 43 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 44 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/Open	close	Safe	Norm	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 360.0sec

Seqn= 83 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 4 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/05 01:15:00 - 05/05 02:59:54	Fixed (0.0, 945.0)	Coalignment N-pole

PROG= 14 1-time(s)

Subr= 1 1-time(s) 360.0sec

Seqn= 21 24-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/05 03:15:00 - 05/05 04:59:54	Fixed (-945.0, 0.0)	Coalignment E-limb

PROG= 06 1-time(s)

Subr= 1 1-time(s) 360.0sec

Seqn= 22 15-time(s) 480.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x512 (1024, 1024)	Q=90	0	0	2.0sec

* * * * * **Flare mode** * * * * *

XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
05/04 07:33:05 - 05/04 09:13:54	Track (-276.4, 328.0) @ 05/04 03:10:00	AR 11203 tracking
05/04 10:57:00 - 05/04 23:59:54	Track (-216.6, 328.6) @ 05/04 10:15:00	AR 11203 tracking cont.
05/05 05:03:00 - 05/05 10:21:54	Track (-54.2, 329.0) @ 05/05 05:00:00	AR 11203 tracking cont.

PROG= 15 1-time(s)

Subr= 1 4-time(s) 2.0sec

Seqn= 55 45-time(s) 20.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 90 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 600.0sec

Seqn= 89 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * **Active Region Search** * * * * *

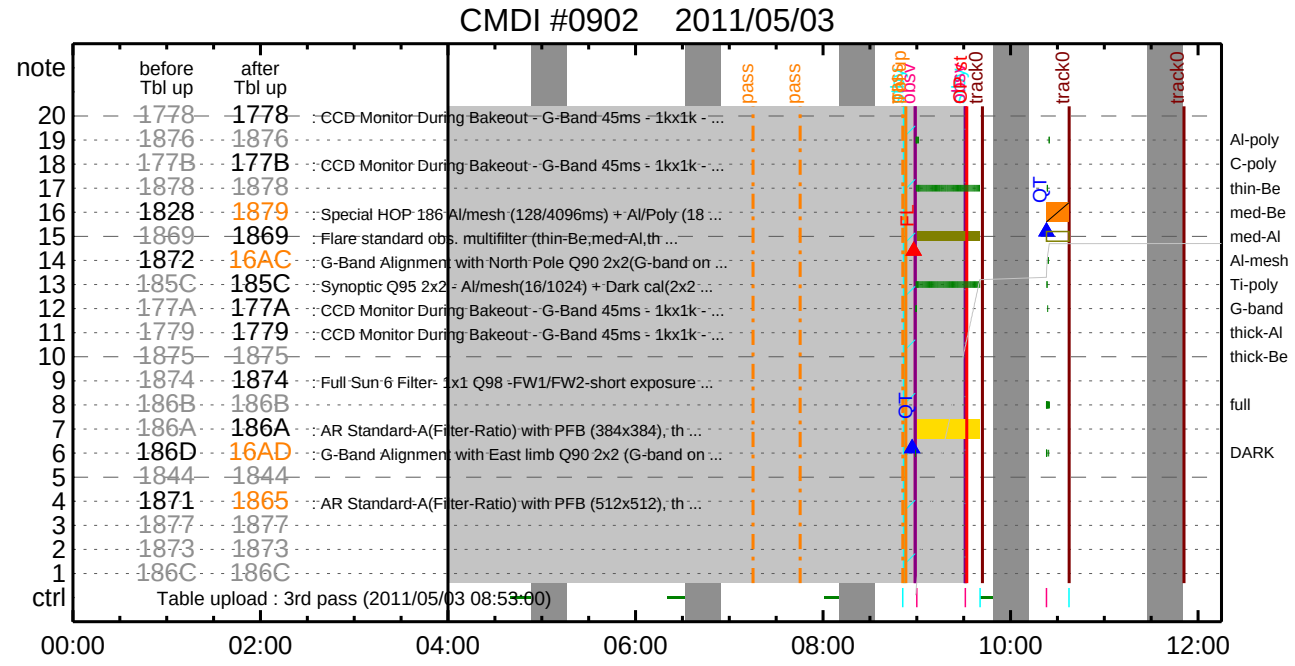
NOT USED

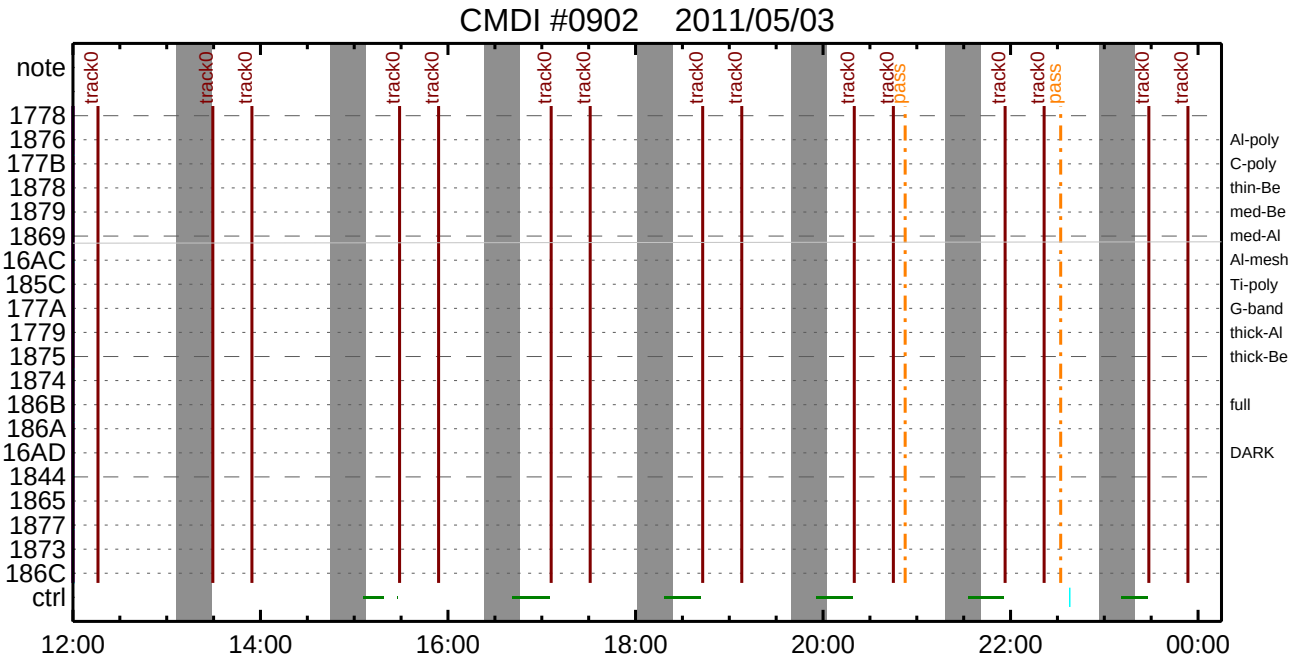
* * * * * **Flare Detection** * * * * *

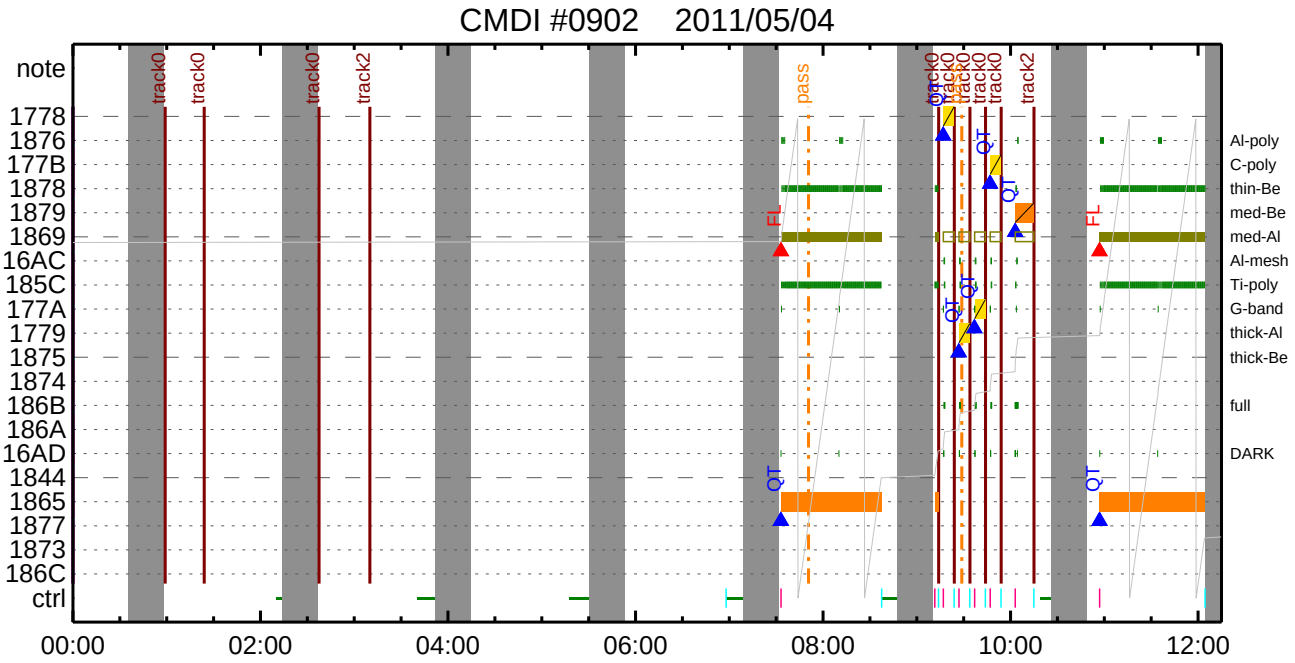
FLD Patrol

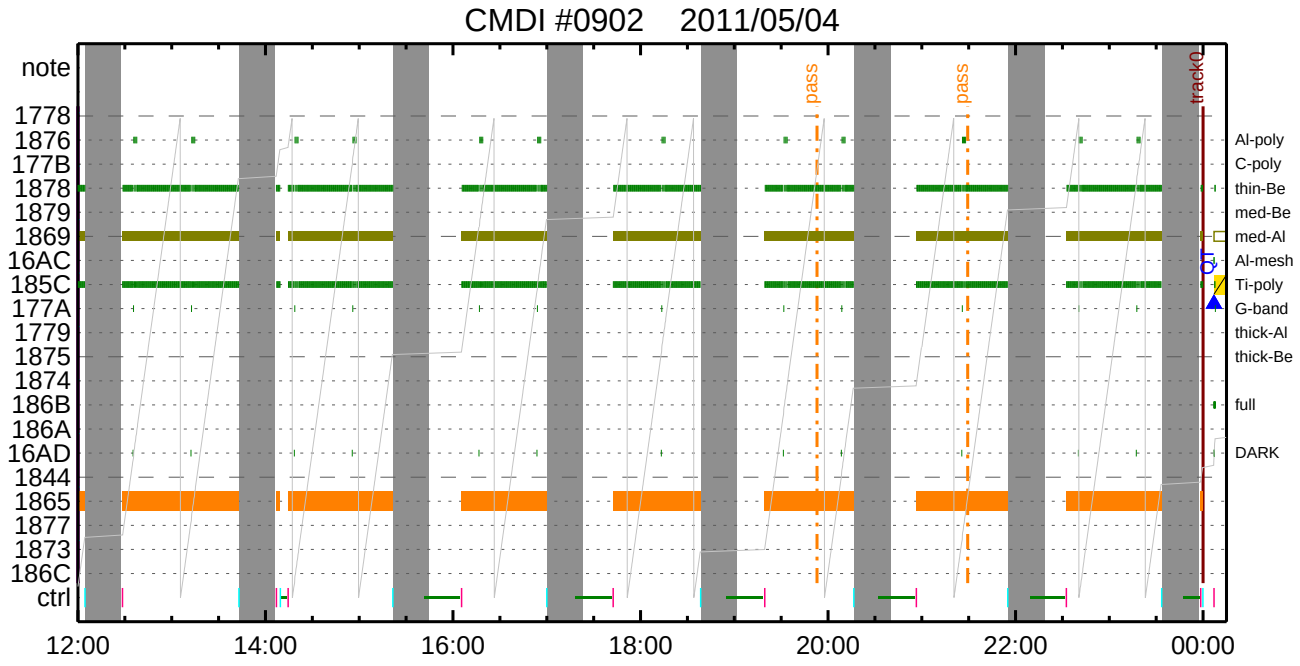
Term	Pointing (x, y)	Comment
05/04 07:32:51 - 05/04 09:16:54	Track (-276.4, 328.0) @ 05/04 03:10:00	AR 11203 tracking
05/04 10:56:46 - 05/05 00:00:16	Track (-216.6, 328.6) @ 05/04 10:15:00	AR 11203 tracking cont.
05/05 05:02:46 - 05/07 10:55:00	Track (-54.2, 329.0) @ 05/05 05:00:00	AR 11203 tracking cont.

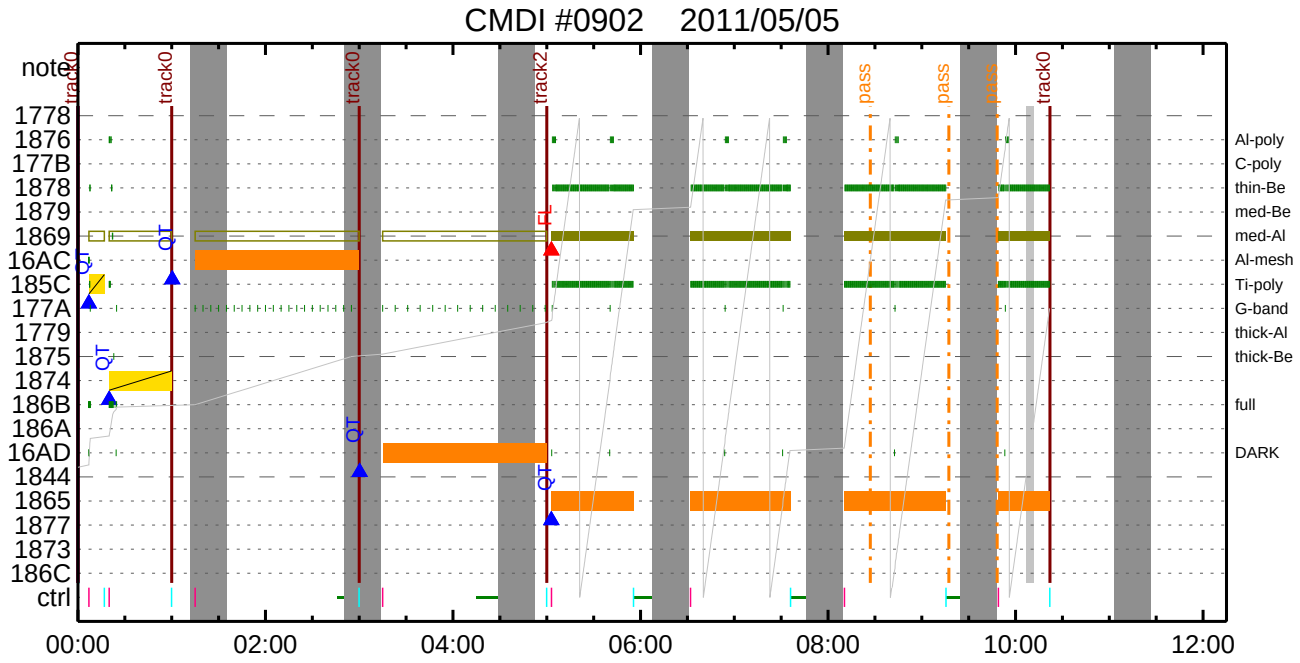
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

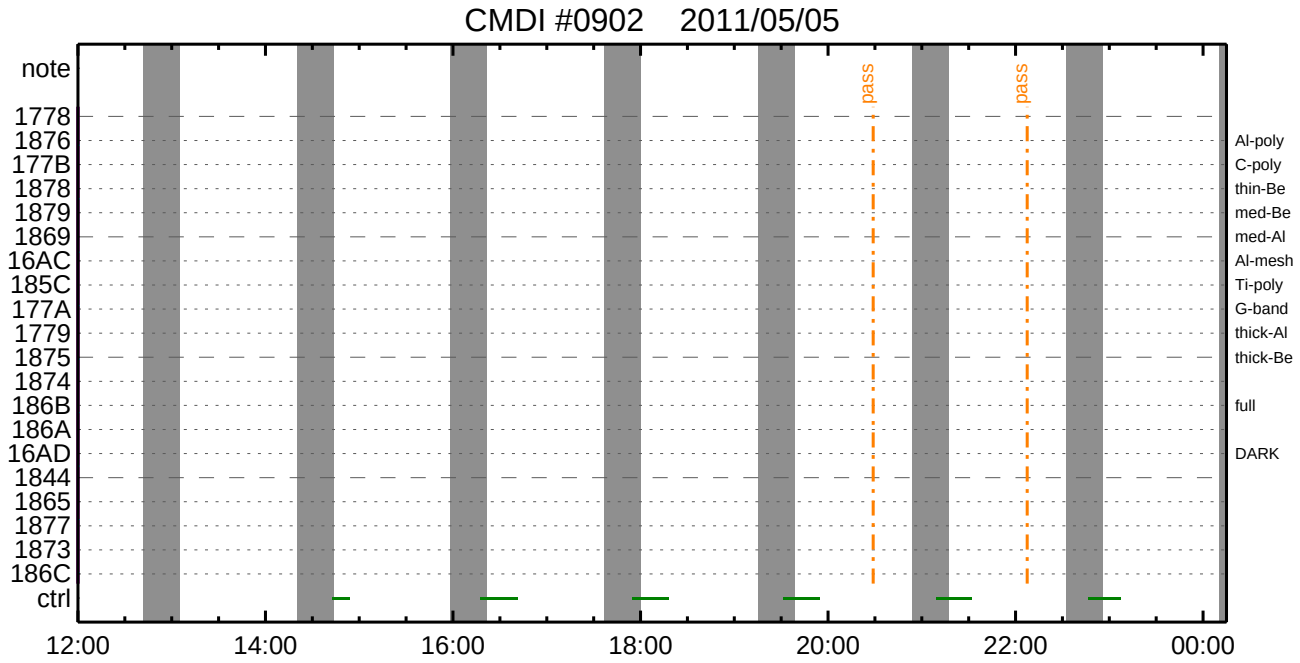

CMDI #0902 2011/05/03
note
before Tbl up
after Tbl up
20 1778 1778 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
19 1776 1776
18 177B 177B : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17 1878 1878
16 1828 1879 : Special HOP 186 Al/mesh (128/4096ms) + Al/Poly (18 ...
15 1869 1869 : Flare standard obs. multifilter (thin-Be,med-Al,th ...
14 1872 16AC : G-Band Alignment with North Pole Q90 2x2(G-band on ...
13 185C 185C : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
12 177A 177A : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
11 1779 1779 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
10 1875 1875
9 1874 1874 : Full Sun 6 Filter- 1x1 Q98 -FW1/FW2-short exposure ...
8 186B 186B
7 186A 186A : AR Standard-A(Filter-Ratio) with PFB (384x384), th ...
6 186D 16AD : G-Band Alignment with East limb Q90 2x2 (G-band on ...
5 1844 1844
4 1871 1865 : AR Standard-A(Filter-Ratio) with PFB (512x512), th ...
3 1877 1877
2 1873 1873
1 186C 186C
ctrl Table upload : 3rd pass (2011/05/03 08:53:00)
pass
pass
track0
track0
track0
QT
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

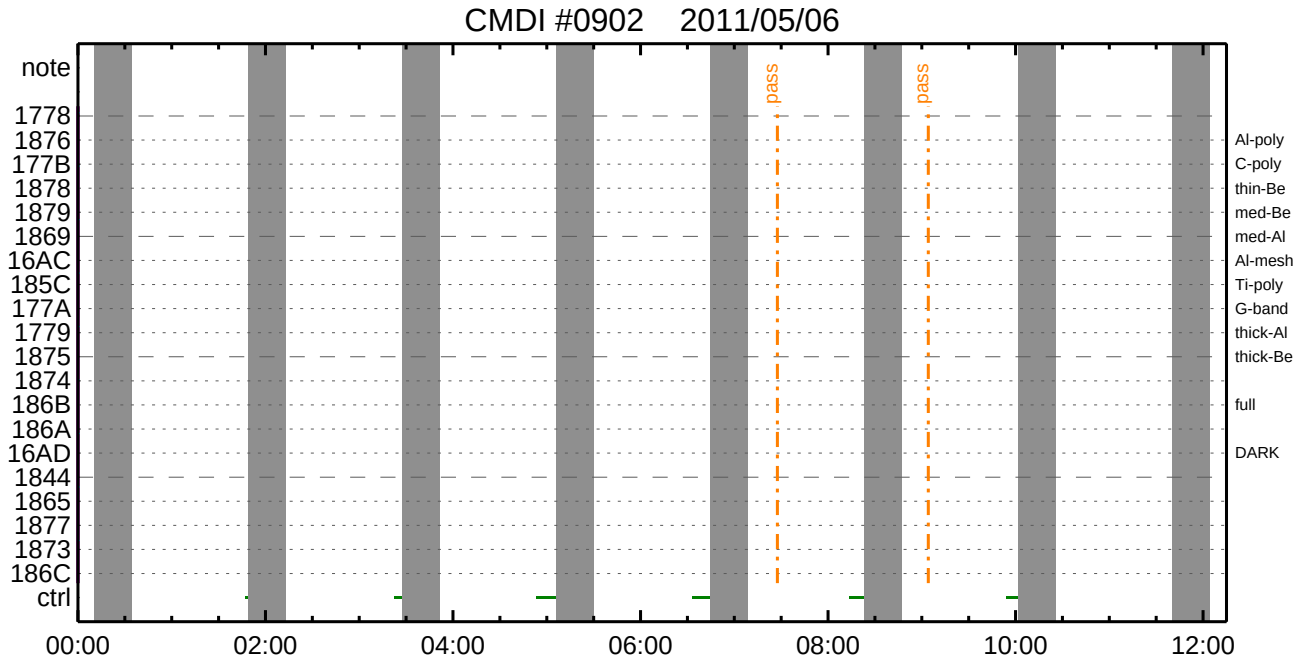

CMDI #0902 2011/05/03
note
track0
pass
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

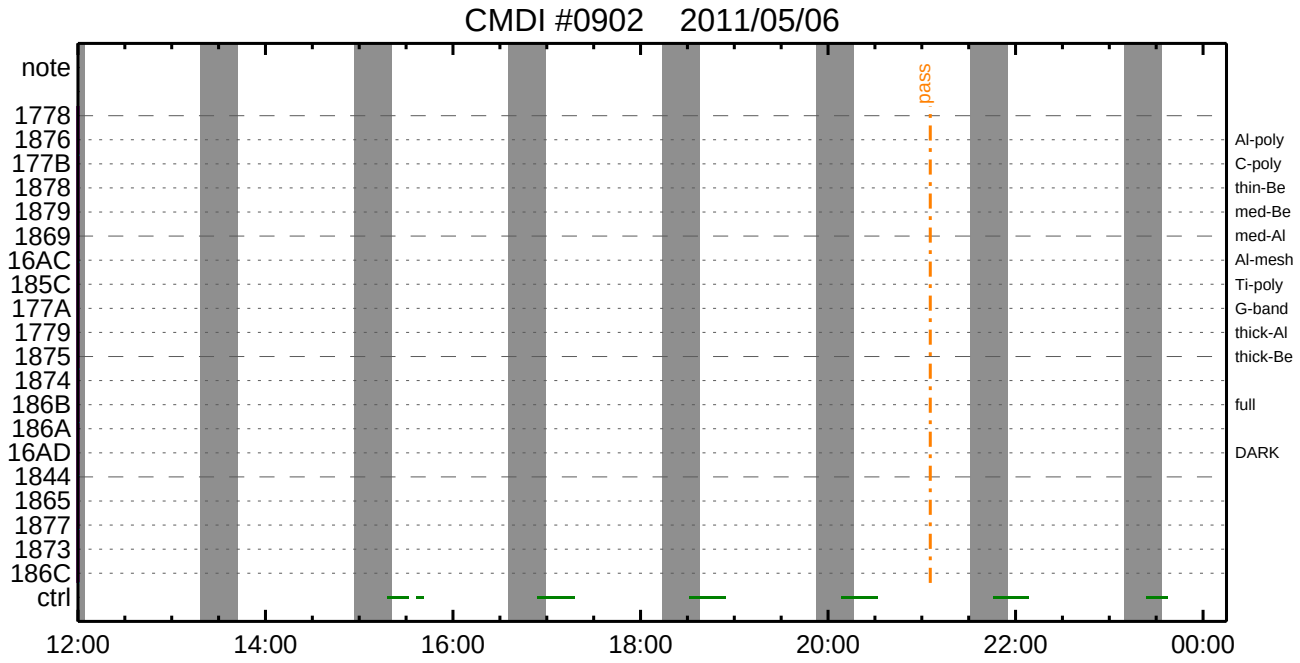

CMDI #0902 2011/05/04
note
track0
track2
pass
QT
FL
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

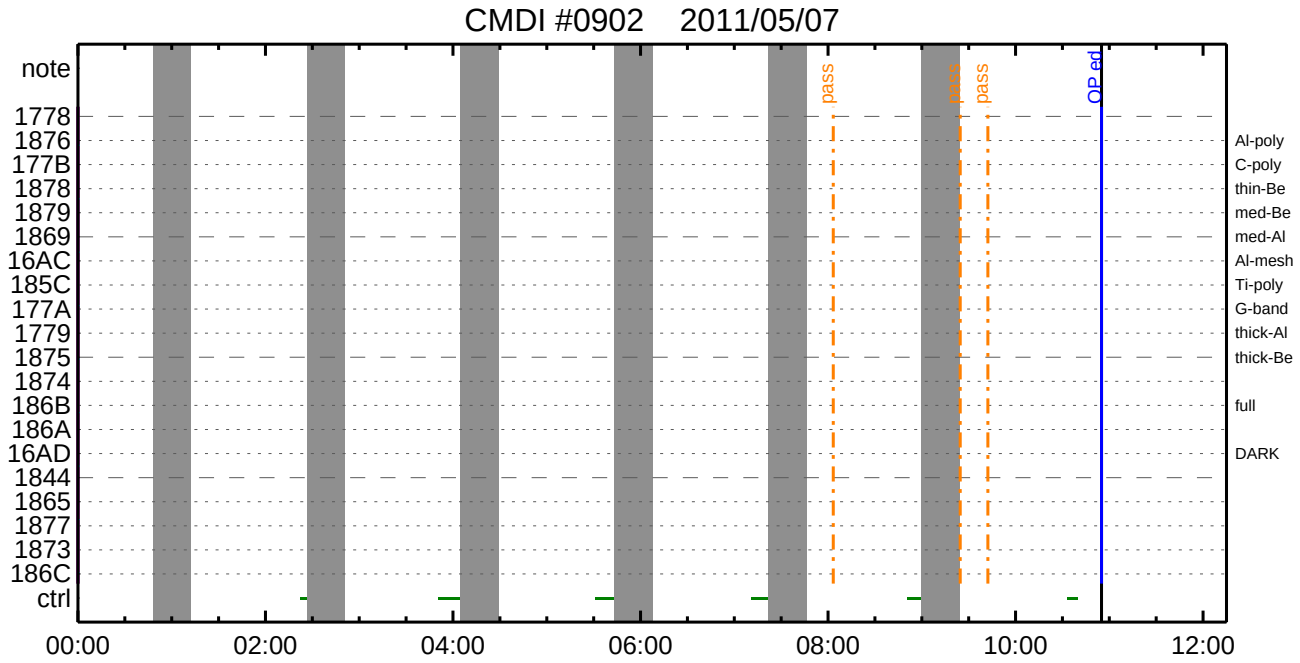

CMDI #0902 2011/05/04
note
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
pass
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

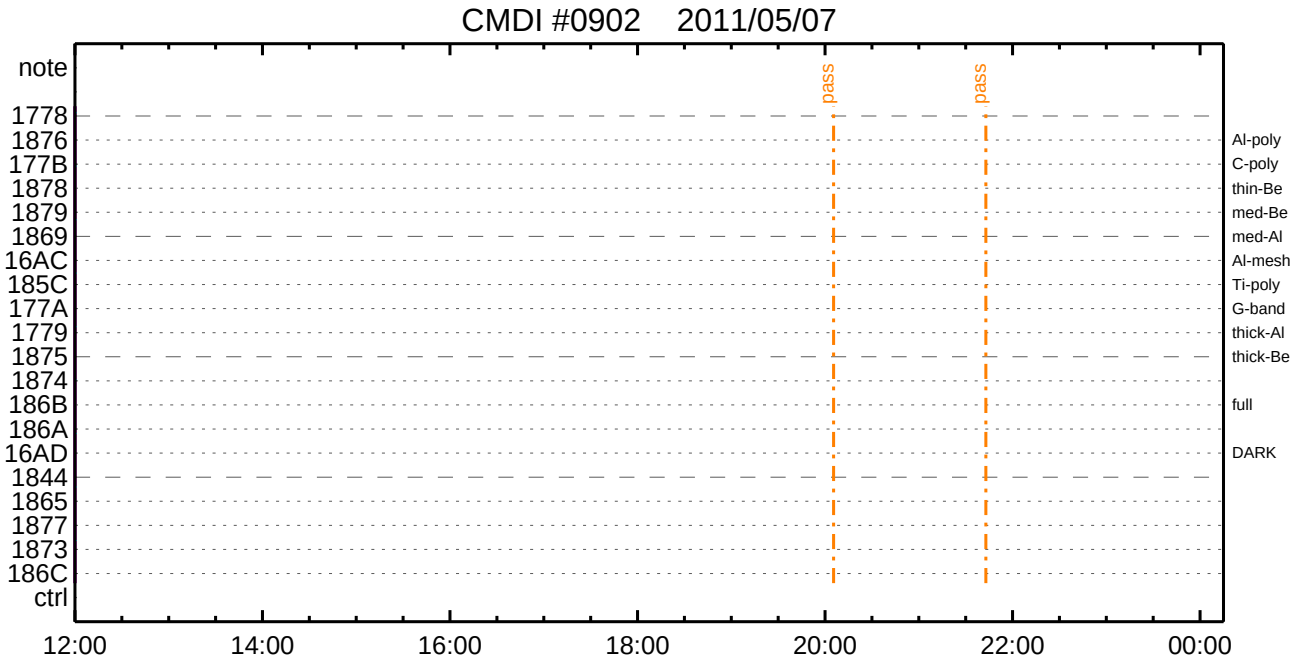

CMDI #0902 2011/05/05
note
track0
track0
track0
track2
pass
pass
pass
track0
QT
QT
QT
QT
FL
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0902 2011/05/05
note
pass
pass
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0902 2011/05/06
note
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0902 2011/05/06
note
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0902 2011/05/07
note
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0902 2011/05/07
note
1778
1876
177B
1878
1879
1869
16AC
185C
177A
1779
1875
1874
186B
186A
16AD
1844
1865
1877
1873
186C
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-043 2011-05-03 12:06:56 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-043:OP
0104     ()
0105   . S. OG    og-043:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2011-05-03 09:27:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2011-05-03 09:27:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2011-05-03 09:27:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2011-05-03 09:31:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ***************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ***************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2011-05-03 09:31:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2011-05-03 09:31:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2011-05-03 09:31:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2011-05-03 09:31:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-044 2011-05-03 12:06:56 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 2478959.2 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 1m19s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2011-05-03 09:31:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-045 2011-05-03 12:06:56 247 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +  DC 07-F0 MDP_XRT_MODE_STBY
0104     BC       (c3)
0105   . C. ----------- Success Verify ?             OK / NG____
0106     C.
0107     C. XRT Obs. Table Upload
0108   . S. RAM   ram-291:MDP_OBS_X
0109     ()
0110     C.
0111  +. DC 07-F0 MDP_DUMP_XRTTBL
0112     BC       (84 07 00 00 00 3a d4)
0113   . C. ----------- Comparison Check ?                      OK / ERR ____
0114     C.
0115     C.
0116  +. DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 01 b1 b1 04 04)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 02 b1 b1 08 08)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 03 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 04 b1 b1 06 06)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 05 85 83 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 06 85 83 08 08)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 07 85 83 06 06)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 08 80 80 20 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 09 80 80 20 08)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 0a 80 80 08 20)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0b c0 c0 10 10)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0c 40 c0 10 10)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0d 40 40 10 10)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0e c0 40 10 10)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 0f 80 80 06 06)
0146  +  DC 07-F0 MDP_XRT_ROI_SET
0147     BC       (cd 10 80 80 08 08)
0148  +  DC 07-F0 MDP_XRT_FLD_ENA
0149     BC       (d8)
0150  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151     BC       (c8)
0152  +  DC 07-F0 MDP_XRT_ARS_DIS
0153     BC       (d5)
0154  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155     BC       (c4 07)
0156  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157     BC       (c5 0f)
0158   . C. ---------- Success Verify ?            OK / NG ____
0159     C.
0160     C.
0161   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0162     C.
0163  +. DC 07-F0 MDP_XRT_MODE_OBSV
0164     BC       (c2)
0165  +. DC 07-F0 MDP_XRT_CTRL_AUTO
0166     BC       (c0)
0167  +. TI 2011-05-03 09:31:02.0
0168     DC 07-F0 MDP_XRT_MODE_OBSV
0169     BC       (c2)
0170  +. TI 2011-05-03 09:31:04.0
0171     DC 07-F0 MDP_XRT_CTRL_AUTO
0172     BC       (c0)
0173   . C. ---------- Success Verify ?            OK / NG ____
0174     C.
0175     C. ************ XRT END ***********
0176   . C. ********************************
0177     C.  SOT table upload
0178     C. ********************************
0179   . C. < Stop FG table >
0180  +. DC 07-F0 MDP_FG_CTRL_MANU
0181     BC       (51)
0182   . C. -------------------------------------
0183     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0184     C. -------------------------------------
0185     C.
0186   . C. <Upload FG Observation Table>
0187   . S. RAM   ram-265:MDP_OBS_F
0188     ()
0189     C.
0190   . C. < Dump RAMID=MDP_OBS_F >
0191  +. DC 07-F0 MDP_DUMP_FGTBL
0192     BC       (82 07 00 00 00 38 b8)
0193     C. -------------------------------------
```

```
0194     C.  MDP_OBS_F verify = OK/NG        [ ]
0195     C. ------------------------------------
0196     C.
0197   . C. < Upload DPL table >
0198     C.
0199     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0200     C.
0201   . S. RAM   ram-271:MDP_DPL
0202     ()
0203     C.
0204   . C. < Dump RAMID=MDP_DPL >
0205  +. DC 07-F0 MDP_DUMP_FGTBL
0206     BC       (82 07 00 38 b8 00 40)
0207     C. ------------------------------------
0208     C.  MDP_DPL verify          = OK    [ ]
0209     C. ------------------------------------
0210     C.
0211     C. STS_CHK¤òON¤Ë¤¹¤ë
0212     C.
0213   . C. < Update MDP DSC PAR1 >
0214  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0215     BC       (4c)
0216     C.  MDP_CMD_CODE          = F04C0700[ ]
0217     C.  MDP_CMD_CNT        (count-up 1) [ ]
0218     C. ------------------------------------
0219     C.
0220   . C.
0221     C. ******************************
0222     C.  SOT TI command set
0223     C. ******************************
0224     C. Execute, after the success of TBL upload.
0225  +. TI 2011-05-03 09:31:18.0
0226     DC 07-F0 MDP_SOT_MODE_OBSV
0227     BC       (40)
0228   . C. ------------------------------------
0229     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0230     C. ------------------------------------
0231     C.
0232     C.
0233   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0234     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0235   . S. DC-BC dcbc-402:DCBC
0236     (MDP_known_event)
0237     C.
0238     C.
0239   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0240   . S. DC-BC dcbc-153:DCBC
0241     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0242     C.
0243     C.
0244   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0245     C.
0246   . C. ********** LOS **********
0247     C.
```

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*** OP Sequence for XRT ***

2011/05/03  09:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/03  09:40:32.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/03  09:40:52.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/03  09:40:54.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/03  09:40:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/03  09:42:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/05/03  10:22:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2011/05/03  10:23:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/03  10:37:24.0  XRT_CTRL_MANU_441_OG [0x1b9]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/03  10:37:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 54 00 01 68
2011/05/03  10:37:54.0  XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2011/05/03  11:51:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 4c 41 01 68
2011/05/03  12:16:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 43 64 01 68
2011/05/03  12:37:54.0  XRT_Custom_444_OG [0x1bc]
2011/05/03  13:29:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 3a 7e 01 68
2011/05/03  13:54:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 31 99 01 68
2011/05/03  14:37:54.0  XRT_Custom_444_OG [0x1bc]
2011/05/03  15:29:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 28 b4 01 68
2011/05/03  15:54:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 1f ce 01 68
2011/05/03  16:37:54.0  XRT_Custom_444_OG [0x1bc]
2011/05/03  17:06:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 16 f1 01 68
2011/05/03  17:31:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 0e 0c 01 68
2011/05/03  18:37:54.0  XRT_Custom_447_OG [0x1bf]
2011/05/03  18:38:04.0  XRT_Custom_444_OG [0x1bc]
2011/05/03  18:43:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 05 26 01 68
2011/05/03  19:08:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 fd 27 01 68
2011/05/03  20:20:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 f4 42 01 68
2011/05/03  20:38:04.0  XRT_Custom_448_OG [0x1c0]
2011/05/03  20:45:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 eb 65 01 68
2011/05/03  21:56:30.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 e2 7f 01 68
2011/05/03  22:21:30.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 d9 9a 01 68
2011/05/03  22:37:42.0  XRT_Custom_447_OG [0x1bf]
2011/05/03  22:37:52.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/03  22:37:54.0  XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2011/05/03  23:28:30.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 d0 b5 01 68
2011/05/03  23:53:30.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 c7 cf 01 68
2011/05/04  00:59:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 be f2 01 68
2011/05/04  01:24:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 b6 0d 01 68
2011/05/04  02:37:30.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 00 ad 27 01 68
2011/05/04  03:10:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/05/04  06:58:00.0  XRT_CTRL_MANU_403_OG [0x193]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  07:32:26.0  XRT_ROI_A_434_OG [0x1b2]
                                MDP_XRT_ROI_SET      6 07-F0 cd 05 85 83 08 08
                                MDP_XRT_ROI_SET      6 07-F0 cd 06 85 83 08 08
                                MDP_XRT_ROI_SET      6 07-F0 cd 07 85 83 06 06
                                MDP_XRT_ROI_SET      6 07-F0 cd 08 80 80 20 20
                                MDP_XRT_ROI_SET      6 07-F0 cd 09 80 80 20 08
                                MDP_XRT_ROI_SET      6 07-F0 cd 0a 80 80 08 20
                                MDP_XRT_ROI_SET      6 07-F0 cd 0b c0 c0 10 10
                                MDP_XRT_ROI_SET      6 07-F0 cd 0c 40 c0 10 10
2011/05/04  07:32:26.5  XRT_ROI_B_404_OG [0x194]
                                MDP_XRT_ROI_SET      6 07-F0 cd 0c 40 c0 10 10
                                MDP_XRT_ROI_SET      6 07-F0 cd 0d 40 40 10 10
                                MDP_XRT_ROI_SET      6 07-F0 cd 0e c0 40 10 10
                                MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2011/05/04  07:32:31.5  XRT_FOCUS_POSITION_409_OG [0x199]
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                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/05/04  07:32:51.5  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/05/04  07:32:53.5  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/05/04  07:32:55.5  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/05/04  07:32:57.5  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  07:32:59.5  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  07:33:01.5  XRT_QT_PROG_SET_405_OG [0x195]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/05/04  07:33:03.5  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0f
2011/05/04  07:33:05.5  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  08:37:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  08:37:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  08:37:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  08:40:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  09:10:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  09:11:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  09:13:54.0  XRT_CTRL_MANU_416_OG [0x1a0]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  09:14:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                    AOCU_NM              5 02-76 00 2e f9 2e f9
2011/05/04  09:16:32.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/04  09:16:52.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2011/05/04  09:16:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/04  09:16:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/04  09:16:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  09:17:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  09:23:54.0  XRT_CTRL_MANU_416_OG [0x1a0]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  09:24:00.5  AOCS_ORe-point_Start_24_OG [0x0ae]
                                    AOCU_NM              5 02-76 00 2e f9 d1 07
2011/05/04  09:26:32.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/04  09:26:52.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2011/05/04  09:26:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/04  09:26:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/04  09:26:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  09:27:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  09:33:54.0  XRT_CTRL_MANU_416_OG [0x1a0]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  09:34:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2011/05/04  09:36:32.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/04  09:36:52.0  XRT_QT_PROG_SET_407_OG [0x197]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2011/05/04  09:36:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/04  09:36:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/04  09:36:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  09:37:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  09:43:54.0  XRT_CTRL_MANU_416_OG [0x1a0]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  09:44:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2011/05/04  09:46:32.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/04  09:46:52.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2011/05/04  09:46:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/04  09:46:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/04  09:46:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  09:47:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  09:53:54.0  XRT_CTRL_MANU_400_OG [0x190]
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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  09:53:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/04  09:54:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2011/05/04  09:54:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/04  09:54:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/04  09:54:20.0  XRT_ARS_DIS_415_OG [0x19f]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  10:02:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2011/05/04  10:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  10:14:54.0  XRT_CTRL_MANU_421_OG [0x1a5]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  10:15:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2011/05/04  10:56:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/05/04  10:56:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/05/04  10:56:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/05/04  10:56:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/05/04  10:56:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/04  10:56:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  10:56:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/05/04  10:56:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0f
2011/05/04  10:57:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  12:04:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  12:04:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  12:04:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  12:07:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  12:27:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  12:28:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  13:43:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  13:43:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  13:43:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  13:46:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  14:06:00.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  14:07:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  14:09:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  14:09:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  14:09:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  14:12:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  14:13:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  14:14:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  15:21:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  15:21:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  15:21:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  15:24:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  16:04:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  16:05:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  17:00:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  17:00:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  17:00:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  17:03:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  17:41:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  17:42:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  18:38:30.0  XRT_CTRL_MANU_408_OG [0x198]
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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  18:38:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  18:38:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  18:41:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  19:18:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  19:19:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  20:16:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  20:16:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  20:16:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  20:19:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  20:55:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  20:56:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  21:55:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  21:55:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  21:55:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  21:58:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  22:31:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  22:32:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  23:33:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  23:33:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/04  23:33:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/04  23:36:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/04  23:57:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/04  23:58:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/04  23:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/04  23:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/05  00:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2011/05/05  00:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/05  00:00:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/05  00:00:20.0  XRT_ARS_DIS_427_OG [0x1ab]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/05  00:06:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2011/05/05  00:07:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  00:16:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  00:16:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/05/05  00:17:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/05  00:17:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/05  00:17:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/05  00:19:58.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2011/05/05  00:20:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  00:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  00:59:56.0  XRT_ROI_A_420_OG [0x1a4]
                                    MDP_XRT_ROI_SET      6 07-F0 cd 05 85 83 08 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 06 85 83 08 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 07 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 08 80 60 20 18
                                    MDP_XRT_ROI_SET      6 07-F0 cd 09 a0 80 18 20
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2011/05/05  01:00:00.0  AOCS_ORe-point_Start_27_OG [0x0b1]
                                    AOCU_NM              5 02-76 00 ac 00 00 00
2011/05/05  01:00:01.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/05/05  01:00:21.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2011/05/05  01:00:23.0  XRT_FLD_DIS_423_OG [0x1a7]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/05  01:00:25.0  XRT_FLRCTRL_DIS_425_OG [0x1a9]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
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2011/05/05  01:00:27.0  XRT_ARS_DIS_430_OG [0x1ae]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/05  01:15:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  02:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  02:59:56.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/05/05  03:00:00.0  AOCS_ORe-point_Start_28_OG [0x0b2]
                                AOCU_NM              5 02-76 00 00 00 54 00
2011/05/05  03:00:16.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2011/05/05  03:00:18.0  XRT_FLD_DIS_423_OG [0x1a7]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/05/05  03:00:20.0  XRT_FLRCTRL_DIS_425_OG [0x1a9]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/05/05  03:00:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/05  03:15:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  04:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  05:00:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/05/05  05:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/05/05  05:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/05/05  05:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/05/05  05:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/05/05  05:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/05/05  05:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/05  05:02:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/05/05  05:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0f
2011/05/05  05:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  05:55:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  05:55:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/05  05:55:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/05  05:58:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/05  06:31:00.0  XRT_Custom_418_OG [0x1a2]
2011/05/05  06:32:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  07:36:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  07:36:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/05  07:36:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/05  07:39:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/05  08:09:30.0  XRT_Custom_418_OG [0x1a2]
2011/05/05  08:10:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  09:15:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  09:15:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/05/05  09:15:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/05/05  09:18:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/05/05  09:48:00.0  XRT_Custom_418_OG [0x1a2]
2011/05/05  09:49:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/05/05  10:21:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/05/05  10:22:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
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