

# XRT Timeline to be uploaded on 2011/05/07

Period: 2011/05/07 10:55:00 - 2011/05/12 10:44:00

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Normal mode

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## XOB #187B: HOP 186 FW1=OPEN AI/mesh (4096ms) + Synoptic Q95 2x2 - AI/mesh(44/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1

Term	Pointing (x, y)	Comment
05/07 11:08:00 - 05/07 12:17:00	Fixed ( 0.0, 0.0)	# OP start + 10min - HOP186 + synoptics
05/07 12:42:30 - 05/07 13:31:54	Fixed ( 95.0, -13.0)	HOP 79 (6/10)
05/08 12:01:00 - 05/08 12:07:54	Fixed ( 0.0, 0.0)	HOP 186 + synoptic
05/09 11:33:00 - 05/09 11:39:54	Fixed ( 0.0, 0.0)	HOP 186 + synoptic
<b>PROG= 01 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 86 1-time(s) 4.0sec</b>		
Open/AI-mesh	Open/AI-mesh close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/AI-mesh	Open/AI-mesh close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Seqn= 5 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-AI close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 37 1-time(s) 4.0sec</b>		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Seqn= 4 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 57 1-time(s) 2.0sec</b>		
Open/AI-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/AI-mesh	Open/G-band close	Safe Dark 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/07 13:47:00 - 05/07 15:31:54	Fixed ( 0.0, 945.0)	Coalignment North Pole
<b>PROG= 14 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 360.0sec</b>		
└─ <b>Seqn= 21 24-time(s) 300.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/07 15:47:00 - 05/07 17:31:40	Fixed ( -945.0, 0.0)	Coalignment East Limb
<b>PROG= 06 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 360.0sec</b>		
└─ <b>Seqn= 22 15-time(s) 480.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1827: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
05/07 17:58:00 - 05/08 00:18:54	Track ( 348.3, 306.8) @ 05/07 17:32:00	AR 11204 tracking
05/08 00:32:00 - 05/08 11:57:54	Track ( 404.1, 304.7) @ 05/08 00:29:00	AR 11204 tracking cont.
<b>PROG= 05 Inf.-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-AI close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ <b>Seqn=100 4-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-AI	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 15 20-time(s) 2.0sec</b>		
Open/AI-mesh	Open/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 30.0sec
Open/AI-mesh	Open/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec
Open/AI-mesh	Open/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec
Open/AI-mesh	Open/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)**

Term	Pointing (x, y)	Comment
05/08 00:22:00 - 05/08 00:28:54	Fixed ( 0.0, 0.0)	synoptic, shifted by 19 min
05/09 00:03:00 - 05/09 00:09:54	Fixed ( 0.0, 0.0)	synoptic
05/10 00:03:00 - 05/10 00:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 19 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 7 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 8 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 4 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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**XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad**

Term	Pointing (x, y)	Comment
05/08 12:11:06 - 05/08 23:59:54	Track ( 493.4, 300.9) <sup>@ 05/08 12:08:00</sup>	AR 11204 tracking cont.
05/09 00:13:00 - 05/09 11:29:54	Track ( 579.1, 296.4) <sup>@ 05/09 00:10:00</sup>	AR 11204 tracking cont.
05/09 11:43:00 - 05/09 23:59:54	Track ( 653.8, 291.7) <sup>@ 05/09 11:40:00</sup>	AR 11204 tracking cont.
05/10 00:13:00 - 05/10 10:47:00	Track ( 725.8, 286.2) <sup>@ 05/10 00:10:00</sup>	AR 11204 tracking cont.

**PROG= 03 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 19 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn=100 4-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 6 18-time(s) 2.0sec
    - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 384x384 (1064, 1048) Q=95 2 1 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 1 20.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 384x384 (1064, 1048) Q=95 2 2 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 2 20.0sec
    - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 20.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
05/07 17:58:00 - 05/08 00:18:54	Track ( 348.3, 306.8) <sup>@ 05/07 17:32:00</sup>	AR 11204 tracking
05/08 00:32:00 - 05/08 11:57:54	Track ( 404.1, 304.7) <sup>@ 05/08 00:29:00</sup>	AR 11204 tracking cont.
05/08 12:11:06 - 05/08 23:59:54	Track ( 493.4, 300.9) <sup>@ 05/08 12:08:00</sup>	AR 11204 tracking cont.
05/09 00:13:00 - 05/09 11:29:54	Track ( 579.1, 296.4) <sup>@ 05/09 00:10:00</sup>	AR 11204 tracking cont.
05/09 11:43:00 - 05/09 23:59:54	Track ( 653.8, 291.7) <sup>@ 05/09 11:40:00</sup>	AR 11204 tracking cont.
05/10 00:13:00 - 05/10 10:47:00	Track ( 725.8, 286.2) <sup>@ 05/10 00:10:00</sup>	AR 11204 tracking cont.

**PROG= 08 1-time(s)**

- Subr= 1 30-time(s) 20.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 60 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
- Subr= 3 30-time(s) 60.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 88 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
<b>Seqn= 90</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>		<b>30-time(s)</b>		<b>60.0sec</b>								
<b>Seqn= 87</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
<b>Seqn= 90</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>		<b>30-time(s)</b>		<b>60.0sec</b>								
<b>Seqn= 87</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4</b>		<b>24-time(s)</b>		<b>600.0sec</b>								
<b>Seqn= 89</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

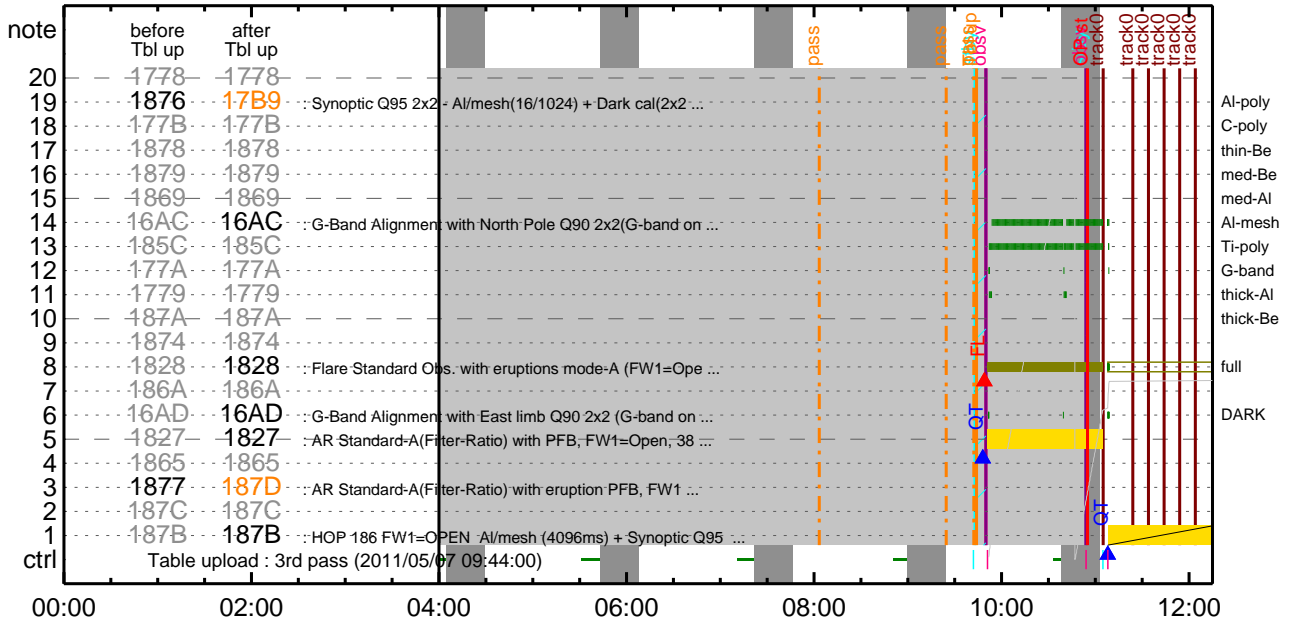
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### Flare Detection

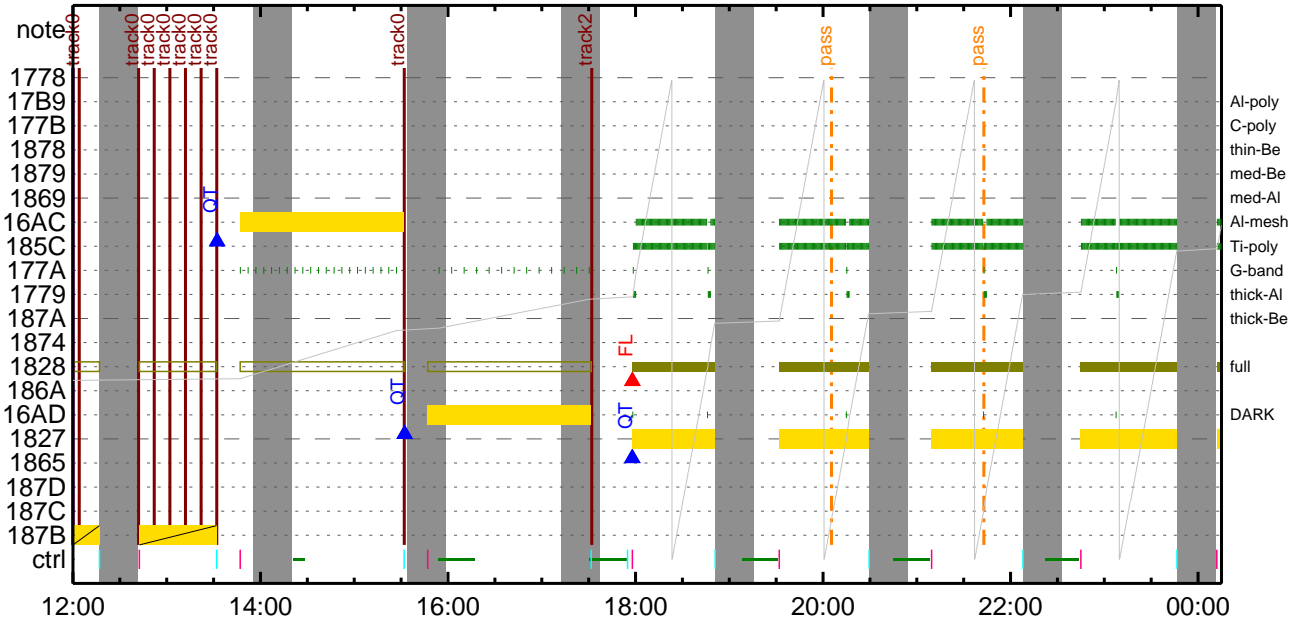
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<b>FLD Patrol</b>											
Term	Pointing (x, y)		Comment								
05/07 17:57:46 - 05/08 00:19:16	Track (	348.3, 306.8)	<sup>Ⓜ</sup> 05/07 17:32:00	AR 11204 tracking							
05/08 00:31:46 - 05/08 11:58:16	Track (	404.1, 304.7)	<sup>Ⓜ</sup> 05/08 00:29:00	AR 11204 tracking cont.							
05/08 12:10:51 - 05/09 00:00:16	Track (	493.4, 300.9)	<sup>Ⓜ</sup> 05/08 12:08:00	AR 11204 tracking cont.							
05/09 00:12:46 - 05/09 11:30:16	Track (	579.1, 296.4)	<sup>Ⓜ</sup> 05/09 00:10:00	AR 11204 tracking cont.							
05/09 11:42:46 - 05/10 00:00:16	Track (	653.8, 291.7)	<sup>Ⓜ</sup> 05/09 11:40:00	AR 11204 tracking cont.							
05/10 00:12:46 - 05/12 10:44:00	Track (	725.8, 286.2)	<sup>Ⓜ</sup> 05/10 00:10:00	AR 11204 tracking cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

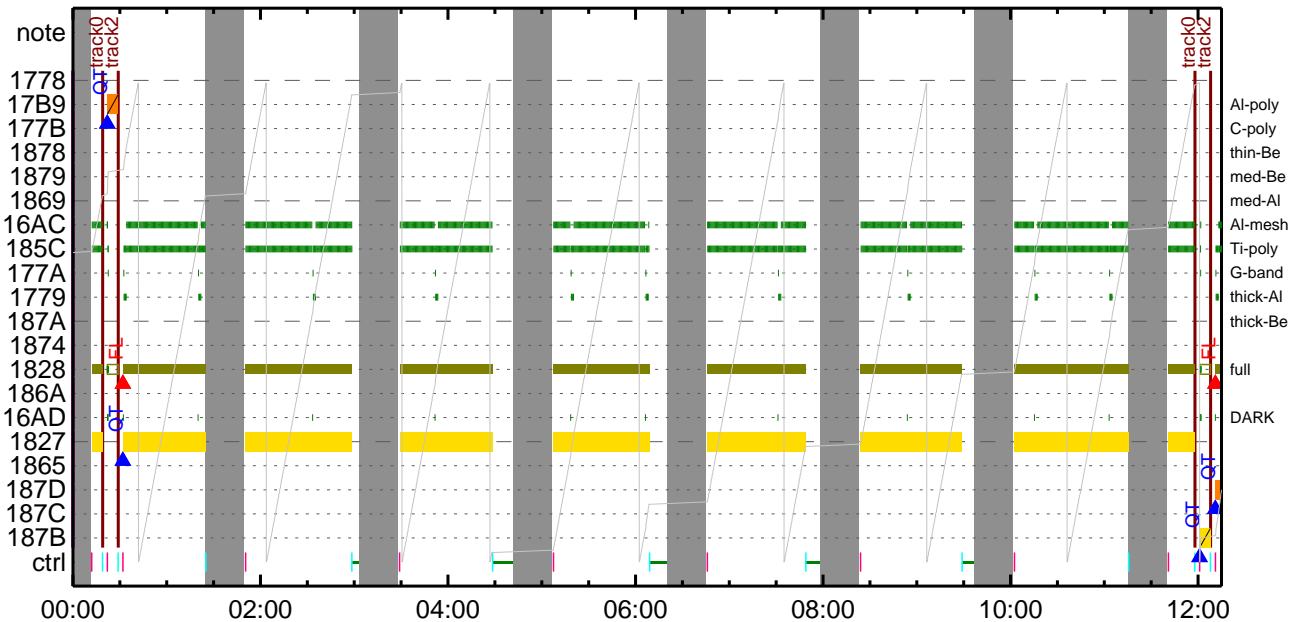
### CMDI #0910 2011/05/07



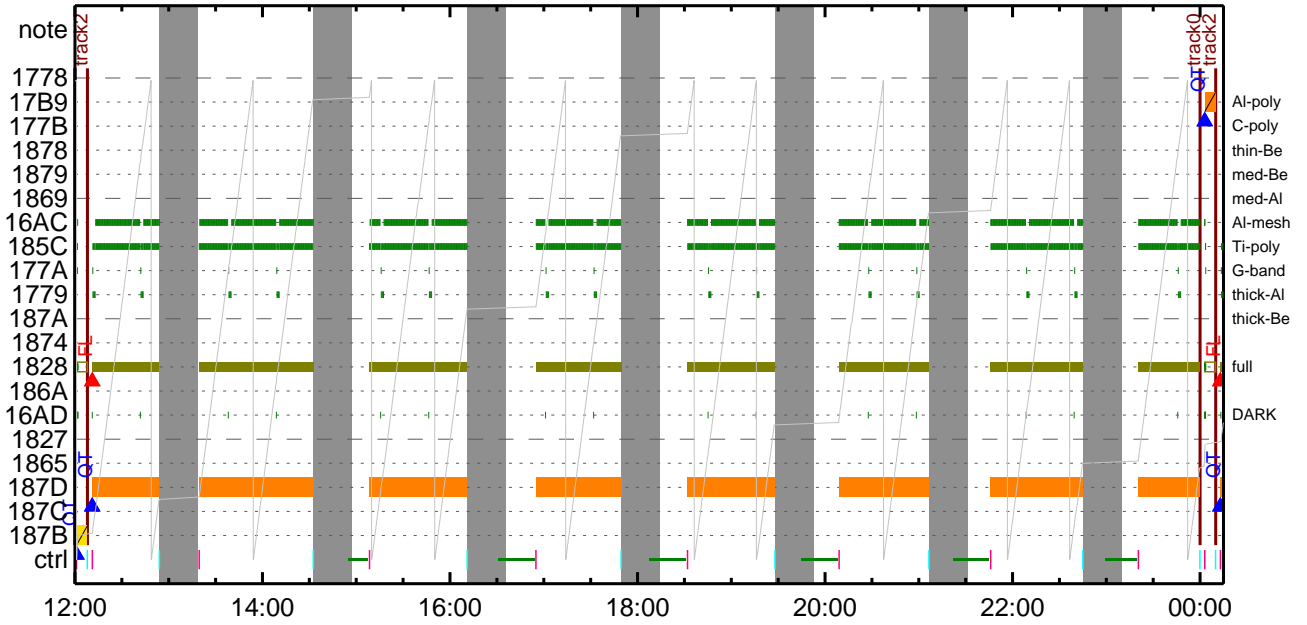
### CMDI #0910 2011/05/07



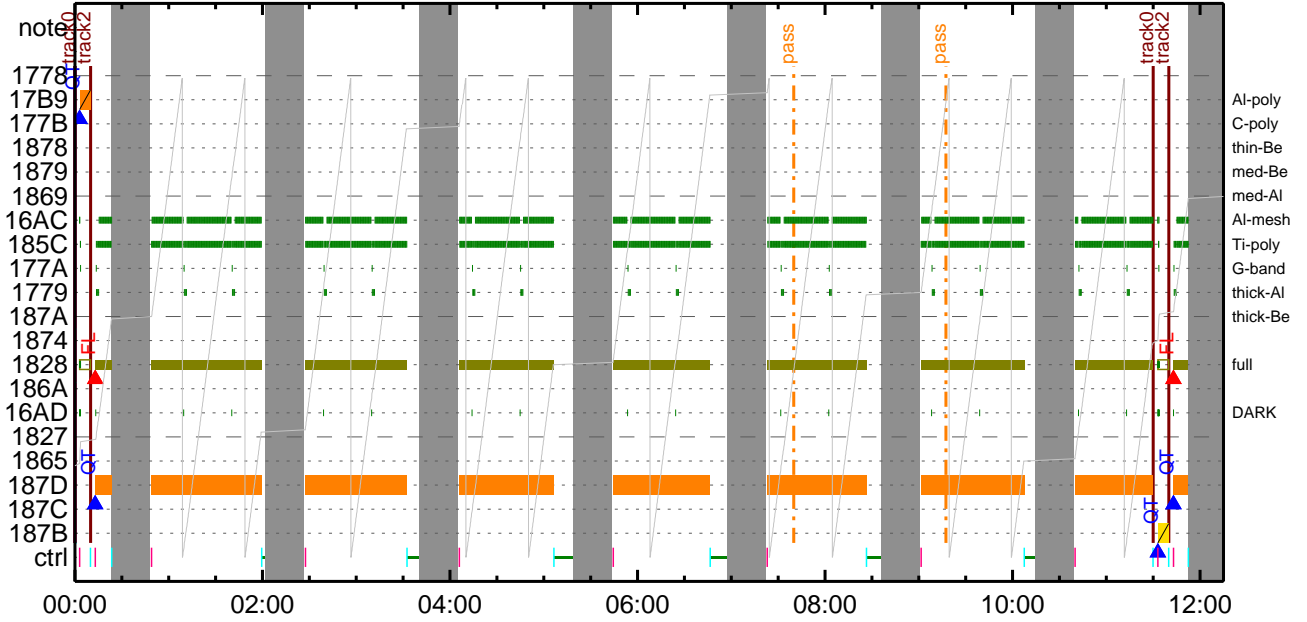
### CMDI #0910 2011/05/08



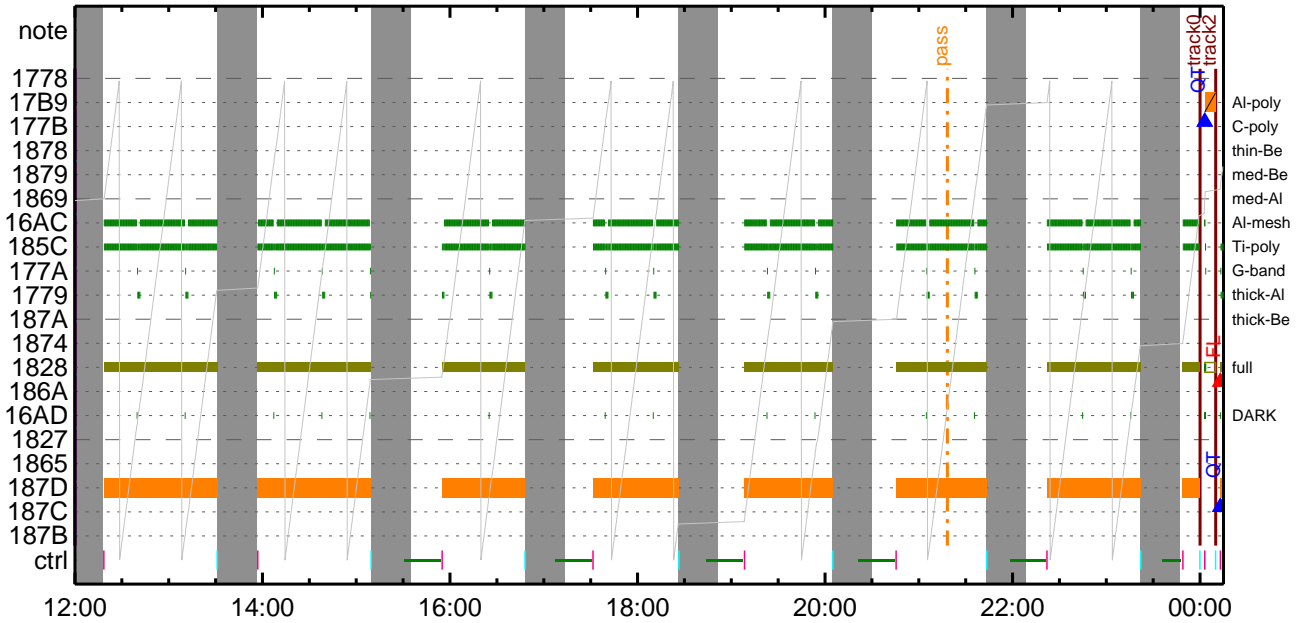
CMDI #0910 2011/05/08



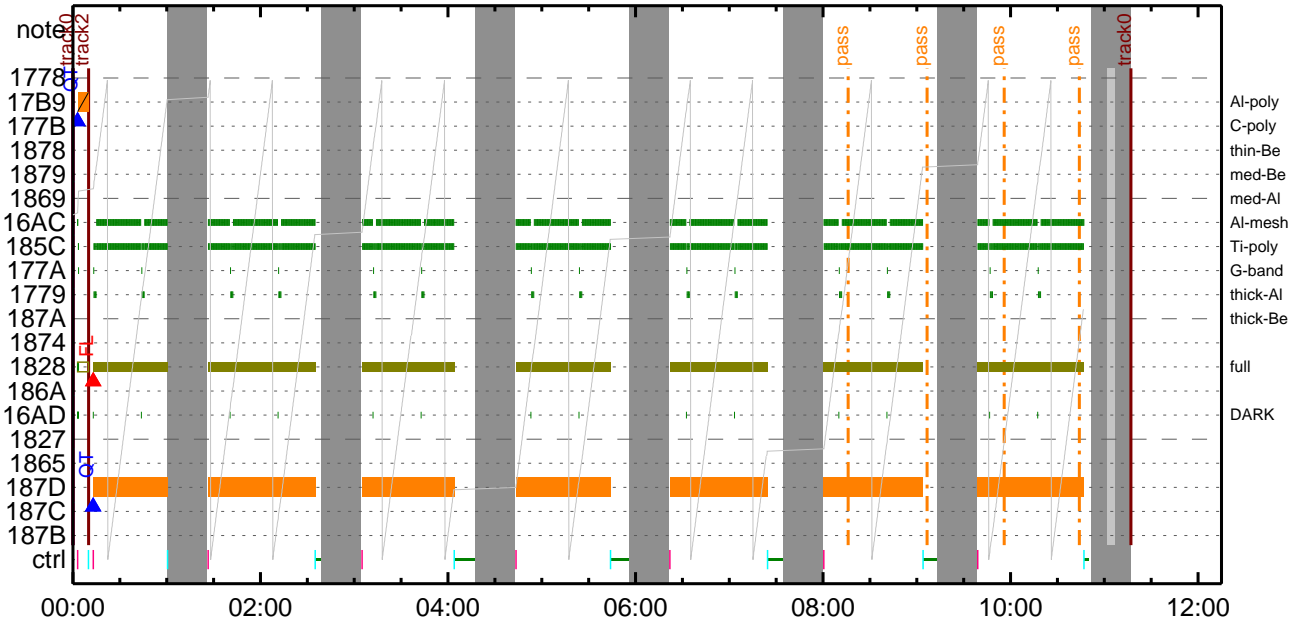
CMDI #0910 2011/05/09



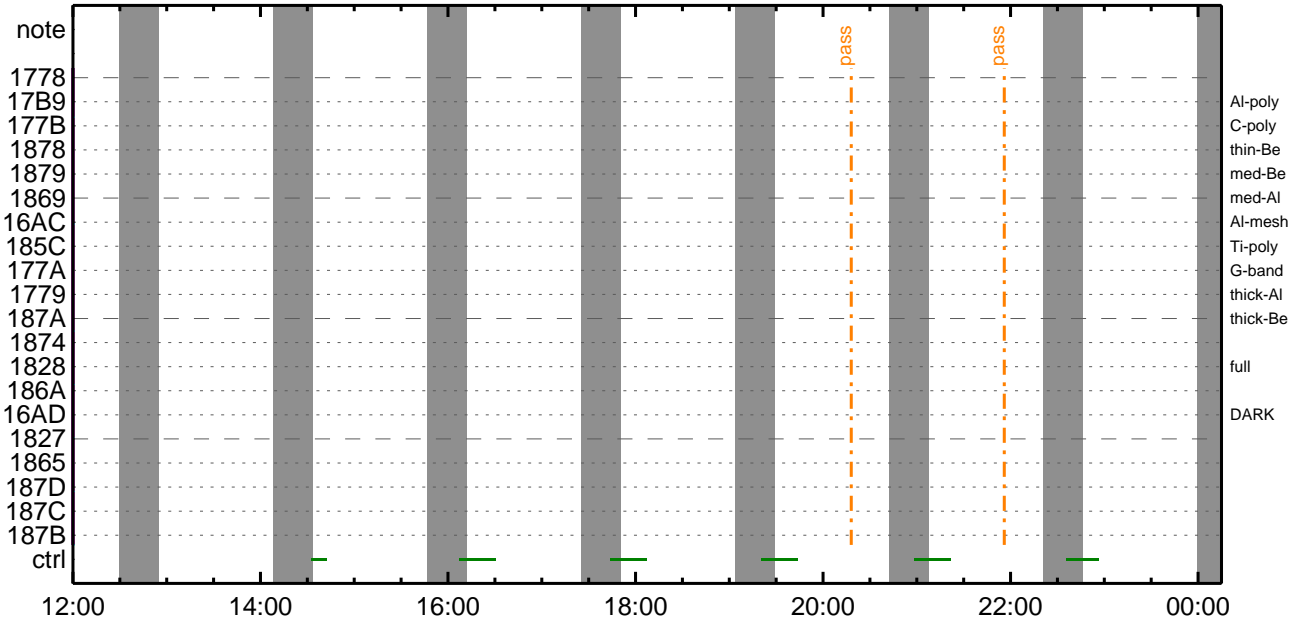
CMDI #0910 2011/05/09



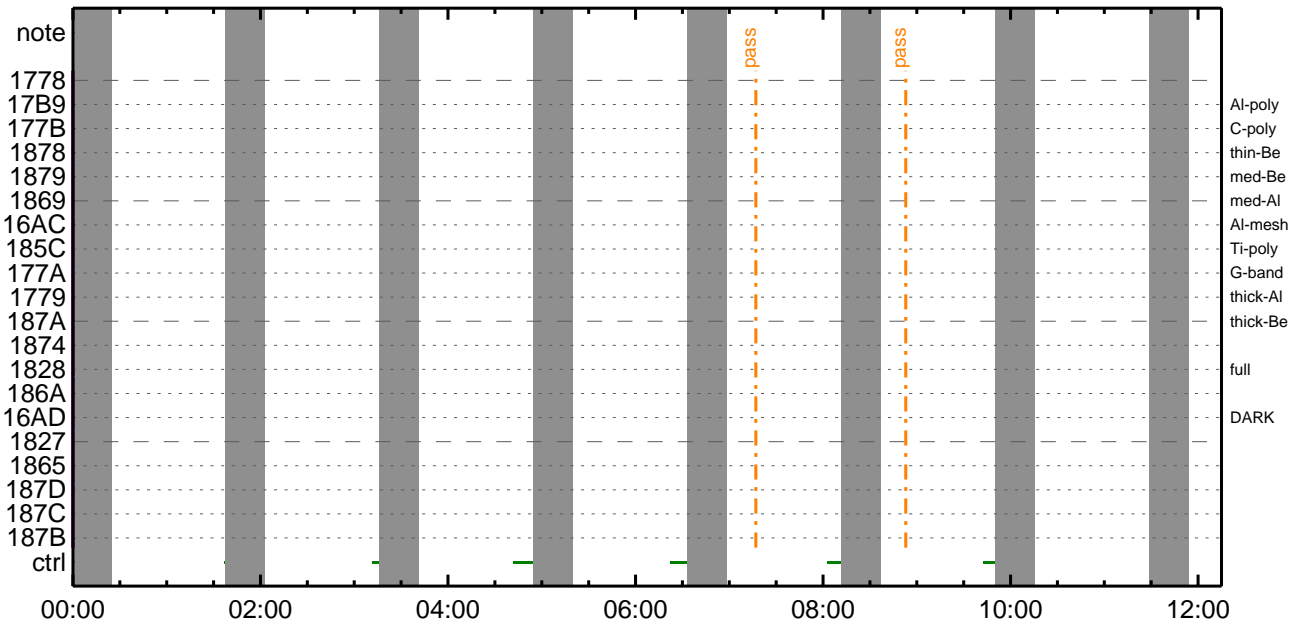
CMDI #0910 2011/05/10



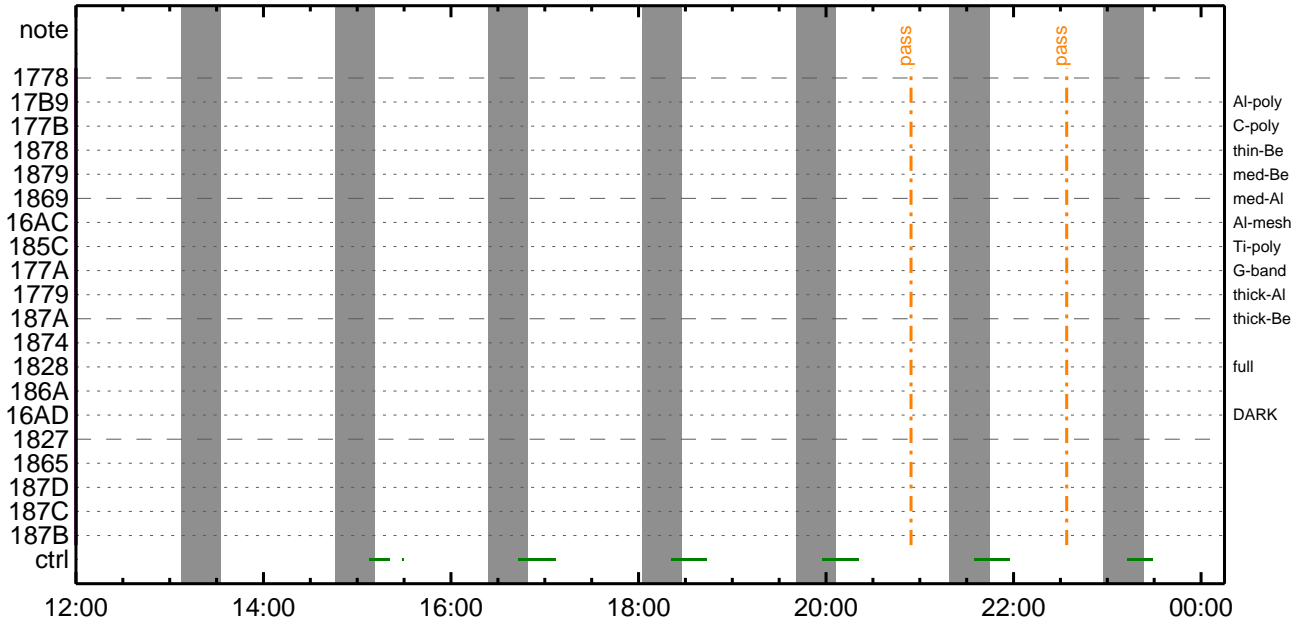
CMDI #0910 2011/05/10



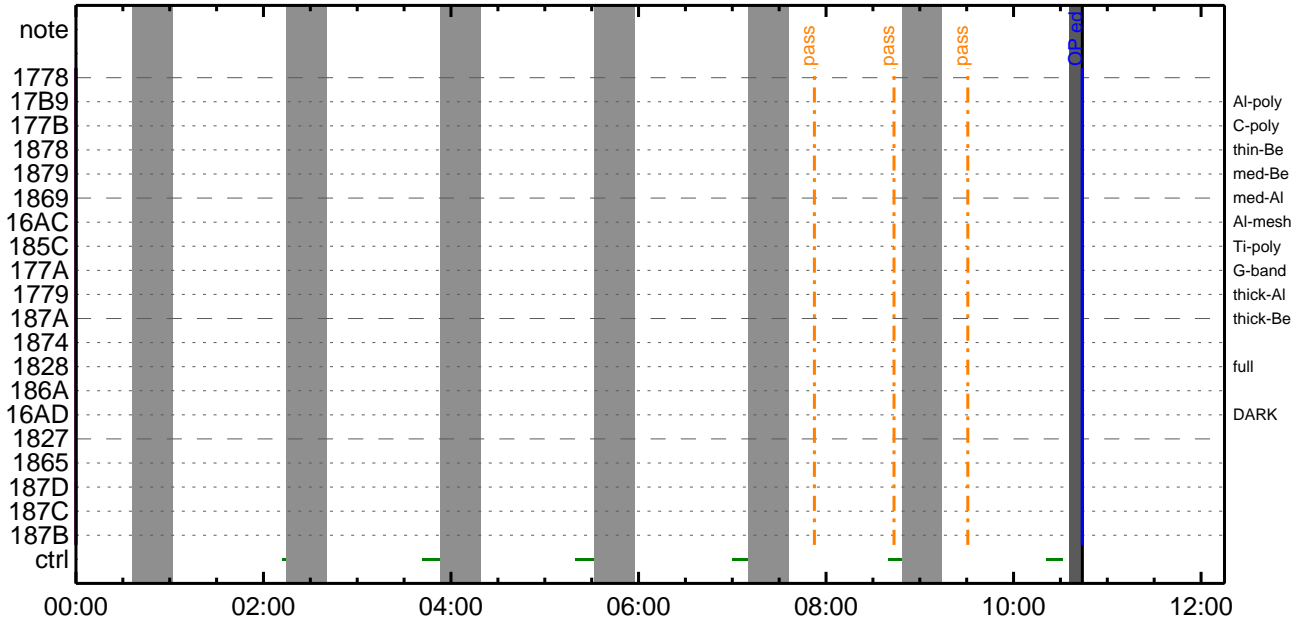
CMDI #0910 2011/05/11



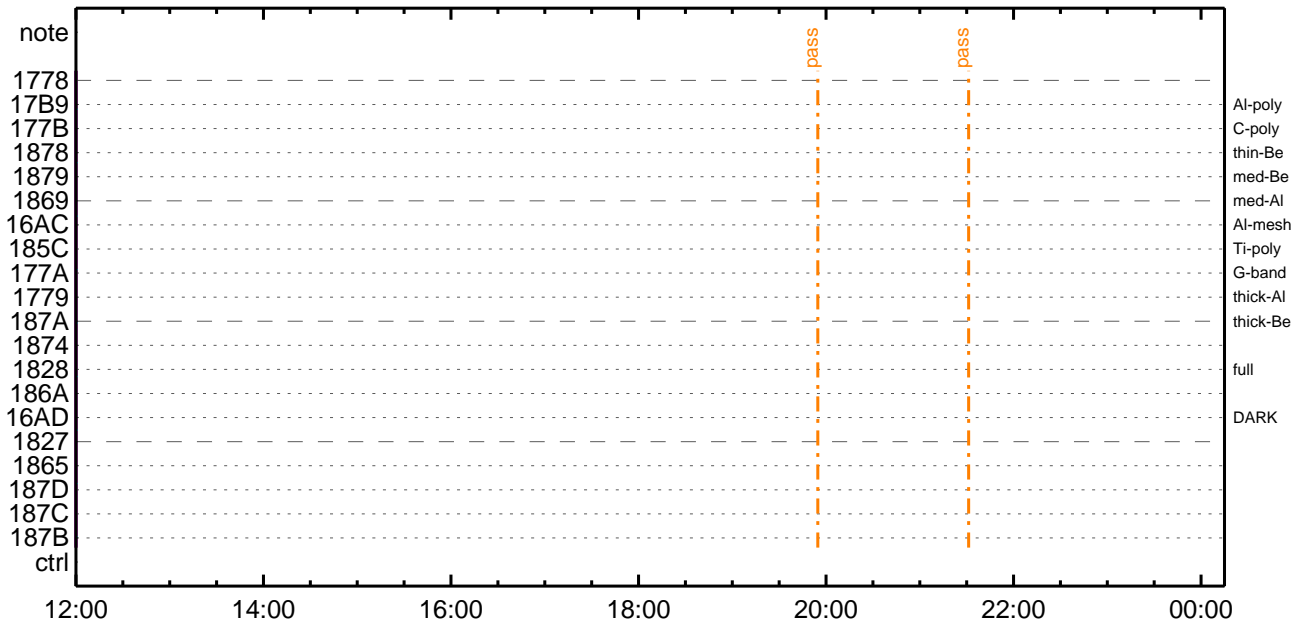
CMDI #0910 2011/05/11



CMDI #0910 2011/05/12



CMDI #0910 2011/05/12







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP      op-053:OP
0104 ( )
0105 . S. OG      og-053:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     127
0115 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0116 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0120 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0121 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0122 C.                çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0123 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0124 . C. YAYOYx½ª î»ð³ îÇ§
0125 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0126 . C. RAM ID=NMOG²î½Ë¹ç•ë² îOK²ð³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     127
0134 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0135 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0139 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0140 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0141 C.                çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0142 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0143 . C. YAYOYx½ª î»ð³ îÇ§
0144 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0145 . C. RAM ID=NMOG²î½Ë¹ç•ë² îOK²ð³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     65
0153 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0154 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0158 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0159 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0160 C.                çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0161 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0162 . C. YAYOYx½ª î»ð³ îÇ§
0163 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î½Ë¹ç•ë² îOK²ð³ îÇ§
0165 C.
0166 . C. ***** °Ë²¼²î½Ë¹ç•ë² îOK²ð³ îÇ§ *****
0167 C. DHUYâ;4YE;Ë½Y½;Yi;4YE;Ëðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.                çç[HK1_PKT_FORM_NO]        EQ      2
0171 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.5S
0172 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32K
0173 C.                çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î½Ë¹ç•ë² îOK²ð³ îÇ§
0180 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.5S
0181 C.
0182 . C. TIY³YpYóYËðððÁDîç(UT)
0183 +. TI 2011-05-07 10:50:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0186 C.
0187 +. TI 2011-05-07 10:50:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0190 C.
0191 +. TI 2011-05-07 10:50:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP

```

0194 C.  
0195 +. TI 2011-05-07 10:54:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0198 C.  
0199 C. °È²¼óîÄè%îíñóîîÝÄÿ\$ÝÄÿ-¹àîü  
0200 C. çç[HK1\_TI\_CMD\_ENA/DIS] EQ ENA  
0201 C. çç[HK1\_TI\_CMD\_NUM] EQ 4  
0202 C. çç[HK1\_NEXT\_EXEC\_PIM] EQ DHU  
0203 C. çç[HK1\_NEXT\_EXEC\_DC] EQ 0xB3  
0204 C.  
0205 C. \*\*\*\*\*  
0206 C. TÍîî°èYÄÿÖÿx  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;\$ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC (03 ab 03 01 02)  
0212 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 07  
0213 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 2B  
0214 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 3  
0215 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0216 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC (07 0b f8)  
0219 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0220 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0221 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0222 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0223 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0224 C.  
0225 C. YÄÿÖÿx½ªî»ò³ÎÇ§  
0226 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0227 C.  
0228 C. RAM ID=TI\_TBLñîî½È¹ç.è²îOKòð³ÎÇ§  
0229 C.  
0230 C. DHUÿâ;¼YÉ;È¼Y½. Yî;¼YÉ;Èòðîáñ¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC (02 0a f8)  
0233 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0234 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0235 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0236 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0237 C.  
0238 C. \*\*\*\*\*  
0239 C. SOT TI command set  
0240 C. \*\*\*\*\*  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2011-05-07 10:54:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC (41)  
0245 C. -----  
0246 C. HK1\_TI\_CMD\_NUM = 1 CNTUP [ ]  
0247 C. -----  
0248 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0249 C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2011-05-07 10:54:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC (21 02)  
0258 +. TI 2011-05-07 10:54:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC (22)  
0261 C. [ ] [HK1\_TI\_CMD\_NUM] EQ 2 COUNTUP  
0262 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2011-05-07 10:54:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC (c3)  
0271 C. [ ] [HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 C. \*\*\*\*\* MDP ´ûÃîñî»ö¼YòËÄñ¹ñèDCBC.x²è \*\*\*\*\*  
0276 C. (¼á°îYÖYÄÿBÿYÉYáYçYèòE¼áñ¼Á»Ûñ¹ñè)  
0277 S. DC-BC dcbc-402:DCBC  
0278 (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 C. \*\*\*\*\* YDÿ¹.î Daily±çîñòÈ´Øñ¹ñèDCBC.x²è \*\*\*\*\*  
0282 S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 C. ¡ãLOSÿÄÿ\$ÝÄÿ-¼Á»Û;ã  
0287 C.  
0288 C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-054 2011-05-07 12:51:50 114 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSŸÁŸŸŸÁŸ~¼Á»Û;ä
0005 C.
0006 C. ŸÁŸŸ;¼ŸŸ³ŸŸŸóŸŸÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÁâ•µ°£»Í×ÁÇçíŸçŸÁŸ×Ÿí;¼ŸÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÖâ•ç¼¼l¹ççÍ;çÁ®,ùâ¹âÈçðçÇÁ+ç®â•çÈççç³çÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. SOT table upload
0047 C. *****
0048 . C. < Stop FG table >
0049 +. DC 07-F0 MDP_FG_CTRL_MANU
0050 BC (51)
0051 . C.
0052 C. MDP_FG_CTRL_MODE = MANU [ ]
0053 C.
0054 C.
0055 . C. <Upload FG Observation Table>
0056 . S. RAM ram-266:MDP_OBS_F
0057 ( )
0058 C.
0059 . C. < Dump RAMID=MDP_OBS_F >
0060 +. DC 07-F0 MDP_DUMP_FGTBL
0061 BC (82 07 00 00 00 38 b8)
0062 C.
0063 C. MDP_OBS_F verify = OK/NG [ ]
0064 C.
0065 C.
0066 C. *****
0067 C. SOT TI command set
0068 C. *****
0069 C. Execute, after the success of TBL upload.
0070 +. TI 2011-05-07 10:54:18.0
0071 DC 07-F0 MDP_SOT_MODE_OBSV
0072 BC (40)
0073 . C.
0074 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0075 C.
0076 C.
0077 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0078 +. DC 07-FC EIS_MODE_MANU
0079 BC (21 02)
0080 . C. Verify EIS in MANUAL mode
0081 . C. Estimated OBSTBL upload time is 23s
0082 C. *****
0083 C. EIS START OBSTBL LOAD
0084 C. *****
0085 . S. RAM ram-820:EIS_OBSTBL
0086 ( )
0087 +. DC 07-FC EIS_DUMP_OBSTBL
0088 BC (07 07 07 00 00 70 00)
0089 C.
0090 C. Execute, after the success of OBSTBL upload.
0091 C. Set EIS TI-commands
0092 +. TI 2011-05-07 10:54:50.0
0093 DC 07-FC EIS_MODE_CHG_ENA
0094 BC (20)
0095 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
```





```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 60 20 18)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b a0 80 18 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0149 BC (c4 05)
0150 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0151 BC (c5 08)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C.
0155 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0156 C.
0157 +. DC 07-F0 MDP_XRT_MODE_OBSV
0158 BC (c2)
0159 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0160 BC (c0)
0161 +. TI 2011-05-07 10:54:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 +. TI 2011-05-07 10:54:04.0
0165 DC 07-F0 MDP_XRT_CTRL_AUTO
0166 BC (c0)
0167 . C. ----- Success Verify ? OK / NG ____
0168 C.
0169 C. ***** XRT END *****
0170 C.
0171 . C. ***** MDP `uãîï»ö%ýðÊâð¹æDCBC•x²è *****
0172 C. (%ã°îÿÓÿÁÿËÿÏÿÛÿàÿçÿèðÊ%¼¼¼¼»Û¹æè)
0173 . S. DC-BC dcbc-402:DCBC
0174 (MDP_known_event)
0175 C.
0176 C.
0177 . C. ***** ¥Ðÿ¹•Ï Daily±çÍÑðË´Ø¹æDCBC•x²è *****
0178 . S. DC-BC dcbc-153:DCBC
0179 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0180 C.
0181 C.
0182 . C. ;ãLOS¥Áÿ$ÿÃÿ-¼Å»Û;ã
0183 C.
0184 . C. ***** LOS *****
0185 C.

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May 07, 11 12:52

XRT\_OGLIST\_0910.chk

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\*\*\* OP Sequence for XRT \*\*\*

2011/05/07	11:04:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	11:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00			
2011/05/07	11:05:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00	00 00			
2011/05/07	11:05:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/07	11:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/07	11:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/07	11:07:58.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/05/07	11:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	11:24:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 01 26 50	00			
2011/05/07	11:34:00.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 01 26 3e	4d			
2011/05/07	11:44:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 01 26 2c	9b			
2011/05/07	11:54:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 01 26 1a	f1			
2011/05/07	12:04:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 01 26 09	3f			
2011/05/07	12:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	12:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	12:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/07	12:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/07	12:41:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/07	12:42:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 01 26 f7	8e			
2011/05/07	12:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	12:52:00.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 01 26 e5	dc			
2011/05/07	13:02:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 01 26 d4	32			
2011/05/07	13:12:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 01 26 c2	7f			
2011/05/07	13:22:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 01 26 b0	cd			
2011/05/07	13:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	13:31:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00			
2011/05/07	13:32:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 ac 00 00	00 00			
2011/05/07	13:32:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2011/05/07	13:32:18.0	XRT_FLD_DIS_423_OG [0x1a7]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/07	13:32:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/07	13:32:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/07	13:47:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	15:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	15:31:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00			
2011/05/07	15:32:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 00 00 54	00			
2011/05/07	15:32:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2011/05/07	15:32:18.0	XRT_FLD_DIS_423_OG [0x1a7]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/07	15:32:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/07	15:32:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/07	15:47:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	17:31:40.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	17:32:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	02 00 00 00	00 00			
2011/05/07	17:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	17:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00			
2011/05/07	17:57:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/07	17:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2011/05/07	17:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/07	17:57:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/07	17:57:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	17:57:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2011/05/07	17:57:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/07	17:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	18:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	18:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	18:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/07	18:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/07	19:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/07	19:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	20:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	20:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	20:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/07	20:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/07	21:08:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/07	21:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	22:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	22:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	22:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/07	22:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/07	22:44:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/07	22:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/07	23:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/07	23:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/07	23:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/07	23:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/08	00:11:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/08	00:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/08	00:18:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/08	00:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/05/08	00:19:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/05/08	00:19:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/08	00:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/08	00:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/08	00:21:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2011/05/08	00:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/08	00:28:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/08	00:29:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/05/08	00:31:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/08	00:31:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/08	00:31:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/08	00:31:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/08	00:31:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/08	00:31:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/08	00:31:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2011/05/08	00:31:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/08	00:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]							



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2011/05/08	01:25:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	01:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	01:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	01:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	01:49:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	01:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	02:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	02:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	02:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	03:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	03:28:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	03:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	04:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	04:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	04:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	04:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	05:06:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	05:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	06:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	06:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	06:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	06:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	06:45:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	06:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	07:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	07:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	07:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	07:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	08:23:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	08:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	09:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	09:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	09:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	09:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	10:01:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	10:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	11:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	11:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/08	11:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/08	11:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/08	11:40:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/08	11:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/08	11:57:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/08	11:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/05/08	11:58:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2011/05/08	11:58:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/05/08	11:58:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/05/08	11:58:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/08	12:00:58.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/05/08	12:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]				

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2011/05/08	12:07:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	12:08:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
			AOCU_NM	5	02-76	02 00 00 00 00			
2011/05/08	12:10:26.0	XRT_ROI_A_420_OG [0x1a4]							
			MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 08 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 20 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 08 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08			
2011/05/08	12:10:31.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/05/08	12:10:51.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/05/08	12:10:53.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/05/08	12:10:55.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/05/08	12:10:57.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/05/08	12:10:59.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	12:11:01.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2011/05/08	12:11:04.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08			
2011/05/08	12:11:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	12:54:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	12:54:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	12:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	12:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/05/08	13:18:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/08	13:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	14:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	14:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	14:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	14:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/05/08	15:07:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/08	15:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	16:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	16:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	16:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	16:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/05/08	16:54:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/08	16:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	17:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	17:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	17:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	17:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/05/08	18:31:00.5	XRT_Custom_418_OG [0x1a2]							
2011/05/08	18:32:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	19:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	19:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	19:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	19:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/05/08	20:08:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/08	20:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/05/08	21:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/05/08	21:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/05/08	21:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/05/08	21:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2011/05/08	21:45:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/08	21:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/08	22:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/08	22:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/08	22:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/08	22:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/08	23:19:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/08	23:20:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/08	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/08	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	00:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/05/09	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/05/09	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/05/09	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/05/09	00:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/09	00:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2011/05/09	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	00:10:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	00:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02 00 00 00 00		
2011/05/09	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/05/09	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/05/09	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/05/09	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/05/09	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/09	00:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2011/05/09	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/05/09	00:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	00:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	00:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	00:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	00:48:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	00:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	01:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	01:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	01:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	02:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	02:26:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	02:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	03:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	03:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	03:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	03:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	04:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	04:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	05:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	05:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	05:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	05:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		

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2011/05/09	05:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/09	05:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	06:46:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	06:46:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	06:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/09	06:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/09	07:22:00.0	XRT_Custom_418_OG [0x1a2]								
2011/05/09	07:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	08:26:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	08:26:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	08:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/09	08:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/09	09:00:30.0	XRT_Custom_418_OG [0x1a2]								
2011/05/09	09:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	10:07:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	10:07:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	10:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/09	10:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/09	10:39:00.0	XRT_Custom_418_OG [0x1a2]								
2011/05/09	10:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	11:29:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	11:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/05/09	11:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	00	00	00	00	00
2011/05/09	11:30:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/09	11:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/09	11:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/09	11:32:58.0	XRT_QT_PROG_SET_415_OG [0x19f]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2011/05/09	11:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	11:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	11:40:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]								
			AOCU_NM	5	02-76	02	00	00	00	00
2011/05/09	11:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/09	11:42:46.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/09	11:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/09	11:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/09	11:42:52.0	XRT_ARS_DIS_431_OG [0x1af]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/09	11:42:54.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	11:42:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2011/05/09	11:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/09	11:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	11:52:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	11:52:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	11:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/09	11:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/09	12:17:30.5	XRT_Custom_418_OG [0x1a2]								
2011/05/09	12:18:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/09	13:31:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/09	13:31:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/09	13:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/09	13:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								

2011/05/09	13:56:00.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	13:57:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	15:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	15:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	15:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	15:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	15:54:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	15:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	16:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	16:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	16:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	16:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	17:30:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	17:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	18:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	18:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	18:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	18:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	19:07:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	19:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	20:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	20:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	20:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	20:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	20:44:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	20:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	21:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	21:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	21:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	21:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	22:21:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	22:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	23:22:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	23:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/09	23:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/09	23:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/09	23:48:00.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/09	23:49:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/05/09	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/09	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/10	00:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/05/10	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/05/10	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/05/10	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/05/10	00:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/10	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2011/05/10	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/10	00:10:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/10	00:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02 00 00 00 00		
2011/05/10	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		

2011/05/10	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/05/10	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/05/10	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/10	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	00:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2011/05/10	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/05/10	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	01:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	01:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	01:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	01:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	01:25:30.5	XRT_Custom_418_OG [0x1a2]					
2011/05/10	01:26:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	02:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	02:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	02:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	02:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	03:04:00.5	XRT_Custom_418_OG [0x1a2]					
2011/05/10	03:05:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	04:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	04:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	04:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	04:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	04:42:30.0	XRT_Custom_418_OG [0x1a2]					
2011/05/10	04:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	05:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	05:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	05:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	05:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	06:21:00.0	XRT_Custom_418_OG [0x1a2]					
2011/05/10	06:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	07:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	07:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	07:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	07:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	07:59:30.0	XRT_Custom_418_OG [0x1a2]					
2011/05/10	08:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	09:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	09:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	09:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	09:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	09:38:00.0	XRT_Custom_418_OG [0x1a2]					
2011/05/10	09:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/10	10:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/10	10:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/10	10:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/10	10:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/10	11:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	