

XRT Timeline to be uploaded on 2011/05/12

Period: 2011/05/12 10:44:00 - 2011/05/17 11:04:00

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Normal mode

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XOB #187F:											
Term		Pointing (x, y)				Comment					
05/12 10:57:00 - 05/12 12:05:30		Fixed (0.0, 0.0)				# OP start + 10min, HOP186 + synoptic					
05/13 11:48:00 - 05/13 11:54:54		Track (523.7, 336.3) @ 05/13 06:30:00				AR 11210 track					
PROG= 17 1-time(s)											
Subr= 1 1-time(s) 12.0sec											
Seqn= 86 1-time(s) 4.0sec											
Open/Al-mesh Open/Al-mesh close		Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close		Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec											
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 37 1-time(s) 4.0sec											
Open/Ti-poly Open/Ti-poly close		Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly Open/Ti-poly close		Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec											
Open/G-band Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec											
Seqn= 42 1-time(s) 2.0sec											
Open/Al-mesh Open/Ti-poly close		Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Ti-poly close		Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/G-band close		Safe	Dark	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1779:											
Term		Pointing (x, y)				Comment					
05/12 12:32:00 - 05/12 13:44:00		Fixed (246.0, 0.0)				HOP182 EW 1					
05/12 14:17:00 - 05/12 15:22:30		Fixed (475.0, 0.0)				HOP182 EW 2					
05/12 16:07:30 - 05/12 17:01:00		Fixed (672.0, 0.0)				HOP182 EW 3					
05/12 17:44:30 - 05/12 18:39:30		Fixed (823.0, 0.0)				HOP182 EW 4					
05/12 19:21:00 - 05/12 20:17:30		Fixed (918.0, 0.0)				HOP182 EW 5					
05/12 20:58:30 - 05/12 21:56:00		Fixed (0.0, -246.0)				HOP182 NS 1					
05/12 22:34:30 - 05/13 00:04:54		Fixed (0.0, -475.0)				HOP182 NS 2					
05/13 01:40:00 - 05/13 02:48:00		Fixed (0.0, -672.0)				HOP182 NS 3					
05/13 03:18:30 - 05/13 04:17:00		Fixed (0.0, -823.0)				HOP182 NS 4					
05/13 04:57:00 - 05/13 05:57:30		Fixed (0.0, -918.0)				HOP182 NS 5					
PROG= 11 1-time(s)											
Subr= 1 1-time(s) 12.0sec											
Seqn= 36 1-time(s) 12.0sec											
Open/G-band Open/G-band open		Safe	Norm	44ms	Obs	1x1	0x0 (0, 0)	Q=90	0	0	2.0sec
Open/G-band Open/G-band open		Safe	Norm	44ms	Obs	1x1	0x0 (0, 0)	Q=90	0	0	2.0sec
Open/thick-Be Open/thick-Be close		Safe	Dark	44ms	Obs	1x1	0x0 (0, 0)	Q=98	0	0	2.0sec
Open/thick-Be Open/thick-Be close		Safe	Dark	44ms	Obs	1x1	0x0 (0, 0)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec											
Seqn= 93 2-time(s) 2.0sec											
Open/Al-mesh Open/Ti-poly close		Safe	Norm	4.00s	Obs	2x2	2048x512 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Norm	8.00s	Obs	2x2	2048x512 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #187A:											
Term		Pointing (x, y)				Comment					
05/13 00:08:00 - 05/13 01:13:00		Fixed (0.0, 0.0)				synoptic					
05/14 00:03:00 - 05/14 00:12:00		Track (523.7, 336.3) @ 05/13 06:30:00				AR 11210 track					
PROG= 10 1-time(s)											
Subr= 1 1-time(s) 12.0sec											
Seqn= 86 1-time(s) 4.0sec											
Open/Al-mesh Open/Al-mesh close		Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close		Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec											
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 37 1-time(s) 4.0sec											
Open/Ti-poly Open/Ti-poly close		Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly Open/Ti-poly close		Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec											
Open/G-band Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #16AC:												
Term			Pointing (x, y)					Comment				
05/13 06:15:00 - 05/13 07:59:54			Fixed (0.0, -918.0)					HOP182 NS 5				
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 21 24-time(s) 300.0sec												
Open/G-band			Open/G-band			open	Safe	Norm	16ms	Obs 2x2	2048x1536 (1024, 768)	Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AD:												
Term			Pointing (x, y)					Comment				
05/13 08:15:00 - 05/13 09:59:54			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 22 15-time(s) 480.0sec												
Open/G-band			Open/G-band			open	Safe	Norm	16ms	Obs 2x2	1536x2048 (1280, 1024)	Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad												
Term			Pointing (x, y)					Comment				
05/13 10:03:00 - 05/13 11:44:54			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
05/13 11:58:00 - 05/13 23:59:54			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
05/14 00:39:00 - 05/14 09:56:00			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
PROG= 03 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
Open/G-band			Open/G-band			open	Safe	Norm	63ms	Obs 1x1	384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn=100 4-time(s) 2.0sec												
Open/Ti-poly			Open/thick-Al			close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
Open/thick-Al			Open/thick-Be			close	Safe	Norm	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 6 18-time(s) 2.0sec												
Open/Al-mesh			Open/thick-Al			close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95 3 0 2.0sec
Open/Ti-poly			Open/thick-Be			close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95 3 0 20.0sec
Open/Al-mesh			Open/Ti-poly			close	Safe	Norm	500ms	Obs 2x2	512x512 (1064, 1048)	Q=95 2 1 2.0sec
Open/Ti-poly			Open/thick-Be			close	Safe	Norm	1.00s	Obs 1x1	512x512 (1064, 1048)	Q=95 2 1 20.0sec
Open/Al-mesh			Open/Ti-poly			close	Safe	Norm	500ms	Obs 2x2	512x512 (1064, 1048)	Q=95 2 2 2.0sec
Open/Ti-poly			Open/thick-Be			close	Safe	Norm	1.00s	Obs 1x1	512x512 (1064, 1048)	Q=95 2 2 20.0sec
Open/Al-mesh			Open/thick-Al			close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95 3 3 2.0sec
Open/Ti-poly			Open/thick-Be			close	Safe	Norm	1.00s	Obs 1x1	512x512 (1064, 1048)	Q=95 3 3 20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term			Pointing (x, y)					Comment				
05/12 12:32:00 - 05/12 13:44:00			Fixed (246.0, 0.0)					HOP182 EW 1				
05/12 14:17:00 - 05/12 15:22:30			Fixed (475.0, 0.0)					HOP182 EW 2				
05/12 16:07:30 - 05/12 17:01:00			Fixed (672.0, 0.0)					HOP182 EW 3				
05/12 17:44:30 - 05/12 18:39:30			Fixed (823.0, 0.0)					HOP182 EW 4				
05/12 19:21:00 - 05/12 20:17:30			Fixed (918.0, 0.0)					HOP182 EW 5				
05/12 20:58:30 - 05/12 21:56:00			Fixed (0.0, -246.0)					HOP182 NS 1				
05/12 22:34:30 - 05/13 00:04:54			Fixed (0.0, -475.0)					HOP182 NS 2				
05/13 01:40:00 - 05/13 02:48:00			Fixed (0.0, -672.0)					HOP182 NS 3				
05/13 03:18:30 - 05/13 04:17:00			Fixed (0.0, -823.0)					HOP182 NS 4				
05/13 04:57:00 - 05/13 05:57:30			Fixed (0.0, -918.0)					HOP182 NS 5				
05/13 10:03:00 - 05/13 11:44:54			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
05/13 11:58:00 - 05/13 23:59:54			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
05/14 00:39:00 - 05/14 09:56:00			Track (523.7, 336.3) ^{Ⓜ 05/13 06:30:00}					AR 11210 track				
PROG= 08 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al			Open/thick-Al			close	Safe	Norm	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
Open/thick-Be			Open/thick-Be			close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
└─ Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh			Open/Al-mesh			close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95 2 0 2.0sec
Open/Ti-poly			Open/Ti-poly			close	Safe	Norm	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=95 2 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
Open/G-band			Open/G-band			open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al			Open/thick-Al			close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al			Open/thick-Al			close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98 0 0 2.0sec
└─ Subr= 3 30-time(s) 60.0sec												

Seqn= 87		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 88		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 90		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
Seqn= 87		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 88		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 90		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
Seqn= 87		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 88		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Subr= 4		24-time(s)		600.0sec											
Seqn= 89		1-time(s)		2.0sec											
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec		
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec		
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

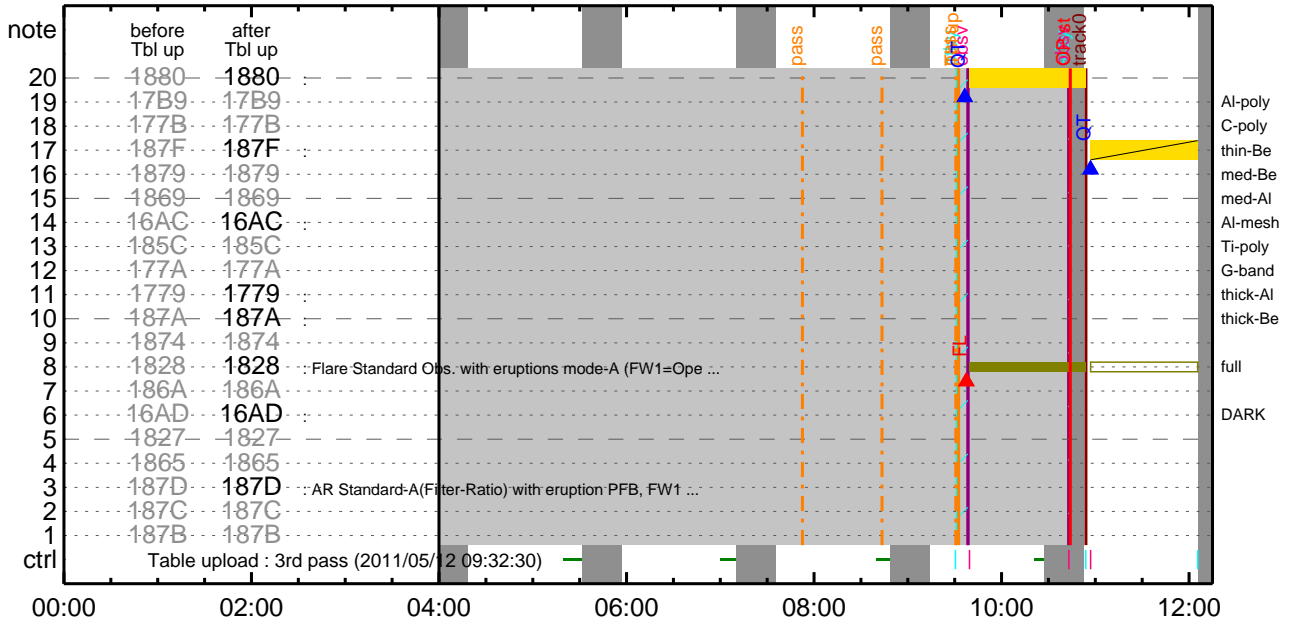
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Flare Detection

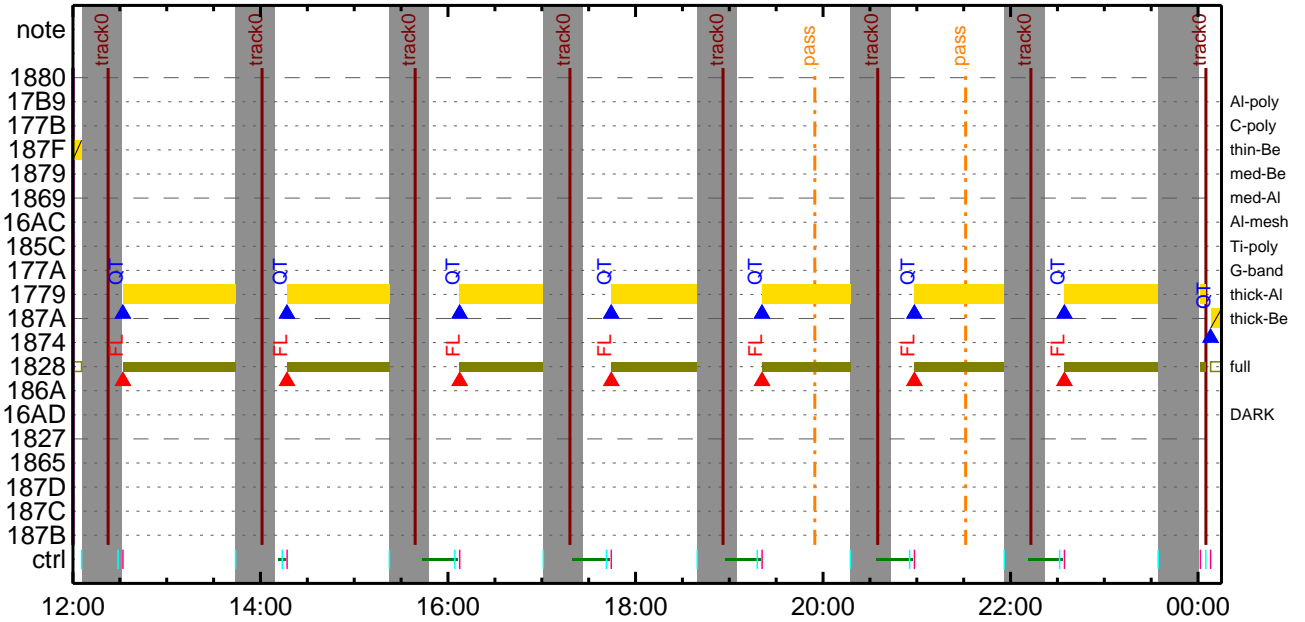
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FLD Patrol												
Term		Pointing (x, y)					Comment					
05/12 12:31:46 - 05/13 00:05:16		Fixed (246.0, 0.0)					HOP182 EW 1					
05/13 01:39:46 - 05/13 06:00:18		Fixed (0.0, -672.0)					HOP182 NS 3					
05/13 10:02:46 - 05/13 11:45:16		Track (523.7, 336.3) @ 05/13 06:30:00					AR 11210 track					
05/13 11:57:46 - 05/14 00:00:16		Track (523.7, 336.3) @ 05/13 06:30:00					AR 11210 track					
05/14 00:38:46 - 05/17 11:04:00		Track (523.7, 336.3) @ 05/13 06:30:00					AR 11210 track					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	ROI: size (center)		Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

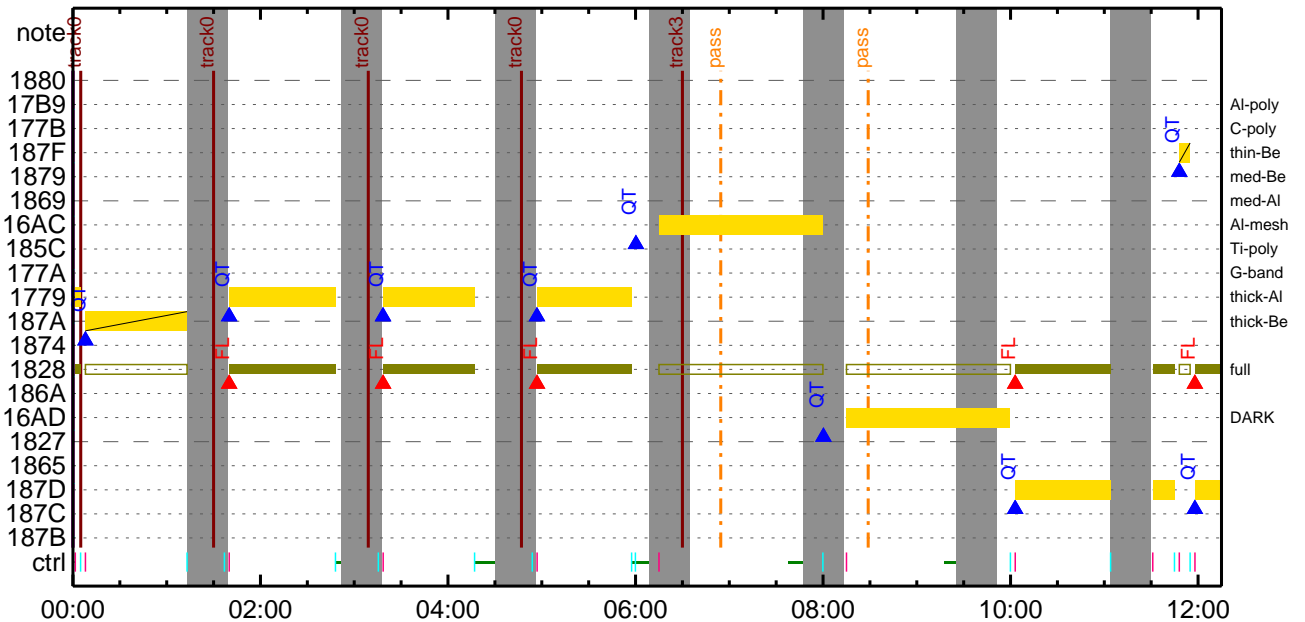
CMDI #0921 2011/05/12



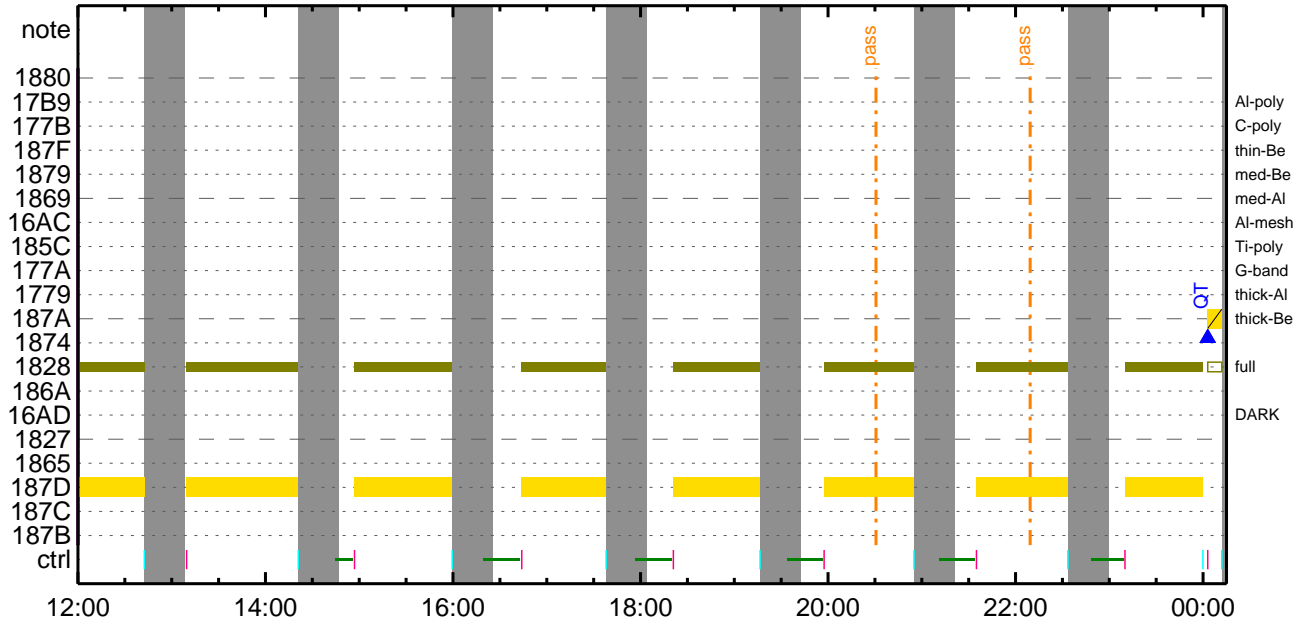
CMDI #0921 2011/05/12



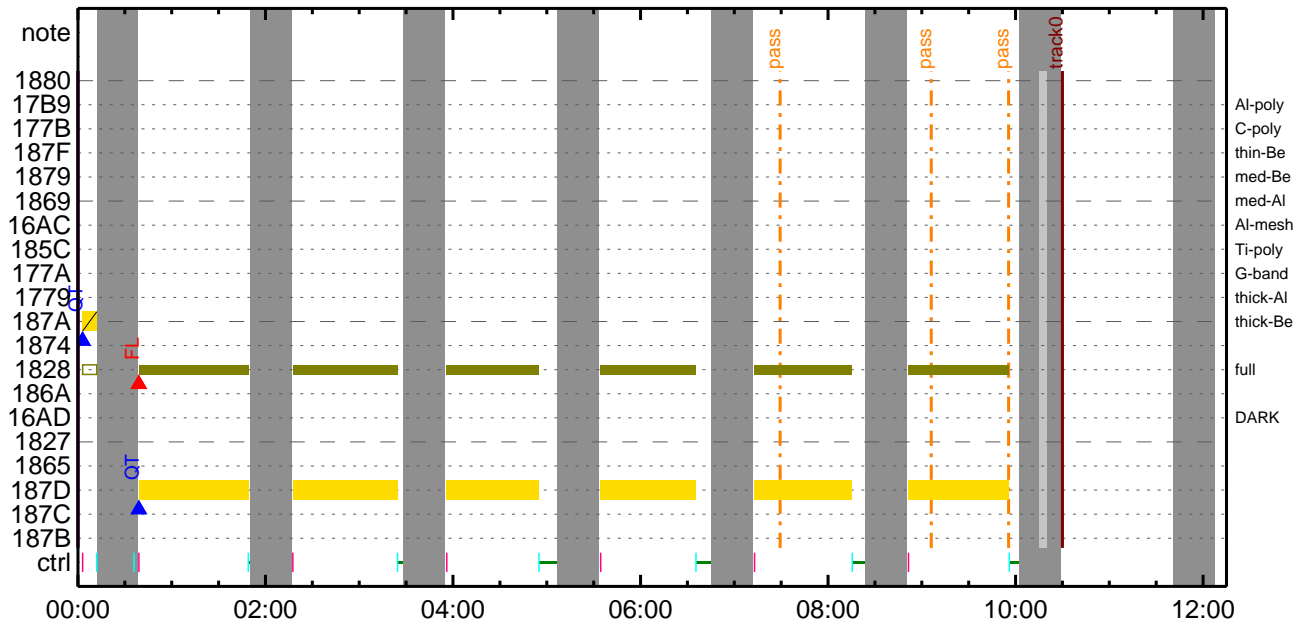
CMDI #0921 2011/05/13



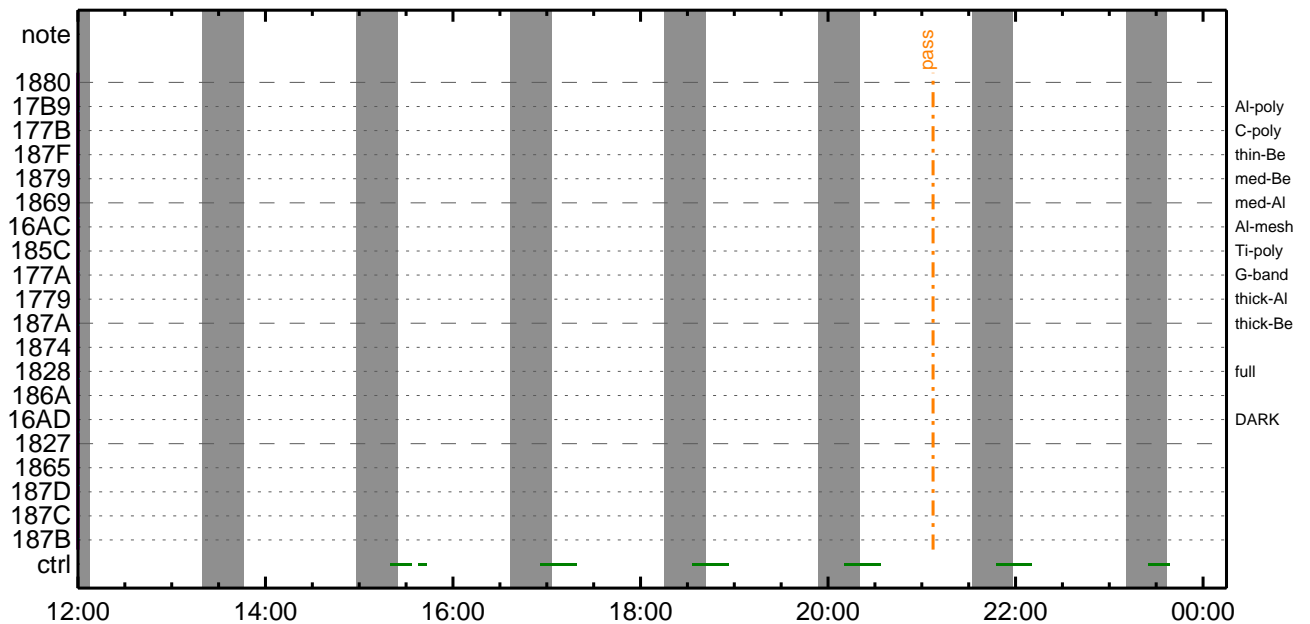
CMDI #0921 2011/05/13



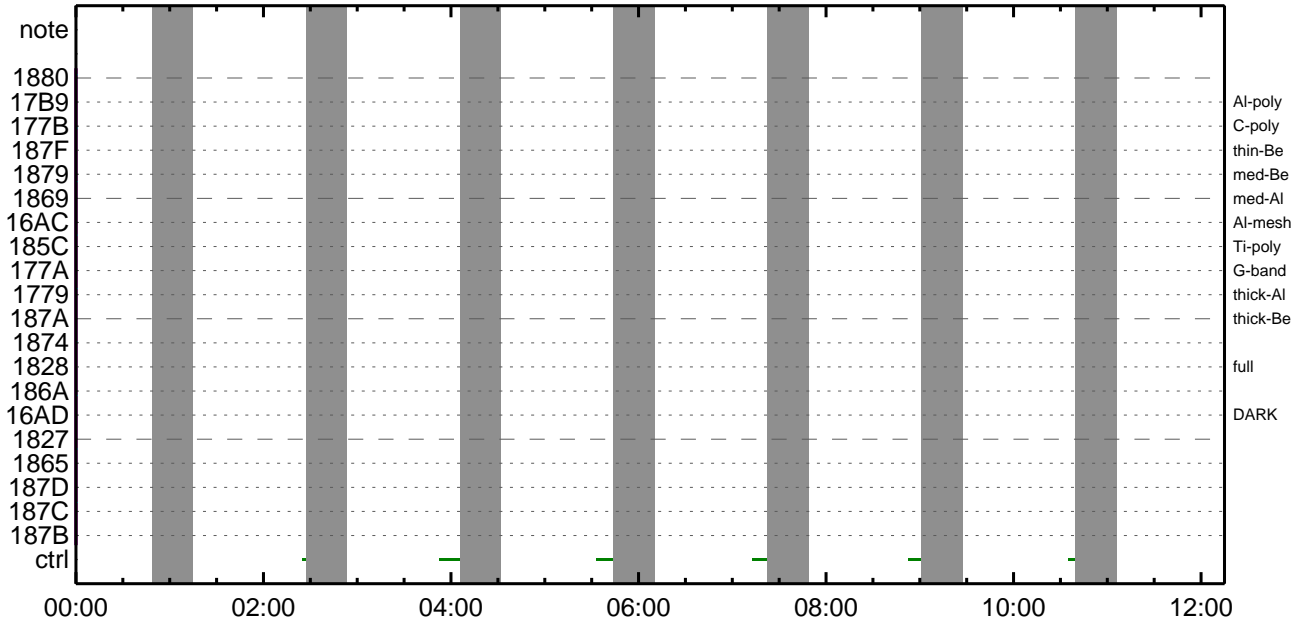
CMDI #0921 2011/05/14



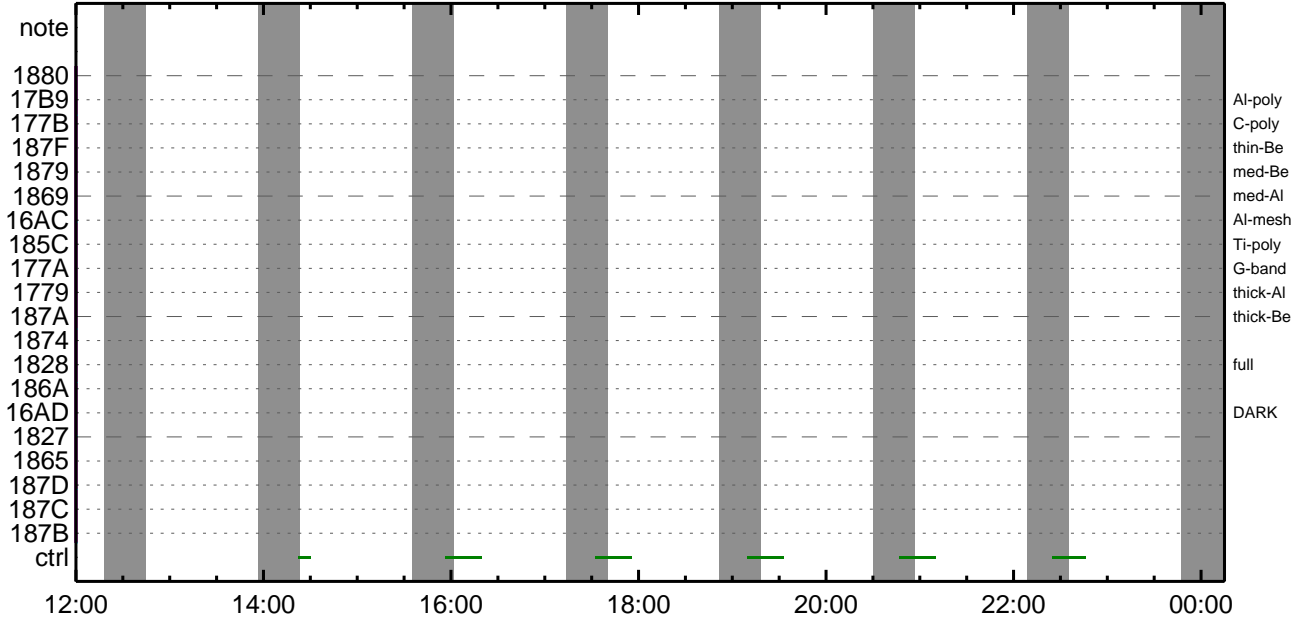
CMDI #0921 2011/05/14



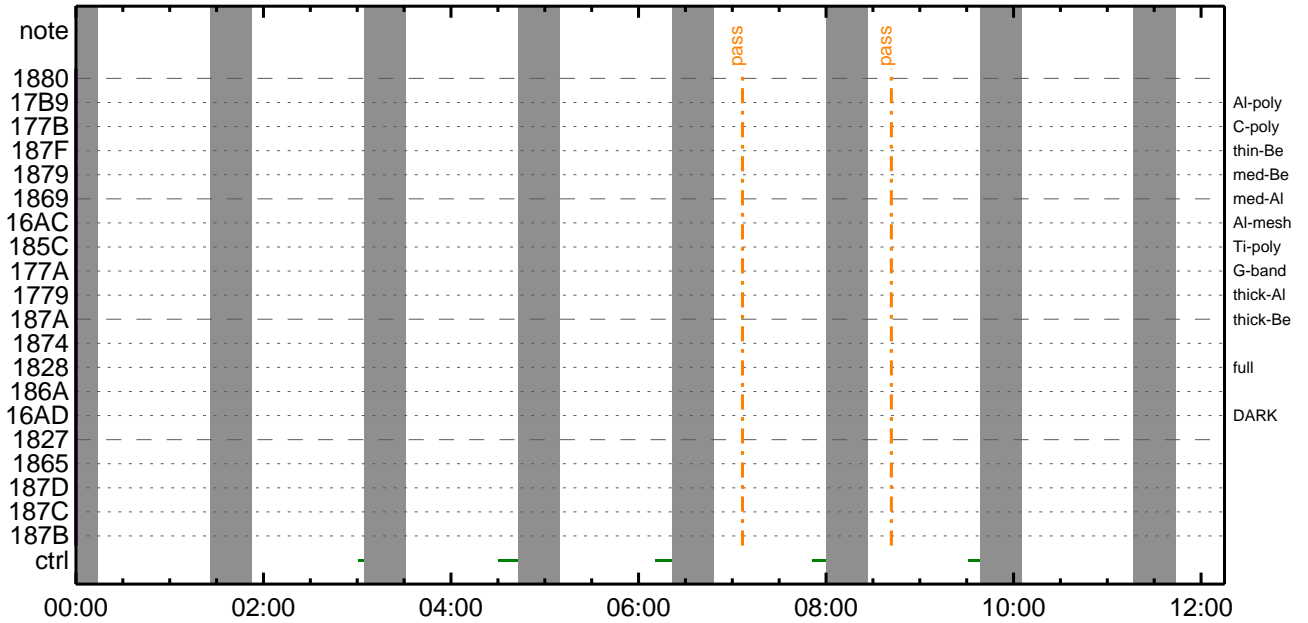
CMDI #0921 2011/05/15



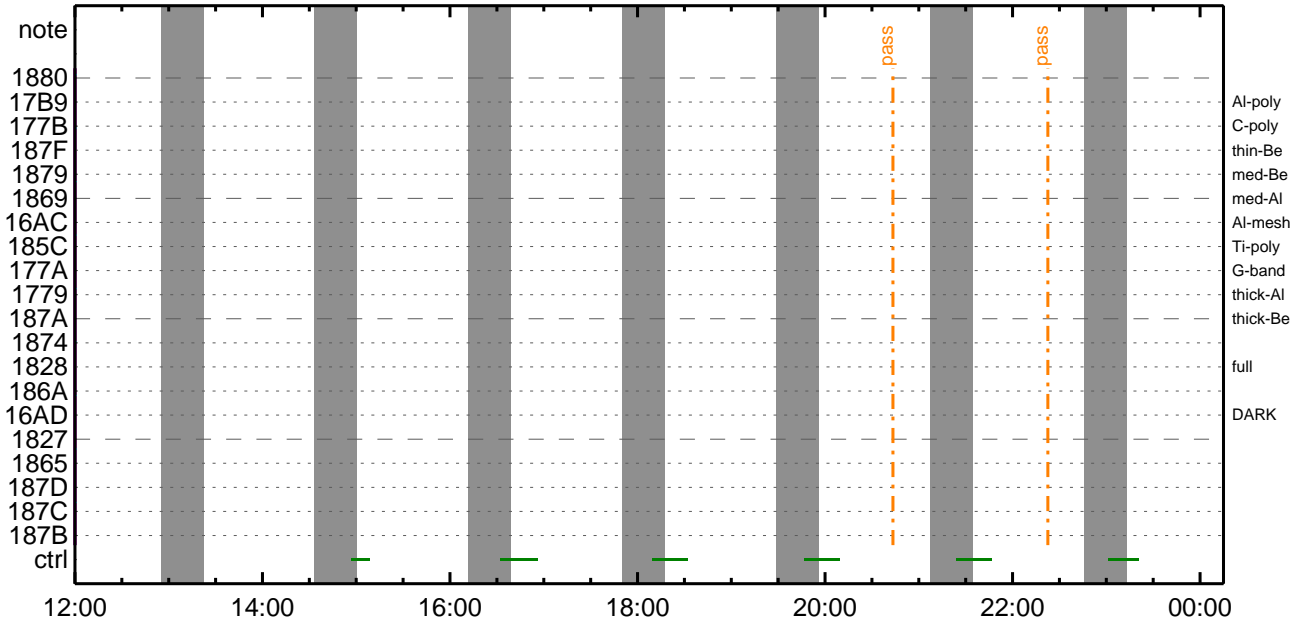
CMDI #0921 2011/05/15



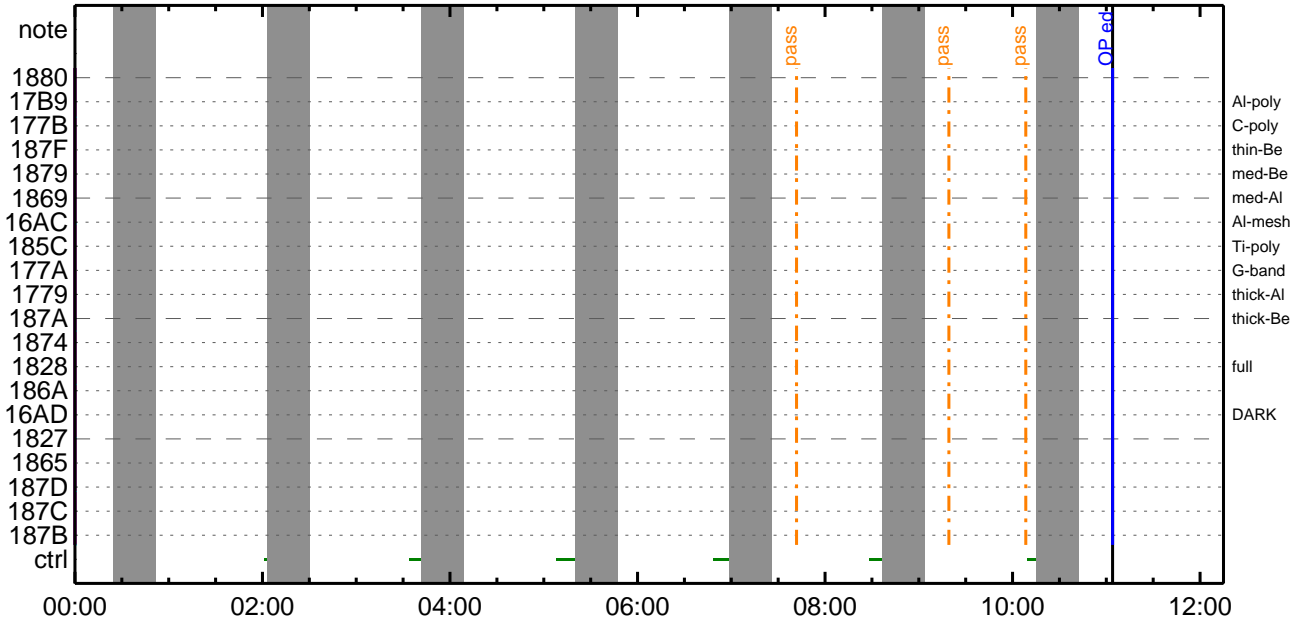
CMDI #0921 2011/05/16



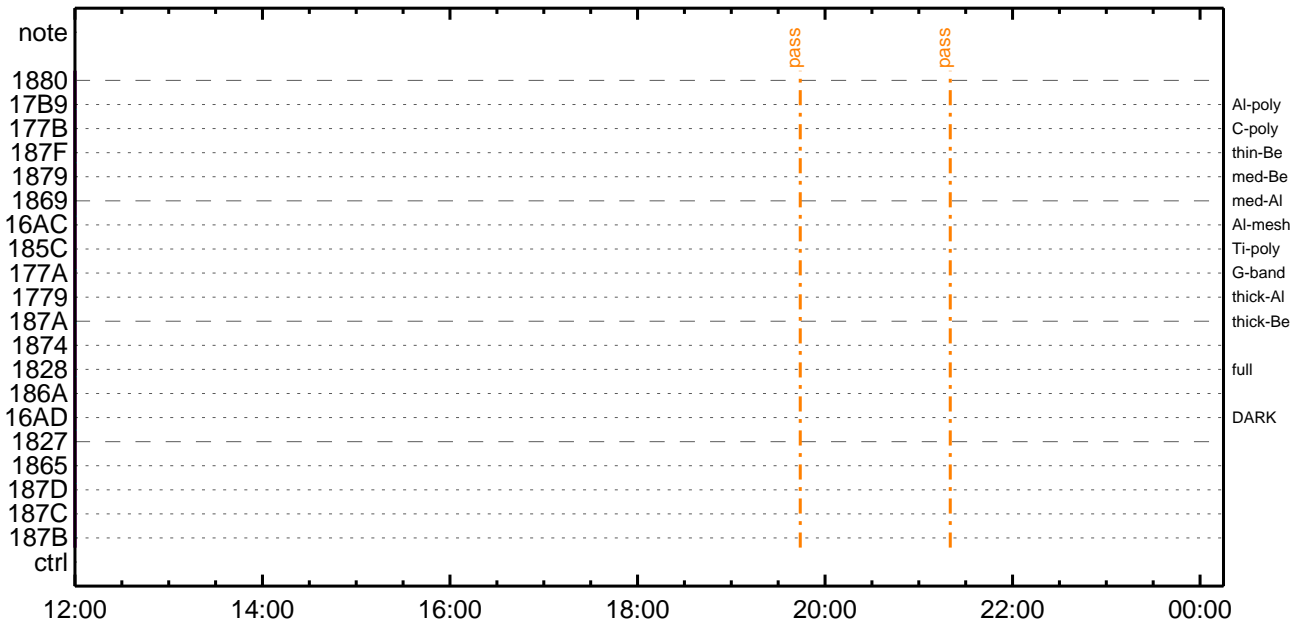
CMDI #0921 2011/05/16



CMDI #0921 2011/05/17



CMDI #0921 2011/05/17



(a) Spacecraft Operation Procedure (real-commands)

```
main-066 2011-05-12 10:16:23 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥S¥Á¥~¼Ä»Û;ã
0005 C.
0006 C. ¥Ä¥È;¼¥³¥È¥Ó¥ÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççÒÄ±·µ°È»í×ÁÇçí¥Ç¥Á¥×¥í;¼¥È;ÈÈÈµ·ííÈ;ÈÈÈ¼°ÇÖ±·ç¼í¹ççí;çÄ®. ù±¹æ±±±±ÇÄ+ç®±·±È±±±±æ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç" °ÄÄ, í×ÈÝÒÄLOS±±ççí»´Ö±±ííí. ±·; çÈÖí×±ÈXÄÖON±í¹Ö±È±í±È±±±±æ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYD¥Ó¥È¥í¥Á¥~¾ÒÄÖ±·±Ä±±·±ç;ç°È²¼±í°ÈÄ, ¼½çç±±¾Ä¹Ö±¹±±;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÄ.
0033 C. *****
0034 C. ç" RESTART;ÈPT1;È±·±ççç¼í¹ççí;ç°È²¼±í°ÈÄ¹Ö±±±·;çDCBC-150±±çÈ±±;ç
0035 C.
0036 . C. ;ãPT1°ÈÄ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ã¥ç¥Ó¥È¥ÈÄÜÄ°;ÈÄ·Ä°²óÈ;È. á±í°ÈÄ. °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÄ, ±·¼«È°Ä±±±±±·±ç;á;ç°È²¼±±¾Ä¹Ö±¹±±;ç
0055 C. ¥ç¥Ó¥È¥ÈÄÜÄ°±Ä·Ä°²óÈ±·±ç¼±í¹ççí°í»±±±±±ÇÄÖ±Ä;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÄ.
0059 C. *****
0060 C. ç" RESTART;ÈPT2;È±·±ççç¼í¹ççí;ç°È²¼±í°ÈÄ¹Ö±±±·;çDCBC-151±±çÈ±±;ç
0061 C.
0062 . C. ;ãPT2°ÈÄ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ã¥ç¥Ó¥È¥ÈÄÜÄ°;ÈÄ·Ä°²óÈ;È. á±í°ÈÄ. °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, Ä±»È;çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, Ä±»È;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```



```
0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;|Y#E;|YAYOYx
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;|Y#E;ã
0103 . S. OP op-066:OP
0104 . C. ( )
0105 . S. OG og-066:OG
0106 . C. ( )
0107 . C.
0108 . C. ;ãNMOG&OPfôeYAYOYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 . C.        çç[HK1_DMP_TOP_ADRS_1]           EQ      40
0113 . C.        çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0114 . C.        çç[HK1_DMP_BLOCK_NUM]           EQ     127
0115 . C.        çç[HK1_DMP_REPEAT_NUM]          EQ      0
0116 . C.        çç[HK1_DMA_DMP_PIM]             EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 . C.        çç[HK1_PKT_FORM_NO]             EQ      7
0120 . C.        çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121 . C.        çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0122 . C.        çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0123 . C.        çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0124 . C. YAYOYx½ªî»oð³îç§
0125 . C.        çç[HK1_DMP_CHK_FLG]             EQ     NON
0126 . C. RAM ID=NMOGâî½E¹ç•ë²îOKoð³îç§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 . C.        çç[HK1_DMP_TOP_ADRS_1]           EQ     41
0132 . C.        çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0133 . C.        çç[HK1_DMP_BLOCK_NUM]           EQ     127
0134 . C.        çç[HK1_DMP_REPEAT_NUM]          EQ      0
0135 . C.        çç[HK1_DMA_DMP_PIM]             EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 . C.        çç[HK1_PKT_FORM_NO]             EQ      7
0139 . C.        çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140 . C.        çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0141 . C.        çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0142 . C.        çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0143 . C. YAYOYx½ªî»oð³îç§
0144 . C.        çç[HK1_DMP_CHK_FLG]             EQ     NON
0145 . C. RAM ID=NMOGâî½E¹ç•ë²îOKoð³îç§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 . C.        çç[HK1_DMP_TOP_ADRS_1]           EQ     42
0151 . C.        çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0152 . C.        çç[HK1_DMP_BLOCK_NUM]           EQ     65
0153 . C.        çç[HK1_DMP_REPEAT_NUM]          EQ      0
0154 . C.        çç[HK1_DMA_DMP_PIM]             EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 . C.        çç[HK1_PKT_FORM_NO]             EQ      7
0158 . C.        çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159 . C.        çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0160 . C.        çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0161 . C.        çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0162 . C. YAYOYx½ªî»oð³îç§
0163 . C.        çç[HK1_DMP_CHK_FLG]             EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OPâî½E¹ç•ë²îOKoð³îç§
0165 . C.
0166 . C. ***** oE²¼oî½Ã'¶Á°oEÉ¬o°Ä÷;¿@ (½âµ-YAYOYx½ê½çoðÄÖÃæoç½ªª°oE½î¹ççççâ) *****
0167 . C. DHUYâ;|Y#E;E½Y½, Yî;|Y#E;Eoðîãª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 . C.        çç[HK1_PKT_FORM_NO]             EQ      2
0171 . C.        çç[HK1_PKT_GEN_TIME]            EQ     0.5S
0172 . C.        çç[HK1_S_TLM_BIT_RATE]          EQ     32K
0173 . C.        çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE |§ OPOG UPLOADo¬Á÷¿@NGuî½î¹ç; ç°E²¼oîTI-CMDÁ÷¿@î½Å¹Ôo•oEoð³çççç; f
0180 . C.        çç[SEToEDUMPoî½ª±oîY½¹ç¹Çoîª³çççç; f
0181 . C.
0182 . C. TIY³YþYöY#Eoð³îçççç(UT)
0183 +. TI 2011-05-12 10:39:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C.        çç[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
0186 . C.
0187 +. TI 2011-05-12 10:39:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C.        çç[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
0190 . C.
0191 +. TI 2011-05-12 10:39:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C.        çç[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
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0194 C.
0195 +. TI 2011-05-12 10:43:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□îÄë%îíñ□îŷÁ¥§ŷÁŷ-¹àîŭ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]             EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]             EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIíŷ°èŷÁŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]           EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ŷÁŷÖŷ×½ªî»□ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□îŷ°èŷÁŷÖŷ×ªªî»□ò³îç§
0229 C.
0230 C. DHUŷª;¼ŷÉ;Ê¼ŷ¼.ŷî;¼ŷÉ;Ê□òîª□
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-05-12 10:43:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-05-12 10:43:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-05-12 10:43:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-05-12 10:43:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîñî»ö¼ŷ□èÄÐ□¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÁŷÉŷŷŷÉŷªŷçŷèè¼□□¼ª»Ū□¹è)
0277 C. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñ□èŷ°□¹èDCBC•x²è *****
0282 C. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÁŷ§ŷÁŷ-¼ª»Ū;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-068 2011-05-12 10:16:23 217 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY^-¼Ä»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YBÝÓYÉÄ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÄò•µ°È»í×ÁÇòíYÇYÁY×Yí;¼YÉ;ÈÈÈ¼µ•ííÈ;ÈòÈ¼°ÇòÄ•ò¿¼í¹ÇòÍ;ÇÄ®, ùò¹òÈòÈòÇÄ+¿@ò•òÈòòòòÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@µ;ON
0016 C. *****
0017 C. Ç " °ÄÄ, í×ÈYòäLOSòÈÇòÍ»P´Òòò¹íí, ò.; ÇÈÔÍ×òÈXÁÓONòí¹ÒòÈòíòÈòòò³òÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.          ÇÇ[HK1_XPA_ON/OFF]          EQ      ON
0025 C.          ÇÇ[HK1_XPA_PWR_HI/LO]        EQ      HI
0026 C.          ÇÇ[HK1_XMOD_ON/OFF]          EQ      ON
0027 C.          ÇÇ[HK1_XMOD_QPSK/PM]          EQ      QPSK
0028 C.
0029 . C. XÝDÝÓYÉYíYÁY^-¾ÓÄÖò-òÄÄêò•ò¿òé; Ç°È²¼òí°ÄÄ, ¼ê¼Çòò¼Ä¹Òò¹òé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÄÄ,
0033 C. *****
0034 C. Ç " RESTART;ÈPT1;Èò•ò¿òò¾¼í¹ÇòÍ; Ç°È²¼òí¼Ä¹Òò»ò°; ÇDCBC-150òØ¿Èòä;f
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC         (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.          ÇÇ[HK1_REP_PT_1/2]          EQ      PT1  (¼Ä¹Ò, ;¼Ú)
0043 C.          ÇÇ[HK1_REP_STA/STP]         EQ      START (¼Ä¹Ò, ;¼Ú)
0044 C.          ÇÇ[HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÈÄÙÄØ;ÈÄ•Ä°²òÈò;È, áòí°ÄÄ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.          ÇÇ[HK1_REP_PT_1/2]          EQ      PT1  (¼Ä¹Ò, ;¼Ú)
0050 C.          ÇÇ[HK1_REP_STA/STP]         EQ      START (¼Ä¹Ò, ;¼Ú)
0051 C.          ÇÇ[HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, ò-¼«È°Ää»ßò•ò¿, á; Ç°È²¼òò¼Ä¹Òò¹òé;f
0055 C. YÇYÓYÉYÈÄÙÄØòÄÄ•Ä°²òÈòò-¼áòò¾¼í¹ÇòÍ´°í»ò¹òÈòÈòÇÄÒòÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÄÄ,
0059 C. *****
0060 C. Ç " RESTART;ÈPT2;Èò•ò¿òò¾¼í¹ÇòÍ; Ç°È²¼òí¼Ä¹Òò»ò°; ÇDCBC-151òØ¿Èòä;f
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC         (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.          ÇÇ[HK1_REP_PT_1/2]          EQ      PT2  (¼Ä¹Ò, ;¼Ú)
0069 C.          ÇÇ[HK1_REP_STA/STP]         EQ      START (¼Ä¹Ò, ;¼Ú)
0070 C.          ÇÇ[HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÈÄÙÄØ;ÈÄ•Ä°²òÈò;È, áòí°ÄÄ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.          ÇÇ[HK1_REP_PT_1/2]          EQ      PT2  (¼Ä¹Ò, ;¼Ú)
0076 C.          ÇÇ[HK1_REP_STA/STP]         EQ      START (¼Ä¹Ò, ;¼Ú)
0077 C.          ÇÇ[HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÄÄ, Ää»ß; ÇXÁ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÄÄ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.          ÇÇ[HK1_REP_STA/STP]          EQ      STOP
0087 C.          ÇÇ[HK1_S_VC4_ON/OFF]         EQ      OFF
0088 C.          ÇÇ[HK1_X_VC4_ON/OFF]         EQ      OFF
0089 C.
0090 . C. ;ãXÁ+¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.          ÇÇ[HK1_XMOD_ON/OFF]          EQ      OFF
0095 C.          ÇÇ[HK1_XPA_ON/OFF]           EQ      OFF
```

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-268:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2011-05-12 10:43:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 08 08)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 60 20 18)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0b a0 80 18 20)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0f 80 80 06 06)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 10 80 80 08 08)
0174 + DC 07-F0 MDP_XRT_FLD_ENA
0175 BC (d8)
0176 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0177 BC (c8)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0181 BC (c4 14)
0182 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0183 BC (c5 08)
0184 . C. ----- Success Verify ? OK / NG ____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0192 BC (c0)
0193 +. TI 2011-05-12 10:43:02.0

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```
0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 +. TI 2011-05-12 10:43:04.0
0197 DC 07-F0 MDP_XRT_CTRL_AUTO
0198 BC (c0)
0199 . C. ----- Success Verify ? OK / NG ____
0200 C.
0201 C. ***** XRT END *****
0202 C.
0203 . C. ***** MDP `úÃîñî»ò¼ŷñÊÂðñ¹ñèDCBC•x²è *****
0204 C. (¼á°îŷÓŷÃŷÈŷŦŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0205 . S. DC-BC dcbc-402:DCBC
0206 (MDP_known_event)
0207 C.
0208 C.
0209 . C. ***** ŷDŷ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0210 . S. DC-BC dcbc-153:DCBC
0211 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0212 C.
0213 C.
0214 . C. ¡ãLOSŷÁŷSŷÃŷ´¼Ã»Û¿ä
0215 C.
0216 . C. ***** LOS *****
0217 C.
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May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 1/7

*** OP Sequence for XRT ***

2011/05/12	10:53:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	10:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00	
2011/05/12	10:54:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00	00 00	
2011/05/12	10:54:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/05/12	10:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/05/12	10:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/12	10:56:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2011/05/12	10:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/12	12:05:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	12:05:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/12	12:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/12	12:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/12	12:22:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00	ea 25	
2011/05/12	12:28:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	12:31:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2011/05/12	12:31:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/05/12	12:31:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/05/12	12:31:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/05/12	12:31:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/12	12:31:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/12	12:31:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2011/05/12	12:31:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/05/12	12:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/12	13:44:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	13:44:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/12	13:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/12	13:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/12	14:01:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00	d5 cb	
2011/05/12	14:13:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	14:16:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2011/05/12	14:16:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/05/12	14:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/05/12	14:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/05/12	14:16:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/05/12	14:16:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/12	14:16:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2011/05/12	14:16:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/05/12	14:17:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/05/12	15:22:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	15:22:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/05/12	15:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/05/12	15:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/05/12	15:39:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 00	c4 42	
2011/05/12	16:04:24.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/05/12	16:06:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2011/05/12	16:07:16.0	XRT_FLD_ENA_411_OG [0x19b]					

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 2/7

2011/05/12	16:07:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/12	16:07:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/12	16:07:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/12	16:07:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	16:07:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/12	16:07:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/12	16:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/12	17:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	17:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	17:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/12	17:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/12	17:18:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00	b6	da
2011/05/12	17:41:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	17:43:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/12	17:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/12	17:44:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/12	17:44:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/12	17:44:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/12	17:44:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	17:44:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/12	17:44:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/12	17:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/12	18:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	18:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	18:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/12	18:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/12	18:56:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	00	00	ae	67
2011/05/12	19:17:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	19:20:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/12	19:20:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/12	19:20:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/12	19:20:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/12	19:20:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/12	19:20:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	19:20:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/12	19:20:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/12	19:21:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/12	20:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	20:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	20:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/12	20:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/12	20:35:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	15	db	00	00
2011/05/12	20:55:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	20:57:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/12	20:58:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/12	20:58:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 3/7

2011/05/12	20:58:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/12	20:58:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/12	20:58:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	20:58:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/05/12	20:58:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/12	20:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/12	21:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	21:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	21:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/12	21:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/12	22:13:00.0	AOCs_Or-e-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 2a 35 00 00				
2011/05/12	22:31:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	22:33:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/12	22:34:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/12	22:34:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/12	22:34:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/12	22:34:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/12	22:34:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	22:34:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/05/12	22:34:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/12	22:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/12	23:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/12	23:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/12	23:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/12	23:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	00:00:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	00:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	00:04:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	00:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/05/13	00:05:00.0	AOCs_Or-e-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/05/13	00:05:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/13	00:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/13	00:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	00:07:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/05/13	00:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	01:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	01:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	01:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	01:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	01:30:00.0	AOCs_Or-e-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 3b be 00 00				
2011/05/13	01:36:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	01:39:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/13	01:39:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/13	01:39:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/13	01:39:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/13	01:39:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	01:39:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 4/7

2011/05/13	01:39:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/13	01:39:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/13	01:40:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	02:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	02:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	02:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	02:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	03:09:00.0	AOCs_Orе-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	49	26	00	00
2011/05/13	03:15:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	03:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/13	03:18:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/13	03:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/13	03:18:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/13	03:18:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	03:18:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	03:18:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/13	03:18:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/13	03:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	04:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	04:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	04:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	04:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	04:47:00.5	AOCs_Orе-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	51	99	00	00
2011/05/13	04:53:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	04:56:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/13	04:56:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/13	04:56:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/13	04:56:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/13	04:56:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	04:56:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	04:56:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/05/13	04:56:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/05/13	04:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	05:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	05:59:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/13	06:00:00.0	AOCs_Orе-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	ac	00	00	00
2011/05/13	06:00:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2011/05/13	06:00:18.0	XRT_FLD_DIS_423_OG [0x1a7]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/13	06:00:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/13	06:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	06:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	07:59:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/05/13	08:00:00.0	AOCs_Orе-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	00	00	54	00
2011/05/13	08:00:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2011/05/13	08:00:18.0	XRT_FLD_DIS_423_OG [0x1a7]							

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 5/7

2011/05/13	08:00:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/13	08:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	08:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	10:00:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	03 00 00 00 00				
2011/05/13	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/13	10:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/13	10:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/13	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/13	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	10:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/05/13	10:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/13	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	11:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	11:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	11:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	11:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	11:30:00.0	XRT_Custom_418_OG [0x1a2]								
2011/05/13	11:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	11:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	11:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/05/13	11:45:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/05/13	11:45:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/13	11:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/13	11:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	11:47:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/05/13	11:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	11:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	11:55:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	03 00 00 00 00				
2011/05/13	11:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/13	11:57:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/05/13	11:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/05/13	11:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/05/13	11:57:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/13	11:57:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	11:57:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/05/13	11:57:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/05/13	11:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	12:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	12:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	12:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	12:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	13:08:30.0	XRT_Custom_418_OG [0x1a2]								
2011/05/13	13:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	14:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	14:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 6/7

2011/05/13	14:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	14:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	14:56:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	14:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	15:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	15:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	15:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	16:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	16:43:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	16:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	17:38:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	17:38:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	17:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	17:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	18:20:00.5	XRT_Custom_418_OG [0x1a2]							
2011/05/13	18:21:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	19:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	19:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	19:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	19:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	19:56:30.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	19:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	20:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	20:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	20:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	20:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	21:34:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	21:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	22:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	22:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/13	22:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/13	22:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/13	23:09:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/13	23:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/13	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/13	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/05/14	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/05/14	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/05/14	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/05/14	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/05/14	00:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/05/14	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/14	00:10:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/05/14	00:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/14	00:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/14	00:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/14	00:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/14	00:35:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/14	00:38:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/05/14	00:38:46.0	XRT_FLD_ENA_411_OG [0x19b]							

May 12, 11 10:16

XRT_OGLIST_0921.chk

Page 7/7

2011/05/14	00:38:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/14	00:38:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/14	00:38:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/14	00:38:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	00:38:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2011/05/14	00:38:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/14	00:39:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	01:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	01:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	01:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	01:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	02:16:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/14	02:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	03:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	03:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	03:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	03:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	03:55:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/14	03:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	04:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	04:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	04:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	04:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	05:33:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/14	05:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	06:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	06:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	06:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	06:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	07:12:00.0	XRT_Custom_418_OG [0x1a2]				
2011/05/14	07:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	08:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	08:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	08:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	08:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	08:50:30.0	XRT_Custom_418_OG [0x1a2]				
2011/05/14	08:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	09:56:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	09:56:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	09:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	09:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	10:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00