

Period: 2011/05/14 10:20:00 - 2011/05/19 09:35:00

XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad														
Term		Pointing (x, y)					Comment							
05/14 10:43:00 - 05/14 23:10:30		Track (699.9, 326.3) @ 05/14 10:40:00					Track AR 11210							
05/15 01:19:06 - 05/15 11:59:54		Track (770.7, 320.7) @ 05/15 01:00:00					Track AR 11210							
05/15 12:13:00 - 05/15 15:35:00		Track (815.9, 316.2) @ 05/15 12:10:00					Track AR 11210							
05/15 18:00:06 - 05/15 23:47:30		Track (834.2, 314.0) @ 05/15 17:30:00					Track AR 11210							
05/16 01:56:36 - 05/16 11:59:54		Fixed (857.6, 310.7)					Obs AR 11210							
PROG= 03 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 19		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Seqn=100		4-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 6		18-time(s)		2.0sec										
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	20.0sec
Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)		Q=95	2	1	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	2	1	20.0sec
Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)		Q=95	2	2	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	2	2	20.0sec
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)		Q=95	3	3	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	3	20.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #187A: Synoptic Q95 2x2 - Al/mesh(44/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + G-band(16)														
Term		Pointing (x, y)				Comment								
05/14 23:43:00 - 05/14 23:50:00		Fixed (0.0, 0.0)				synoptic, SOT flat field (1 orbit)								
05/16 00:18:00 - 05/16 00:24:20		Fixed (0.0, 0.0)				synoptic, SOT flat field (1 orbit)								
05/17 00:03:00 - 05/17 00:09:54		Fixed (0.0, 0.0)				synoptic								
PROG= 10 1-time(s)														
└ Subr= 1 1-time(s) 12.0sec														
└ Seqn= 86 1-time(s) 4.0sec														
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 5 1-time(s) 2.0sec														
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 37 1-time(s) 4.0sec														

	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
05/14 23:53:06 - 05/15 00:49:00	Fixed (0.0, 0.0)	synoptic, SOT flat field (1 orbit)
05/16 00:27:26 - 05/16 01:26:00	Fixed (0.0, 0.0)	synoptic, SOT flat field (1 orbit)

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	120.0sec											
Seqn= 33	1-time(s)	4.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad

Term	Pointing (x, y)	Comment
05/15 16:23:36 - 05/15 17:13:30	Track (-56.7, -229.8) @ 05/15 16:00:00	HOP181
05/16 17:00:06 - 05/16 17:50:30	Track (156.6, -232.1) @ 05/16 16:30:00	HOP181

PROG= 20 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec											
Seqn= 19	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41	4-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1	1-time(s)	2.0sec											
Seqn= 13	55-time(s)	22.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1886: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 512x512 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
05/16 12:13:00 - 05/16 16:12:00	Track (-93.6, 349.6) @ 05/16 12:10:00	Track AR at NE (no. 11193 in previous rotation)
05/16 18:36:36 - 05/16 23:59:54	Track (-43.7, 349.4) @ 05/16 18:00:00	Track AR at NE (no. 11193 in pre rotation)
05/17 00:13:00 - 05/19 09:35:00	Track (9.2, 349.0) @ 05/17 00:10:00	Track AR at NE (no. 11193 in pre rotation)

PROG= 12 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
Seqn= 19	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 49	4-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 6	18-time(s)	2.0sec											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	1	20.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	2	20.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	20.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
05/14 10:43:00 - 05/14 23:10:30	Track (699.9, 326.3) @ 05/14 10:40:00	Track AR 11210
05/14 23:53:06 - 05/15 00:49:00	Fixed (0.0, 0.0)	synoptic, SOT flat field (1 orbit)
05/15 01:19:06 - 05/15 11:59:54	Track (770.7, 320.7) @ 05/15 01:00:00	Track AR 11210
05/15 12:13:00 - 05/15 15:35:00	Track (815.9, 316.2) @ 05/15 12:10:00	Track AR 11210
05/15 16:23:36 - 05/15 17:13:30	Track (-56.7, -229.8) @ 05/15 16:00:00	HOP181
05/15 18:00:06 - 05/15 23:47:30	Track (834.2, 314.0) @ 05/15 17:30:00	Track AR 11210
05/16 00:27:26 - 05/16 01:26:00	Fixed (0.0, 0.0)	synoptic, SOT flat field (1 orbit)
05/16 01:56:36 - 05/16 11:59:54	Fixed (857.6, 310.7)	Obs AR 11210
05/16 12:13:00 - 05/16 16:12:00	Track (-93.6, 349.6) @ 05/16 12:10:00	Track AR at NE (no. 11193 in previous rotation)
05/16 17:00:06 - 05/16 17:50:30	Track (156.6, -232.1) @ 05/16 16:30:00	HOP181

PROG= 08	Subr= 1		1-time(s)		30-time(s)		20.0sec											
	Seqn= 87		1-time(s)		2.0sec													
			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
			Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
	Seqn= 60		1-time(s)		2.0sec													
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
	Subr= 2		1-time(s)		2.0sec													
	Seqn= 90		1-time(s)		2.0sec													
			Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
			Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
			Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec		
	Subr= 3		30-time(s)		60.0sec													
	Seqn= 87		1-time(s)		2.0sec													
			Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
			Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
	Seqn= 88		1-time(s)		2.0sec													
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
	Subr= 2		1-time(s)		2.0sec													
Seqn= 90		1-time(s)		2.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec			
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec			
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec														
Seqn= 87		1-time(s)		2.0sec														
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec			
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec			
Seqn= 88		1-time(s)		2.0sec														
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec			
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec			
Subr= 2		1-time(s)		2.0sec														
Seqn= 90		1-time(s)		2.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec			
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec			
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec														
Seqn= 87		1-time(s)		2.0sec														
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec			
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec			
Seqn= 88		1-time(s)		2.0sec														
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec			
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec			

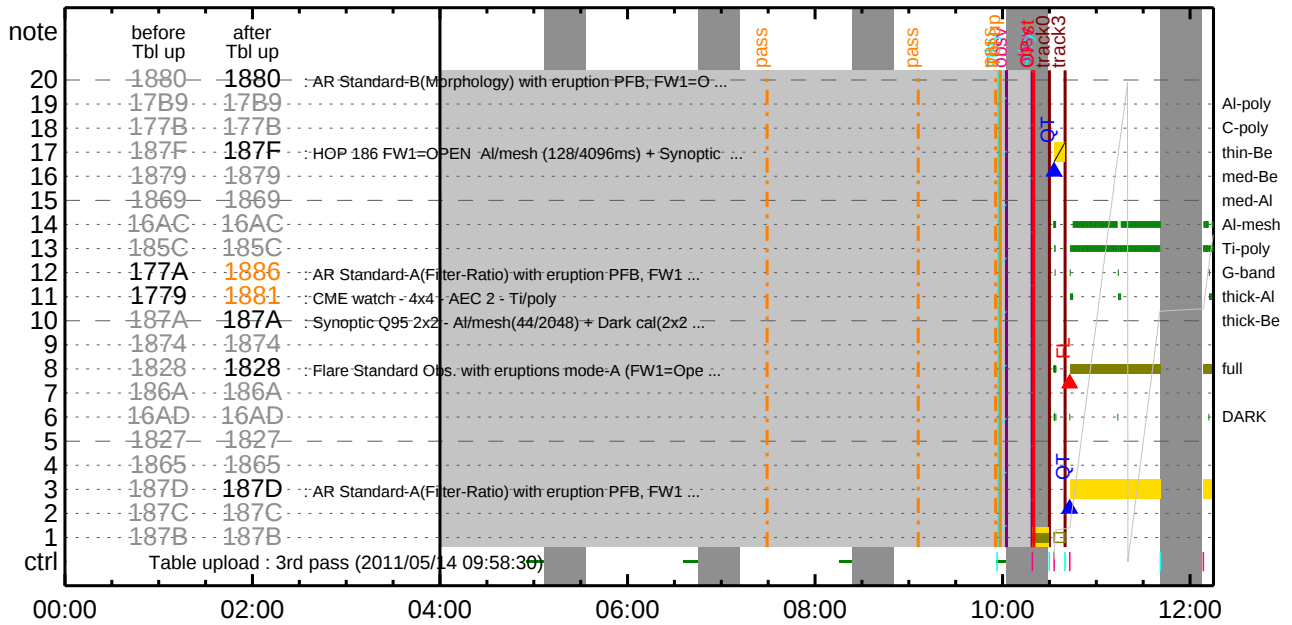
✱ ✱ ✱ ✱ ✱

✱ ✱ ✱ ✱ ✱

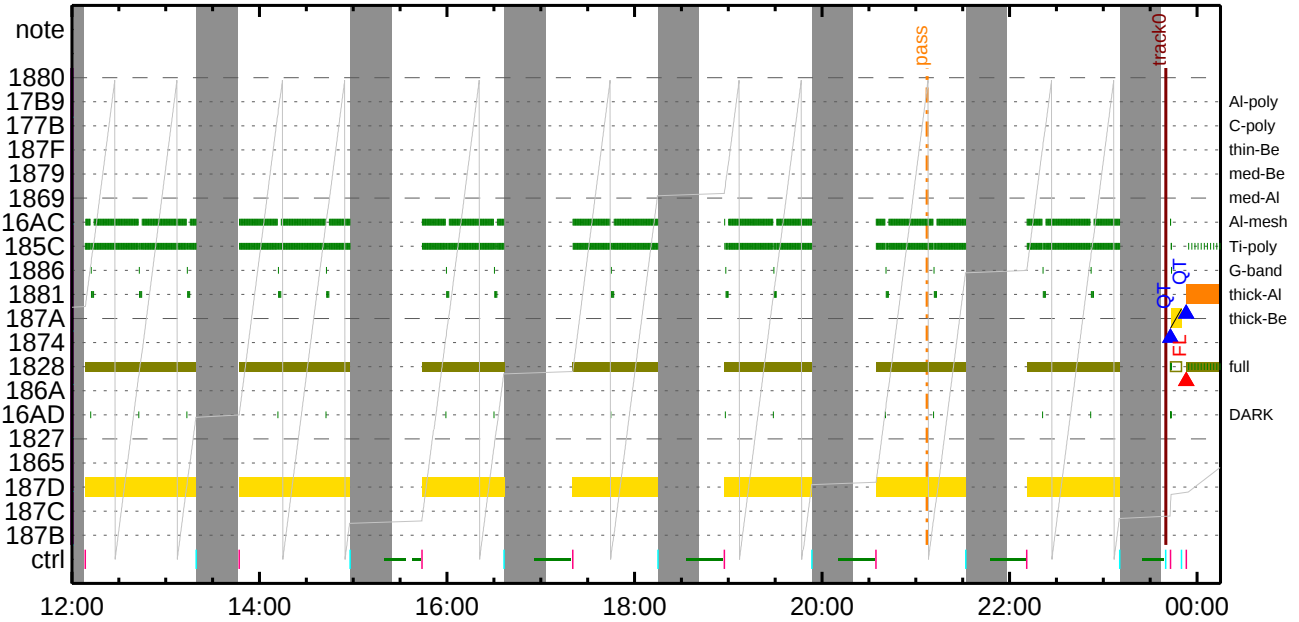
✱ ✱ ✱ ✱ ✱

FLD Patrol												
Term		Pointing (x, y)					Comment					
05/14 10:42:46 - 05/14 23:40:16	Track (	699.9,	326.3)	@ 05/14 10:40:00		Track AR 11210						
05/14 23:52:52 - 05/15 12:00:16	Fixed (	0.0,	0.0)			synoptic, SOT flat field (1 orbit)						
05/15 12:12:46 - 05/16 00:15:16	Track (	815.9,	316.2)	@ 05/15 12:10:00		Track AR 11210						
05/16 00:27:12 - 05/16 12:00:16	Fixed (	0.0,	0.0)			synoptic, SOT flat field (1 orbit)						
05/16 12:12:46 - 05/17 00:00:16	Track (	-93.6,	349.6)	@ 05/16 12:10:00		Track AR at NE (no. 11193 in previous rotation)						
05/17 00:12:46 - 05/19 09:35:00	Track (	9.2,	349.0)	@ 05/17 00:10:00		Track AR at NE (no. 11193 in pre rotation)						
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exo.	CCD	Bin		ROI: size (center)	Comp.	AEC Buffer Interval

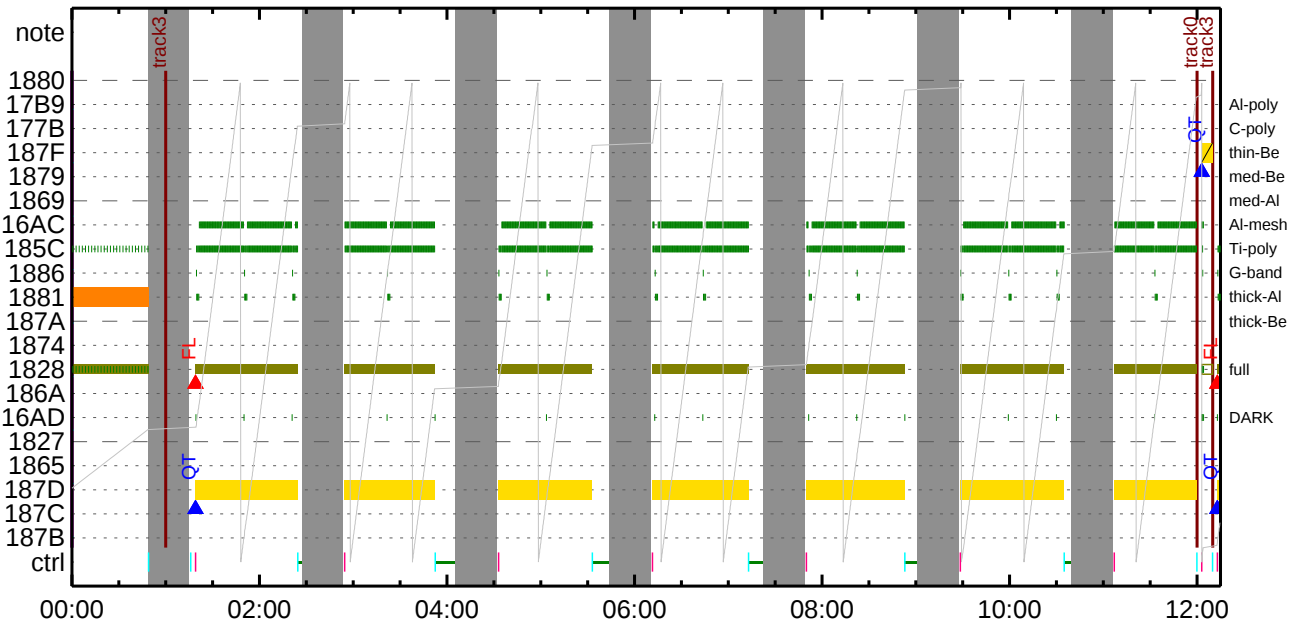
CMDI #0927 2011/05/14



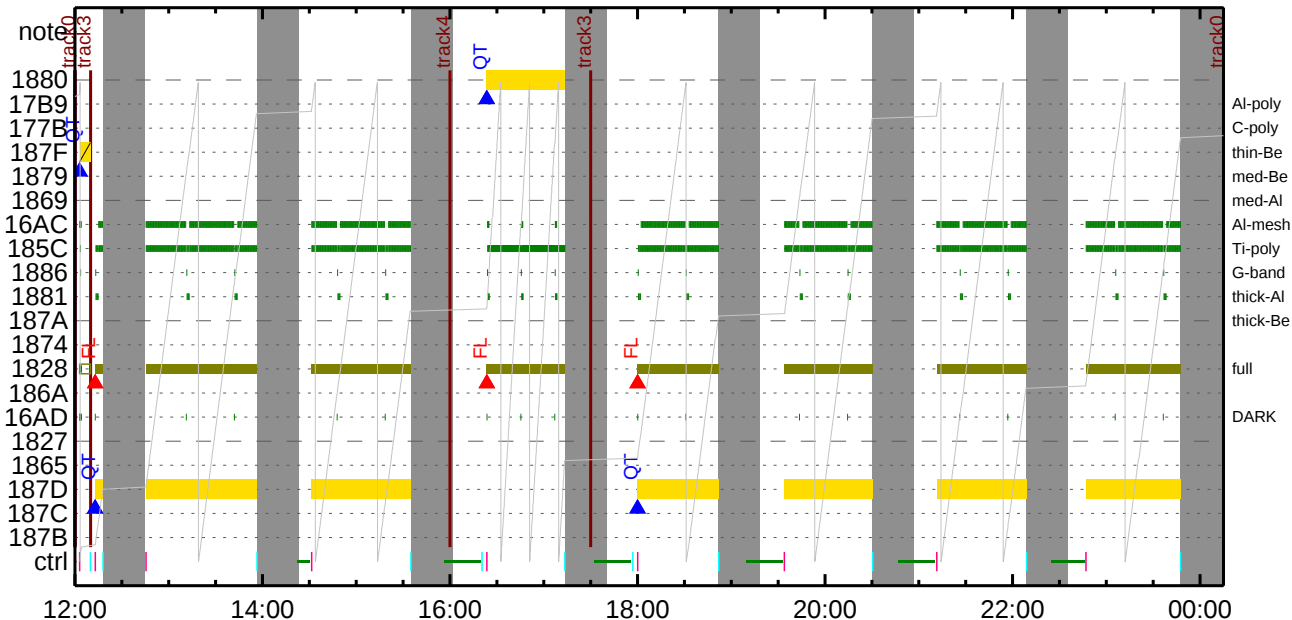
CMDI #0927 2011/05/14



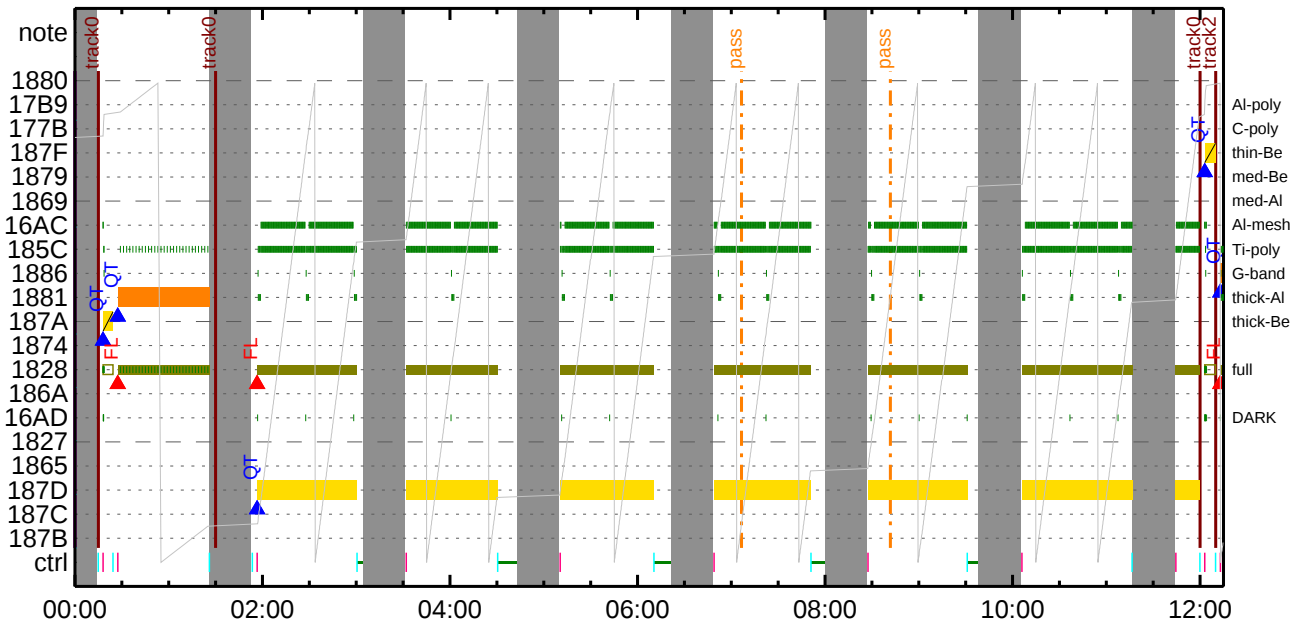
CMDI #0927 2011/05/15



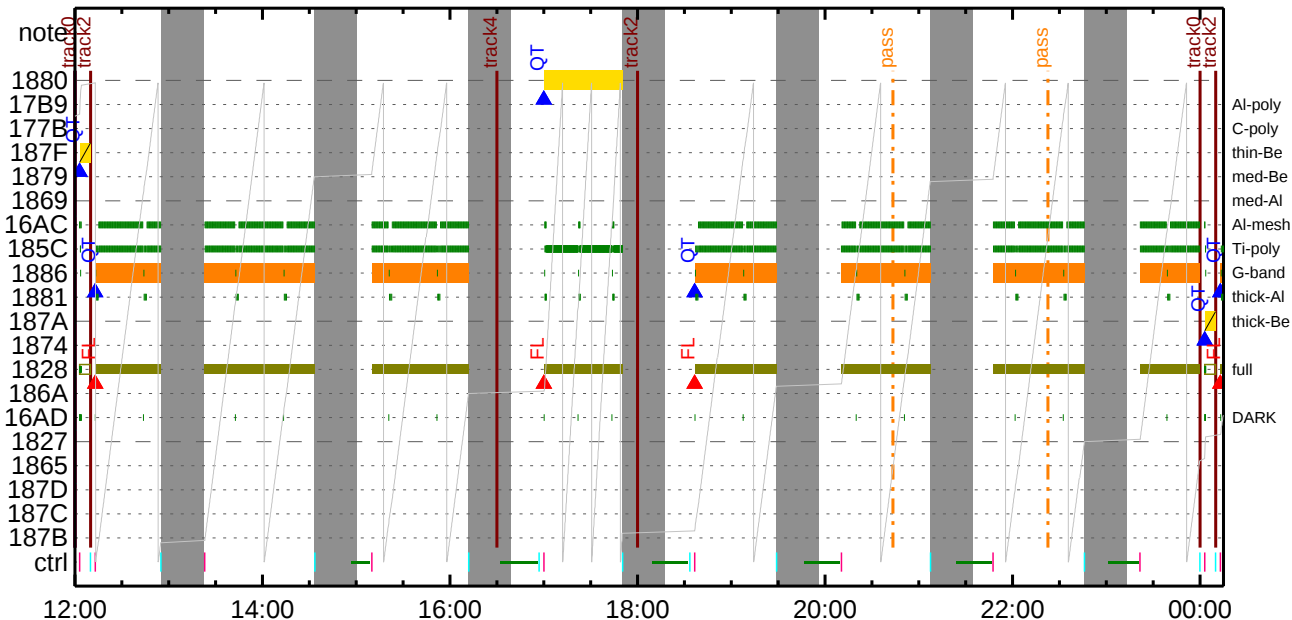
CMDI #0927 2011/05/15



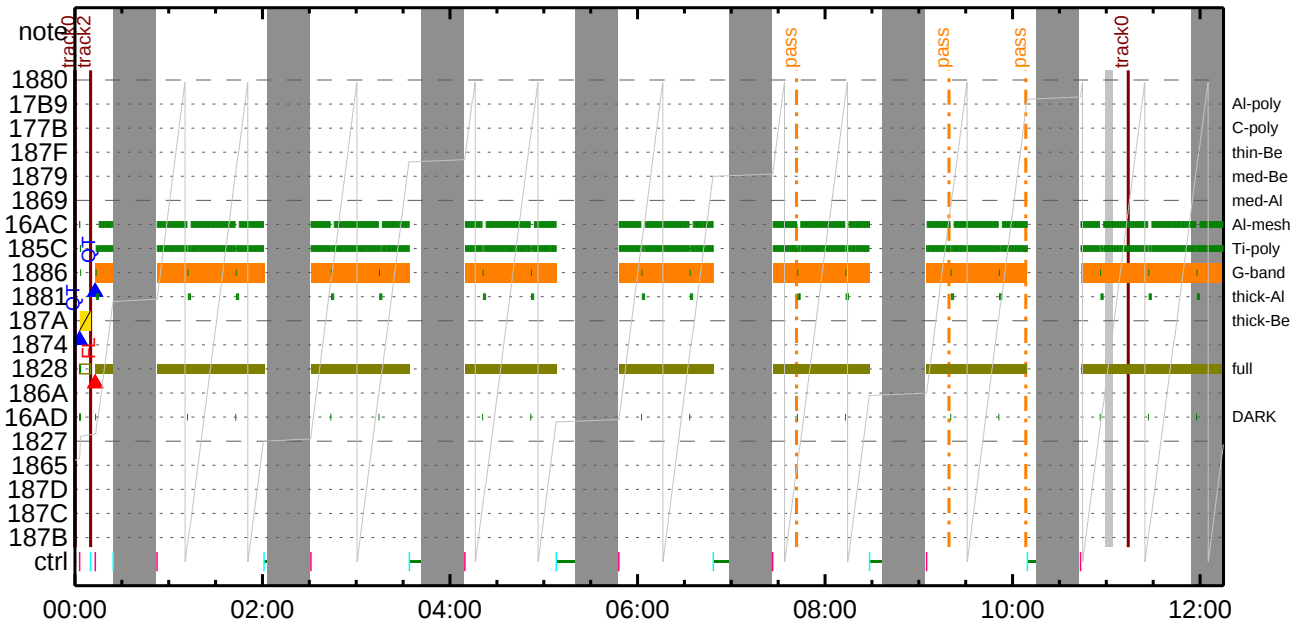
CMDI #0927 2011/05/16



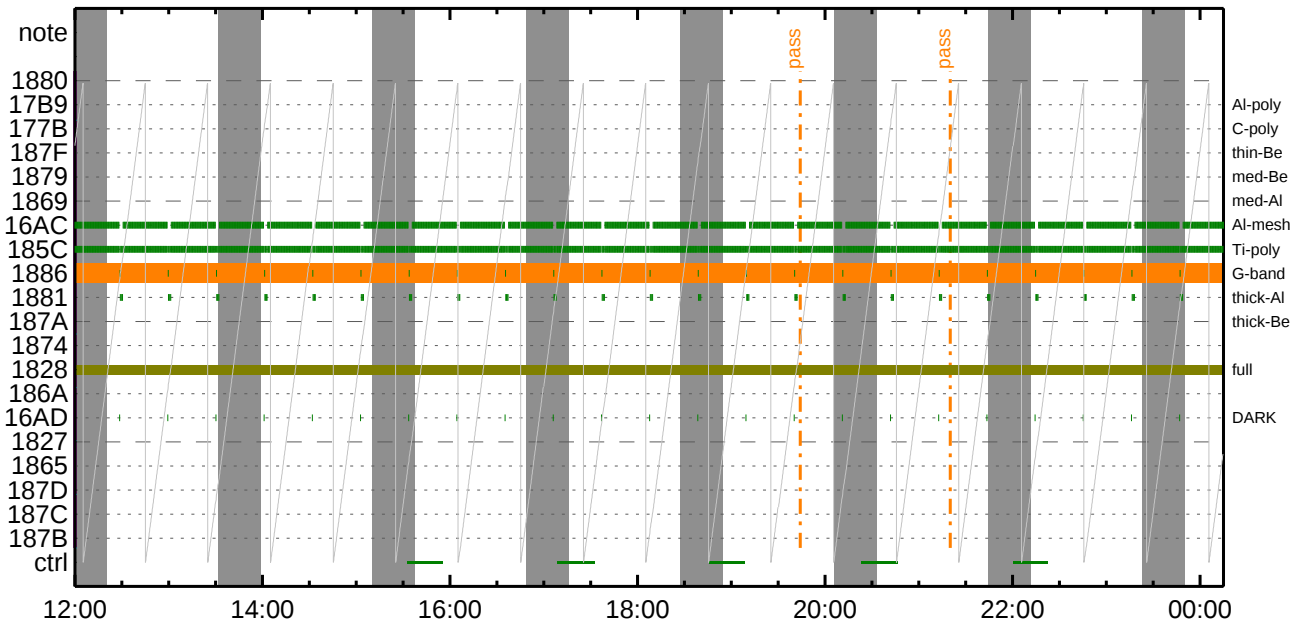
CMDI #0927 2011/05/16



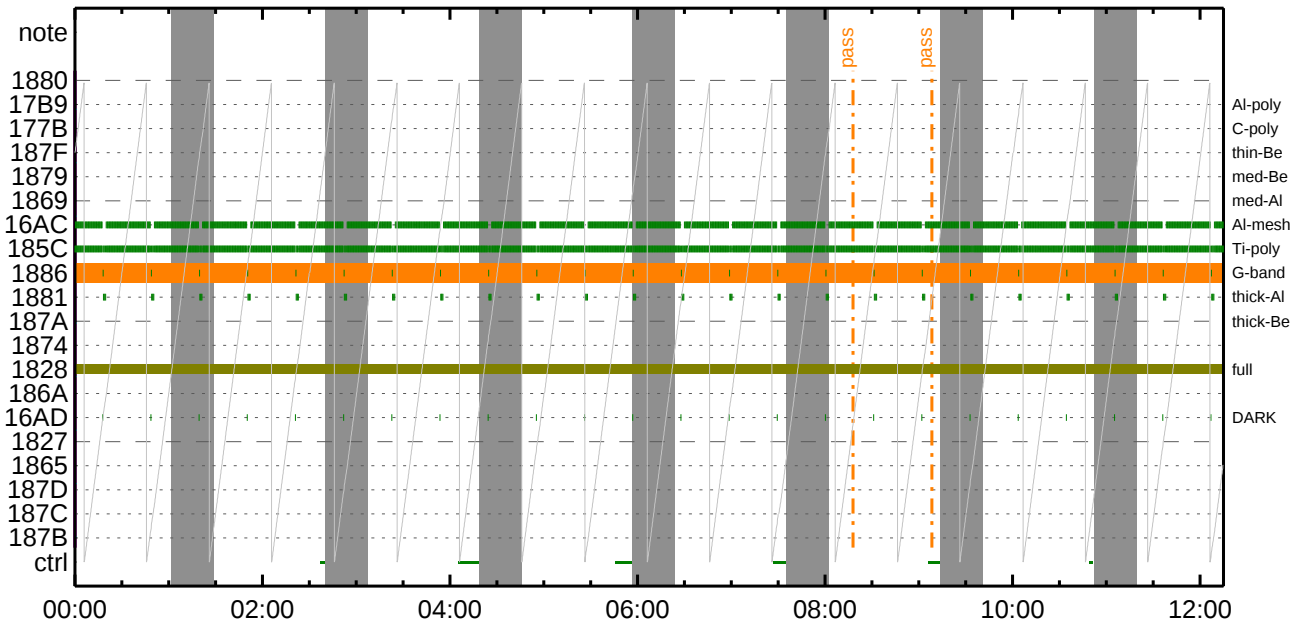
CMDI #0927 2011/05/17



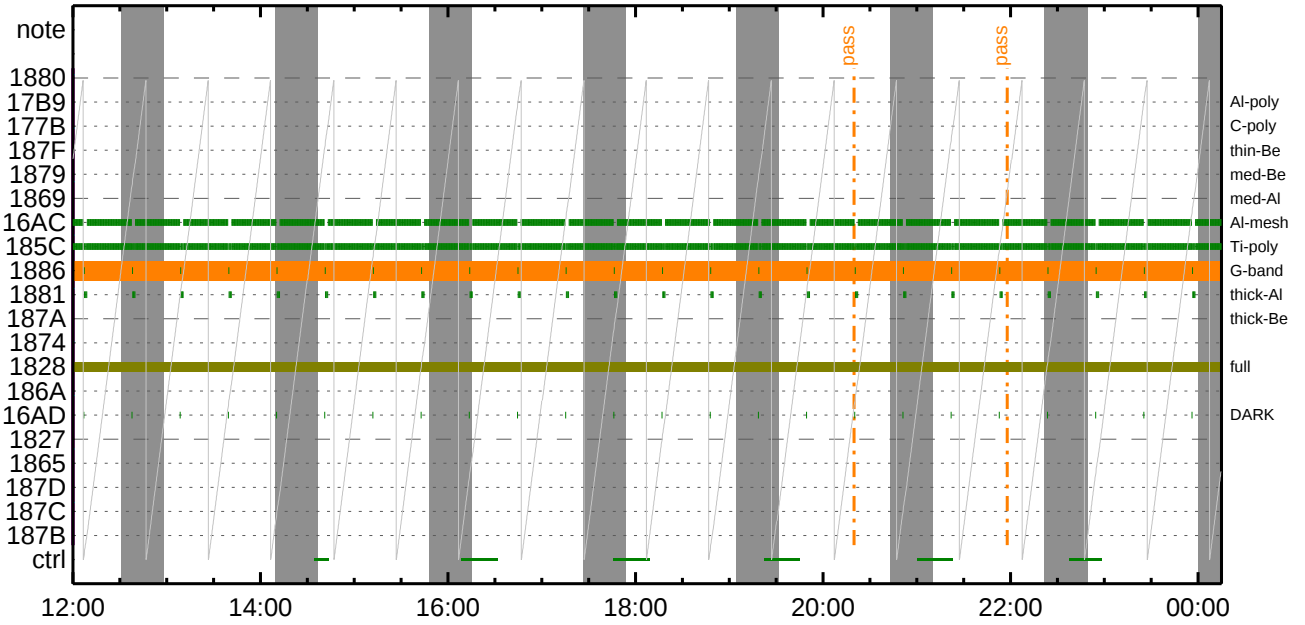
CMDI #0927 2011/05/17



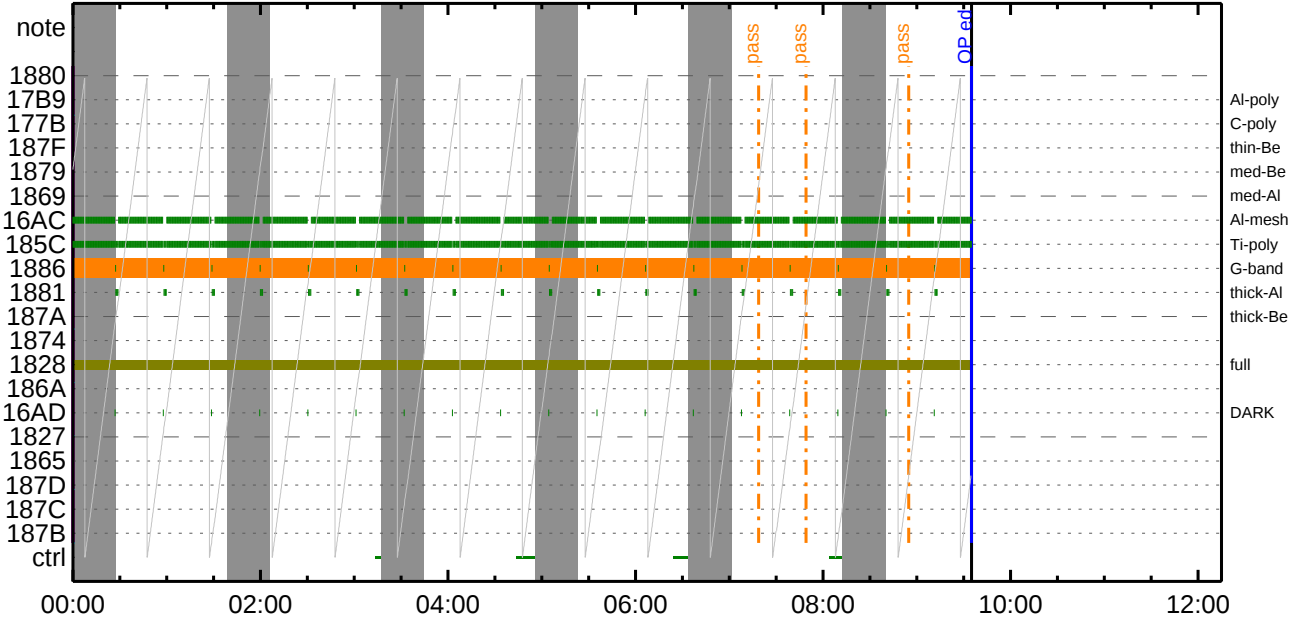
CMDI #0927 2011/05/18



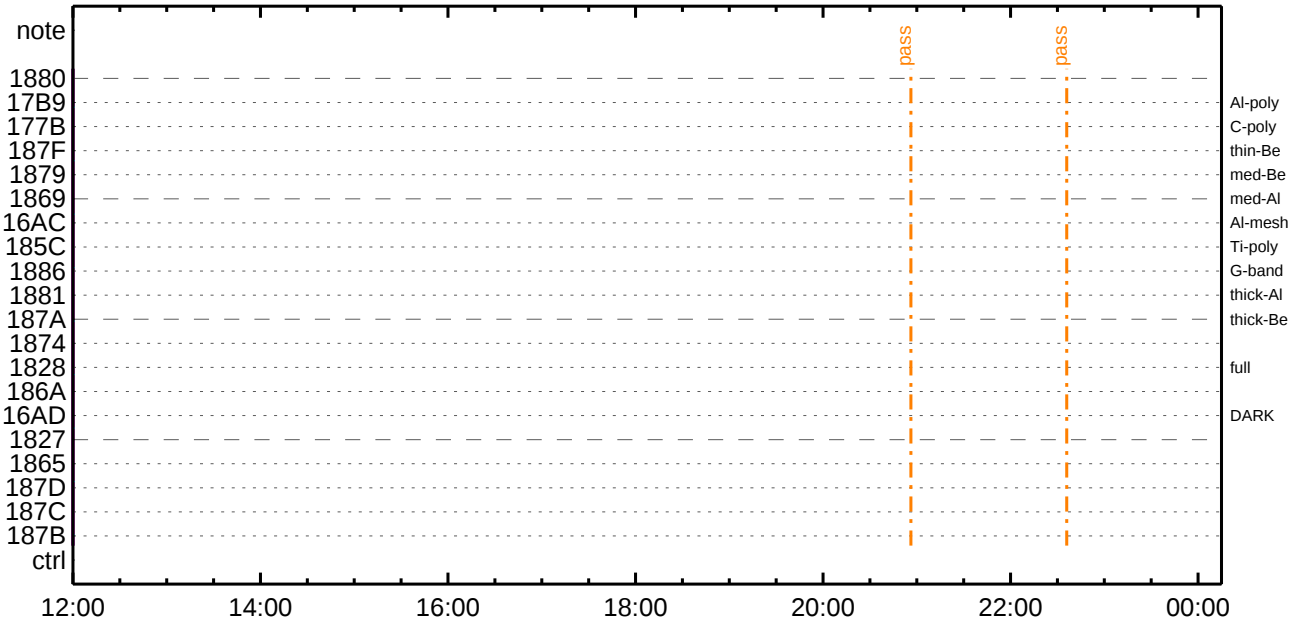
CMDI #0927 2011/05/18



CMDI #0927 2011/05/19



CMDI #0927 2011/05/19



(a) Spacecraft Operation Procedure (real-commands)

```
main-070 2011-05-14 13:31:19 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¿İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¿İ»p´Ŏ¤Ŏ¹İĒ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İ¹Ŏ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥É¥İ¥Á¥¼ŎĀŎ¤¬°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤¬¼«Æ°ĀĀ»İ¤•¤¿, ĀjÇ°Ē²¼¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎ¤ĀĀ•Ā²ŎĒŎ¤¬ŎĀ¤¼¹Ç¿İ °İ»¤¹¤Ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.  
0097 C.  
0098 . C. *****  
0099 C. OP/OG¥¡¼¥É;| ¥À¥ó×  
0100 C. *****  
0101 C.  
0102 C. ¡ãOP/OG¥¡¼¥É;ä  
0103 S. OP op-070:OP  
0104 (  
0105 S. OG og-070:OG  
0106 )  
0107 C.  
0108 C. ¡ãNMOG&OPIÎ°è¥À¥ó×;ä  
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0111 BC (20 00 7f 01 02)  
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40  
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0117 BC (07 0b f8)  
0118 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0119 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0120 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0121 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0122 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0123 C. ¥À¥ó×½ªİ»ð³İÇŞ  
0124 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0125 C. RAM ID=NMOG¤I%Ê¹Ç•ê²IOK¤ò³İÇŞ  
0126 C.  
0127 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0130 BC (20 80 7f 01 02)  
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41  
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0137 BC (07 0b f8)  
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0143 C. ¥À¥ó×½ªªİ»ð³İÇŞ  
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0145 C. RAM ID=NMOG¤I%Ê¹Ç•ê²IOK¤ò³İÇŞ  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0149 BC (21 00 41 01 02)  
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42  
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65  
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0155 BC (07 0b f8)  
0156 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0157 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0158 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0159 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0160 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0161 C. ¥À¥ó×½ªªİ»ð³İÇŞ  
0162 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0163 C. RAM ID=NMOG,RAM ID=OP¤I%Ê¹Ç•ê²IOK¤ò³İÇŞ  
0164 C.  
0165 C. ***** °Ê²¼¤I%A´¶Á¤òĖĖ-¤òÁ÷Ł® (%âµ-¥À¥ó×¾ē½Ç¤òÄÖÃæ¤ç½¤`¤ë%ı₁Ç¤Ç¤â) *****  
0166 C. DHU¥â¼¥É;Ė¼Y½, ¥ı¼¥ÉjĖ¤òİā¤¹  
0167 + DC 01-22 DHU_MODE_CHNG  
0169 BC (02 0a f8)  
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2  
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0174 C.  
0175 C. *****  
0176 C. TI-CMD SET (POPG STOP/COPY/START)  
0177 C. *****  
0178 C.  
0179 C. NOTICE i$ OPOG UPLOAD¤-Á÷ŁNG¤I%ı₁Çj¢°Ê²¼¤IT I-CMDÁ÷Ł®¤I%A¹Ô¤•¤Ė¤¤³¤Ėj£  
0180 C. ¤¤¤Łj¢SET¤ĖDUMP¤IA±°ıŃN¥¹Çq¹Ô¤ı³¤Ėj£  
0181 C.  
0182 C. TI¥³¥¤¥ó¥É¤òÄöİŁ(UT)  
+ TI 2011-05-14 10:15:00.0  
0184 DC 01-B3 DHU_OP_STOP  
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0186 C.  
+ TI 2011-05-14 10:15:01.0  
0188 DC 01-B4 DHU_OP_COPY  
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0190 C.  
0191 + TI 2011-05-14 10:15:01.0  
0192 DC 01-B5 DHU_OPOG_COPY  
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

```

0194 C.
0195 +. TI 2011-05-14 10:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C. °Ê²¼□İÄê%İİÎñİ¥Á¥$¥Ä¥¹àİÜ
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C. *****
0206 C. TIIÎ°è¥Ä¥Ó¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C. ¥Ä¥Ó¥×½ªİ»ð³İÇ$
0226 C.
0227 C.
0228 C. RAM ID=TI_TBL□İ%Ê¹Ç•è²İOKð³İÇ$
0229 C.
0230 C. DHU¥â;¼¥Ê;Ê¼¥½, ¥ì;¼¥Ê;Ëð³İä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-05-14 10:19:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C.
0246 C.
0247 C.
0248 C.
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-05-14 10:19:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2011-05-14 10:19:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-05-14 10:19:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C.
0272 C.
0273 C.
0274 C.
0275 C. ***** MDP `ÜÄİ□İ»ð¾¥□ÊÄð¹¹èDCBC•×²è *****
0276 C. (%â°İ¥Ö¥Ä¥Ê¥Ð¥Ê¥Ä¥Ç¥Ê¥ð³¼¼¼A»Ü¹¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİÎñÈ´Ø¹¹èDCBC•×²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. j äLOS¥Ä¥$¥Ä¥ ¼Ä»Üj ä
0287 C.
0288 C. ***** LOS *****
0289 C.

```



```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 55s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-05-14 10:19:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÃÎï»ö¼ÝðĒĀð¹ðĒDCBC•x²è *****
0153 C. (%ā°İ¥Ō¥Ā¥Ē¥ð¥Ē¥Ā¥Ĉ¥Ē¥Ē¼¼¼¼Ā»Ū¹¼Ē)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥Đ¥¹•İ Daily±¿İŃðĒ'ð¹¼ĒDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ¡āLOS¥Ā¥$¥Ā¥¬¼Ā»Ū¡ä
0164 C.
0165 . C. ***** LOS *****
0166 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-072 2011-05-14 13:31:19 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿Aα•μ°£»İ×AÇ¿İ¥C¥A¥×¥i;¼¥ÉjĒĒ¿μ•īĒjĒ¿¼°Ç¿α•¿¿i¹¿¿İjÇÀ®, ùα¹αĒαβαÇÁ÷¿®α•αĒααα³αĒj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-287:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2011-05-14 10:19:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F8 XRT_OPERATE
0068 BC (03 02)
0069 +. DC 07-F0 MDP_XRT_CTRL_MANU
0070 BC (c1)
0071 +. DC 07-F0 MDP_XRT_MODE_STBY
0072 BC (c3)
0073 . C. ----- Success Verify ? OK / NG____
0074 C.
0075 C. XRT Obs. Table Upload
0076 . S. RAM ram-291:MDP_OBS_X
0077 ( )
0078 C.
0079 +. DC 07-F0 MDP_DUMP_XRTTBL
0080 BC (84 07 00 00 00 3a d4)
0081 . C. ----- Comparison Check ? OK / ERR ____
0082 C.
0083 C.
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 01 b1 b1 04 04)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 02 b1 b1 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 03 b1 b1 08 08)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 04 b1 b1 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 05 85 83 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 06 80 80 20 20)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 07 80 80 20 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 08 80 80 08 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 09 85 83 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0a 85 83 08 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0f 80 80 06 06)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 10 80 80 08 08)
0108 + DC 07-F0 MDP_XRT_FLD_ENA
0109 BC (d8)
0110 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0111 BC (c8)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 . C. ----- Success Verify ? OK / NG ____
0115 C.
0116 C.
0117 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0118 C.
0119 +. DC 07-F0 MDP_XRT_MODE_OBSV
0120 BC (c2)
0121 +. TI 2011-05-14 10:19:02.0
0122 DC 07-F0 MDP_XRT_MODE_OBSV
0123 BC (c2)
0124 +. TI 2011-05-14 10:19:04.0
0125 DC 07-F0 MDP_XRT_CTRL_AUTO
0126 BC (c0)
0127 . C. ----- Success Verify ? OK / NG ____
0128 C.
0129 C. ***** XRT END *****
0130 C.
0131 . C. ***** MDP '0AÎ0Î»0%Ý0ÊÂ0010èDCBC•x2è *****
0132 C. (%â°î¥0¥Á¥Ê¥ð¥Ë¥â¥ç¥è0Ê½00¥Á»Û010è)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ¥Ð¥¹•İ Daily±¿İŇ0Ê´0010èDCBC•x2è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ¡ãLOS¥Á¥$¥Á¥¬¼Á»Û¡ã
0143 C.
0144 . C. ***** LOS *****
0145 C.
```

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 1/9

*** OP Sequence for XRT ***

2011/05/14	10:29:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	10:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2011/05/14	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 00 00 00 00 00
2011/05/14	10:30:16.0	XRT_FLD_DIS_402_OG [0x192]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2011/05/14	10:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2011/05/14	10:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/05/14	10:32:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 11
2011/05/14	10:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	10:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	10:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 03 00 00 00 00
2011/05/14	10:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2011/05/14	10:42:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2011/05/14	10:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2011/05/14	10:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2011/05/14	10:42:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/05/14	10:42:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	10:42:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2011/05/14	10:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2011/05/14	10:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	11:41:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	11:41:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	11:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/14	11:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/14	12:07:30.0	XRT_Custom_418_OG [0x1a2]		
2011/05/14	12:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	13:19:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	13:19:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	13:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/14	13:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/14	13:46:00.0	XRT_Custom_418_OG [0x1a2]		
2011/05/14	13:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	14:58:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	14:58:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	14:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/14	15:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/14	15:43:00.0	XRT_Custom_418_OG [0x1a2]		
2011/05/14	15:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	16:36:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	16:36:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	16:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/14	16:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/14	17:19:30.0	XRT_Custom_418_OG [0x1a2]		
2011/05/14	17:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/14	18:15:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/14	18:15:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/14	18:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/14	18:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/14	18:56:30.0	XRT_Custom_418_OG [0x1a2]		

Saturday May 14, 2011

1/9

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 2/9

2011/05/14	18:57:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	19:53:30.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	19:53:32.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	19:53:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	19:56:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	20:33:30.0	XRT_Custom_418_0G [0x1a2]			
2011/05/14	20:34:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	21:32:00.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	21:32:02.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	21:32:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	21:35:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	22:10:00.0	XRT_Custom_418_0G [0x1a2]			
2011/05/14	22:11:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	23:10:30.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	23:10:32.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	23:10:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/14	23:13:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/14	23:39:54.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	23:39:56.0	XRT_FOCUS_POSITION_401_0G [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/05/14	23:40:00.0	AOCS_0Re-point_Start_1_0G [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/05/14	23:40:16.0	XRT_FLD_DIS_402_0G [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/05/14	23:40:18.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/05/14	23:40:20.0	XRT_ARS_DIS_438_0G [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/14	23:42:58.0	XRT_QT_PROG_SET_416_0G [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2011/05/14	23:43:00.0	XRT_CTRL_AUTO_406_0G [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/14	23:50:00.0	XRT_CTRL_MANU_439_0G [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/14	23:52:32.0	XRT_FOCUS_POSITION_409_0G [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/14	23:52:52.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/14	23:52:54.0	XRT_FLRCTRL_ENA_413_0G [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/14	23:52:56.0	XRT_AEC_RESET_443_0G [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/14	23:52:58.0	XRT_ARS_DIS_431_0G [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/14	23:53:00.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/14	23:53:02.0	XRT_QT_PROG_SET_446_0G [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/05/14	23:53:04.0	XRT_FL_PROG_SET_414_0G [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/14	23:53:06.0	XRT_CTRL_AUTO_406_0G [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/15	00:49:00.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	00:49:02.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	00:49:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/15	00:52:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/15	01:00:00.0	AOCS_0Re-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/05/15	01:16:00.0	XRT_CTRL_MANU_439_0G [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	01:18:32.0	XRT_FOCUS_POSITION_409_0G [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/15	01:18:52.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/15	01:18:54.0	XRT_FLRCTRL_ENA_413_0G [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/15	01:18:56.0	XRT_AEC_RESET_443_0G [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/15	01:18:58.0	XRT_ARS_DIS_431_0G [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/15	01:19:00.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	01:19:02.0	XRT_QT_PROG_SET_421_0G [0x1a5]			

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 3/9

2011/05/15	01:19:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2011/05/15	01:19:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/05/15	02:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	02:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	02:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	02:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	02:53:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	02:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	03:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	03:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	03:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	03:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	04:32:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	04:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	05:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	05:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	05:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	05:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	06:10:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	06:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	07:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	07:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	07:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	07:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	07:49:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	07:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	08:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	08:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	08:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	08:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	09:27:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	09:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	10:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	10:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	10:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	10:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	11:06:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	11:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	11:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	12:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/05/15	12:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00 00 00 00
2011/05/15	12:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/05/15	12:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/05/15	12:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/15	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2011/05/15	12:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	12:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	12:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03	00 00 00 00
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00

Saturday May 14, 2011

3/9

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 4/9

2011/05/15	12:12:46.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/15	12:12:48.0	XRT_FLRCTRL_ENA_413_0G [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/15	12:12:50.0	XRT_AEC_RESET_443_0G [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/15	12:12:52.0	XRT_ARS_DIS_431_0G [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/15	12:12:54.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	12:12:56.0	XRT_QT_PROG_SET_421_0G [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2011/05/15	12:12:58.0	XRT_FL_PROG_SET_414_0G [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/15	12:13:00.0	XRT_CTRL_AUTO_406_0G [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/15	12:18:00.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	12:18:02.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	12:18:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/15	12:21:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/15	12:44:30.0	XRT_Custom_418_0G [0x1a2]			
2011/05/15	12:45:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/15	13:56:30.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	13:56:32.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	13:56:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/15	13:59:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/15	14:30:30.0	XRT_Custom_418_0G [0x1a2]			
2011/05/15	14:31:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/15	15:35:00.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	15:35:02.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	15:35:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/15	15:38:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/15	16:00:00.0	AOCS_ORe-point_Start_3_0G [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/05/15	16:20:30.0	XRT_CTRL_MANU_439_0G [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	16:23:02.0	XRT_FOCUS_POSITION_409_0G [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/15	16:23:22.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/15	16:23:24.0	XRT_FLRCTRL_ENA_413_0G [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/15	16:23:26.0	XRT_AEC_RESET_443_0G [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/15	16:23:28.0	XRT_ARS_DIS_431_0G [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/15	16:23:30.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	16:23:32.0	XRT_QT_PROG_SET_427_0G [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/05/15	16:23:34.0	XRT_FL_PROG_SET_414_0G [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/15	16:23:36.0	XRT_CTRL_AUTO_406_0G [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/15	17:13:30.0	XRT_CTRL_MANU_408_0G [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	17:13:32.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	17:13:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/15	17:16:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/15	17:30:00.0	AOCS_ORe-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/05/15	17:57:00.0	XRT_CTRL_MANU_439_0G [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/15	17:59:32.0	XRT_FOCUS_POSITION_409_0G [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/15	17:59:52.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/15	17:59:54.0	XRT_FLRCTRL_ENA_413_0G [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/15	17:59:56.0	XRT_AEC_RESET_443_0G [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/15	17:59:58.0	XRT_ARS_DIS_431_0G [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/15	18:00:00.0	XRT_FLD_RESET_412_0G [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/15	18:00:02.0	XRT_QT_PROG_SET_421_0G [0x1a5]			

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 5/9

2011/05/15	18:00:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2011/05/15	18:00:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/05/15	18:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	18:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	18:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	18:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	19:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	19:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	20:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	20:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	20:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	20:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	21:10:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	21:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	22:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	22:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	22:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	22:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/15	22:46:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/15	22:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/05/15	23:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/15	23:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/15	23:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/15	23:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/16	00:14:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/16	00:14:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
2011/05/16	00:15:00.0	AOCs_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/05/16	00:15:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00 00 00 00
2011/05/16	00:15:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/05/16	00:15:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/05/16	00:17:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/16	00:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2011/05/16	00:24:20.0	XRT_CTRL_MANU_439_OG [0x1b7]					
2011/05/16	00:26:52.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	00:27:12.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/05/16	00:27:14.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/05/16	00:27:16.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/05/16	00:27:18.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/05/16	00:27:20.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/16	00:27:22.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	00:27:24.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2011/05/16	00:27:26.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/05/16	01:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/16	01:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	01:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	01:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/16	01:30:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/16	01:53:30.0	XRT_CTRL_MANU_439_OG [0x1b7]	AOCU_NM	5	02-76	00	e4 63 b3 c7

Saturday May 14, 2011

5/9

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 6/9

2011/05/16	01:56:02.0	XRT_FOCUS_POSITION_409_0G [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	01:56:22.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/16	01:56:24.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/16	01:56:26.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/16	01:56:28.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/16	01:56:30.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/16	01:56:32.0	XRT_QT_PROG_SET_421_0G [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	01:56:34.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2011/05/16	01:56:36.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/16	03:00:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	03:00:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	03:00:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	03:03:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	03:31:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	03:32:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	04:30:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	04:30:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	04:30:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	04:33:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	05:09:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	05:10:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	06:10:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	06:10:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	06:10:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	06:13:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	06:48:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	06:49:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	07:51:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	07:51:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	07:51:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	07:54:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	08:26:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	08:27:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	09:31:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	09:31:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	09:31:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	09:34:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	10:05:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	10:06:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	11:16:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	11:16:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	11:16:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	11:19:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	11:43:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	11:44:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]				
2011/05/16	11:59:54.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	11:59:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	12:00:00.0	AOCs_Ore-point_Start_1_0G [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/05/16	12:00:16.0	XRT_FLD_DIS_402_0G [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
			MDP_XRT_FLD_DIS	1	07-F0	d9

May 14, 11 13:31

XRT_OGLIST_0927.chk

2011/05/16	12:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/05/16	12:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/16	12:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/05/16	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	12:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	12:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/05/16	12:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/16	12:12:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/16	12:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/16	12:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/16	12:12:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/16	12:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	12:12:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/05/16	12:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/16	12:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	12:55:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	12:55:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	12:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	12:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	13:22:00.0	XRT_Custom_418_OG [0x1a2]			
2011/05/16	13:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	14:33:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	14:33:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	14:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	14:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	15:09:00.0	XRT_Custom_418_OG [0x1a2]			
2011/05/16	15:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	16:12:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	16:12:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	16:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	16:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	16:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/05/16	16:57:00.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	16:59:32.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/05/16	16:59:52.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/05/16	16:59:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/05/16	16:59:56.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/05/16	16:59:58.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/05/16	17:00:00.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	17:00:02.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/05/16	17:00:04.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/05/16	17:00:06.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/05/16	17:50:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/05/16	17:50:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/05/16	17:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/05/16	17:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/05/16	18:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/05/16	18:33:30.0	XRT_CTRL_MANU_439_OG [0x1b7]			

Saturday May 14, 2011

7/9

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 8/9

2011/05/16	18:36:02.0	XRT_FOCUS_POSITION_409_0G [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	18:36:22.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/05/16	18:36:24.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/05/16	18:36:26.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/05/16	18:36:28.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/05/16	18:36:30.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/16	18:36:32.0	XRT_QT_PROG_SET_428_0G [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	18:36:34.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2011/05/16	18:36:36.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/05/16	19:29:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/16	19:29:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	19:29:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	19:32:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/16	20:09:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/16	20:10:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]					
2011/05/16	21:07:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/16	21:07:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	21:07:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	21:10:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/16	21:46:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/16	21:47:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]					
2011/05/16	22:46:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/16	22:46:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/16	22:46:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/16	22:49:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/16	23:20:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/16	23:21:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]					
2011/05/16	23:59:54.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/16	23:59:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/17	00:00:00.0	AOCs_Ore-point_Start_1_0G [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/05/17	00:00:16.0	XRT_FLD_DIS_402_0G [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/05/17	00:00:18.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/05/17	00:00:20.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/05/17	00:02:58.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/17	00:03:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2011/05/17	00:09:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/17	00:10:00.0	AOCs_Ore-point_Start_5_0G [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/17	00:12:26.0	XRT_FOCUS_POSITION_409_0G [0x199]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/05/17	00:12:46.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/05/17	00:12:48.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/05/17	00:12:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/05/17	00:12:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/05/17	00:12:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/17	00:12:56.0	XRT_QT_PROG_SET_428_0G [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/17	00:12:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2011/05/17	00:13:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/05/17	00:24:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/17	00:24:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

May 14, 11 13:31

XRT_OGLIST_0927.chk

Page 9/9

2011/05/17	00:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	00:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	00:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	00:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	02:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	02:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	02:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	02:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	02:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	02:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	03:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	03:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	03:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	03:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	04:08:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	04:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	05:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	05:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	05:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	05:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	05:47:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	05:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	06:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	06:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	06:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	06:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	07:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	07:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	08:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	08:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	08:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	08:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	09:04:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	09:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	10:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/05/17	10:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/05/17	10:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0 da
2011/05/17	10:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/05/17	10:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	10:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/05/17	11:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_AUTO	1	07-F0 c0
		AOCU_NM		5	02-76 00 00 00 00 00