

XRT Timeline to be uploaded on 2011/05/25

Period: 2011/05/25 10:01:00 - 2011/05/28 10:47:00

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Normal mode

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XOB #187F: HOP 186 FW1=OPEN Al/mesh (128/4096ms) + Synoptic Q95 2x2 - Al/mesh(44/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048

Term	Pointing (x, y)	Comment
05/25 10:46:00 - 05/25 11:00:01	Fixed (0.0, 0.0)	# OP start + 10min Synoptic and HOP186
05/26 10:18:00 - 05/26 10:24:54	Fixed (0.0, 0.0)	synoptic, and HOP186

PROG= 17 1-time(s)											
Subr= 1		1-time(s)		12.0sec							
Seqn= 86		1-time(s)		4.0sec							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Seqn= 5		1-time(s)		2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0 0 2.0sec
Seqn= 37		1-time(s)		4.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Seqn= 4		1-time(s)		2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec

Subr= 2 1-time(s) 2.0sec											
Seqn= 42		1-time(s)		2.0sec							
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh	Open/G-band	close	Safe	Dark	4.00s	Obs	1x1	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/26 05:15:00 - 05/26 06:59:54	Fixed (0.0, 945.0)	Co-alignment at N-limb

PROG= 14 1-time(s)											
Subr= 1		1-time(s)		360.0sec							
Seqn= 21		24-time(s)		300.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/26 07:15:00 - 05/26 08:59:54	Fixed (-945.0, 0.0)	Co-alignment at east limb

PROG= 06 1-time(s)											
Subr= 1		1-time(s)		360.0sec							
Seqn= 22		15-time(s)		480.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
05/26 09:03:05 - 05/26 09:08:00	Fixed (-528.4, -528.4)	Quadrant pointing 1/4

PROG= 13 1-time(s)											
Subr= 1		1-time(s)		12.0sec							
Seqn= 38		1-time(s)		12.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
Subr= 2		1-time(s)		2.0sec							
Seqn= 93		2-time(s)		2.0sec							
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long

Term	Pointing (x, y)	Comment
05/26 09:48:00 - 05/26 09:54:54	Fixed (528.4, -528.4)	2/4

PROG= 07 1-time(s)											
Subr= 1		1-time(s)		12.0sec							
Seqn= 36		1-time(s)		12.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec

Subr= 2	1-time(s)	2.0sec	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Seqn= 93	2-time(s)	2.0sec	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment											
05/26 09:58:00 - 05/26 10:04:54	Fixed (528.4, 528.4)	3/4											
PROG= 09	1-time(s)												
Subr= 1	1-time(s)	12.0sec											
Seqn= 39	1-time(s)	12.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x1536 (1024, 768)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x1536 (1024, 768)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 93	2-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment											
05/26 10:08:00 - 05/26 10:14:54	Fixed (-528.4, 528.4)	4/4											
PROG= 18	1-time(s)												
Subr= 1	1-time(s)	12.0sec											
Seqn= 40	1-time(s)	12.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1536x2048 (1280, 1024)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1536x2048 (1280, 1024)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 93	2-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad

Term	Pointing (x, y)	Comment											
05/26 10:28:00 - 05/26 23:49:54	Track (397.2, -280.9) ^{05/26 10:25:00}	Track AR11218											
05/27 00:31:00 - 05/27 08:06:00	Track (498.2, -283.1) ^{05/27 00:00:00}	Track AR11218											
PROG= 20	Inf.-time(s)												
Subr= 2	1-time(s)	2.0sec											
Seqn= 19	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41	4-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1	1-time(s)	2.0sec											
Seqn= 13	55-time(s)	22.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #187A: Synoptic Q95 2x2 - Al/mesh(44/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + G-band(16)

Term	Pointing (x, y)	Comment											
05/26 23:53:00 - 05/26 23:59:54	Fixed (0.0, 0.0)	synoptic											
PROG= 10	1-time(s)												
Subr= 1	1-time(s)	12.0sec											
Seqn= 86	1-time(s)	4.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 37	1-time(s)	4.0sec											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term		Pointing (x, y)						Comment				
05/26 10:28:00 - 05/26 23:49:54		Track (397.2, -280.9) ^{© 05/26 10:25:00}						Track AR11218				
05/27 00:31:00 - 05/27 08:06:00		Track (498.2, -283.1) ^{© 05/27 00:00:00}						Track AR11218				
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

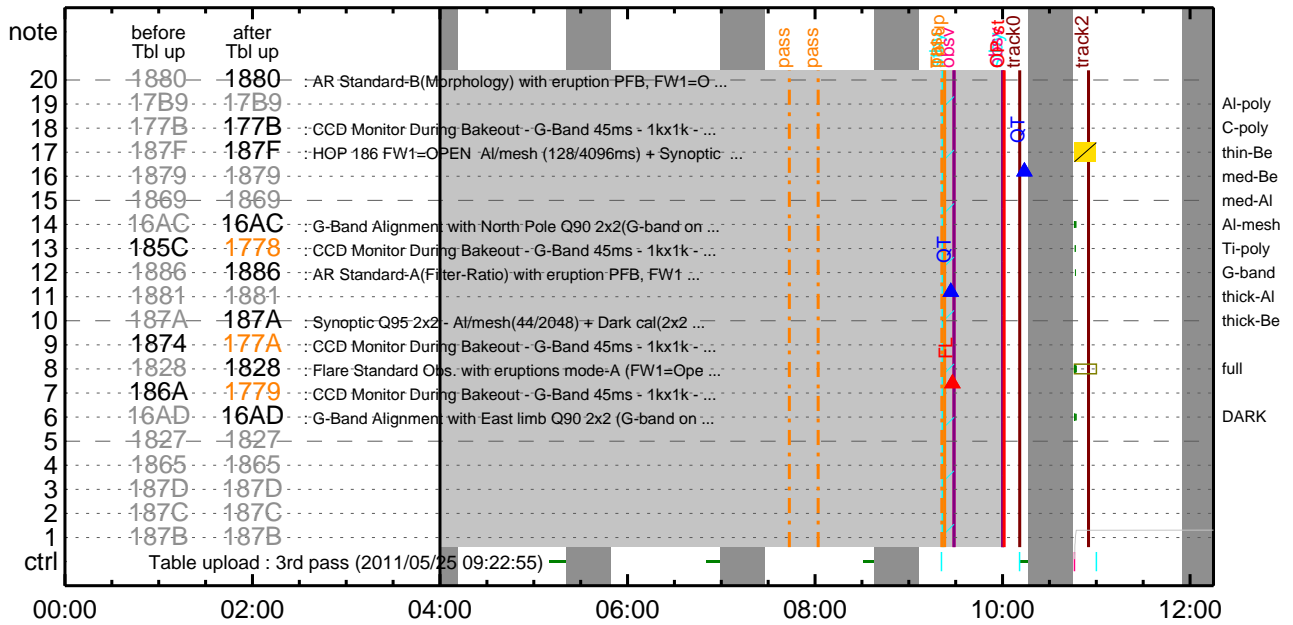
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Flare Detection

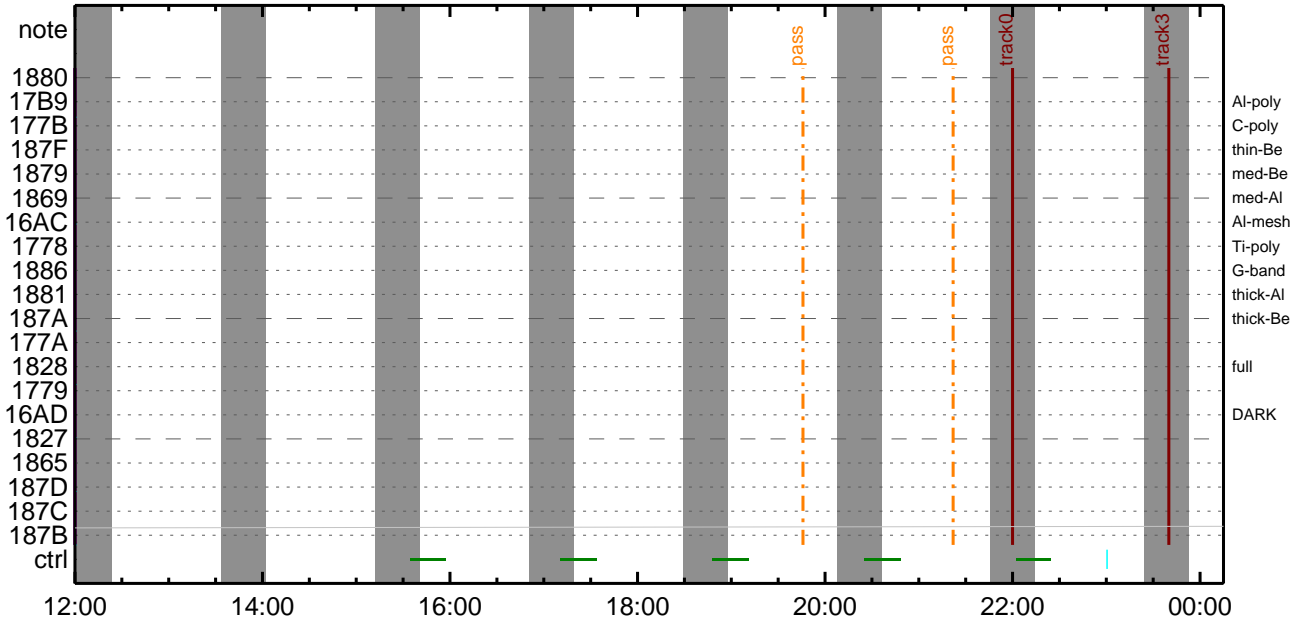
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FLD Patrol												
Term		Pointing (x, y)						Comment				
05/26 10:27:46 - 05/26 23:50:16		Track (397.2, -280.9) ^{© 05/26 10:25:00}						Track AR11218				
05/27 00:02:46 - 05/28 10:47:00		Track (498.2, -283.1) ^{© 05/27 00:00:00}						Track AR11218				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

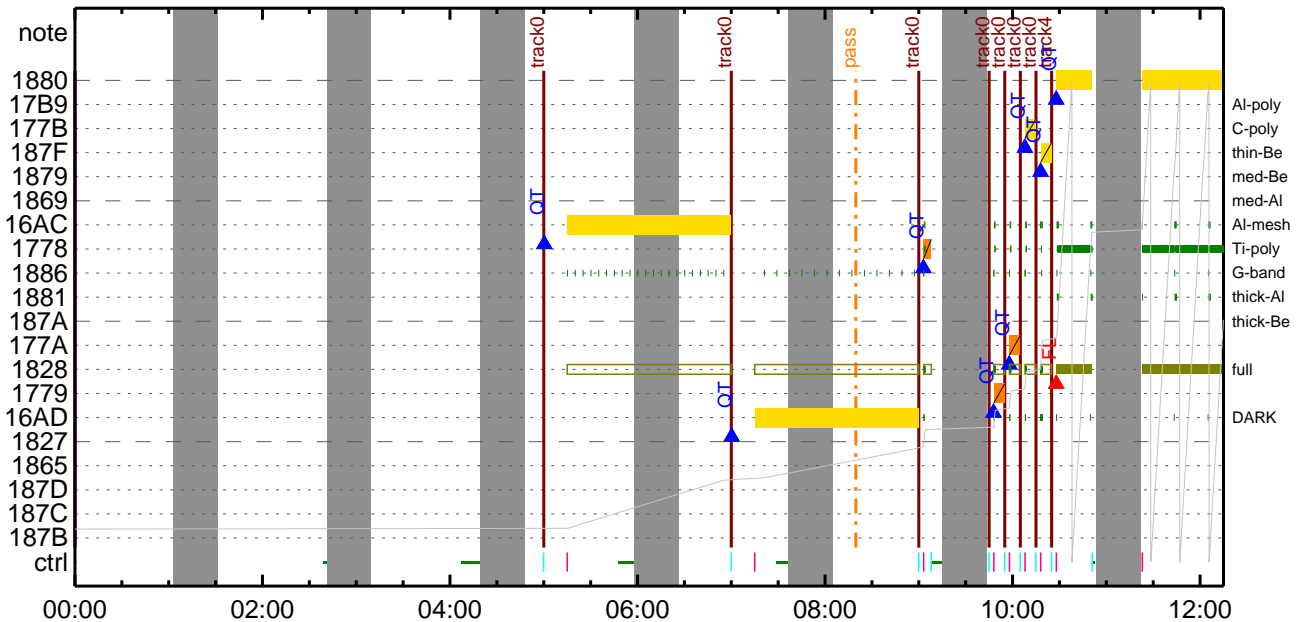
CMDI #0947 2011/05/25



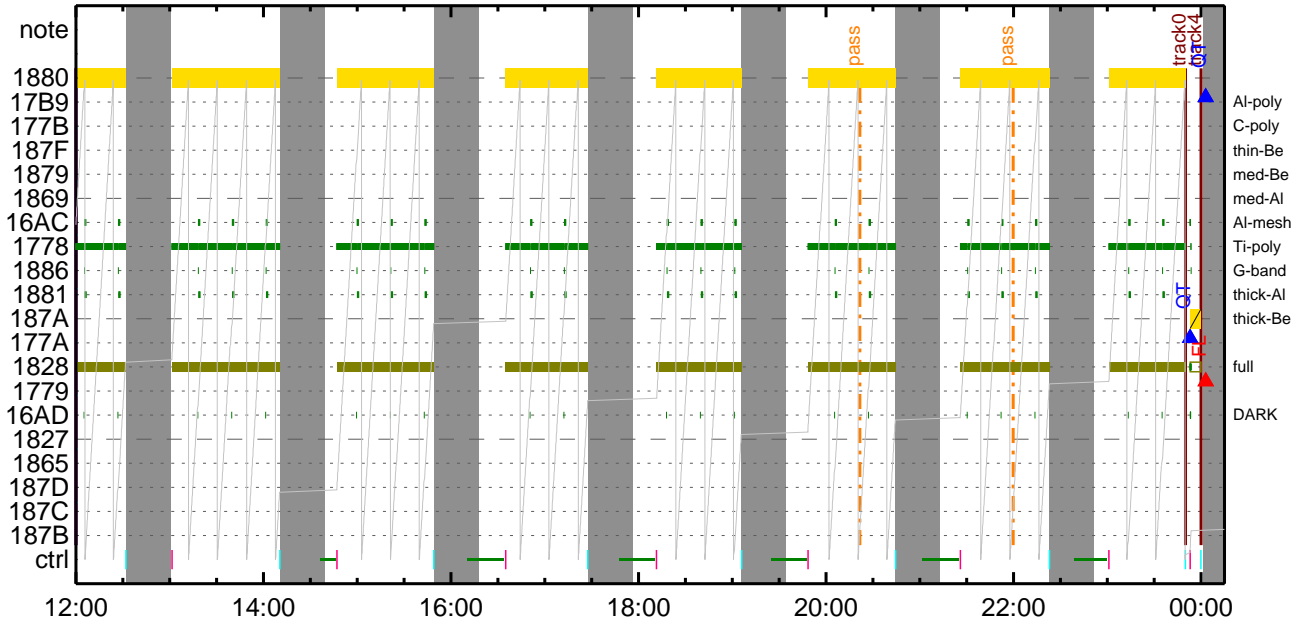
CMDI #0947 2011/05/25



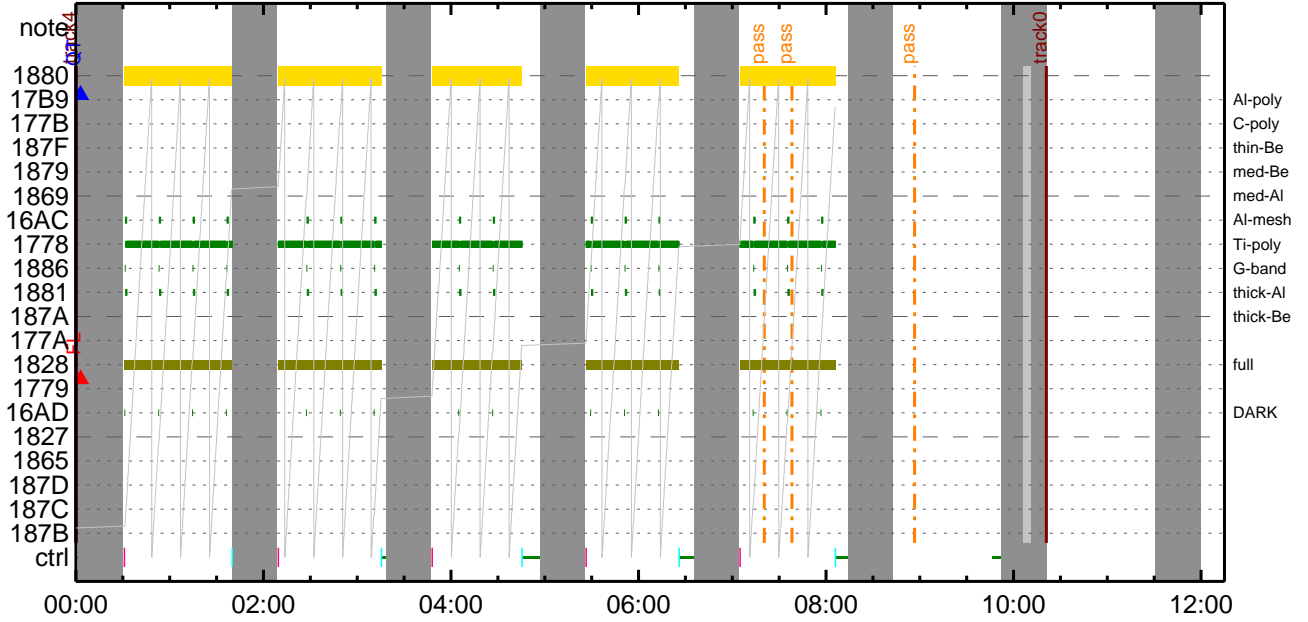
CMDI #0947 2011/05/26



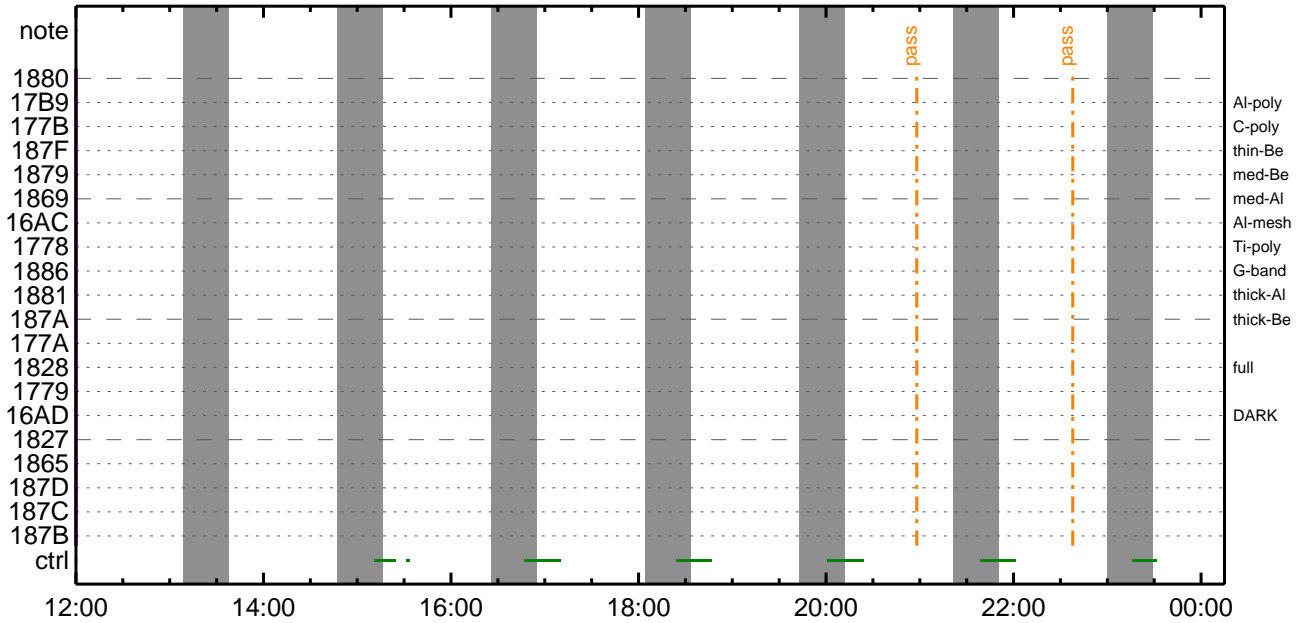
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```
0194 C.
0195 +. TI 2011-05-25 10:00:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼ºïÄë%îíñºîî¥Á¥§¥Á¥¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»º³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLºîî¼Ê¹ç•è²îOKºº³îç§
0229 C.
0230 C. DHU¥â;¼¥Ê;Ê¼¥¼. ¥î;¼¥Ê;Êººîâº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-05-25 10:00:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2011-05-25 10:00:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-05-25 10:00:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC          (41)
0261 C. -----
0262 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-05-25 10:00:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîâî»ö¼ÝºÈÄº¹ºèDCBC•x²è *****
0276 C. (¼áºîî¥Á¥Ê¥¥¥Á¥ç¥èº¼ºº¼Á»Ûº¹ºè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñºè'ºº¹ºèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥¹¼Á»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-094 2011-05-25 11:59:06 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÄ»µ°È»Í×ÁÇ¿ÍYçYÄY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÖ»•µ¿¼l¹çµÍ;çÄ®, ù¹µèµµµÇÁ+¿®»•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 46s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2011-05-25 10:00:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÄî¿î»ó¼YµÈÄ¿µ¹µèDCBC•x²è *****
0069 C. (¼á°íYÓYÄYÈYÞYÉYáYçYèµÈ%¼µ¼Ä»Ûµ¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹.İ Daily±¿İÑµÈ`Øµ¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁYŞYÄY-¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 60 20 18)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c a0 80 18 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d c0 c0 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 40 c0 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0c)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 08)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2011-05-25 10:00:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 . C. *****
0172 C. SOT table upload
0173 . C. *****
0174 . C. < Stop FG table >
0175 +. DC 07-F0 MDP_FG_CTRL_MANU
0176 BC (51)
0177 . C. -----
0178 C. MDP_FG_CTRL_MODE = MANU [ ]
0179 C. -----
0180 C.
0181 . C. <Upload FG Observation Table>
0182 . S. RAM ram-265:MDP_OBS_F
0183 ( )
0184 C.
0185 . C. < Dump RAMID=MDP_OBS_F >
0186 +. DC 07-F0 MDP_DUMP_FGTBL
0187 BC (82 07 00 00 00 38 b8)
0188 C. -----
0189 C. MDP_OBS_F verify = OK/NG [ ]
0190 C. -----
0191 C.
0192 C. *****
0193 C. SOT TI command set

```

```
0194 C. *****
0195 C. Execute, after the success of TBL upload.
0196 +. TI 2011-05-25 10:00:18.0
0197 DC 07-F0 MDP_SOT_MODE_OBSV
0198 BC (40)
0199 . C. -----
0200 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0201 C. -----
0202 C.
0203 C.
0204 . C. ***** MDP 'uAÎaî»ô¼YôEÂDâ¹aèDCBC•x²è *****
0205 C. (¼â°îYÓYÃYÈYÞYÈYâYçYèaE¼¼a¼A»Ûa¹aè)
0206 . S. DC-BC dcbc-402:DCBC
0207 (MDP_known_event)
0208 C.
0209 C.
0210 . C. ***** YDY¹•ï Daily±¿îÑaE'Øa¹aèDCBC•x²è *****
0211 . S. DC-BC dcbc-153:DCBC
0212 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0213 C.
0214 C.
0215 . C. ;ãLOS¥Ã¥S¥Ã¥-¼A»Û;ã
0216 C.
0217 . C. ***** LOS *****
0218 C.
```

*** OP Sequence for XRT ***

```

2011/05/25 10:10:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/25 10:10:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/05/25 10:11:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/05/25 10:11:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2011/05/25 10:11:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2011/05/25 10:11:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/05/25 10:13:58.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2011/05/25 10:46:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/05/25 10:55:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 02 00 00 00 00
2011/05/25 11:00:01.0 XRT_CTRL_MANU_441_OG [0x1b9]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/25 11:00:31.0 XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]
                        TCIB_XRT_S_HTR_A_ENA 0 04-BC
2011/05/25 13:00:31.0 XRT_Custom_444_OG [0x1bc]
2011/05/25 15:00:31.0 XRT_Custom_444_OG [0x1bc]
2011/05/25 17:00:31.0 XRT_Custom_444_OG [0x1bc]
2011/05/25 19:00:31.0 XRT_Custom_447_OG [0x1bf]
2011/05/25 19:00:41.0 XRT_Custom_444_OG [0x1bc]
2011/05/25 21:00:41.0 XRT_Custom_448_OG [0x1c0]
2011/05/25 22:00:00.5 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/05/25 23:00:19.0 XRT_Custom_447_OG [0x1bf]
2011/05/25 23:00:29.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/25 23:00:31.0 XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2011/05/25 23:40:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 03 00 00 00 00
2011/05/26 04:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/26 04:59:56.0 XRT_ROI_A_435_OG [0x1b3]
                        MDP_XRT_ROI_SET 6 07-F0 cd 05 85 83 08 08
                        MDP_XRT_ROI_SET 6 07-F0 cd 06 85 83 06 06
                        MDP_XRT_ROI_SET 6 07-F0 cd 07 85 83 08 08
                        MDP_XRT_ROI_SET 6 07-F0 cd 08 80 80 20 20
                        MDP_XRT_ROI_SET 6 07-F0 cd 09 80 80 20 08
                        MDP_XRT_ROI_SET 6 07-F0 cd 0a 80 80 08 20
                        MDP_XRT_ROI_SET 6 07-F0 cd 0b 80 60 20 18
                        MDP_XRT_ROI_SET 6 07-F0 cd 0c a0 80 18 20
2011/05/26 04:59:56.5 XRT_ROI_B_403_OG [0x193]
                        MDP_XRT_ROI_SET 6 07-F0 cd 0c a0 80 18 20
                        MDP_XRT_ROI_SET 6 07-F0 cd 0d c0 c0 10 10
                        MDP_XRT_ROI_SET 6 07-F0 cd 0e 40 c0 10 10
                        MDP_XRT_ROI_SET 6 07-F0 cd 0f 80 80 06 06
                        MDP_XRT_ROI_SET 6 07-F0 cd 10 80 80 08 08
2011/05/26 05:00:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 ac 00 00 00
2011/05/26 05:00:01.5 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/05/26 05:00:21.5 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2011/05/26 05:00:23.5 XRT_FLD_DIS_423_OG [0x1a7]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2011/05/26 05:00:25.5 XRT_FLRCTRL_DIS_425_OG [0x1a9]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2011/05/26 05:00:27.5 XRT_ARS_DIS_404_OG [0x194]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/05/26 05:15:00.5 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/05/26 06:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/26 06:59:56.0 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/05/26 07:00:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM 5 02-76 00 00 00 54 00
2011/05/26 07:00:16.0 XRT_QT_PROG_SET_432_OG [0x1b0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 06
2011/05/26 07:00:18.0 XRT_FLD_DIS_423_OG [0x1a7]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2011/05/26 07:00:20.0 XRT_FLRCTRL_DIS_425_OG [0x1a9]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2011/05/26 07:00:22.0 XRT_ARS_DIS_437_OG [0x1b5]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/05/26 07:15:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/05/26 08:59:54.0 XRT_CTRL_MANU_405_OG [0x195]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/05/26 09:00:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM 5 02-76 00 2e f9 2e f9
2011/05/26 09:02:32.0 XRT_ROI_A_407_OG [0x197]
                        MDP_XRT_ROI_SET 6 07-F0 cd 05 85 83 08 08
                        MDP_XRT_ROI_SET 6 07-F0 cd 06 85 83 06 06

```

			MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83	08	08
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20	20
			MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	20	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0a	80	80	08	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0b	40	40	10	10
			MDP_XRT_ROI_SET	6	07-F0	cd	0c	c0	40	10	10
2011/05/26	09:02:32.5	XRT_ROI_B_436_OG	{0x1b4}								
			MDP_XRT_ROI_SET	6	07-F0	cd	0c	c0	40	10	10
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	c0	c0	10	10
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	40	c0	10	10
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2011/05/26	09:02:37.5	XRT_FOCUS_POSITION_440_OG	{0x1b8}								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2011/05/26	09:02:57.5	XRT_QT_PROG_SET_410_OG	{0x19a}								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d				
2011/05/26	09:02:59.5	XRT_FLD_DIS_402_OG	{0x192}								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2011/05/26	09:03:01.5	XRT_FLRCTRL_DIS_433_OG	{0x1b1}								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2011/05/26	09:03:03.5	XRT_ARS_DIS_431_OG	{0x1af}								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/05/26	09:03:05.5	XRT_CTRL_AUTO_419_OG	{0x1a3}								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/05/26	09:08:00.0	XRT_CTRL_MANU_408_OG	{0x198}								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/05/26	09:08:02.0	XRT_FLD_RESET_412_OG	{0x19c}								
			MDP_XRT_FLD_RESET	1	07-F0	da					
2011/05/26	09:08:04.0	XRT_PREFLR_STRT_422_OG	{0x1a6}								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/05/26	09:11:14.0	XRT_PREFLR_STOP_424_OG	{0x1a8}								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/05/26	09:44:54.0	XRT_CTRL_MANU_405_OG	{0x195}								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/05/26	09:45:00.0	AOCS_Ore-point_Start_7_OG	{0x09d}								
			AOCU_NM	5	02-76	00	2e	f9	d1	07	
2011/05/26	09:47:32.0	XRT_FOCUS_POSITION_440_OG	{0x1b8}								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2011/05/26	09:47:52.0	XRT_QT_PROG_SET_415_OG	{0x19f}								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07				
2011/05/26	09:47:54.0	XRT_FLD_DIS_402_OG	{0x192}								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2011/05/26	09:47:56.0	XRT_FLRCTRL_DIS_433_OG	{0x1b1}								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2011/05/26	09:47:58.0	XRT_ARS_DIS_431_OG	{0x1af}								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/05/26	09:48:00.0	XRT_CTRL_AUTO_419_OG	{0x1a3}								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/05/26	09:54:54.0	XRT_CTRL_MANU_405_OG	{0x195}								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/05/26	09:55:00.0	AOCS_Ore-point_Start_8_OG	{0x09e}								
			AOCU_NM	5	02-76	00	d1	07	d1	07	
2011/05/26	09:57:32.0	XRT_FOCUS_POSITION_440_OG	{0x1b8}								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2011/05/26	09:57:52.0	XRT_QT_PROG_SET_417_OG	{0x1a1}								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	09				
2011/05/26	09:57:54.0	XRT_FLD_DIS_402_OG	{0x192}								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2011/05/26	09:57:56.0	XRT_FLRCTRL_DIS_433_OG	{0x1b1}								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2011/05/26	09:57:58.0	XRT_ARS_DIS_431_OG	{0x1af}								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/05/26	09:58:00.0	XRT_CTRL_AUTO_419_OG	{0x1a3}								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/05/26	10:04:54.0	XRT_CTRL_MANU_405_OG	{0x195}								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/05/26	10:05:00.0	AOCS_Ore-point_Start_9_OG	{0x09f}								
			AOCU_NM	5	02-76	00	d1	07	2e	f9	
2011/05/26	10:07:32.0	XRT_FOCUS_POSITION_440_OG	{0x1b8}								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2011/05/26	10:07:52.0	XRT_QT_PROG_SET_420_OG	{0x1a4}								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2011/05/26	10:07:54.0	XRT_FLD_DIS_402_OG	{0x192}								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2011/05/26	10:07:56.0	XRT_FLRCTRL_DIS_433_OG	{0x1b1}								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2011/05/26	10:07:58.0	XRT_ARS_DIS_431_OG	{0x1af}								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/05/26	10:08:00.0	XRT_CTRL_AUTO_419_OG	{0x1a3}								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/05/26	10:14:54.0	XRT_CTRL_MANU_400_OG	{0x190}								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/05/26	10:14:56.0	XRT_FOCUS_POSITION_401_OG	{0x191}								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2011/05/26	10:15:00.0	AOCS_Ore-point_Start_1_OG	{0x097}								
			AOCU_NM	5	02-76	00	00	00	00	00	
2011/05/26	10:15:16.0	XRT_FLD_DIS_402_OG	{0x192}								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2011/05/26	10:15:18.0	XRT_FLRCTRL_DIS_433_OG	{0x1b1}								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2011/05/26	10:15:20.0	XRT_ARS_DIS_438_OG	{0x1b6}								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/05/26	10:17:58.0	XRT_QT_PROG_SET_426_OG	{0x1aa}								

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2011/05/26	10:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	10:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	10:25:00.0	AOCs_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	04	00 00 00 00
2011/05/26	10:27:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/05/26	10:27:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/05/26	10:27:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/05/26	10:27:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/05/26	10:27:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/05/26	10:27:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	10:27:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/05/26	10:27:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/05/26	10:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	10:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	10:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	10:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	10:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	11:22:00.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	11:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	12:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	12:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	12:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	12:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	13:00:30.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	13:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	14:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	14:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	14:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	14:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	14:46:00.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	14:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	15:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	15:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	15:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	15:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	16:34:00.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	16:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	17:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	17:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	17:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	17:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	18:10:30.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	18:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	19:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/05/26	19:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/05/26	19:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/05/26	19:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/05/26	19:47:30.0	XRT_Custom_418_OG [0x1a2]					
2011/05/26	19:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/05/26	20:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/05/27	06:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/27	06:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/27	06:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/27	07:04:00.0	XRT_Custom_418_OG [0x1a2]							
2011/05/27	07:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/05/27	08:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/05/27	08:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/05/27	08:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/05/27	08:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/05/27	10:21:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				