

XRT Timeline to be uploaded on 2011/06/04

Period: 2011/06/04 09:39:00 - 2011/06/09 09:29:00

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Normal mode

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XOB #1889: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1

Term	Pointing (x, y)	Comment
06/04 10:28:00 - 06/04 10:34:54	Fixed (0.0, 0.0)	# OP start + 10min, synoptic, shifted manually, with HOP 186.
06/05 11:58:00 - 06/05 12:04:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
06/06 12:03:00 - 06/06 12:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
PROG= 16 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 7 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 81 1-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1886: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 512x512 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
06/04 10:38:00 - 06/04 12:31:54	Track (214.5, -375.5) ^{Ⓜ 06/04 10:35:00}	* Track AR 11226.
06/04 19:33:00 - 06/04 23:59:54	Track (284.4, -376.2) ^{Ⓜ 06/04 19:30:00}	* Track AR 11226.
06/05 00:23:00 - 06/05 11:54:54	Track (321.5, -376.5) ^{Ⓜ 06/05 00:20:00}	# Cont.
06/05 12:08:00 - 06/05 23:38:30	Track (408.7, -377.4) ^{Ⓜ 06/05 12:05:00}	# Cont.
06/06 06:43:00 - 06/06 09:19:54	Track (-205.2, 147.8) ^{Ⓜ 06/06 06:20:00}	* Track AR 11231, supporting EIS study of multiple-return AR.
06/06 10:00:00 - 06/06 11:59:54	Track (552.8, -378.7) ^{Ⓜ 06/06 09:20:00}	* Track AR 11226.
06/06 12:13:00 - 06/06 23:59:54	Track (570.4, -378.9) ^{Ⓜ 06/06 12:10:00}	# Cont.
06/07 06:13:00 - 06/07 10:01:30	Track (672.4, -379.6) ^{Ⓜ 06/07 06:10:00}	* Track AR 11226.

PROG= 12 Inf.-time(s)

└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 49 4-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 6 18-time(s) 2.0sec		
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 1 20.0sec
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 2 20.0sec
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 3 20.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
06/04 12:35:00 - 06/04 13:10:30	Fixed (-914.4, 202.7)	* HOP 79, E-W and Equatorial scans.
06/04 13:41:00 - 06/04 14:00:54	Fixed (-365.9, 217.6)	
06/04 14:04:00 - 06/04 14:49:00	Fixed (19.5, 218.4)	
06/04 15:27:00 - 06/04 15:32:30	Fixed (540.2, 216.3)	
06/04 15:36:30 - 06/04 15:45:54	Fixed (722.5, 214.2)	
06/04 15:49:00 - 06/04 16:05:54	Fixed (902.4, 202.7)	
06/04 16:09:00 - 06/04 16:27:30	Fixed (-885.3, -37.0)	
06/04 17:13:00 - 06/04 17:42:54	Fixed (-686.2, -37.0)	
06/04 17:46:00 - 06/04 18:06:00	Fixed (-89.0, -37.0)	
06/04 18:50:00 - 06/04 19:19:54	Fixed (309.1, -37.0)	
06/04 19:23:00 - 06/04 19:29:54	Fixed (906.3, -37.0)	* HOP 79, final pointing.
06/05 00:03:00 - 06/05 00:19:54	Fixed (0.0, 0.0)	synoptic, shifted manually, extended for SOT jitter diagnostics.
06/06 00:13:00 - 06/06 00:19:54	Fixed (0.0, 0.0)	synoptic, shifted manually.

PROG= 19 1-time(s)

- └─ **Subr= 1 1-time(s) 12.0sec**
 - └─ **Seqn= 7 1-time(s) 4.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ **Seqn= 5 1-time(s) 2.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - └─ **Seqn= 8 1-time(s) 4.0sec**
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ **Seqn= 4 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
06/06 00:23:00 - 06/06 06:03:00	Fixed (-4.0, 859.0)	# HOP 81, polar monitoring, N pole.
06/07 00:13:00 - 06/07 06:09:54	Fixed (-4.0, -941.0)	# HOP 81, polar monitoring, S pole.

PROG= 11 Inf.-time(s)

- └─ **Subr= 1 1-time(s) 120.0sec**
 - └─ **Seqn= 33 1-time(s) 4.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
06/04 10:38:00 - 06/04 12:31:54	Track (214.5, -375.5) ^{Ⓜ 06/04 10:35:00}	* Track AR 11226.
06/04 19:33:00 - 06/04 23:59:54	Track (284.4, -376.2) ^{Ⓜ 06/04 19:30:00}	* Track AR 11226.
06/05 00:23:00 - 06/05 11:54:54	Track (321.5, -376.5) ^{Ⓜ 06/05 00:20:00}	# Cont.
06/05 12:08:00 - 06/05 23:38:30	Track (408.7, -377.4) ^{Ⓜ 06/05 12:05:00}	# Cont.
06/06 00:23:00 - 06/06 06:03:00	Fixed (-4.0, 859.0)	# HOP 81, polar monitoring, N pole.
06/06 06:43:00 - 06/06 09:19:54	Track (-205.2, 147.8) ^{Ⓜ 06/06 06:20:00}	* Track AR 11231, supporting EIS study of multiple-return AR.
06/06 10:00:00 - 06/06 11:59:54	Track (552.8, -378.7) ^{Ⓜ 06/06 09:20:00}	* Track AR 11226.
06/06 12:13:00 - 06/06 23:59:54	Track (570.4, -378.9) ^{Ⓜ 06/06 12:10:00}	# Cont.
06/07 00:13:00 - 06/07 06:09:54	Fixed (-4.0, -941.0)	# HOP 81, polar monitoring, S pole.
06/07 06:13:00 - 06/07 10:01:30	Track (672.4, -379.6) ^{Ⓜ 06/07 06:10:00}	* Track AR 11226.

PROG= 08 1-time(s)

- └─ **Subr= 1 30-time(s) 20.0sec**
 - └─ **Seqn= 87 1-time(s) 2.0sec**
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ **Seqn= 60 1-time(s) 2.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- └─ **Subr= 2 1-time(s) 2.0sec**
 - └─ **Seqn= 90 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
- └─ **Subr= 3 30-time(s) 60.0sec**
 - └─ **Seqn= 87 1-time(s) 2.0sec**
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ **Seqn= 88 1-time(s) 2.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- └─ **Subr= 2 1-time(s) 2.0sec**
 - └─ **Seqn= 90 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
- └─ **Subr= 3 30-time(s) 60.0sec**
 - └─ **Seqn= 87 1-time(s) 2.0sec**
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - └─ **Seqn= 88 1-time(s) 2.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- └─ **Subr= 2 1-time(s) 2.0sec**
 - └─ **Seqn= 90 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s)		2.0sec								
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s)		2.0sec								
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)		600.0sec										
	Seqn= 89		1-time(s)		2.0sec								
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

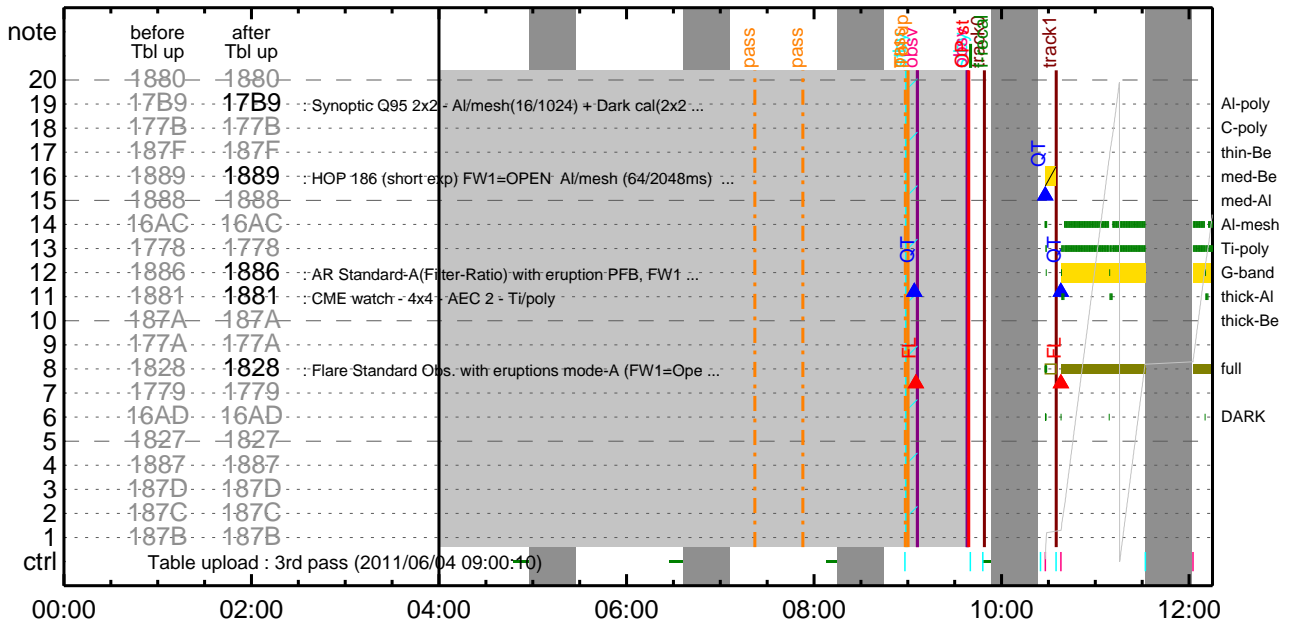
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Flare Detection

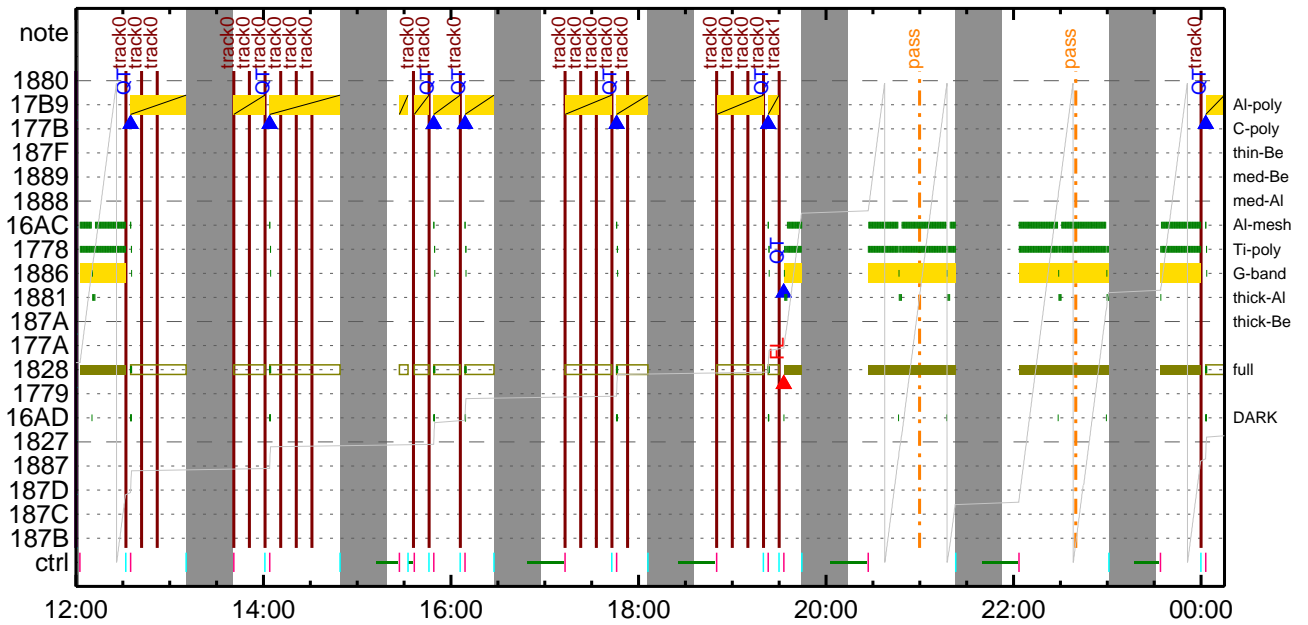
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FLD Patrol												
Term	Pointing (x, y)					Comment						
06/04 10:37:46 - 06/04 12:32:16	Track (214.5, -375.5) [Ⓜ] 06/04 10:35:00					* Track AR 11226.						
06/04 19:32:46 - 06/05 00:00:16	Track (284.4, -376.2) [Ⓜ] 06/04 19:30:00					* Track AR 11226.						
06/05 00:22:46 - 06/05 11:55:16	Track (321.5, -376.5) [Ⓜ] 06/05 00:20:00					# Cont.						
06/05 12:07:46 - 06/06 00:10:16	Track (408.7, -377.4) [Ⓜ] 06/05 12:05:00					# Cont.						
06/06 00:22:46 - 06/06 12:00:16	Fixed (-4.0, 859.0)					# HOP 81, polar monitoring, N pole.						
06/06 12:12:46 - 06/07 00:00:16	Track (570.4, -378.9) [Ⓜ] 06/06 12:10:00					# Cont.						
06/07 00:12:46 - 06/09 09:29:00	Fixed (-4.0, -941.0)					# HOP 81, polar monitoring, S pole.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

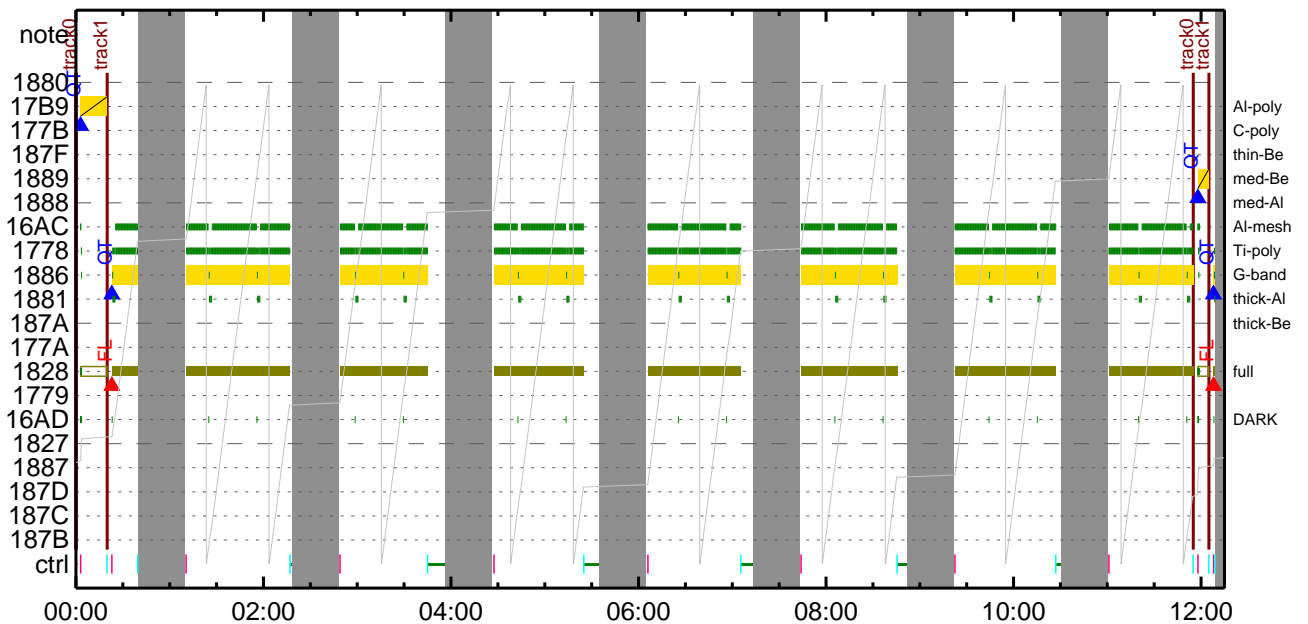
CMDI #0969 2011/06/04



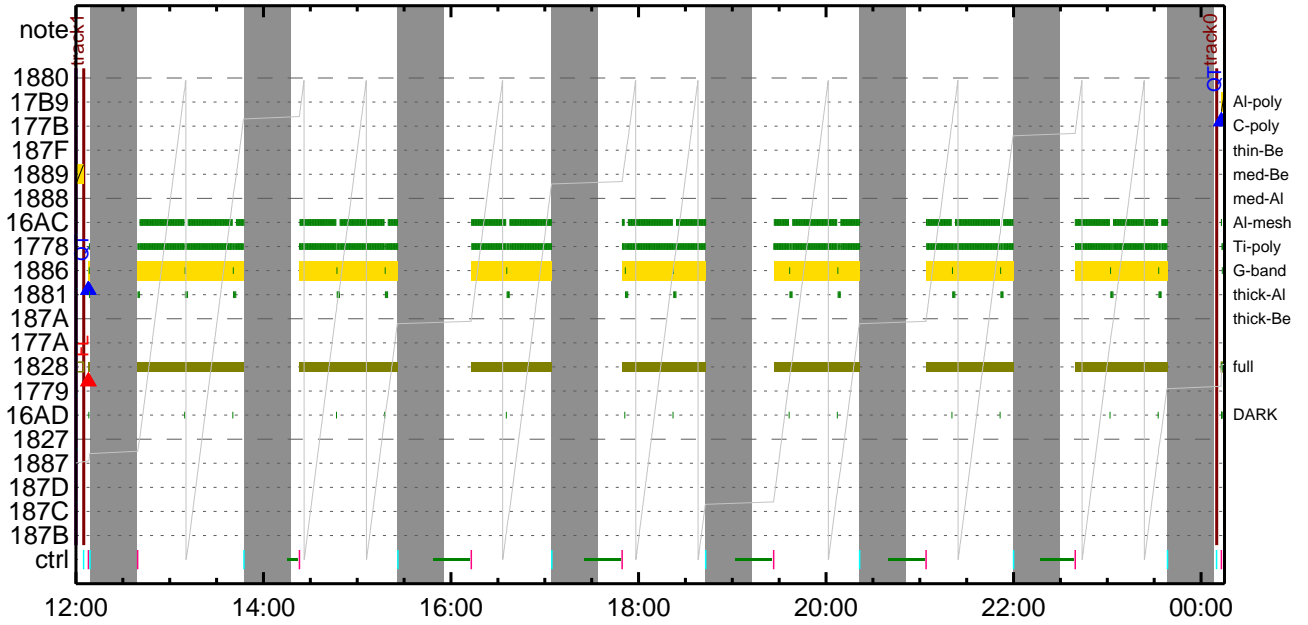
CMDI #0969 2011/06/04



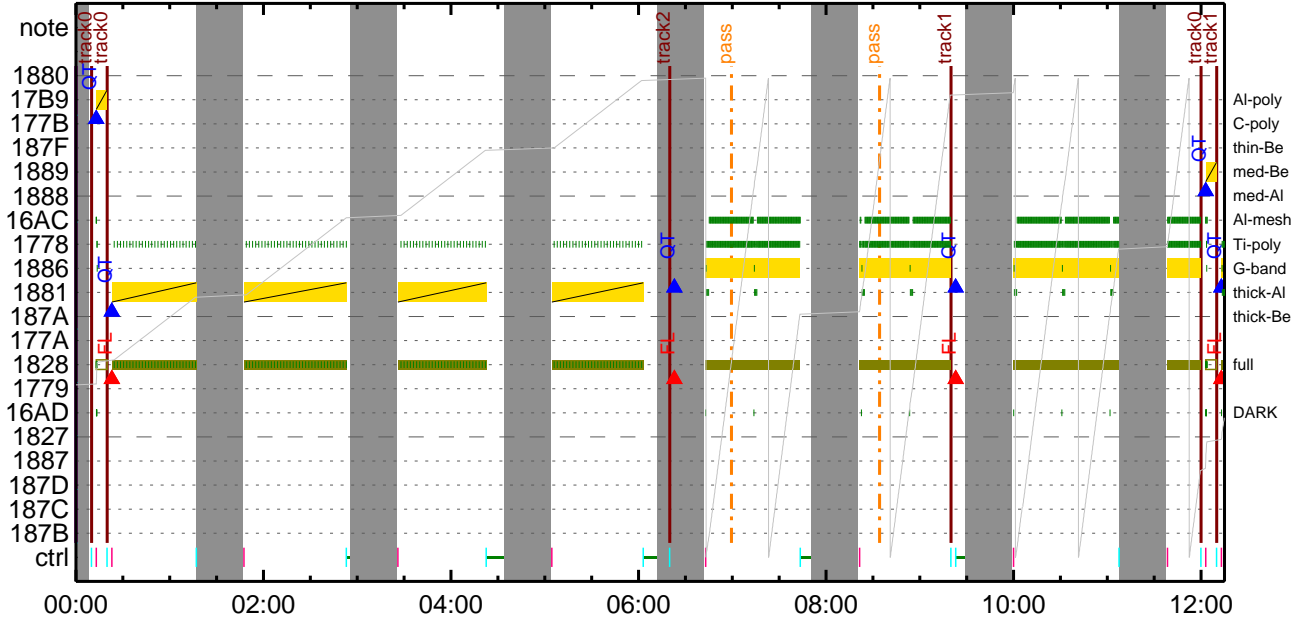
CMDI #0969 2011/06/05



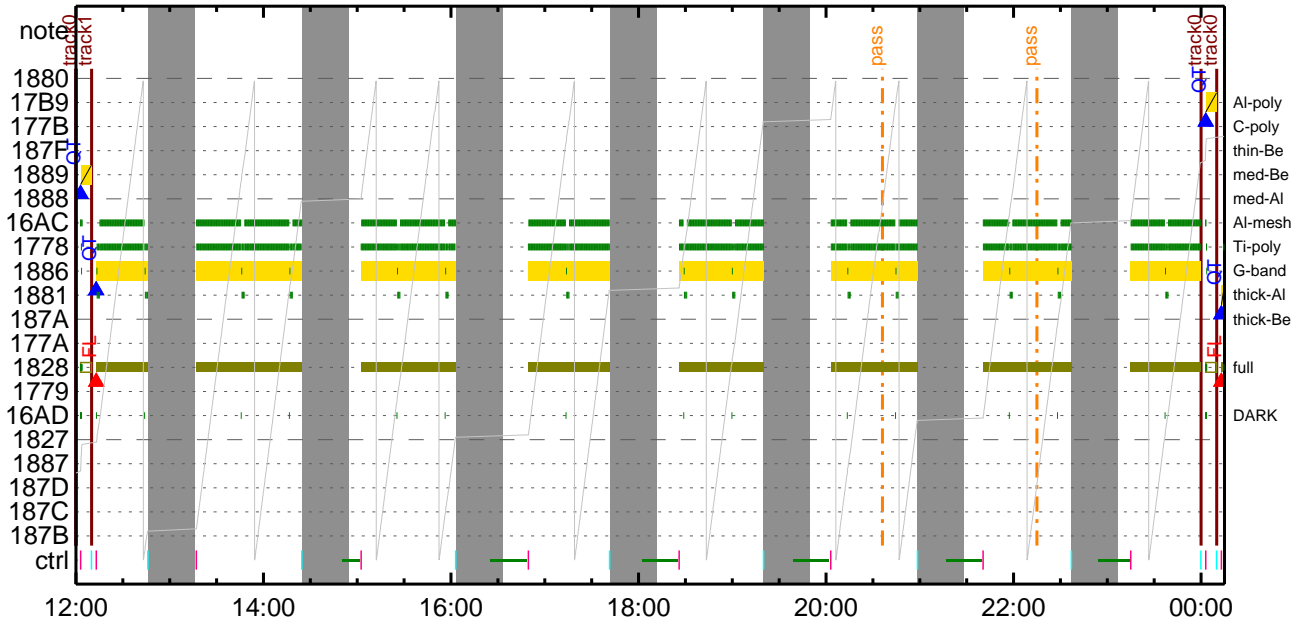
CMDI #0969 2011/06/05



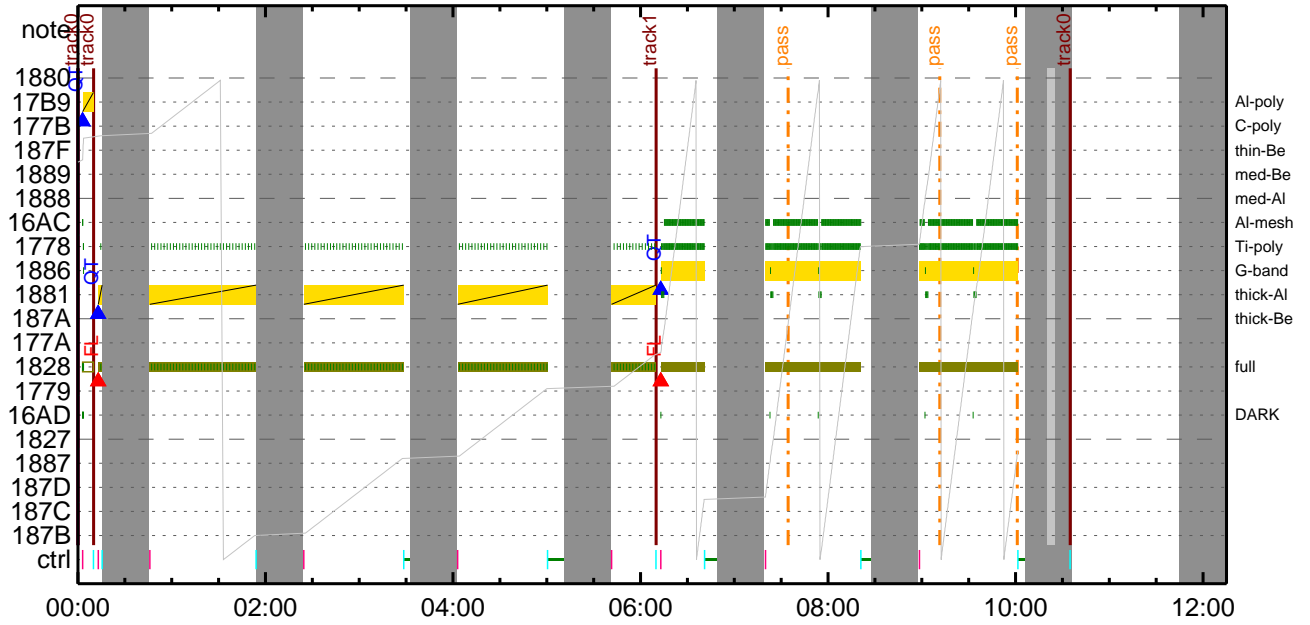
CMDI #0969 2011/06/06



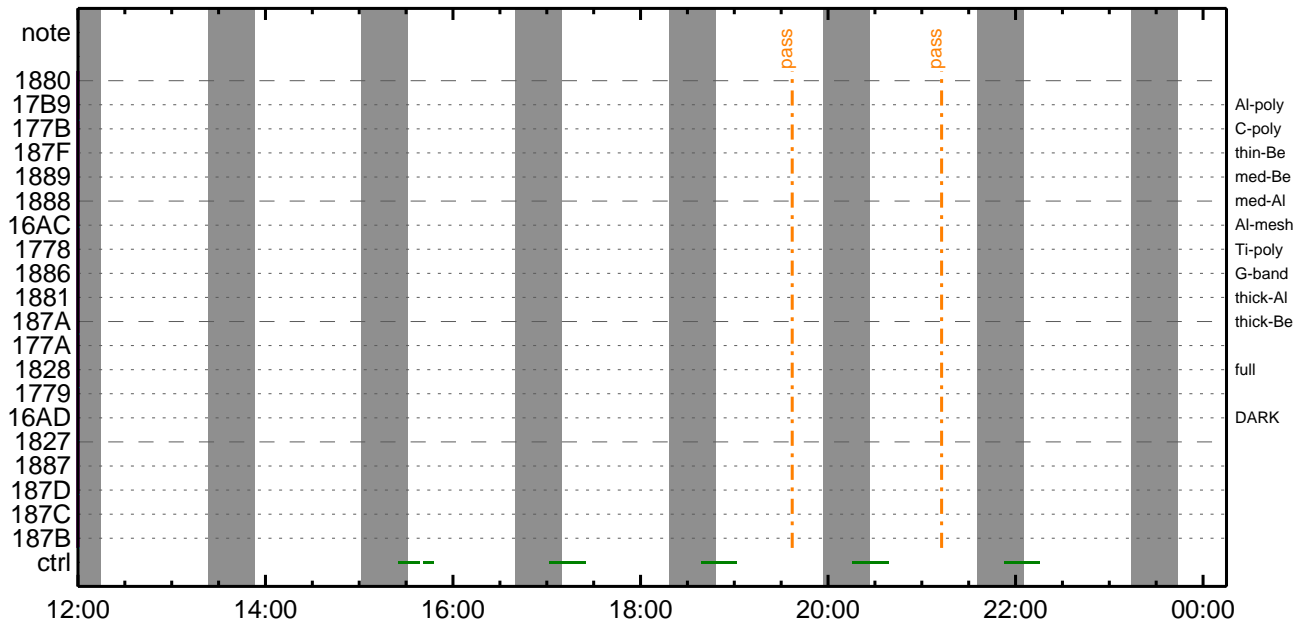
CMDI #0969 2011/06/06



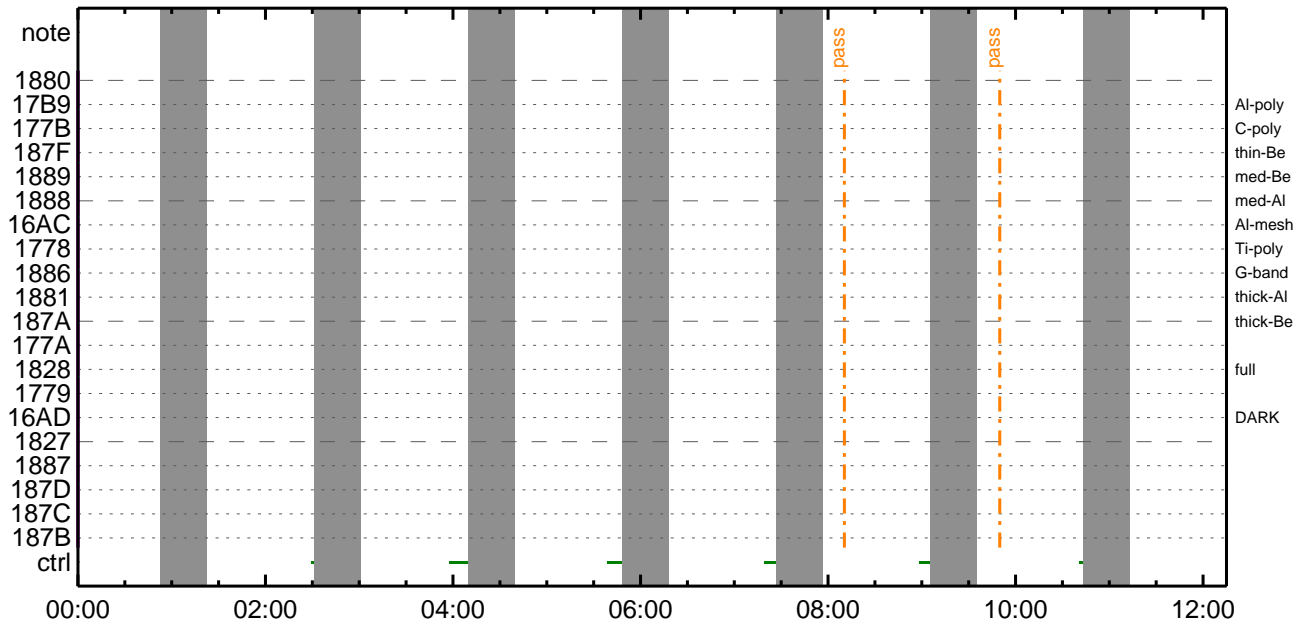
CMDI #0969 2011/06/07



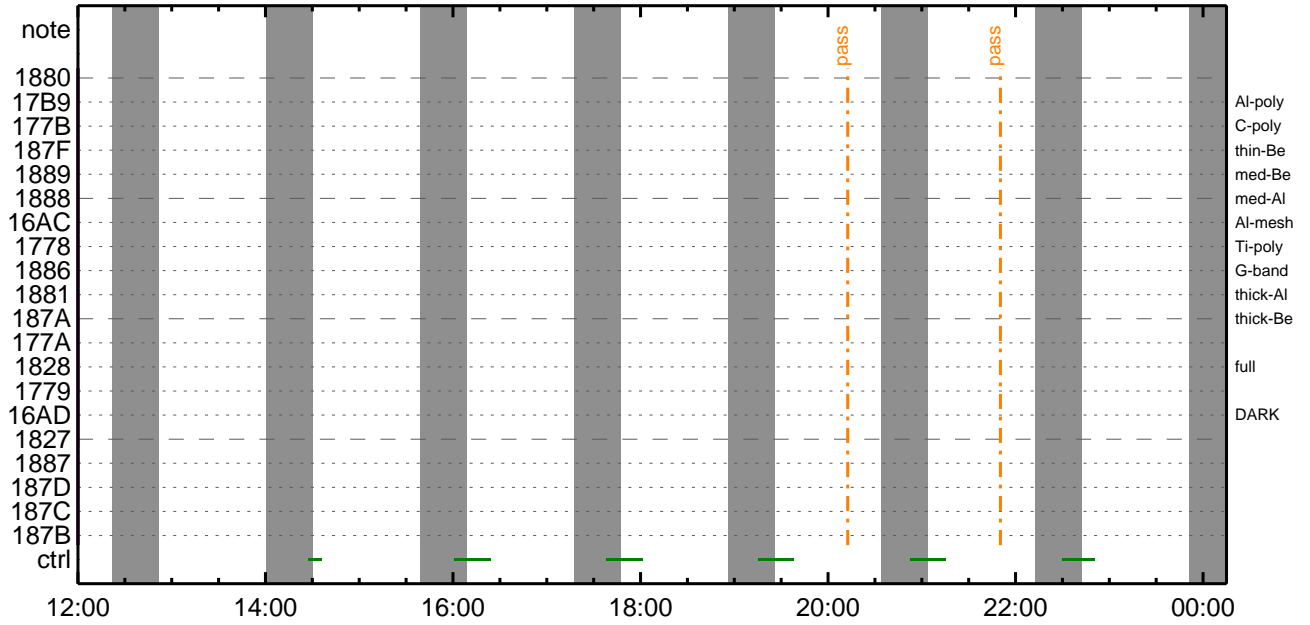
CMDI #0969 2011/06/07



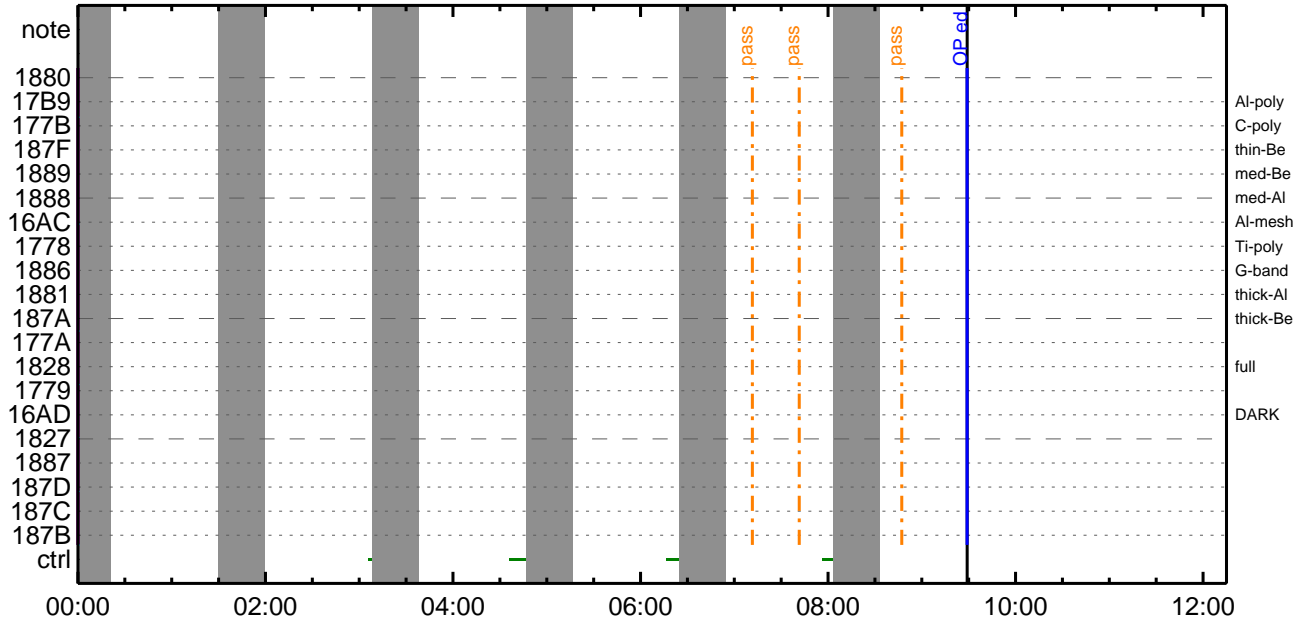
CMDI #0969 2011/06/08



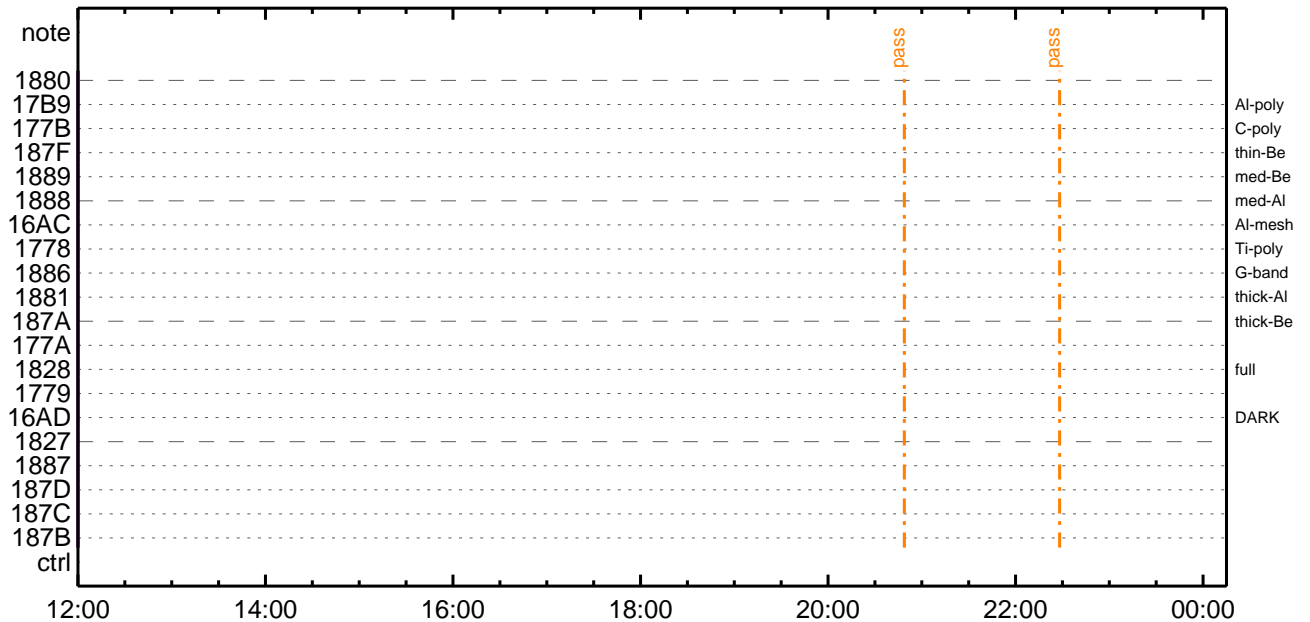
CMDI #0969 2011/06/08



CMDI #0969 2011/06/09



CMDI #0969 2011/06/09




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-115:OP
0104 ( )
0105 S. OG og-115:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPf^°eYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.      çç[HK1_DMP_BLOCK_NUM]      EQ     127
0115 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0116 C.      çç[HK1_DMA_DMP_PIM]        EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0120 C.      çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0121 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0122 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0123 C.      çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0124 C. YAYôYx½^a î»ð³ îÇ§
0125 C.      çç[HK1_DMP_CHK_FLG]      EQ     NON
0126 C. RAM ID=NMOGαî¼E¹ç•ë² îOKαð³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.      çç[HK1_DMP_BLOCK_NUM]      EQ     127
0134 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0135 C.      çç[HK1_DMA_DMP_PIM]        EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0139 C.      çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0140 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0141 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0142 C.      çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0143 C. YAYôYx½^a î»ð³ îÇ§
0144 C.      çç[HK1_DMP_CHK_FLG]      EQ     NON
0145 C. RAM ID=NMOGαî¼E¹ç•ë² îOKαð³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.      çç[HK1_DMP_BLOCK_NUM]      EQ     65
0153 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0154 C.      çç[HK1_DMA_DMP_PIM]        EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0158 C.      çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0159 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32k
0160 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0161 C.      çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0162 C. YAYôYx½^a î»ð³ îÇ§
0163 C.      çç[HK1_DMP_CHK_FLG]      EQ     NON
0164 C. RAM ID=NMOG, RAM ID=OPαî¼E¹ç•ë² îOKαð³ îÇ§
0165 C.
0166 C. ***** °Ê²¼α î¼Ā' ¶Ā°ÊĒ-α°Ā÷¿® (¼āµ-YAYôYx½^e¼çαðĀŌĀæαÇ¼^α°°ðë¼i¹çαÇαĀ) *****
0167 C. DHUã;4YE;Ê¼Y½;Yi;4YE;ËαðîĀα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.      çç[HK1_PKT_FORM_NO]        EQ      2
0171 C.      çç[HK1_PKT_GEN_TIME]      EQ     0.5S
0172 C.      çç[HK1_S_TLM_BIT_RATE]     EQ     32K
0173 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ; § OPOG UPLOADα-Ā÷¿@NGuî¼i¹ç; ç°Ê²¼α îTI-CMDĀ÷¿@αî¼Ā¹Ōα°°Êααα³ ðÊ; f
0180 C.      αβα¿; çSETαÊDUMPαĪÆ±°iYŇY¹αÇ¹Ōα|α³ ðÊ; f
0181 C.
0182 C. TIY³YpYôYĒððĀĀî¿ (UT)
0183 +. TI 2011-06-04 09:34:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0186 C.
0187 +. TI 2011-06-04 09:34:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0190 C.
0191 +. TI 2011-06-04 09:34:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP

```

```

0194 C.
0195 +. TI 2011-06-04 09:38:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C.  °È²¼∞îÄè%îíÑ∞îŷÄŷ$ŷÄŷ⁻¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C.  *****
0206 C.  TIîî°èŷÄŷóŷx
0207 C.  *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PIM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C.  ŷÄŷóŷx¼ªî»∞³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C.  RAM ID=TI_TBL∞îî¼È¹ç•è²îOK∞∞³îÇ§
0229 C.
0230 C.  DHUŷä;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;È∞∞îä∞¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C.
0239 C.  ***** XRT START *****
0240 C.  Execute, after the success of OP upload.
0241 +. TI 2011-06-04 09:38:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC      (c3)
0244 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0245 C.
0246 C.  ***** XRT END *****
0247 C.  Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C.  ***** Start EIS operation (TI set) *****
0251 C.  Execute, after the success of OP upload.
0252 C.  Set EIS TI-commands
0253 +. TI 2011-06-04 09:38:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC      (21 02)
0256 +. TI 2011-06-04 09:38:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC      (22)
0259 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0260 C.  ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C.  *****
0264 C.  SOT TI command set
0265 C.  *****
0266 C.  Execute, after the success of OP upload.
0267 +. TI 2011-06-04 09:38:16.0
0268 DC 07-F0 MDP_SOT_MODE_STBY
0269 BC      (41)
0270 C.  -----
0271 C.    HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0272 C.  -----
0273 C.  ***** SOT END *****
0274 C.
0275 C.  ***** MDP `ûÃî∞î»ö¼ŷ∞ÈÄ∞¹∞èDCBC•x²è *****
0276 C.  (%ã°îŷóŷÄŷÈŷŷŷÄŷçŷèèÈ¼∞∞¼Ä»Ű∞¹∞è)
0277 C.  S. DC-BC dcbc-402:DCBC
0278 C.    (MDP_known_event)
0279 C.
0280 C.
0281 C.  ***** ŷÐŷ¹•î Daily±çîÑ∞È'∞¹∞èDCBC•x²è *****
0282 C.  S. DC-BC dcbc-153:DCBC
0283 C.    (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.  ;ãLOSŷÄŷ$ŷÄŷ⁻¼Ä»Ű;ä
0287 C.
0288 C.  ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-116 2011-06-04 12:11:01 82 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁYSYÁY-¼Á»Û;ã
0005  C.
0006  C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCSS : Reload orbital element (send every contact) *****
0010  C. Áí;Èø¿ðÁß•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈøÈ¼°ÇÏß•ø¿¼ì¹çøÍ;çÀ®, ùø¹øÈøÈøçÁ+¿®ß•øÈøøø³øÈ;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. ***** AOCSS Commands (Tracking Curve Upload) *****
0015  C. Upload the Orbit Element and the Target Attitude
0016  C. RAM-ID:TARGET_ATT
0017  . S. RAM ram-150:TARGET_ATT
0018  ( )
0019  C.
0020  C.
0021  C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023  BC      (07 00 00 00 18 00)
0024  C.
0025  C.      <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026  C.
0027  C.
0028  C. Change the TLMFormatNo for the AOCSS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030  BC      (04 0b f8)
0031  C.
0032  C. Wait for AOCSDUMP to end
0033  C.
0034  . C. Check the dump memory
0035  C.
0036  C.                                Result = OK [ ]
0037  C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039  BC      (02 0a f8)
0040  C.
0041  C.                                <A_***>[TLM STS] FMT = 2 [ ]
0042  C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044  . C.
0045  . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046  +. DC 07-FC EIS_MODE_MANU
0047  BC      (21 02)
0048  . C. Verify EIS in MANUAL mode
0049  . C. Estimated OBSTBL upload time is 1m31s
0050  C. *****
0051  C. EIS START OBSTBL LOAD
0052  C. *****
0053  . S. RAM ram-820:EIS_OBSTBL
0054  ( )
0055  +. DC 07-FC EIS_DUMP_OBSTBL
0056  BC      (07 07 07 00 00 70 00)
0057  C.
0058  C. Execute, after the success of OBSTBL upload.
0059  C. Set EIS TI-commands
0060  +. TI 2011-06-04 09:38:50.0
0061  DC 07-FC EIS_MODE_CHG_ENA
0062  BC      (20)
0063  . C.                                [ ] [HK1_TI_CMD_NUM]   EQ   1 COUNTUP
0064  C. *****
0065  C. EIS END OBSTBL LOAD
0066  C. *****
0067  C.
0068  . C. ***** MDP `ûÁÍøÍ»ó¼YøÈÁðø¹øÈDCBC•x²è *****
0069  C. (¼á°ÍYÓYÁYÈYÈYÈYáYçYÈøÈ¼ø¼Á»Ûø¹øÈ)
0070  . S. DC-BC dcbc-402:DCBC
0071  (MDP_known_event)
0072  C.
0073  C.
0074  . C. ***** YDY¹.İ Daily±¿ÍÑøÈ`øø¹øÈDCBC•x²è *****
0075  . S. DC-BC dcbc-153:DCBC
0076  (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077  C.
0078  C.
0079  . C. ;ãLOSÁYYSYÁY-¼Á»Û;ã
0080  C.
0081  . C. ***** LOS *****
0082  C.

```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 +. DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 +. DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0c)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 08)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2011-06-04 09:38:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-261:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 . C. < Stop SP table >
0189 +. DC 07-F0 MDP_SP_CTRL_MANU
0190 BC (61)
0191 C. -----
0192 C. MDP_SP_CTRL_MODE = MANU [ ]
0193 C. -----

```

```
0194 C.
0195 . C. <Upload SP Observation Table>
0196 . S. RAM ram-289:MDP_OBS_S
0197 ( )
0198 C.
0199 . C. < Dump RAMID=MDP_OBS_S >
0200 +. DC 07-F0 MDP_DUMP_SPTBL
0201 BC (83 07 00 00 00 38 b8)
0202 C. -----
0203 C. MDP_OBS_S verify = OK/NG [ ]
0204 C. -----
0205 C.
0206 C. *****
0207 C. SOT TI command set
0208 C. *****
0209 C. Execute, after the success of TBL upload.
0210 +. TI 2011-06-04 09:38:18.0
0211 DC 07-F0 MDP_SOT_MODE_OBSV
0212 BC (40)
0213 . C. -----
0214 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0215 C. -----
0216 C.
0217 C.
0218 . C. ***** MDP `úÃîî»ö%ÝðËÂð¹æDCBC•x²è *****
0219 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèæË¼ð¼Á»Û¹æ)
0220 . S. DC-BC dcbc-402:DCBC
0221 (MDP_known_event)
0222 C.
0223 C.
0224 . C. ***** ¥DÝ¹•Ï Daily±¿íÑæË´Ø¹æDCBC•x²è *****
0225 . S. DC-BC dcbc-153:DCBC
0226 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0227 C.
0228 C.
0229 . C. ¡ãLOS¥Á¥$¥Ã¥¼¥»Û¡ä
0230 C.
0231 . C. ***** LOS *****
0232 C.
```

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XRT_OGLIST_0969.chk

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*** OP Sequence for XRT ***

2011/06/04	09:40:00.5	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	09:40:10.5	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2011/06/04	09:44:10.5	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/06/04	09:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	09:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/04	09:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/04	09:49:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	00	00
2011/06/04	09:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/04	10:24:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	10:24:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/06/04	10:25:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/04	10:25:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/04	10:25:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/04	10:27:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2011/06/04	10:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/04	10:34:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	10:35:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/06/04	10:37:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/06/04	10:37:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/04	10:37:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/04	10:37:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/04	10:37:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/04	10:37:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/04	10:37:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2011/06/04	10:37:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/06/04	10:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/04	11:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	11:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/04	11:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/04	11:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/04	12:01:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	12:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/04	12:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	12:31:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/06/04	12:32:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	ed	fc	51	47
2011/06/04	12:32:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/04	12:32:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/04	12:32:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/04	12:34:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2011/06/04	12:35:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/04	12:42:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	ec	f6	41	47
2011/06/04	12:52:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	ec	c5	31	16
2011/06/04	13:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/04	13:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/04	13:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/04	13:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2011/06/04	13:40:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	13:41:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	ec	ad	20	83
2011/06/04	13:41:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	13:51:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	ec	9c	0f	95
2011/06/04	14:00:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	14:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/06/04	14:01:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ec	94	fe	46
2011/06/04	14:01:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2011/06/04	14:01:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2011/06/04	14:01:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/06/04	14:03:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4			13
2011/06/04	14:04:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	14:11:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	ec	9c	f1	79
2011/06/04	14:21:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	ec	ad	e0	8c
2011/06/04	14:31:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	ec	c5	cf	f8
2011/06/04	14:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	14:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/06/04	14:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/06/04	14:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/06/04	15:26:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	15:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	15:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	15:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/06/04	15:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/06/04	15:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	15:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/06/04	15:36:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	ec	f6	bf	c7
2011/06/04	15:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	15:45:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	15:45:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/06/04	15:46:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	ed	fc	af	c7
2011/06/04	15:46:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2011/06/04	15:46:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2011/06/04	15:46:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/06/04	15:48:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4			13
2011/06/04	15:49:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	16:05:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	16:05:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/06/04	16:06:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	03	4b	4e	b0
2011/06/04	16:06:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2011/06/04	16:06:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2011/06/04	16:06:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/06/04	16:08:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4			13
2011/06/04	16:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/06/04	16:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/06/04	16:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2011/06/04	16:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/06/04	16:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			

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2011/06/04	17:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	17:13:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	03	4b	3c	fd
2011/06/04	17:13:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2011/06/04	17:23:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	03	4b	2b	4b
2011/06/04	17:33:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	03	4b	19	99
2011/06/04	17:42:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2011/06/04	17:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/06/04	17:43:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	03	4b	07	e7
2011/06/04	17:43:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2011/06/04	17:43:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2011/06/04	17:43:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2011/06/04	17:45:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 13
2011/06/04	17:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2011/06/04	17:53:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	03	4b	f6	36
2011/06/04	18:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2011/06/04	18:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2011/06/04	18:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2011/06/04	18:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2011/06/04	18:49:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	18:50:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	03	4b	e4	84
2011/06/04	18:50:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2011/06/04	19:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	03	4b	d2	d1
2011/06/04	19:10:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	03	4b	c1	1f
2011/06/04	19:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2011/06/04	19:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/06/04	19:20:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	03	4b	af	75
2011/06/04	19:20:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2011/06/04	19:20:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2011/06/04	19:20:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2011/06/04	19:22:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 13
2011/06/04	19:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2011/06/04	19:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2011/06/04	19:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/06/04	19:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2011/06/04	19:32:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2011/06/04	19:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2011/06/04	19:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2011/06/04	19:32:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2011/06/04	19:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2011/06/04	19:32:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0c
2011/06/04	19:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 08
2011/06/04	19:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2011/06/04	19:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2011/06/04	19:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2011/06/04	19:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2011/06/04	19:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2011/06/04	20:26:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/04	20:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0

2011/06/04	21:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/04	21:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/04	21:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/04	21:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/04	22:02:30.0	XRT_Custom_418_OG [0x1a2]						
2011/06/04	22:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/04	23:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/04	23:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/04	23:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/04	23:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/04	23:33:00.0	XRT_Custom_418_OG [0x1a2]						
2011/06/04	23:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/04	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/04	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/05	00:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/06/05	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/05	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/05	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/05	00:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2011/06/05	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/05	00:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/05	00:20:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00		
2011/06/05	00:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/06/05	00:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/06/05	00:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/06/05	00:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/06/05	00:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/05	00:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/05	00:22:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2011/06/05	00:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/06/05	00:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/05	00:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/05	00:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/05	00:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/05	00:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/05	01:09:30.0	XRT_Custom_418_OG [0x1a2]						
2011/06/05	01:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/05	02:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/05	02:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/05	02:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/05	02:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/05	02:48:00.0	XRT_Custom_418_OG [0x1a2]						
2011/06/05	02:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/05	03:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/05	03:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/05	03:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/05	03:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/05	04:26:30.0	XRT_Custom_418_OG [0x1a2]						
2011/06/05	04:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

2011/06/05	05:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	05:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	05:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	05:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	06:05:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	06:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	07:05:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	07:05:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	07:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	07:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	07:43:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	07:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	08:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	08:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	08:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	08:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	09:21:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	09:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	10:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	10:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	10:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	10:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	11:00:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	11:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	11:54:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	11:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/05	11:55:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/05	11:55:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/05	11:55:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/05	11:55:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/05	11:57:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2011/06/05	11:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	12:04:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	12:05:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/06/05	12:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/06/05	12:07:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/05	12:07:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/05	12:07:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/05	12:07:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/05	12:07:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	12:07:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/06/05	12:07:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/05	12:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	12:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	12:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	12:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	12:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	12:38:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	12:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/06/05	13:47:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	13:47:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	13:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	13:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	14:22:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	14:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	15:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	15:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	15:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	15:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	16:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	16:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	17:04:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	17:04:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	17:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	17:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	17:48:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	17:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	18:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	18:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	18:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	18:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	19:25:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	19:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	20:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	20:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	20:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	20:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	21:03:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	21:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	22:00:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	22:00:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	22:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	22:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/05	22:38:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/05	22:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/05	23:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/05	23:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/05	23:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/05	23:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/06	00:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/06	00:09:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/06	00:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/06	00:10:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/06	00:10:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/06	00:10:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/06	00:12:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2011/06/06	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/06	00:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/06	00:20:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]							

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2011/06/06	00:22:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	5	02-76 00 b3 a6 00 5a
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2011/06/06	00:22:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2011/06/06	00:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2011/06/06	00:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2011/06/06	00:22:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/06/06	00:22:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	00:22:56.0	XRT_QT_PROG_SET_446_OG [0x1be]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2011/06/06	00:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2011/06/06	00:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/06/06	01:17:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	01:17:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	01:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/06/06	01:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/06/06	01:46:30.0	XRT_Custom_418_OG [0x1a2]		
2011/06/06	01:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/06/06	02:53:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	02:53:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	02:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/06/06	02:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/06/06	03:25:00.0	XRT_Custom_418_OG [0x1a2]		
2011/06/06	03:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/06/06	04:22:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	04:22:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	04:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/06/06	04:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/06/06	05:03:30.0	XRT_Custom_418_OG [0x1a2]		
2011/06/06	05:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/06/06	06:03:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	06:03:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	06:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/06/06	06:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/06/06	06:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	06:20:00.0	AOCS_Orе-point_Start_25_OG [0x0af]		
		AOCU_NM	5	02-76 02 00 00 00 00
2011/06/06	06:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2011/06/06	06:22:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2011/06/06	06:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2011/06/06	06:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2011/06/06	06:22:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/06/06	06:22:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	06:22:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0c
2011/06/06	06:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2011/06/06	06:42:00.0	XRT_Custom_418_OG [0x1a2]		
2011/06/06	06:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/06/06	07:43:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/06/06	07:43:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/06/06	07:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/06/06	07:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/06/06	08:20:30.0	XRT_Custom_418_OG [0x1a2]		
2011/06/06	08:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		

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2011/06/06	09:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	09:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2011/06/06	09:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/06/06	09:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/06	09:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/06	09:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/06	09:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/06	09:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/06	09:22:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/06/06	09:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/06	09:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	09:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/06	09:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/06	09:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/06	09:59:00.0	XRT_Custom_418_OG [0x1a2]				
2011/06/06	10:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/06	11:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	11:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/06	11:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/06	11:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/06	11:37:30.0	XRT_Custom_418_OG [0x1a2]				
2011/06/06	11:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/06	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	11:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/06/06	12:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2011/06/06	12:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/06/06	12:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/06/06	12:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/06	12:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2011/06/06	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/06	12:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	12:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2011/06/06	12:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/06/06	12:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/06	12:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/06	12:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/06	12:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/06	12:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/06	12:12:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/06/06	12:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/06	12:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/06	12:46:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/06	12:46:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/06	12:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/06	12:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/06	13:16:00.0	XRT_Custom_418_OG [0x1a2]				
2011/06/06	13:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/06	14:24:30.0	XRT_CTRL_MANU_408_OG [0x198]				

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2011/06/06	14:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	14:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	14:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	15:01:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	15:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	16:03:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	16:03:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	16:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	16:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	16:48:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	16:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	17:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	17:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	17:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	17:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	18:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	18:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	19:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	19:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	19:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	19:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	20:02:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	20:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	20:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	20:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	20:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	21:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	21:39:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	21:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	22:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	22:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/06	22:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/06/06	22:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/06/06	23:14:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/06	23:15:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/06	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/07	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/06/07	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/07	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/07	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/07	00:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2011/06/07	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/07	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/07	00:10:00.0	AOCS_Ore-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 53 a5 00 5a		
2011/06/07	00:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/07	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/06/07	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/06/07	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		

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2011/06/07	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/07	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	00:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/06/07	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/07	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	00:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	00:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	00:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/07	00:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/07	00:45:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/07	00:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	01:54:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	01:54:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	01:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/07	01:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/07	02:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/07	02:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	03:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	03:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	03:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/07	03:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/07	04:02:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/07	04:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	05:00:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	05:00:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	05:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/07	05:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/07	05:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/07	05:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/06/07	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/06/07	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/07	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/07	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/07	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/07	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	06:12:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/06/07	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/07	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	06:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	06:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	06:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/07	06:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/07	07:19:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/07	07:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/07	08:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/07	08:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/07	08:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2011/06/07	08:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/07	08:57:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/07	08:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/07	10:01:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/07	10:01:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/07	10:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/07	10:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/07	10:34:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/07	10:35:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00