

# XRT Timeline to be uploaded on 2011/06/14

Period: 2011/06/14 09:50:00 - 2011/06/18 10:40:00

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**Normal mode**

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**XOB #1889: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1**

Term	Pointing (x, y)	Comment
06/14 10:03:00 - 06/14 10:09:54	Fixed ( 0.0, 0.0)	# OP start + 10min, synoptic and HOP-186.
06/15 10:43:00 - 06/15 11:00:00	Fixed ( 0.0, 0.0)	synoptic and HOP-186.
06/16 09:57:00 - 06/16 10:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted and HOP-186.

**PROG= 16 1-time(s)**

- Subr= 1 1-time(s) 12.0sec**
  - Seqn= 7 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 8 1-time(s) 4.0sec**
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 4 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec**
  - Seqn= 81 1-time(s) 2.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

**XOB #1886: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 512x512 at 1064 1048, 100s cad**

Term	Pointing (x, y)	Comment
06/14 10:13:00 - 06/14 23:59:54	Track ( 34.5, -323.0) <sup>Ⓞ 06/14 10:10:00</sup>	# Track AR 11234
06/15 00:13:00 - 06/15 07:59:54	Track ( 153.2, -323.8) <sup>Ⓞ 06/15 00:10:00</sup>	# Return to AR. HOP-126 from 8-10UT.
06/16 04:44:00 - 06/16 05:40:00	Track ( 270.6, -324.3) <sup>Ⓞ 06/15 14:20:00</sup>	# Return to AR. HOP-126 from 8-10UT.

**PROG= 12 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec**
  - Seqn= 19 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 49 4-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec**
  - Seqn= 6 18-time(s) 2.0sec**
    - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 1 20.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 2 20.0sec
    - Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 3 20.0sec

**XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16**

Term	Pointing (x, y)	Comment
06/15 00:03:00 - 06/15 00:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 19 1-time(s)**

- Subr= 1 1-time(s) 12.0sec**
  - Seqn= 7 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 8 1-time(s) 4.0sec**
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 4 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

<b>XOB #188A: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 40s cadence</b>													
Term		Pointing (x, y)					Comment						
06/15 08:03:00 - 06/15 10:39:54		Track ( 153.2, -323.8) @ 06/15 00:10:00					# Return to AR. HOP-126 from 8-10UT.						
06/16 06:20:30 - 06/16 09:53:54		Fixed ( 0.0, -975.0)					# South Pole observations with La Palma (HOP-126).						
<b>PROG= 02 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 180.0sec</b>													
└─ <b>Seqn= 84 60-time(s) 40.0sec</b>													
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec
└─ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long</b>													
Term		Pointing (x, y)					Comment						
06/16 10:07:05 - 06/16 10:13:54		Fixed ( -528.4, -528.4)					# XRT post bake-out quadrant pointings 1/4						
<b>PROG= 13 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 12.0sec</b>													
└─ <b>Seqn= 38 1-time(s) 12.0sec</b>													
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>													
└─ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long</b>													
Term		Pointing (x, y)					Comment						
06/16 10:17:00 - 06/16 10:23:54		Fixed ( 528.4, -528.4)					# 2/4						
<b>PROG= 07 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 12.0sec</b>													
└─ <b>Seqn= 36 1-time(s) 12.0sec</b>													
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>													
└─ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long</b>													
Term		Pointing (x, y)					Comment						
06/16 10:27:00 - 06/16 10:33:54		Fixed ( 528.4, 528.4)					# 3/4						
<b>PROG= 09 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 12.0sec</b>													
└─ <b>Seqn= 39 1-time(s) 12.0sec</b>													
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>													
└─ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long</b>													
Term		Pointing (x, y)					Comment						
06/16 10:37:00 - 06/16 10:42:30		Fixed ( -528.4, 528.4)					# 4/4						
<b>PROG= 18 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 12.0sec</b>													
└─ <b>Seqn= 40 1-time(s) 12.0sec</b>													
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x512 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x512 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 93 2-time(s) 2.0sec</b>													
└─ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)										
Term	Pointing (x, y)		Comment							
06/14 10:13:00 - 06/14 23:59:54	Track ( 34.5, -323.0) @ 06/14 10:10:00		# Track AR 11234							
06/15 00:13:00 - 06/15 07:59:54	Track ( 153.2, -323.8) @ 06/15 00:10:00		# Return to AR. HOP-126 from 8-10UT.							
06/15 08:03:00 - 06/15 10:39:54	Track ( 153.2, -323.8) @ 06/15 00:10:00		# Return to AR. HOP-126 from 8-10UT.							
06/16 04:44:00 - 06/16 05:40:00	Track ( 270.6, -324.3) @ 06/15 14:20:00		# Return to AR. HOP-126 from 8-10UT.							
06/16 06:20:30 - 06/16 09:53:54	Fixed ( 0.0, -975.0)		# South Pole observations with La Palma (HOP-126).							
<b>PROG= 08 1-time(s)</b>										
<b>Subr= 1 30-time(s) 20.0sec</b>										
<b>Seqn= 87 1-time(s) 2.0sec</b>										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>										
<b>Seqn= 90 1-time(s) 2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>										
<b>Seqn= 87 1-time(s) 2.0sec</b>										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>										
<b>Seqn= 90 1-time(s) 2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>										
<b>Seqn= 87 1-time(s) 2.0sec</b>										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>										
<b>Seqn= 90 1-time(s) 2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>										
<b>Seqn= 87 1-time(s) 2.0sec</b>										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>										
<b>Seqn= 89 1-time(s) 2.0sec</b>										
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1 0 2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1 0 2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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### Active Region Search

\* \* \* \* \*

NOT USED

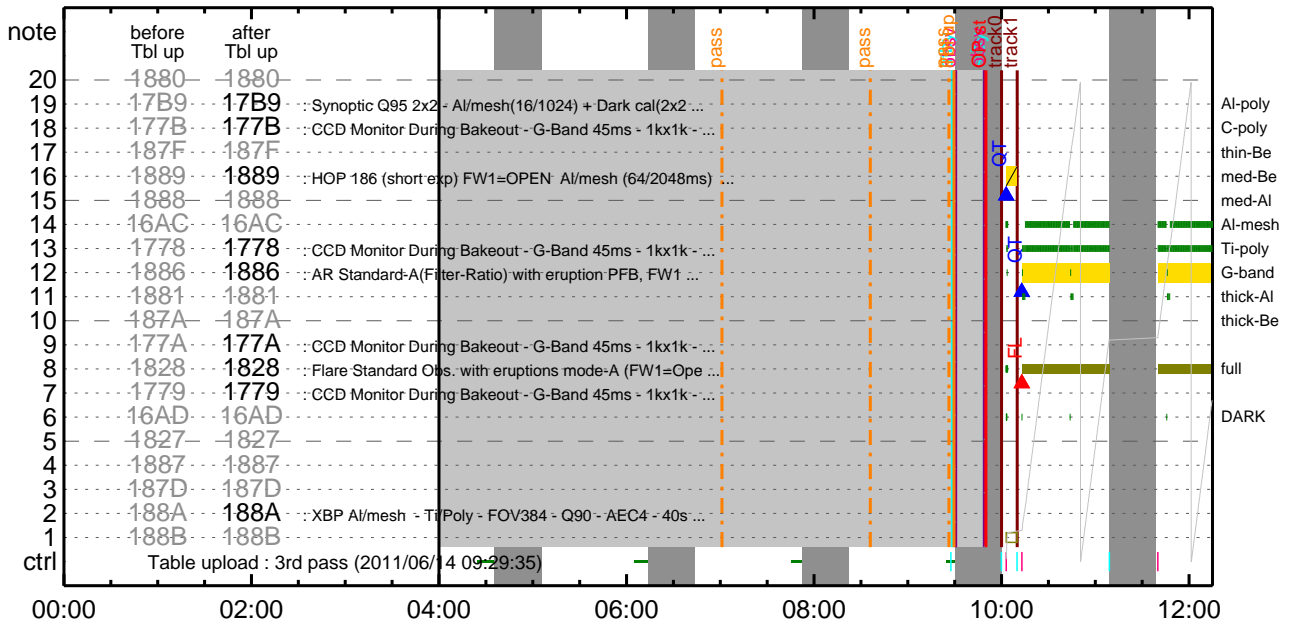
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### Flare Detection

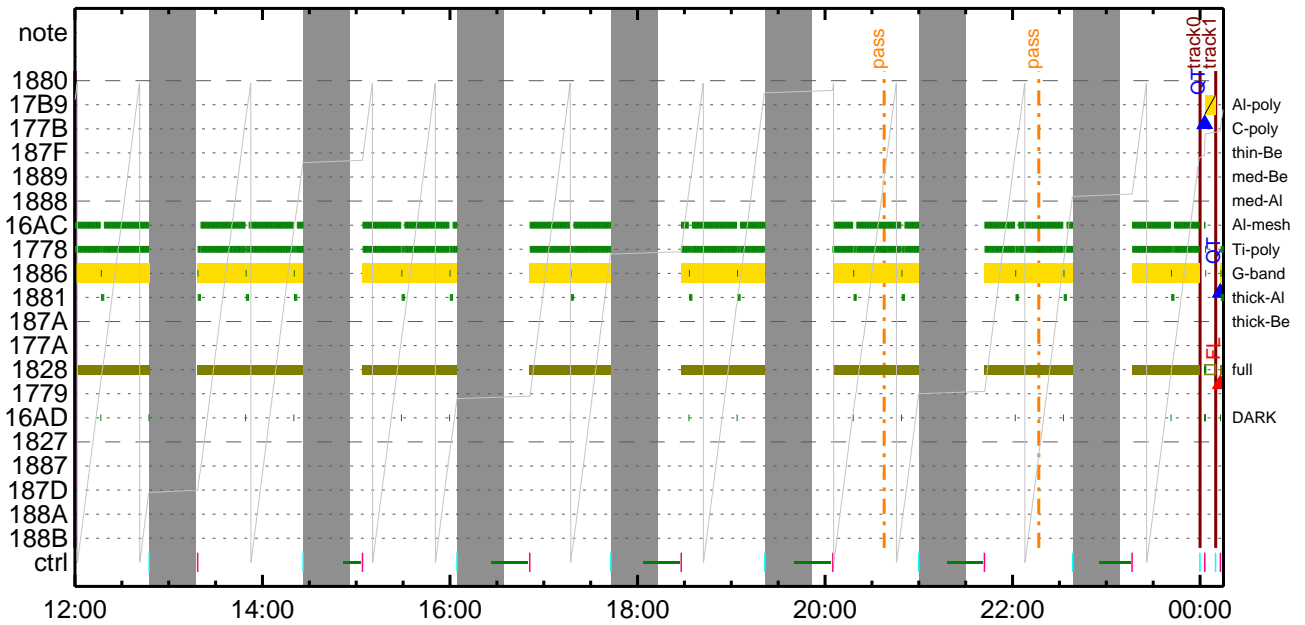
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FLD Patrol										
Term	Pointing (x, y)		Comment							
06/14 10:12:46 - 06/15 00:00:16	Track ( 34.5, -323.0) @ 06/14 10:10:00		# Track AR 11234							
06/15 00:12:46 - 06/15 10:40:16	Track ( 153.2, -323.8) @ 06/15 00:10:00		# Return to AR. HOP-126 from 8-10UT.							
06/16 04:43:46 - 06/16 09:54:16	Track ( 270.6, -324.3) @ 06/15 14:20:00		# Return to AR. HOP-126 from 8-10UT.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

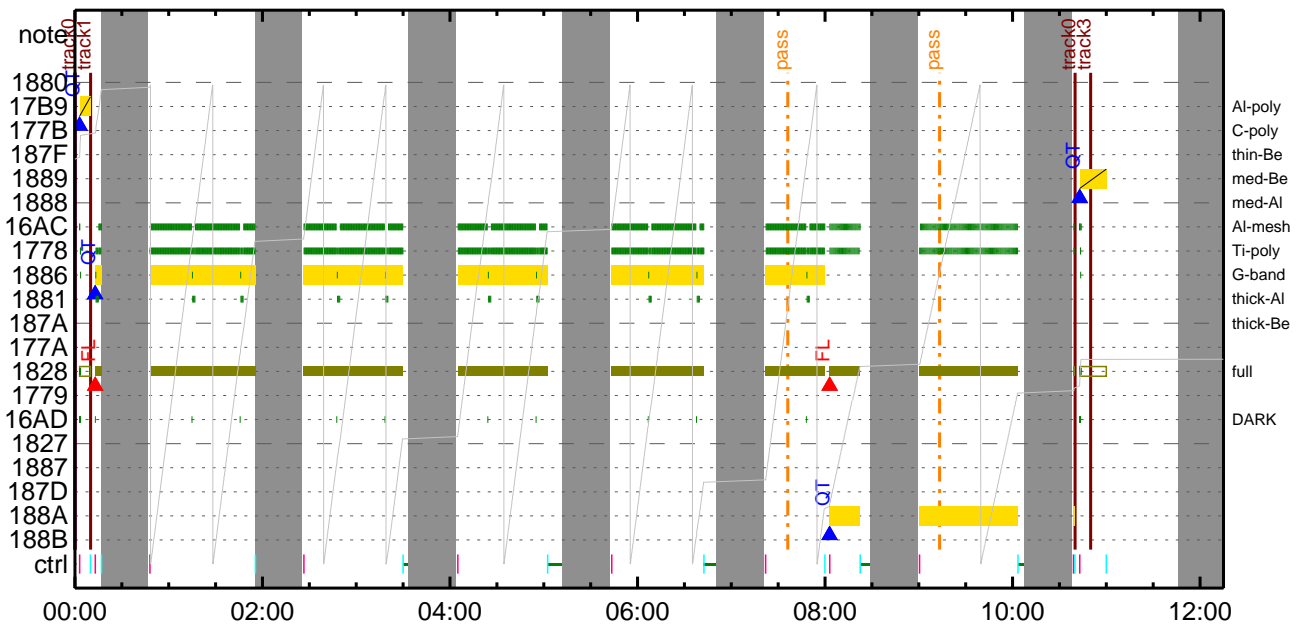
### CMDI #0984 2011/06/14



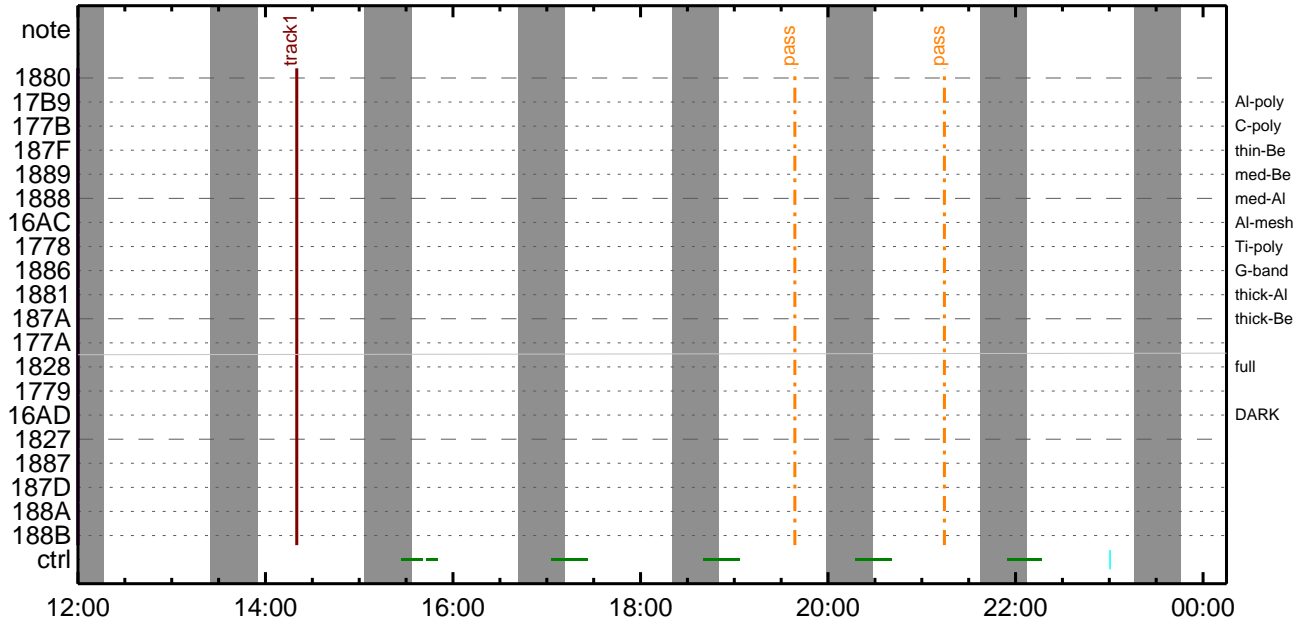
### CMDI #0984 2011/06/14



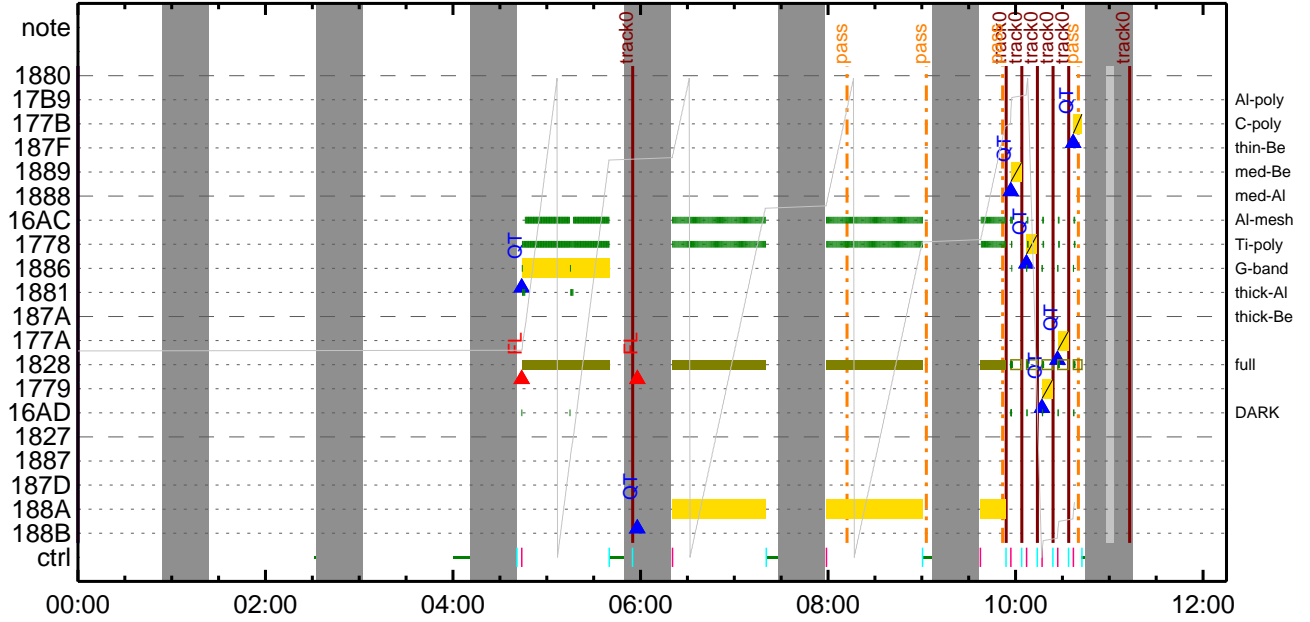
### CMDI #0984 2011/06/15



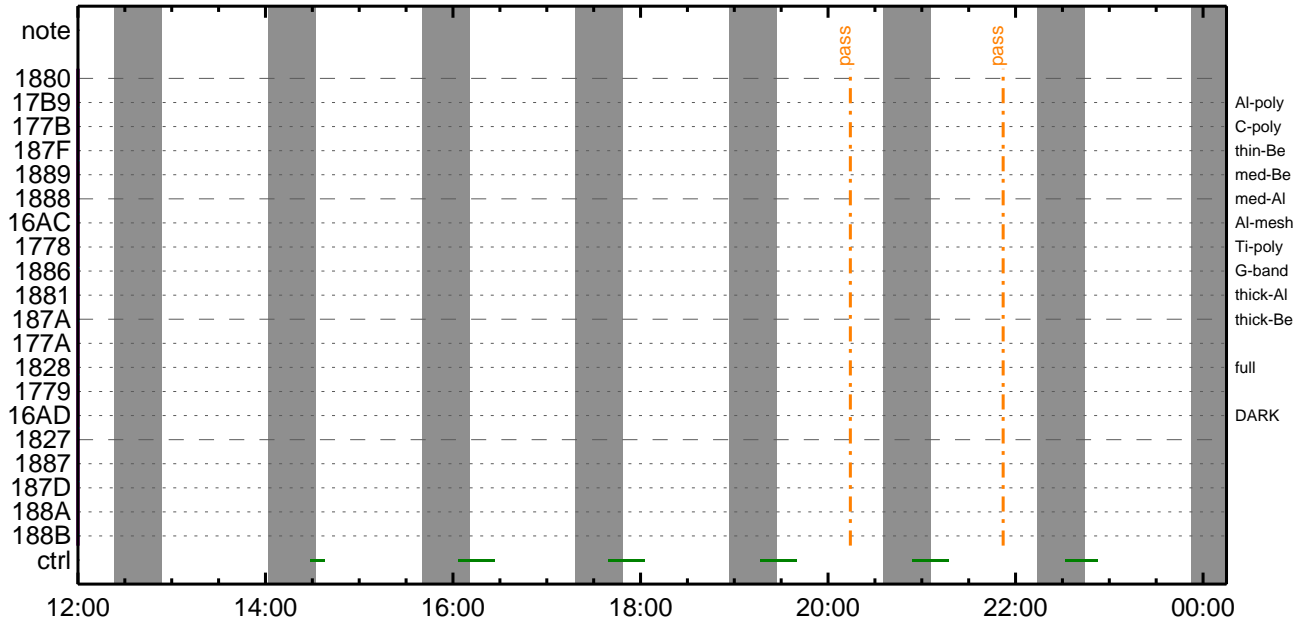
CMDI #0984 2011/06/15



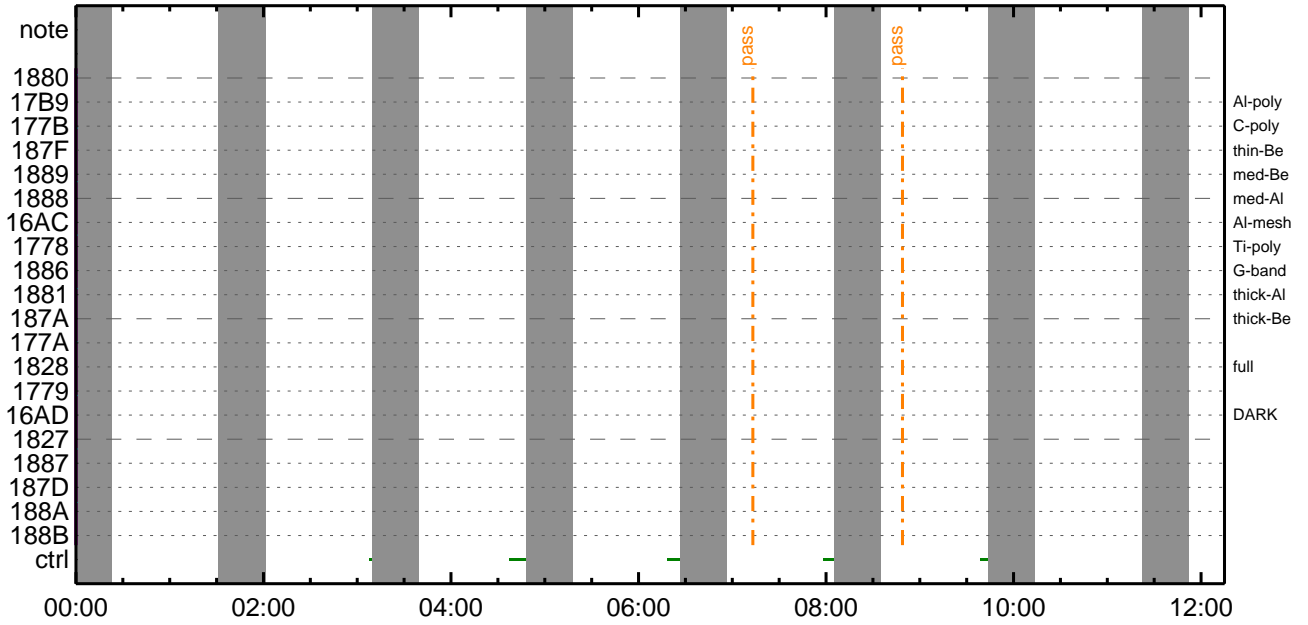
CMDI #0984 2011/06/16



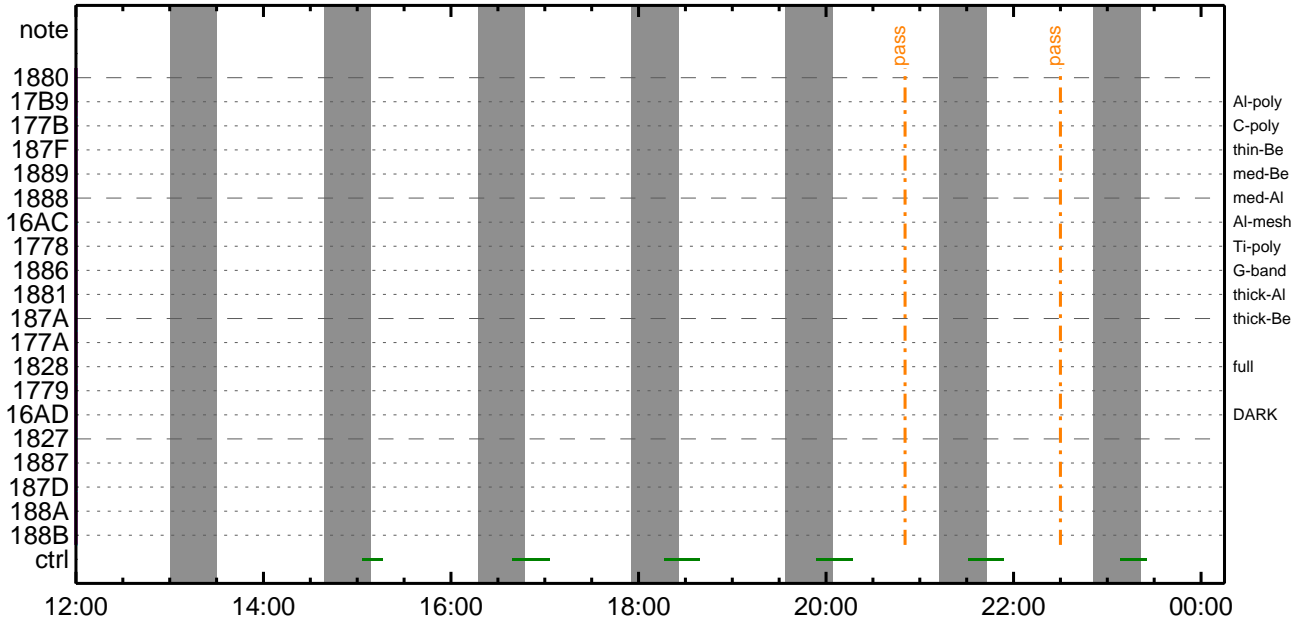
CMDI #0984 2011/06/16



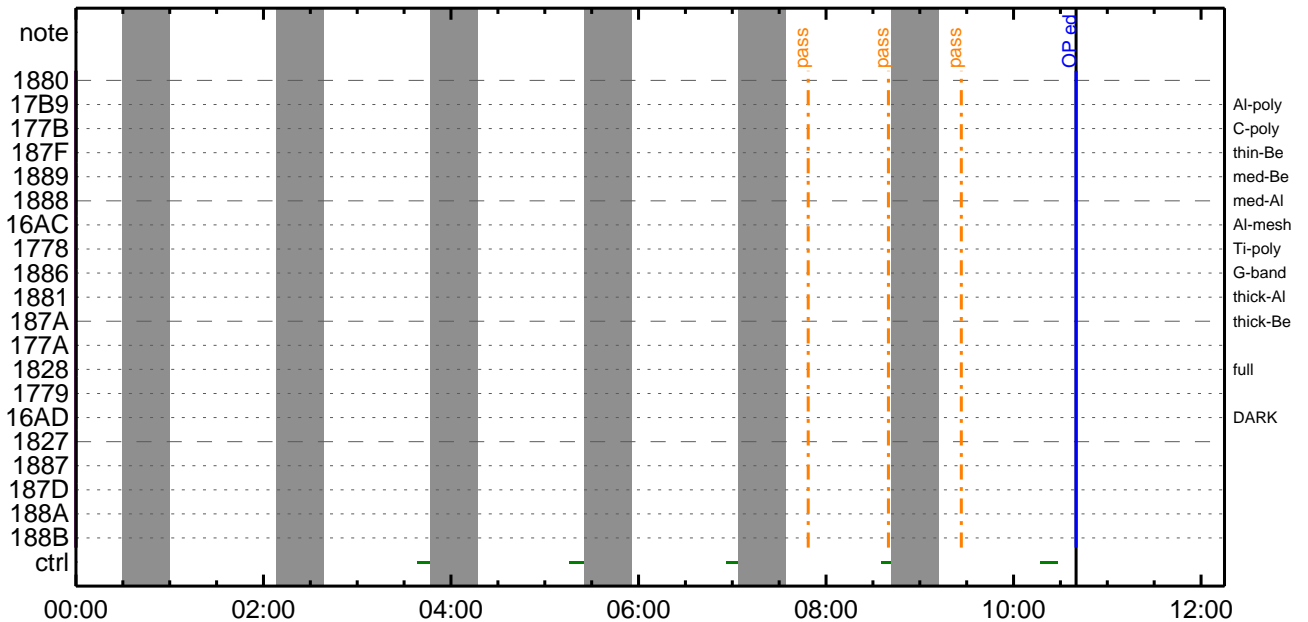
CMDI #0984 2011/06/17



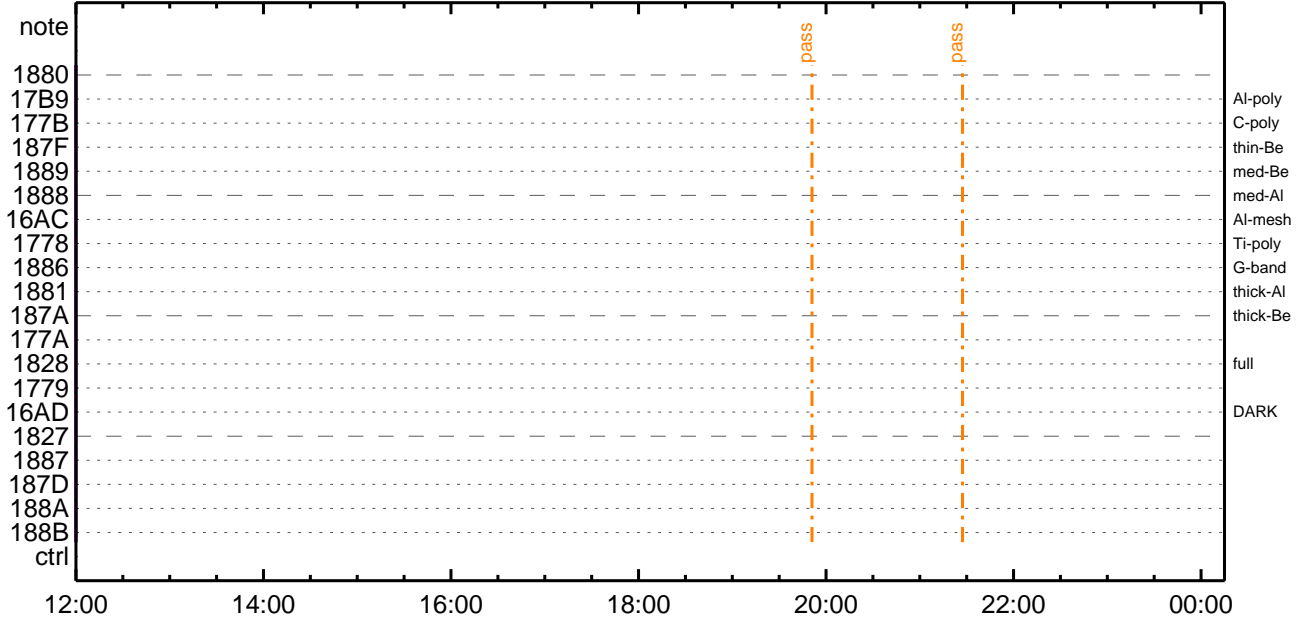
CMDI #0984 2011/06/17



CMDI #0984 2011/06/18



CMDI #0984 2011/06/18







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-133:OP
0104 ( )
0105 S. OG og-133:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°eYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•ë²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°²òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ô²°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹²ç¹Ô²|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2011-06-14 09:45:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-06-14 09:45:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-06-14 09:45:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0194 C.
0195 +. TI 2011-06-14 09:49:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼αîÄè%îíñαîî¥Ä¥§¥Ä¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÍîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHU¥â;¼¥È;È¼¥½. ¥î;¼¥È;Èαòîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-06-14 09:49:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-06-14 09:49:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-06-14 09:49:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-06-14 09:49:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÄîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Ä¥È¥¥¥È¥á¥ç¥èèÈ¼αα¼Ä»Üα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñèË'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-135 2011-06-14 13:01:43 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀã•µ°È»Í×ÁÇãíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÈ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãí;çÀ®, ùã¹ãèãããÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-265:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãíÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2011-06-14 09:49:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```

0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 08 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 08 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 85 83 06 06)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 85 83 08 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c c0 c0 10 10)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 40 c0 10 10)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0e 40 40 10 10)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0f 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 10 80 80 08 08)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C.
0123 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0124 C.
0125 +. DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 +. TI 2011-06-14 09:49:02.0
0128 DC 07-F0 MDP_XRT_MODE_OBSV
0129 BC (c2)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C. ***** XRT END *****
0133 C.
0134 . C. ***** MDP 'úÃîâî»ö¼ÝðËÃÐð¹æDCBC•x²è *****
0135 C. (%á°îÿÓÿÁÿËÿPÿËÿáÿçÿÈðË¼ðð¼Ã»Ûð¹æè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ¥Ðÿ¹•Ï Daily±¿ÎÑðË'Øð¹æDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOS¥Áÿ$ÿÃÿ-¼Ã»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.

```

Jun 14, 11 13:01

## XRT\_OGLIST\_0984.chk

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\*\*\* OP Sequence for XRT \*\*\*

2011/06/14	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	09:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/06/14	10:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/06/14	10:00:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/06/14	10:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/06/14	10:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/14	10:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2011/06/14	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	10:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	10:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/06/14	10:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/06/14	10:12:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/14	10:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/14	10:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/14	10:12:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/14	10:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	10:12:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/06/14	10:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/14	10:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	11:09:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	11:09:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	11:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/14	11:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/14	11:39:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/14	11:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	12:47:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	12:47:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	12:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/14	12:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/14	13:17:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/14	13:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	14:26:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	14:26:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	14:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/14	14:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/14	15:03:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/14	15:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	16:04:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	16:04:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	16:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/14	16:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/14	16:50:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/14	16:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/14	17:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/14	17:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/14	17:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/14	17:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/14	18:27:00.0	XRT_Custom_418_OG [0x1a2]			

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2011/06/15	02:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	03:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	03:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	03:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/15	03:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/15	04:04:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/15	04:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	05:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	05:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	05:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/15	05:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/15	05:42:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/15	05:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	06:42:30.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	06:42:32.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	06:42:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/15	06:45:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/15	07:21:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/15	07:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/06/15	08:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/15	08:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/15	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/15	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/15	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	08:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2011/06/15	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/15	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	08:22:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	08:22:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	08:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/15	08:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/15	08:59:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/15	09:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	10:03:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	10:03:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/15	10:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/15	10:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/15	10:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/15	10:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/15	10:39:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/15	10:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/15	10:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/15	10:40:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/15	10:40:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/15	10:40:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/15	10:42:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2011/06/15	10:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/06/15	10:50:00.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	03	00	00	00	00	
2011/06/15	11:00:00.0	XRT_CTRL_MANU_441_OG [0x1b9]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/15	11:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]								
		TCIB_XRT_S_HTR_A_ENA	0	04-BC						
2011/06/15	13:00:30.0	XRT_Custom_444_OG [0x1bc]								
2011/06/15	14:20:00.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	01	00	00	00	00	
2011/06/15	15:00:30.0	XRT_Custom_444_OG [0x1bc]								
2011/06/15	17:00:30.0	XRT_Custom_444_OG [0x1bc]								
2011/06/15	19:00:30.0	XRT_Custom_447_OG [0x1bf]								
2011/06/15	19:00:40.0	XRT_Custom_444_OG [0x1bc]								
2011/06/15	21:00:40.0	XRT_Custom_448_OG [0x1c0]								
2011/06/15	23:00:18.0	XRT_Custom_447_OG [0x1bf]								
2011/06/15	23:00:28.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/15	23:00:30.0	XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]								
		TCIB_XRT_S_HTR_A_DIS	0	04-C0						
2011/06/16	04:40:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/16	04:43:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2011/06/16	04:43:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2011/06/16	04:43:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2011/06/16	04:43:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2011/06/16	04:43:52.0	XRT_ARS_DIS_431_OG [0x1af]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/06/16	04:43:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/06/16	04:43:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c				
2011/06/16	04:43:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08				
2011/06/16	04:44:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/06/16	05:40:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/16	05:40:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/06/16	05:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/06/16	05:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/06/16	05:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/16	05:55:00.0	AOCS_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	00	56	a7	00	00	
2011/06/16	05:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2011/06/16	05:57:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2011/06/16	05:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2011/06/16	05:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2011/06/16	05:57:52.0	XRT_ARS_DIS_431_OG [0x1af]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/06/16	05:57:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/06/16	05:57:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02				
2011/06/16	05:57:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08				
2011/06/16	06:19:30.0	XRT_Custom_418_OG [0x1a2]								
2011/06/16	06:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/06/16	07:20:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/16	07:20:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/06/16	07:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/06/16	07:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/06/16	07:58:00.0	XRT_Custom_418_OG [0x1a2]								
2011/06/16	07:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/06/16	09:00:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/06/16	09:00:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/06/16	09:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/06/16	09:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/06/16	09:36:30.0	XRT_Custom_418_OG [0x1a2]								
2011/06/16	09:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/06/16	09:53:54.0	XRT_CTRL_MANU_400_OG [0x190]								

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2011/06/16	09:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/16	09:54:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/16	09:54:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/16	09:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/16	09:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/16	09:56:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/16	09:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2011/06/16	10:03:54.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/16	10:04:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/16	10:06:32.0	XRT_ROI_A_436_OG [0x1b4]	AOCU_NM	5	02-76	00 2e f9 2e f9				
2011/06/16	10:06:37.0	XRT_FOCUS_POSITION_440_OG [0x1b8]	MDP_XRT_ROI_SET	6	07-F0	cd 06 80 80 20 20				
2011/06/16	10:06:57.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ROI_SET	6	07-F0	cd 07 c0 40 10 10				
2011/06/16	10:06:59.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_ROI_SET	6	07-F0	cd 0c c0 c0 10 10				
2011/06/16	10:07:01.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_ROI_SET	6	07-F0	cd 0d 40 c0 10 10				
2011/06/16	10:07:03.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ROI_SET	6	07-F0	cd 0e 40 40 10 10				
2011/06/16	10:07:05.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/16	10:13:54.0	XRT_CTRL_MANU_435_OG [0x1b3]	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4 0d				
2011/06/16	10:14:00.0	AOCs_OrE-point_Start_6_OG [0x09c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/06/16	10:16:32.0	XRT_FOCUS_POSITION_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/16	10:16:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	XRT_FLRCTRL_DIS_433_OG [0x1b1]	1	07-F0	d9				
2011/06/16	10:16:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/16	10:16:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/16	10:16:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/16	10:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/16	10:23:54.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/16	10:24:00.0	AOCs_OrE-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/16	10:26:32.0	XRT_FOCUS_POSITION_440_OG [0x1b8]	AOCU_NM	5	02-76	00 d1 07 d1 07				
2011/06/16	10:26:52.0	XRT_QT_PROG_SET_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/16	10:26:54.0	XRT_FLD_DIS_402_OG [0x192]	XRT_QT_PROG_SET_403_OG [0x193]	2	07-F0	c4 09				
2011/06/16	10:26:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/06/16	10:26:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/16	10:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/16	10:33:54.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/16	10:34:00.5	AOCs_OrE-point_Start_8_OG [0x09e]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/16	10:36:32.0	XRT_FOCUS_POSITION_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/16	10:36:52.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/16	10:36:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/16	10:36:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/16	10:36:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/16	10:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	AOCU_NM	5	02-76	00 d1 07 2e f9				
2011/06/16	10:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/16	10:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_QT_PROG_SET_404_OG [0x194]	2	07-F0	c4 12				
2011/06/16	10:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/06/16	10:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2011/06/16 11:13:00.0 AACS\_ORe-point\_Start\_1\_OG [0x097]  
AOCU\_NM 5 02-76 00 00 00 00 00