

XRT Timeline to be uploaded on 2011/06/23

Period: 2011/06/23 10:29:00 - 2011/06/28 10:18:00

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Normal mode

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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly												
Term		Pointing (x, y)					Comment					
06/23 10:42:00 - 06/23 23:59:54		Track (586.8, 207.3) [Ⓜ] 06/23 10:39:00					# OP start + 10min, track AR 11236					
06/24 00:13:00 - 06/24 00:19:54		Fixed (580.0, 676.0)					# HOP-189 (Akatsuki)					
06/24 07:03:00 - 06/24 09:59:54		Track (712.3, 210.6) [Ⓜ] 06/24 07:00:00					# AR cont.					
06/25 00:25:30 - 06/25 00:35:24		Fixed (157.0, 870.0)					# HOP-189 (Akatsuki)					
06/25 03:48:00 - 06/25 03:54:54		Fixed (157.0, 870.0)					# HOP-189 (Akatsuki)					
PROG= 11 Inf.-time(s)												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term		Pointing (x, y)					Comment					
06/24 00:03:00 - 06/24 00:09:54		Fixed (0.0, 0.0)					# XRT synoptic shifted.					
06/24 23:43:00 - 06/24 23:54:00		Fixed (0.0, 0.0)					synoptic, shifted.					
PROG= 19 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #188F: HOP189 draft - Al/mesh, Ti/Poly - 384x512FOV												
Term		Pointing (x, y)					Comment					
06/24 00:23:00 - 06/24 06:59:54		Fixed (580.0, 676.0)					# HOP-189 (Akatsuki)					
06/25 00:38:30 - 06/25 03:44:54		Fixed (157.0, 870.0)					# HOP-189 (Akatsuki)					
06/25 03:58:00 - 06/25 06:59:54		Fixed (157.0, 870.0)					# HOP-189 (Akatsuki)					
PROG= 10 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 28 1-time(s) 2.0sec												
Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	8.00s	Obs	1x1	384x512 (1024, 1152)	Q=98	0	0 2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	16.0s	Obs	1x1	384x512 (1024, 1152)	Q=98	0	0 2.0sec
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 25 45-time(s) 40.0sec												
Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x512 (1024, 1152)	Q=95	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x512 (1024, 1152)	Q=95	0	0 2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #189I: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512)												
Term		Pointing (x, y)					Comment					
06/24 10:03:00 - 06/24 10:09:54		Fixed (0.0, 0.0)					synoptic, shifted and HOP-186					
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												

Seqn= 72	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	2.00s	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
06/24 10:13:00 - 06/24 23:39:54	Track (729.4, 211.2) ^{Ⓜ 06/24 10:10:00}	# AR cont.
06/25 07:03:00 - 06/25 09:40:00	Track (824.8, 215.9) ^{Ⓜ 06/25 07:00:00}	# AR cont.

PROG= 04 Inf-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn=100	4-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 6	18-time(s)	2.0sec										
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	1	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	2	20.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
06/23 10:42:00 - 06/23 23:59:54	Track (586.8, 207.3) ^{Ⓜ 06/23 10:39:00}	# OP start + 10min, track AR 11236
06/24 00:13:00 - 06/24 00:19:54	Fixed (580.0, 676.0)	# HOP-189 (Akatsuki)
06/24 00:23:00 - 06/24 06:59:54	Fixed (580.0, 676.0)	# HOP-189 (Akatsuki)
06/24 07:03:00 - 06/24 09:59:54	Track (712.3, 210.6) ^{Ⓜ 06/24 07:00:00}	# AR cont.
06/24 10:13:00 - 06/24 23:39:54	Track (729.4, 211.2) ^{Ⓜ 06/24 10:10:00}	# AR cont.
06/25 00:25:30 - 06/25 00:35:24	Fixed (157.0, 870.0)	# HOP-189 (Akatsuki)
06/25 00:38:30 - 06/25 03:44:54	Fixed (157.0, 870.0)	# HOP-189 (Akatsuki)
06/25 03:48:00 - 06/25 03:54:54	Fixed (157.0, 870.0)	# HOP-189 (Akatsuki)
06/25 03:58:00 - 06/25 06:59:54	Fixed (157.0, 870.0)	# HOP-189 (Akatsuki)
06/25 07:03:00 - 06/25 09:40:00	Track (824.8, 215.9) ^{Ⓜ 06/25 07:00:00}	# AR cont.

PROG= 08 1-time(s)

Subr= 1	30-time(s)	20.0sec										
Seqn= 87	1-time(s)	2.0sec										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 90	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec										
Seqn= 87	1-time(s)	2.0sec										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 90	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec										
Seqn= 87	1-time(s)	2.0sec										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 90	1-time(s)	2.0sec										

	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

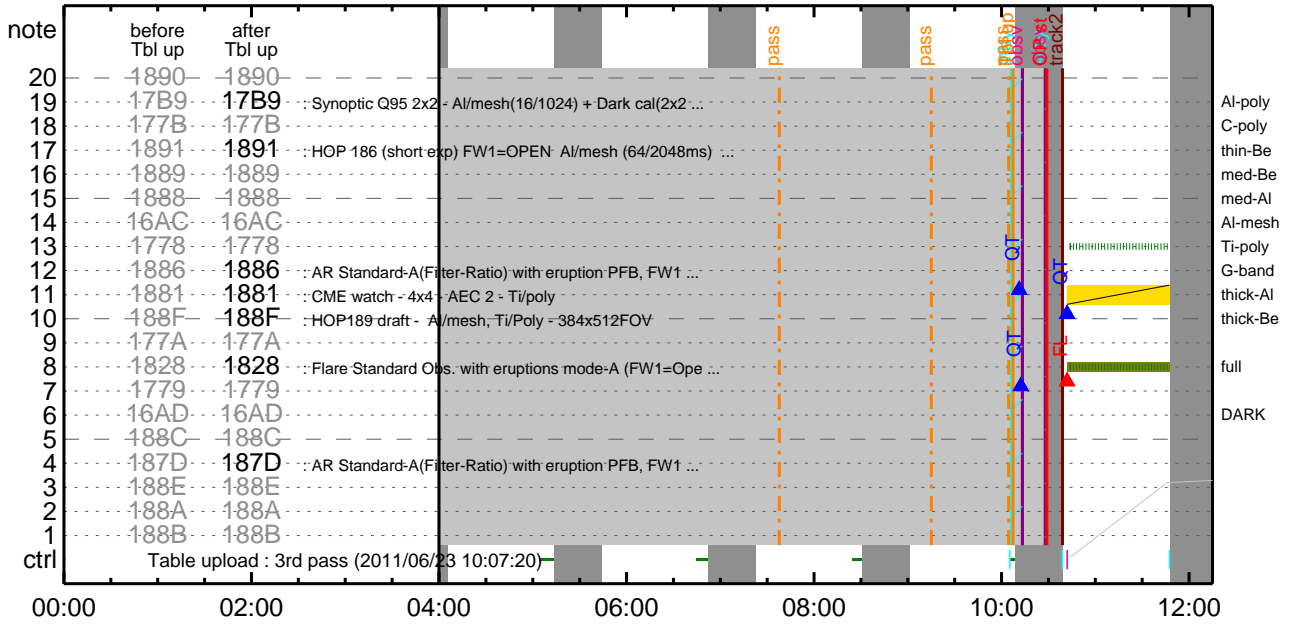
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Flare Detection

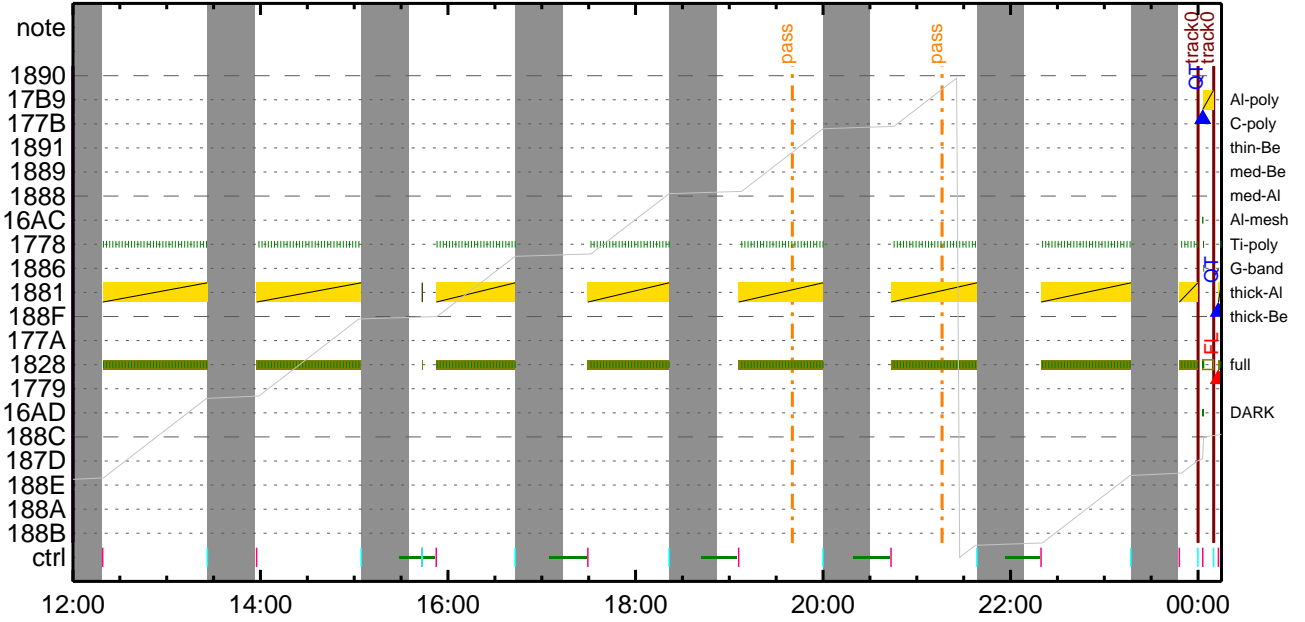
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FLD Patrol												
Term	Pointing (x, y)								Comment			
06/24 00:12:46 - 06/24 10:00:16	Fixed (580.0, 676.0)								# HOP-189 (Akatsuki)			
06/24 10:12:46 - 06/24 23:40:16	Track (729.4, 211.2) ^{® 06/24 10:10:00}								# AR cont.			
06/25 00:02:46 - 06/28 10:18:00	Fixed (157.0, 870.0)								# HOP-189 (Akatsuki)			
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

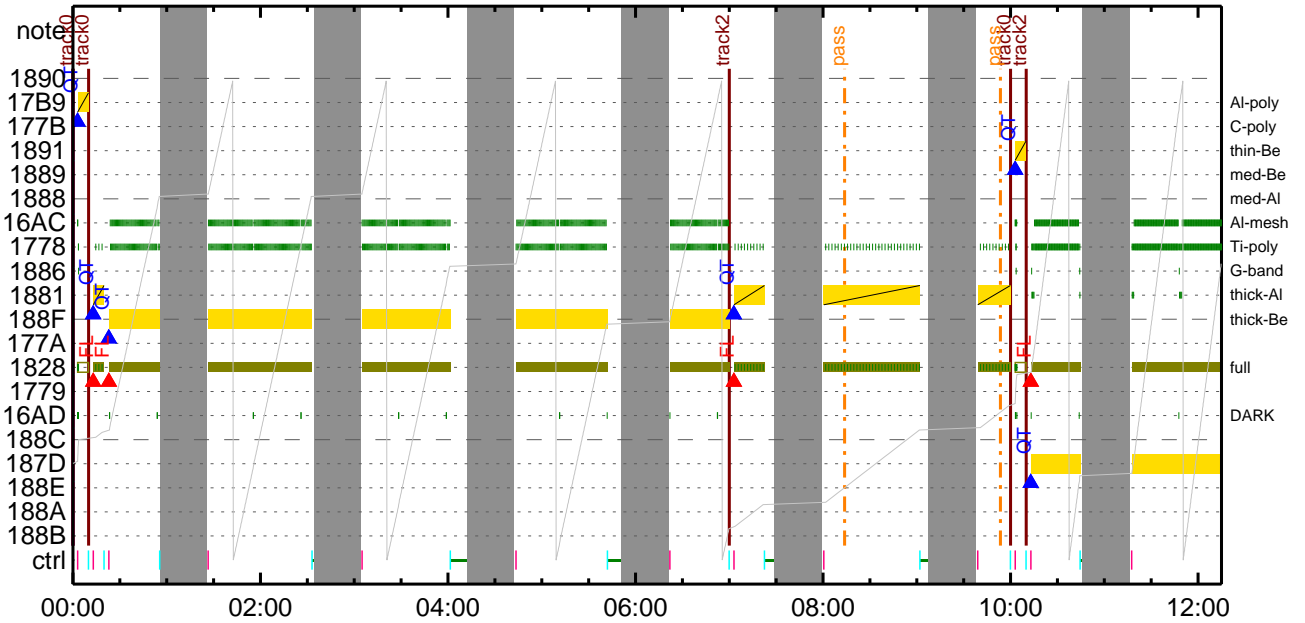
CMDI #0998 2011/06/23



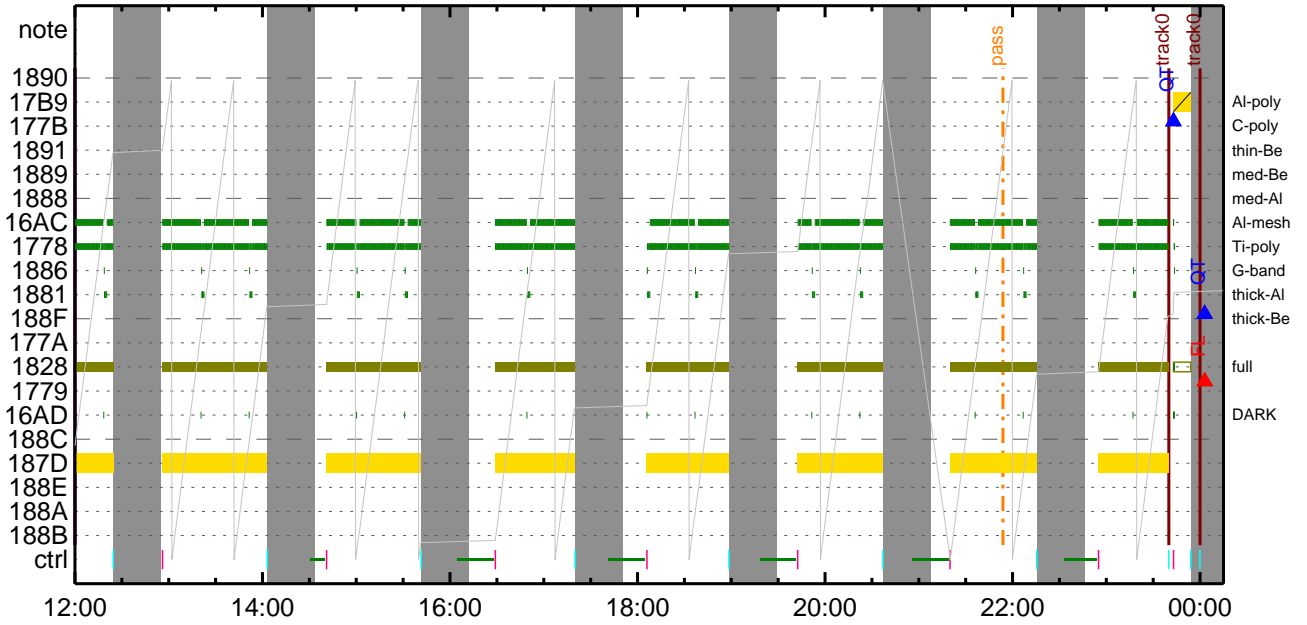
CMDI #0998 2011/06/23



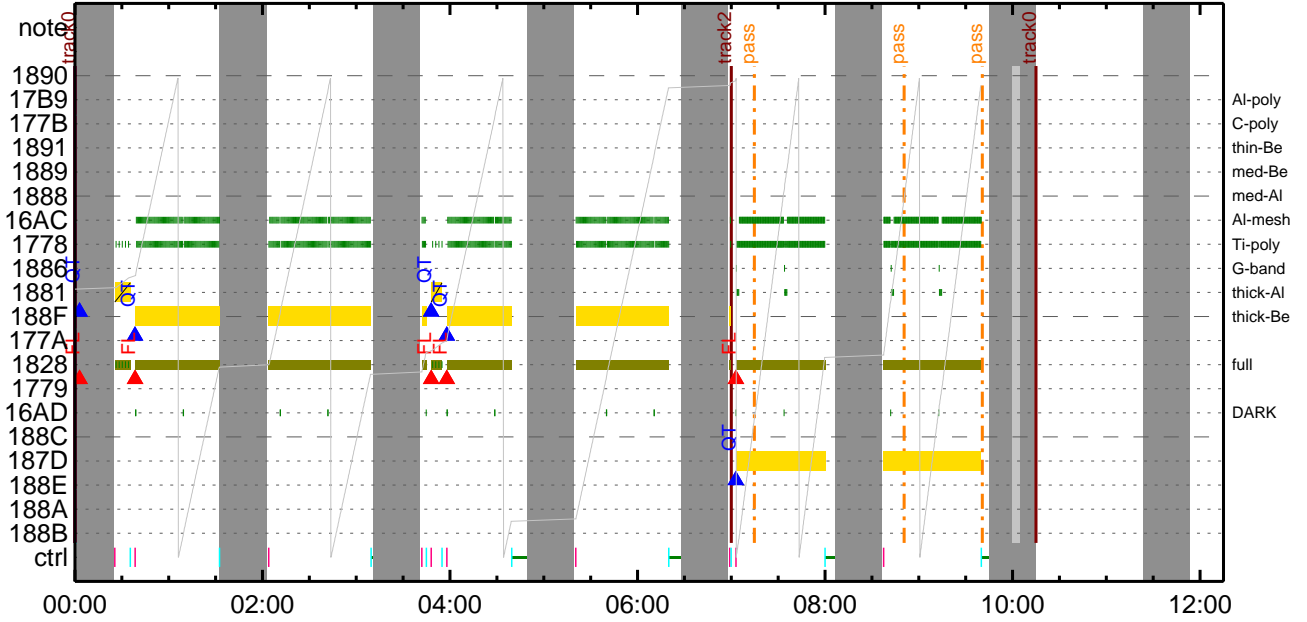
CMDI #0998 2011/06/24



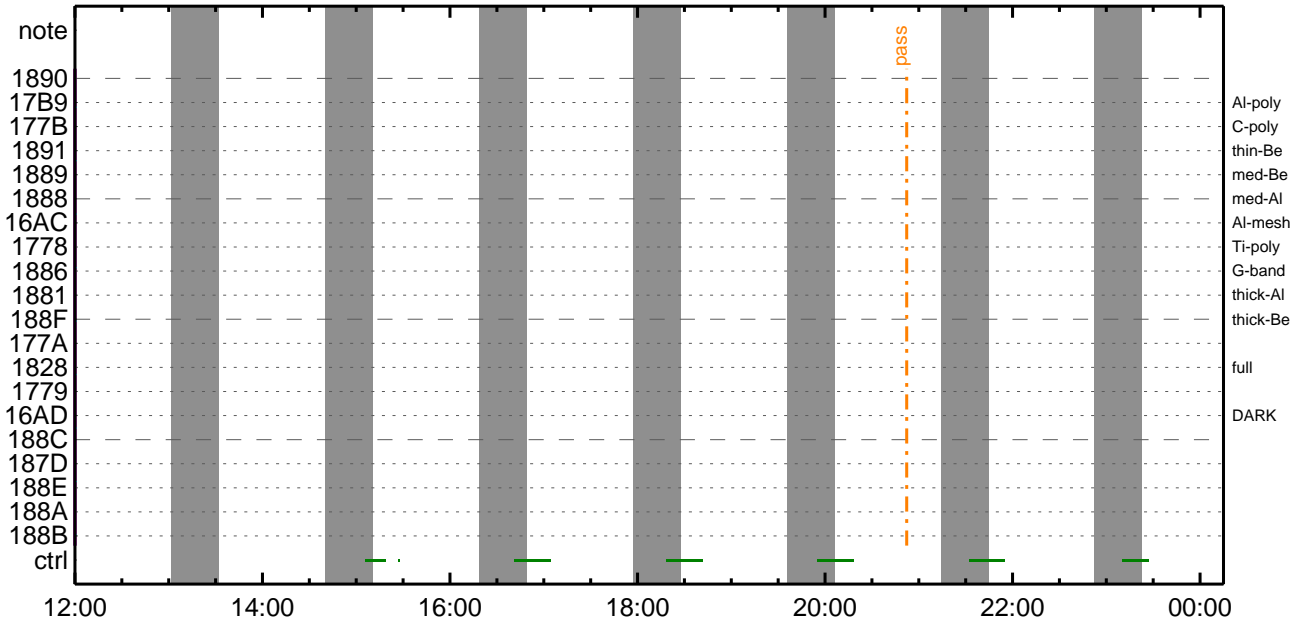
CMDI #0998 2011/06/24



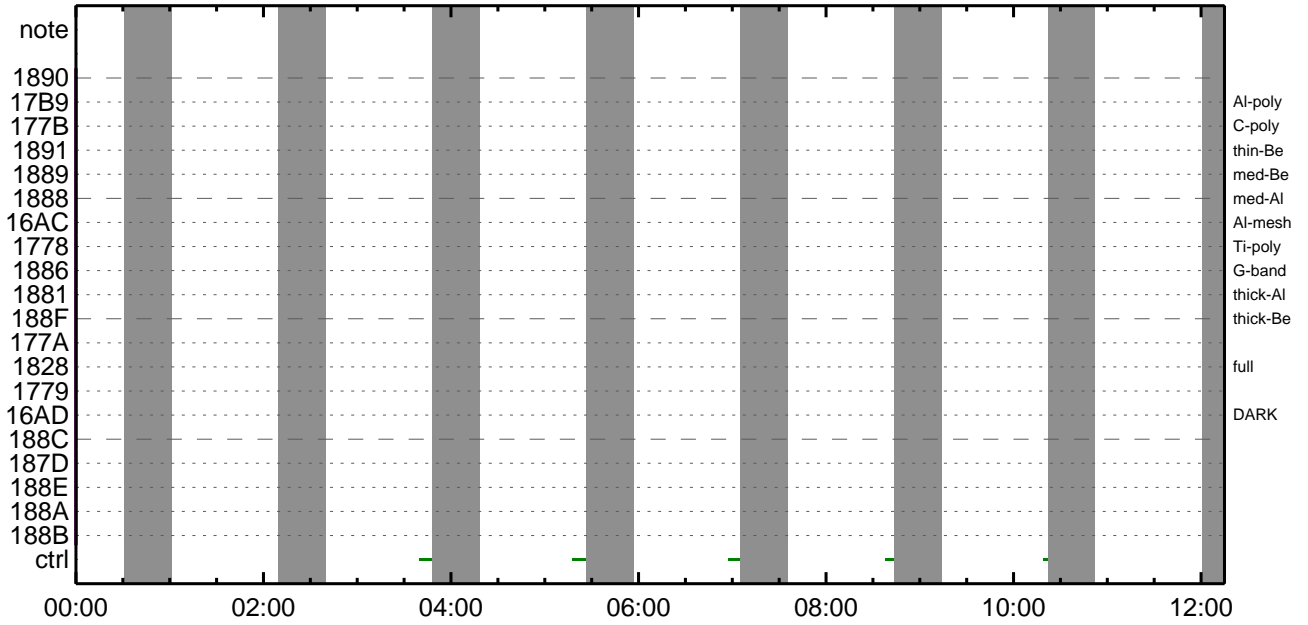
CMDI #0998 2011/06/25



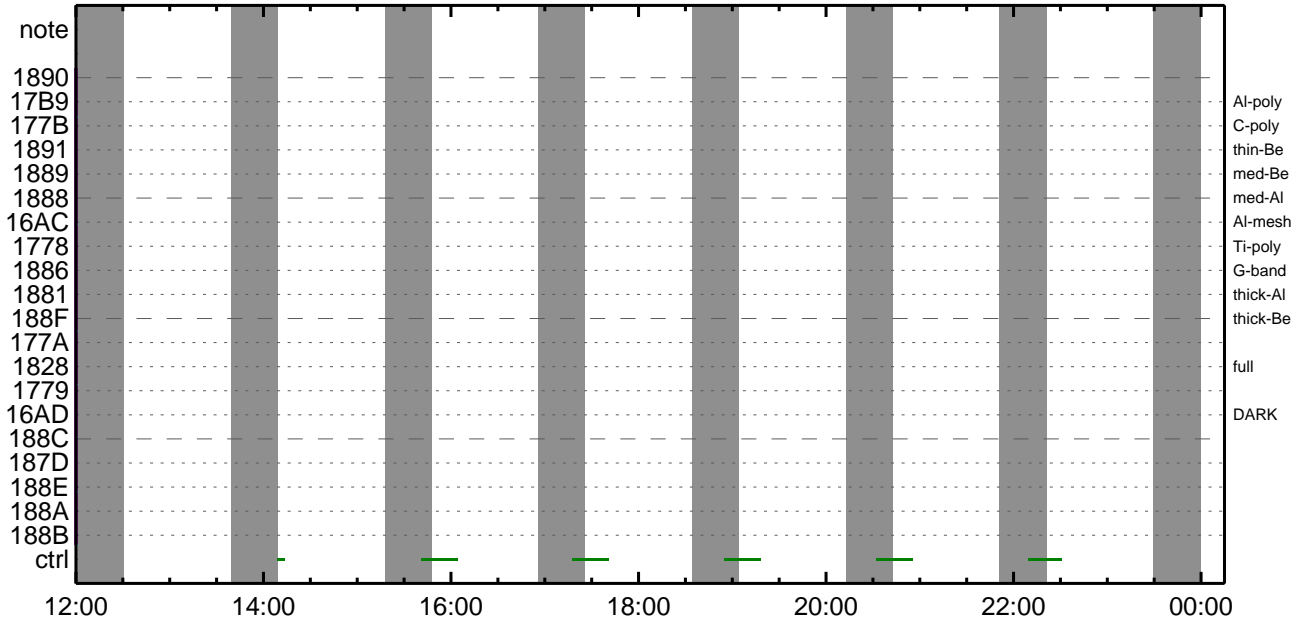
CMDI #0998 2011/06/25



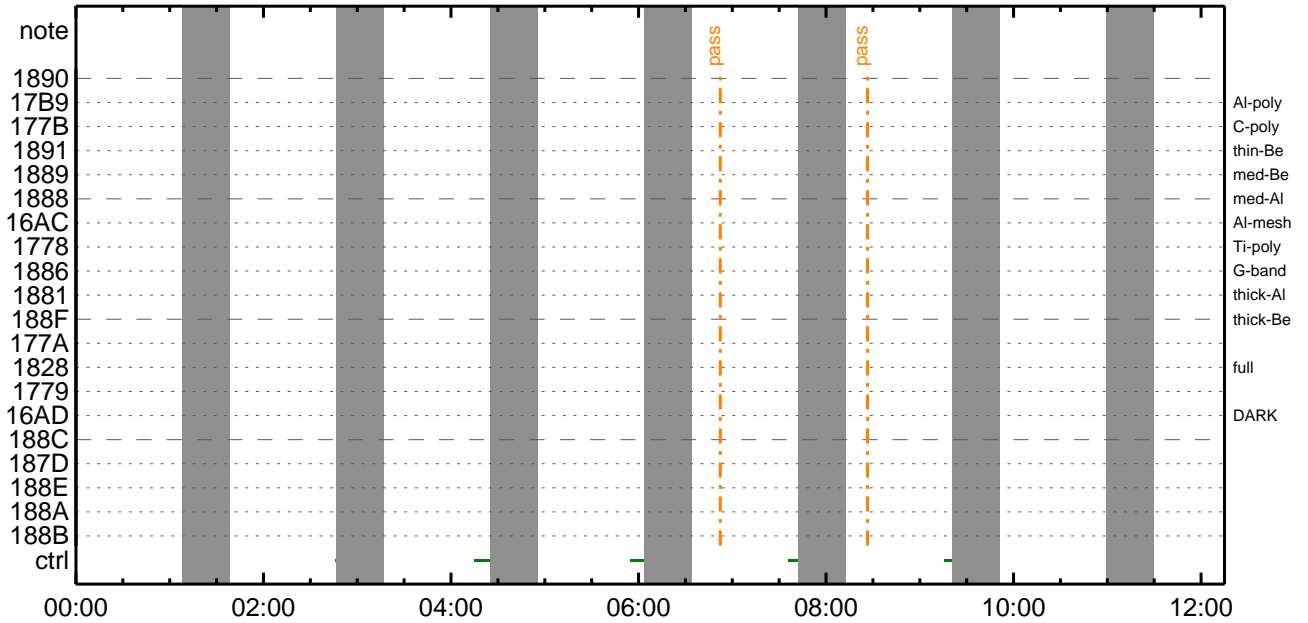
CMDI #0998 2011/06/26



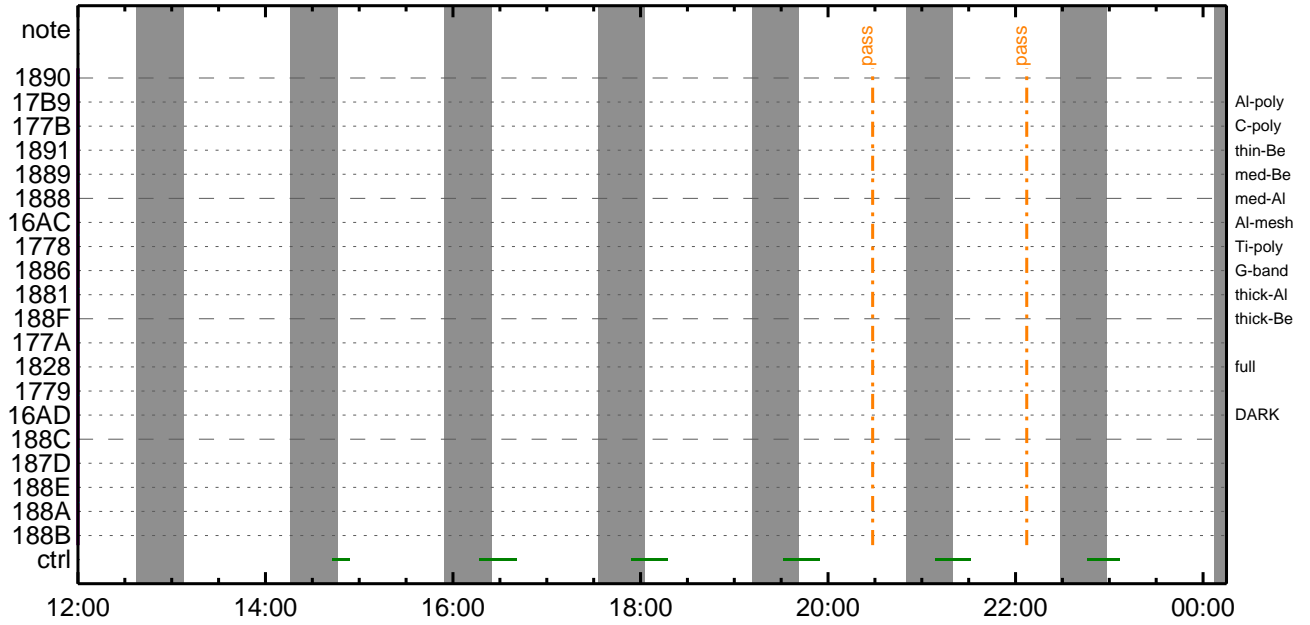
CMDI #0998 2011/06/26



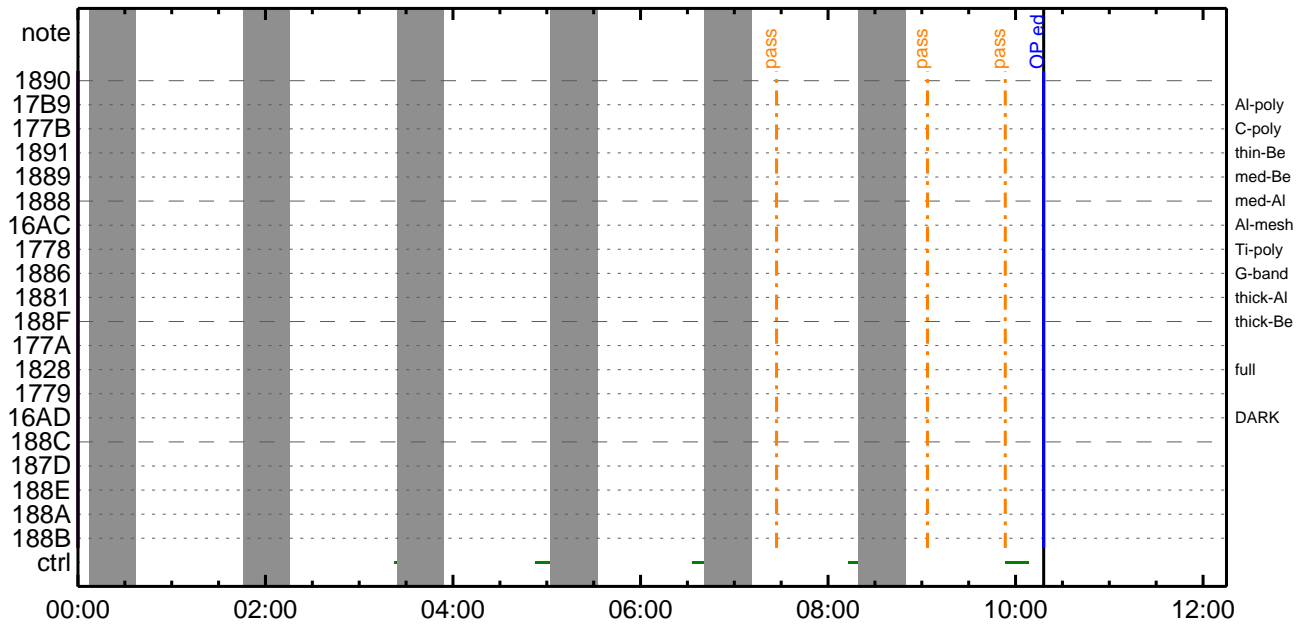
CMDI #0998 2011/06/27



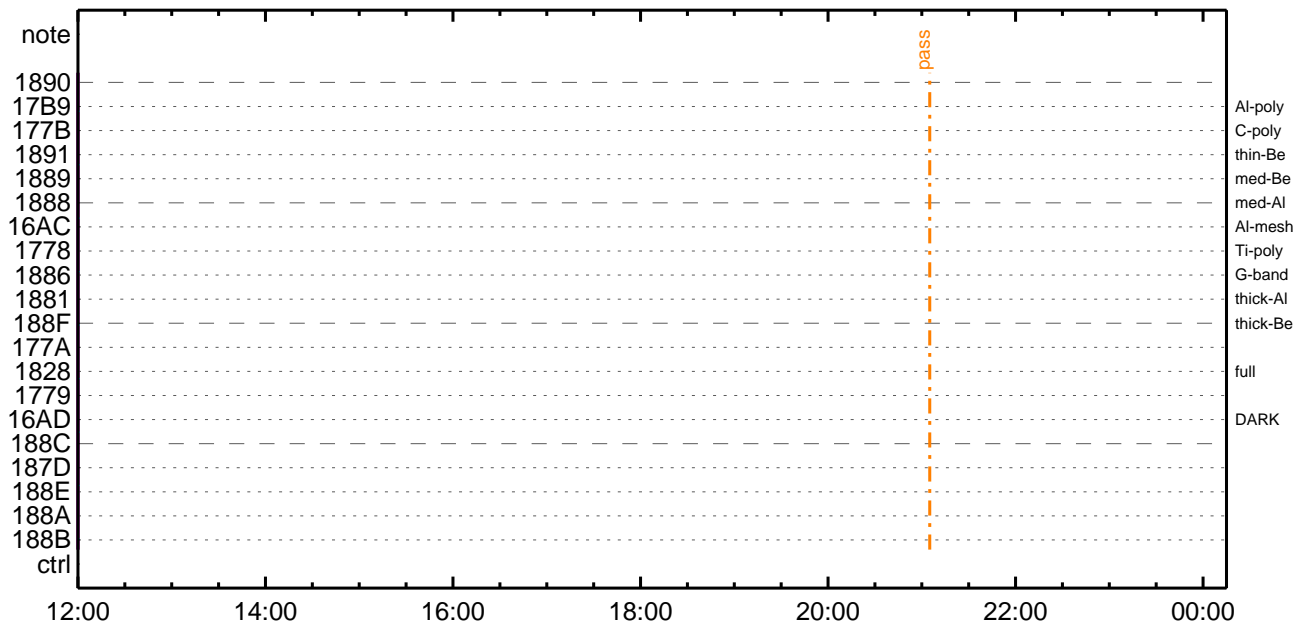
CMDI #0998 2011/06/27



CMDI #0998 2011/06/28



CMDI #0998 2011/06/28




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-153:OP
0104 ( )
0105 S. OG og-153:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°eãYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÀYôYx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼E¹ç•ë² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÀYôYx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼E¹ç•ë² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÀYôYx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼E¹ç•ë² îOKò³ îÇ§
0165 C.
0166 C. ***** òE²¼ò î¼Ã´ ¶Á°òEÈ¬ò°Ã÷¿® (¼ãµ-YÀYôYx½ª ê½çòðÁÔÃæç¼ª°¬òE¼i¹çççã) *****
0167 C. DHUYã;¼YE;Ê½Y½, Yî;¼YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿®NGUî¼i¹ç;ç°E²¼ò îTI-CMDÁ÷¿®ãî¼Á¹Ô°¬òEòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±òîYÑY¹ç¹Ôò|ò³òE;f
0181 C.
0182 C. TIY³YôYôYÈòðÁDî¿¿(UT)
0183 +. TI 2011-06-23 10:24:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-06-23 10:24:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-06-23 10:24:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2011-06-23 10:28:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïãîÄë%îíñòîî¥Ä¥§¥Ä¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                  EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                      EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                   EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                    EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                  EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                  EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                   EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                  EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                     EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                     EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                  EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                  EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                     EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                     EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî¼È¹ç•è²îOKòò³îÇ§
0229 C.
0230 C. DHU¥â;¼¥È;È¼¥½. ¥î;¼¥È;Èòòîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                     EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                  EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                  EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-06-23 10:28:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-06-23 10:28:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]                    EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-06-23 10:28:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-06-23 10:28:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ÝòÈÄò¹òèDCBC•x²è *****
0276 C. (%ã°îî¥Ö¥Ä¥È¥¥¥¥Ä¥ç¥èòÈ¼òò¼Ä»Ûò¹òè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñòÈ´Øò¹òèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCS_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS_DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCS_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 25s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-06-23 10:28:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP `úÃîâî»ö¼ŷñÈÄðñ¹ñèDCBC•x²è *****
0153 C. (¼ã°îŷÖŷÄŷÈŷŷŷÈŷÄŷçŷèñ¼¼ñ¼Ä»Ûñ¹ñè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ŷĐŷ¹•î Daily±¿îÑñÈ´Øñ¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSŷÄŷŷŷÄŷ¹¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-155 2011-06-23 11:31:07 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿ðÁð•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÁ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 90 06 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 +. DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 0c)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 08)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2011-06-23 10:28:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 . C. *****
0086 C. SOT table upload
0087 C. *****
0088 . C. < Stop SP table >
0089 +. DC 07-F0 MDP_SP_CTRL_MANU
0090 BC (61)
0091 C. -----
0092 C. MDP_SP_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload SP Observation Table>
```

```
0096 . S. RAM ram-282:MDP_OBS_S
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_S >
0100 +. DC 07-F0 MDP_DUMP_SPTBL
0101 BC (83 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_S verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 C. *****
0107 C. SOT TI command set
0108 C. *****
0109 C. Execute, after the success of TBL upload.
0110 +. TI 2011-06-23 10:28:18.0
0111 DC 07-F0 MDP_SOT_MODE_OBSV
0112 BC (40)
0113 C. -----
0114 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0115 C. -----
0116 C.
0117 C.
0118 . C. ***** MDP 'ûÃîñî»ö%ÿñÊÃñ¹ñèDCBC•x²è *****
0119 C. (%ã°îÿÔÿÃÿÊÿPÿËÿâÿçÿèñ%¼ñ¼Ã»Û¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ÿDÿ¹•î Daily±;îññË'Ø¹ñèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.
```

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

Page 1/7

*** OP Sequence for XRT ***

2011/06/23	10:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	10:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/06/23	10:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/06/23	10:41:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/23	10:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/23	10:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/23	10:41:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/23	10:41:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	10:41:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/06/23	10:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/23	10:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	11:47:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	11:47:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	11:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	11:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	12:18:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	12:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	13:26:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	13:26:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	13:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	13:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	13:56:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	13:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	15:04:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	15:04:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	15:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	15:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	15:42:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	15:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	15:43:30.5	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	15:43:32.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	15:43:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	15:46:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	15:51:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	15:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	16:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	16:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	16:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	16:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	17:28:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	17:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	18:21:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	18:21:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/23	18:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/23	18:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/23	19:05:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/23	19:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/23	20:00:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/23	20:00:02.0	XRT_FLD_RESET_412_OG [0x19c]			

Thursday June 23, 2011

1/7

2011/06/23	20:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/23	20:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/23	20:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/23	20:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/23	21:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/23	21:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/23	21:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/23	21:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/23	22:18:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/23	22:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/23	23:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/23	23:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/23	23:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/23	23:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/23	23:47:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/23	23:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/23	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/23	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/24	00:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/24	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/06/24	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/06/24	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/06/24	00:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/24	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2011/06/24	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/24	00:10:00.0	AOCs_OrE-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/24	00:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 c3 e8 cc 73	
2011/06/24	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/24	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/24	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/24	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/24	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/24	00:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/24	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/06/24	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/24	00:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/24	00:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/24	00:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/06/24	00:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/24	00:22:50.0	XRT_AEC_RESET_405_OG [0x195]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/24	00:22:52.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/24	00:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/24	00:22:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/24	00:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2011/06/24	00:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/24	00:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/24	00:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/24	00:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

2011/06/24	00:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	01:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	01:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	02:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	02:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	02:33:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	02:36:14.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	03:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	03:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	04:01:30.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	04:01:32.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	04:01:34.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	04:04:44.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	04:42:30.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	04:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	05:42:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	05:42:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	05:42:04.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	05:45:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	06:21:00.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	06:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	06:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	07:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00														
2011/06/24	07:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00														
2011/06/24	07:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8														
2011/06/24	07:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8														
2011/06/24	07:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0														
2011/06/24	07:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5														
2011/06/24	07:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	07:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b														
2011/06/24	07:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08														
2011/06/24	07:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	07:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	07:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	07:22:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	07:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	07:59:30.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	08:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	09:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	09:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da														
2011/06/24	09:02:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8														
2011/06/24	09:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	09:38:00.0	XRT_CUSTOM_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/06/24	09:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2011/06/24	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2011/06/24	09:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00														
2011/06/24	10:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00														
2011/06/24	10:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9														
2011/06/24	10:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9														

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

Page 4/7

2011/06/24	10:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/24	10:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/06/24	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	10:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	10:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/06/24	10:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/06/24	10:12:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/24	10:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/24	10:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/24	10:12:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/24	10:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	10:12:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/06/24	10:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/24	10:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	10:44:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	10:44:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	10:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	10:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	11:16:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	11:17:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	12:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	12:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	12:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	12:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	12:55:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	12:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	14:03:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	14:03:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	14:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	14:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	14:40:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	14:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	15:41:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	15:41:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	15:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	15:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	16:28:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	16:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	17:20:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	17:20:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	17:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	17:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	18:05:00.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	18:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/24	18:58:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/24	18:58:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/24	18:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/24	19:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/24	19:41:30.0	XRT_Custom_418_OG [0x1a2]			
2011/06/24	19:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

2011/06/24	20:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/24	20:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/24	20:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/24	20:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/24	21:19:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2011/06/24	21:20:00.0		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/24	22:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/24	22:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/24	22:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/24	22:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/24	22:54:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2011/06/24	22:55:00.0		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/24	23:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/24	23:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/24	23:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/24	23:40:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/24	23:40:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/24	23:40:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/24	23:42:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2011/06/24	23:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/24	23:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/24	23:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/24	23:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/24	23:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/24	23:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/25	00:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b2 a8 f2 0d				
2011/06/25	00:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/25	00:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/25	00:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/25	00:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/25	00:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/25	00:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/25	00:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/06/25	00:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/25	00:24:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2011/06/25	00:25:30.0		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/25	00:35:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/25	00:37:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/06/25	00:38:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/25	00:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/25	00:38:20.0	XRT_AEC_RESET_405_OG [0x195]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/25	00:38:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/25	00:38:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/25	00:38:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/06/25	00:38:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/25	00:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/25	01:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/25	01:32:32.0	XRT_FLD_RESET_412_OG [0x19c]								

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

Page 6/7

2011/06/25	01:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/25	01:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/25	02:03:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/25	02:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	03:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	03:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/25	03:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/25	03:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/25	03:41:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/25	03:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	03:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	03:47:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/25	03:47:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/25	03:47:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/25	03:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/25	03:47:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/25	03:47:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/25	03:47:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/06/25	03:47:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/25	03:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	03:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	03:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/06/25	03:57:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/25	03:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/25	03:57:50.0	XRT_AEC_RESET_405_OG [0x195]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/25	03:57:52.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/25	03:57:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/25	03:57:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2011/06/25	03:57:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/25	03:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	04:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	04:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/25	04:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/25	04:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/25	05:19:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/25	05:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	06:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	06:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/25	06:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/25	06:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/25	06:58:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/25	06:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/25	06:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/25	07:00:00.0	AOCs_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/06/25	07:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/06/25	07:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/25	07:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/25	07:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	

Jun 23, 11 11:31

XRT_OGLIST_0998.chk

Page 7/7

2011/06/25	07:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/25	07:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/25	07:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/06/25	07:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/25	07:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/25	08:00:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/25	08:00:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/25	08:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/25	08:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/25	08:36:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/25	08:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/25	09:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/25	09:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/25	09:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/25	09:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/25	10:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				