

XRT Timeline to be uploaded on 2011/06/25

Period: 2011/06/25 10:05:00 - 2011/06/30 09:54:00

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Normal mode

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XOB #1891: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1

Term	Pointing (x, y)	Comment
06/25 10:18:00 - 06/25 18:29:54	Fixed (0.0, 0.0)	# OP start + 10min / synoptic for HOP 186
06/26 11:03:00 - 06/26 11:19:54	Fixed (0.0, 0.0)	synoptic for HOP 186
06/27 10:03:00 - 06/27 10:19:54	Fixed (0.0, 0.0)	synoptic for HOP 186
PROG= 17 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 7 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 72 1-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
06/25 18:33:00 - 06/25 20:29:54	Fixed (0.0, 945.0)	Co-alignment N-limb
PROG= 14 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 21 24-time(s) 300.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
06/25 20:33:00 - 06/25 23:29:54	Fixed (-945.0, 0.0)	Co-alignment E-limb
PROG= 06 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 22 15-time(s) 480.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
06/25 23:33:00 - 06/25 23:39:54	Fixed (0.0, 0.0)	synoptic
06/27 00:43:00 - 06/27 00:59:54	Fixed (0.0, 0.0)	synoptic
06/27 23:48:00 - 06/28 00:06:30	Fixed (0.0, 0.0)	synoptic
PROG= 19 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 7 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
06/25 23:43:00 - 06/25 23:50:00	Fixed (-521.0, 726.0)	HOP 189, Akatsuki Day 3 (00:00-07:30 UT)
PROG= 11 Inf.-time(s)		
└ Subr= 1 1-time(s) 120.0sec		
└└ Seqn= 33 1-time(s) 4.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec		
└└└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

Term	Pointing (x, y)	Comment
06/25 23:53:06 - 06/26 06:57:30	Fixed (-521.0, 726.0)	HOP 189, Akatsuki Day 3 (00:00-07:30 UT)
XOB #188F: HOP189 draft - Al/mesh, Ti/Poly - 384x512FOV		
PROG= 10 Inf.-time(s)		
└ Subr= 2 1-time(s) 2.0sec		
└└ Seqn= 28 1-time(s) 2.0sec		
└└└ Open/Al-mesh Open/Al-mesh close Safe Dark 8.00s Obs 1x1 384x512 (1024, 1152) Q=98 0 0 2.0sec		
└└└ Open/Al-mesh Open/Al-mesh close Safe Dark 16.0s Obs 1x1 384x512 (1024, 1152) Q=98 0 0 2.0sec		
└ Subr= 1 1-time(s) 2.0sec		
└└ Seqn= 25 45-time(s) 40.0sec		
└└└ Open/Al-mesh Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x512 (1024, 1152) Q=95 0 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x512 (1024, 1152) Q=95 0 0 2.0sec		
└└└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

Term	Pointing (x, y)	Comment
06/26 07:38:06 - 06/26 10:19:00	Track (314.5, 256.5) ^{06/26 07:30:00}	AR 11241
06/26 11:23:00 - 06/27 00:39:54	Fixed (875.0, 215.0)	AR 11236
06/27 07:03:00 - 06/27 09:15:30	Fixed (875.0, 215.0)	AR 11236
06/27 10:23:00 - 06/27 23:44:54	Fixed (875.0, 215.0)	AR 11236
06/28 00:43:00 - 06/28 09:53:30	Fixed (875.0, 215.0)	AR 11236
PROG= 04 Inf.-time(s)		
└ Subr= 1 1-time(s) 2.0sec		
└└ Seqn= 19 1-time(s) 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└└ Seqn=100 4-time(s) 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└└└ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└ Subr= 2 1-time(s) 2.0sec		
└└ Seqn= 6 18-time(s) 2.0sec		
└└└ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└└└ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec		
└└└ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 1 20.0sec		
└└└ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec		
└└└ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 2 20.0sec		
└└└ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec		
└└└ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 3 20.0sec		
└└└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

Term	Pointing (x, y)	Comment
06/27 01:02:58 - 06/27 06:59:54	Fixed (-689.0, 580.0)	HOP 189, Akatsuki Day 4 (01:00-07:00)
XOB #1892: CME watch - 4x4 - AEC 2 - Ti/poly Al/Mesh		
PROG= 15 Inf.-time(s)		
└ Subr= 1 1-time(s) 120.0sec		
└└ Seqn= 66 1-time(s) 4.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec		
└└└ Open/Al-mesh Open/thick-Al close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec		
└└└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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Flare mode

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Term	Pointing (x, y)	Comment
06/25 23:43:00 - 06/25 23:50:00	Fixed (-521.0, 726.0)	HOP 189, Akatsuki Day 3 (00:00-07:30 UT)
06/25 23:53:06 - 06/26 06:57:30	Fixed (-521.0, 726.0)	HOP 189, Akatsuki Day 3 (00:00-07:30 UT)
06/26 07:38:06 - 06/26 10:19:00	Track (314.5, 256.5) ^{06/26 07:30:00}	AR 11241
06/26 11:23:00 - 06/27 00:39:54	Fixed (875.0, 215.0)	AR 11236
06/27 07:03:00 - 06/27 09:15:30	Fixed (875.0, 215.0)	AR 11236
06/27 10:23:00 - 06/27 23:44:54	Fixed (875.0, 215.0)	AR 11236
06/28 00:43:00 - 06/28 09:53:30	Fixed (875.0, 215.0)	AR 11236
PROG= 08 1-time(s)		
└ Subr= 1 30-time(s) 20.0sec		
└└ Seqn= 87 1-time(s) 2.0sec		
└└└ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└└└ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└└ Seqn= 60 1-time(s) 2.0sec		

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 90		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 90		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 90		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 87		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)		600.0sec										
	Seqn= 89		1-time(s) 2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

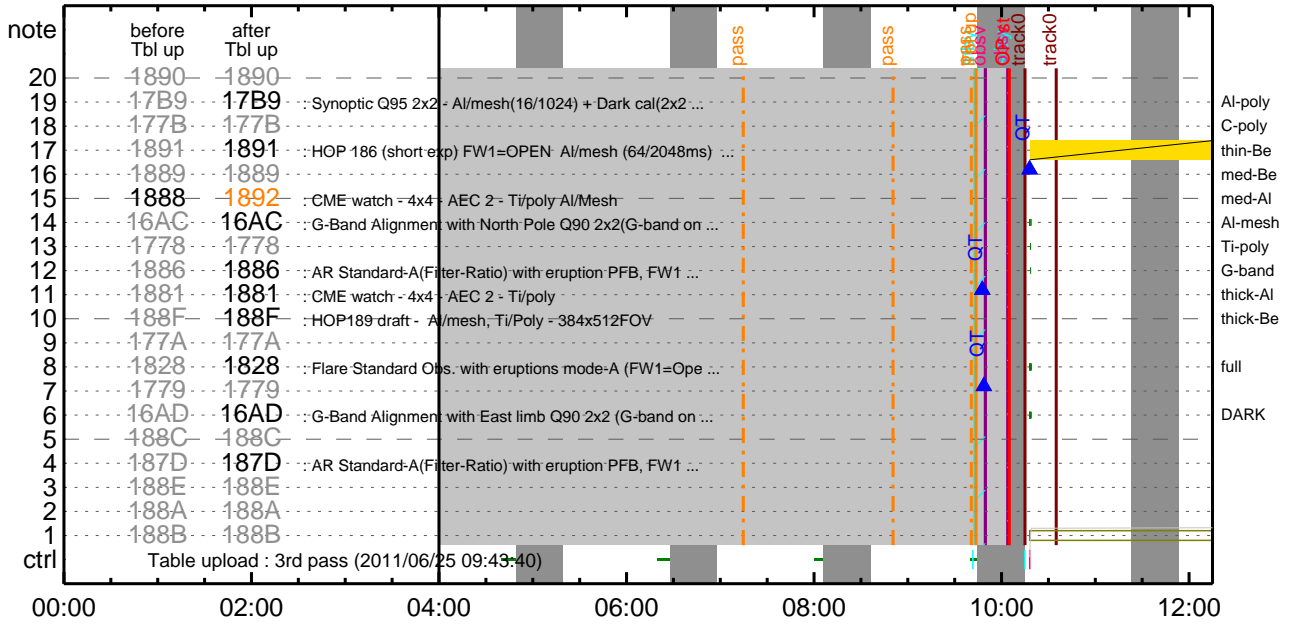
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Flare Detection

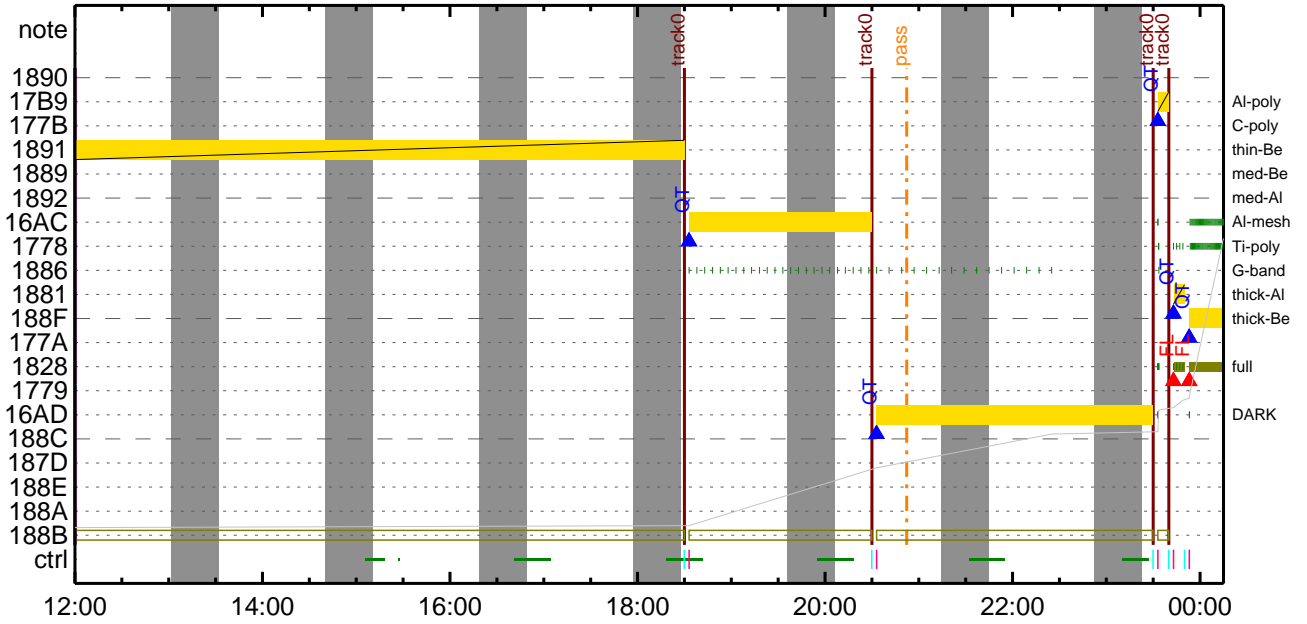
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FLD Patrol												
Term		Pointing (x, y)					Comment					
06/25 23:42:46 - 06/26 11:00:16		Fixed (-521.0, 726.0)					HOP 189, Akatsuki Day 3 (00:00-07:30 UT)					
06/26 11:22:46 - 06/27 00:40:16		Fixed (875.0, 215.0)					AR 11236					
06/27 07:02:46 - 06/27 10:00:16		Fixed (875.0, 215.0)					AR 11236					
06/27 10:22:46 - 06/27 23:45:16		Fixed (875.0, 215.0)					AR 11236					
06/28 00:42:46 - 06/30 09:54:00		Fixed (875.0, 215.0)					AR 11236					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

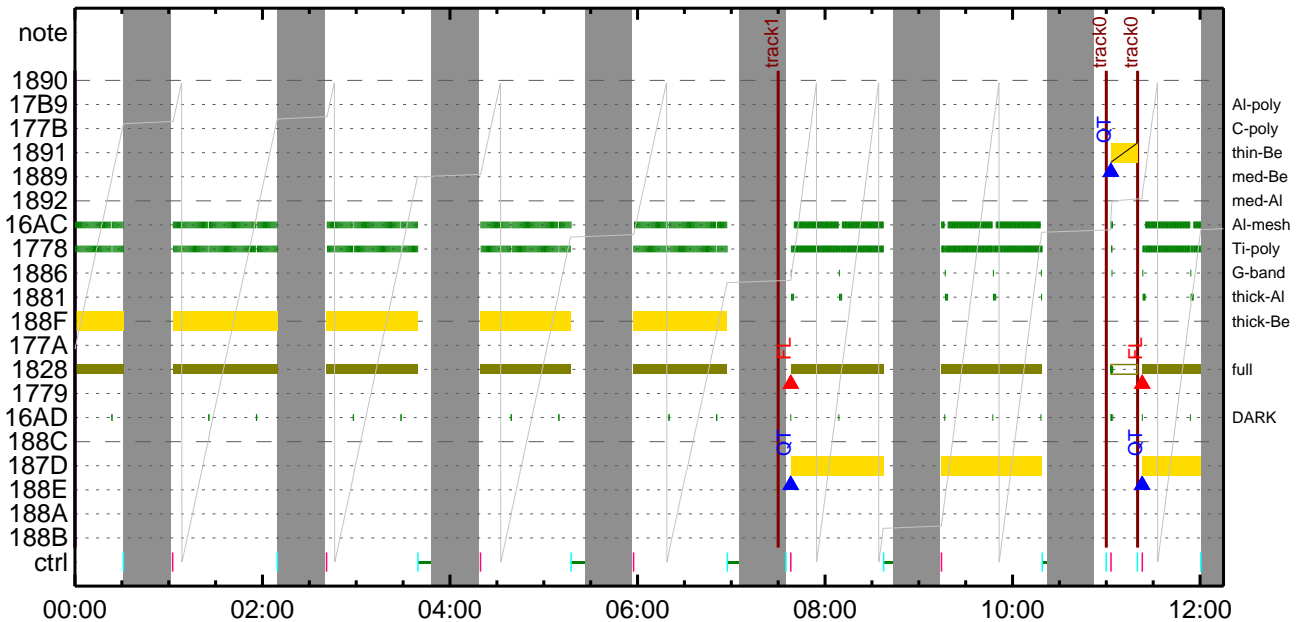
CMDI #0001 2011/06/25



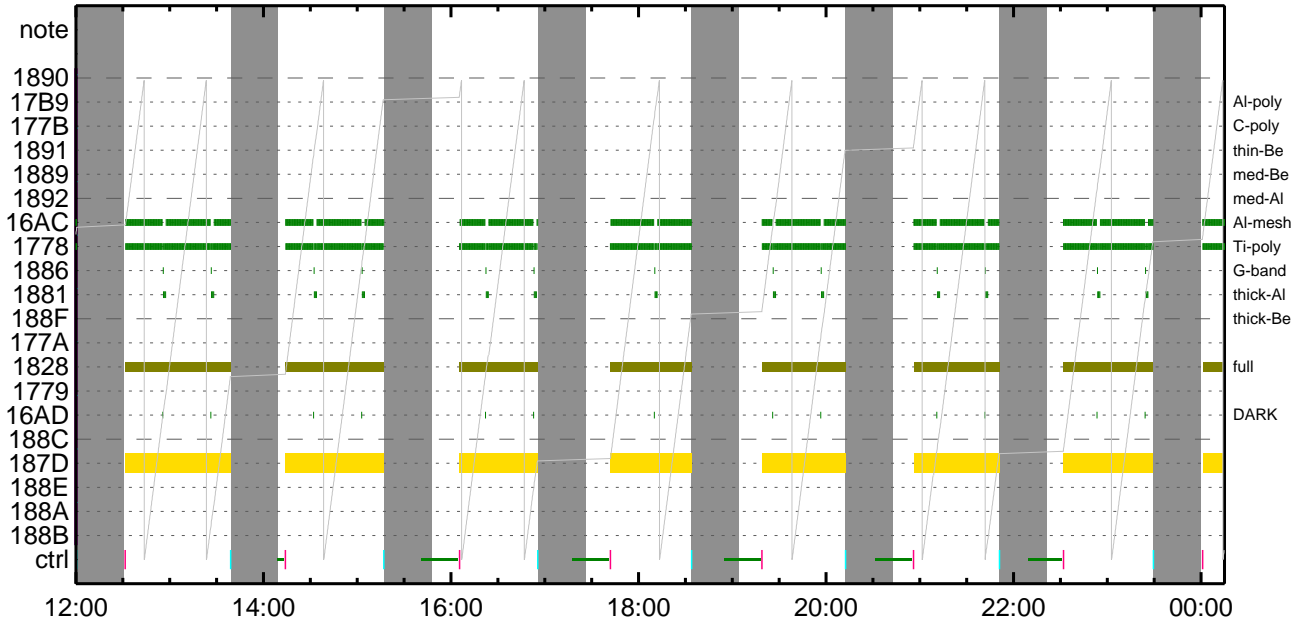
CMDI #0001 2011/06/25



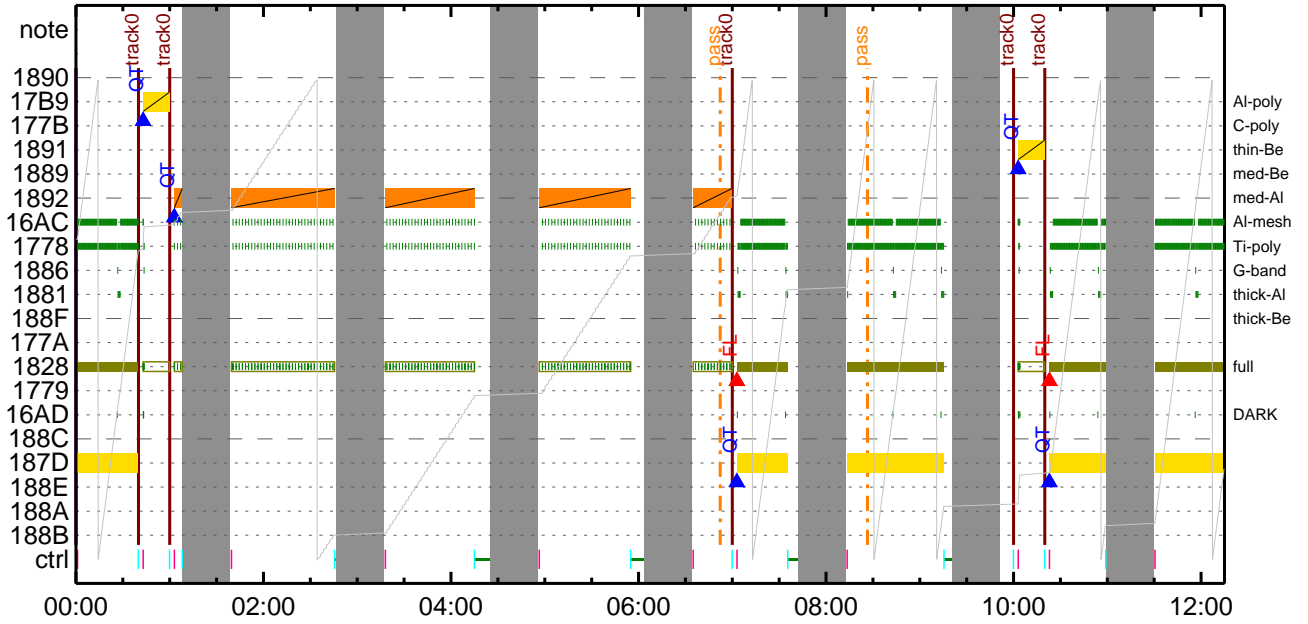
CMDI #0001 2011/06/26



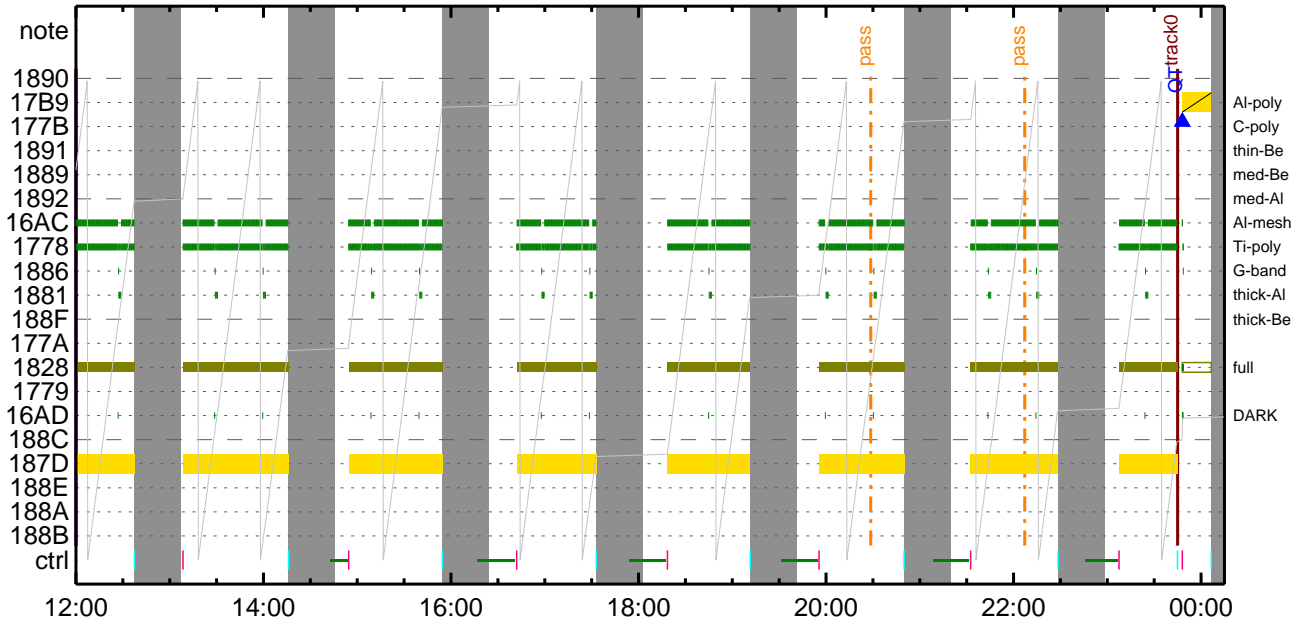
CMDI #0001 2011/06/26



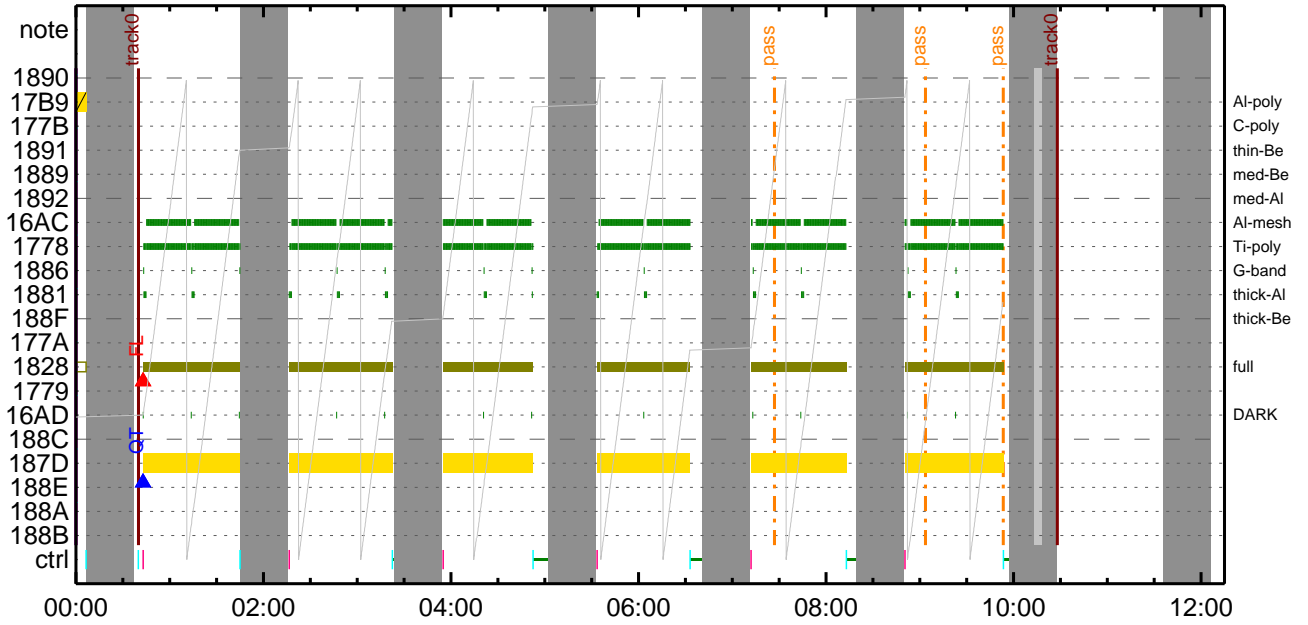
CMDI #0001 2011/06/27



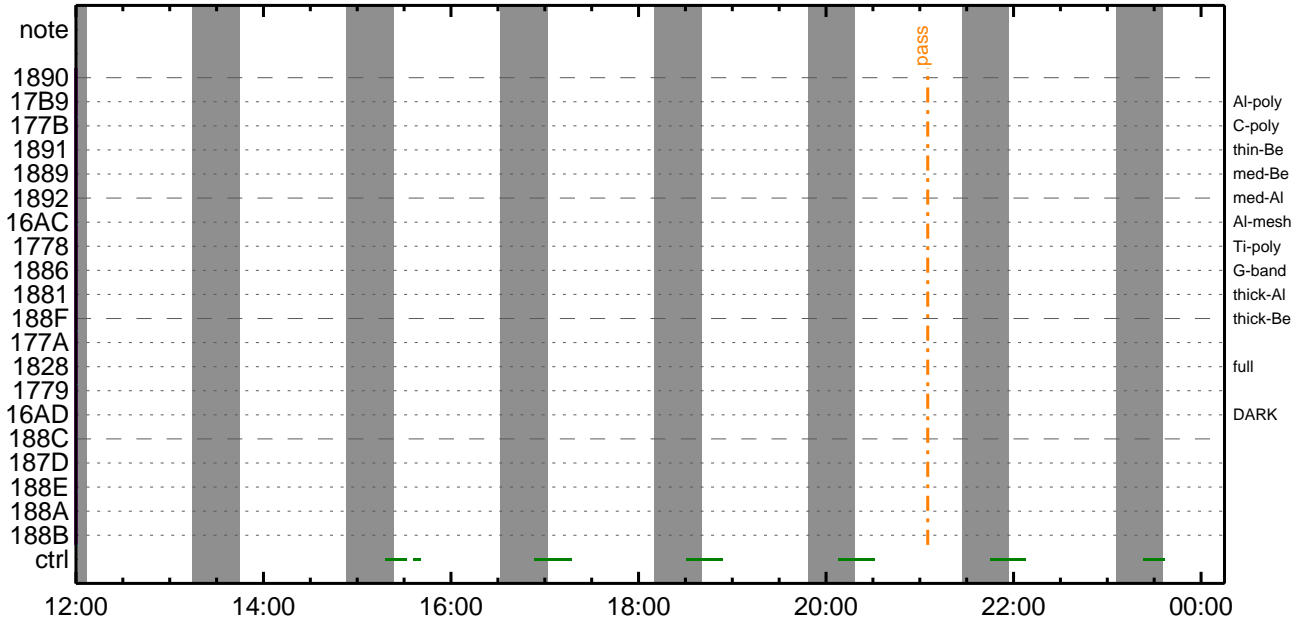
CMDI #0001 2011/06/27



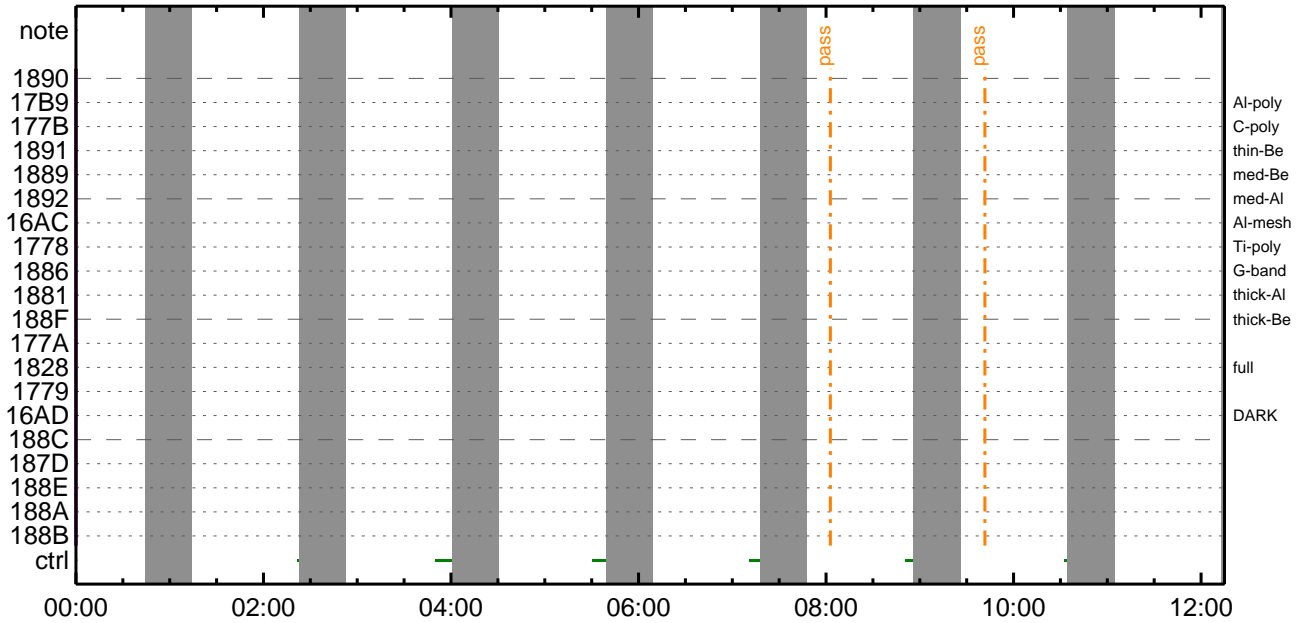
CMDI #0001 2011/06/28



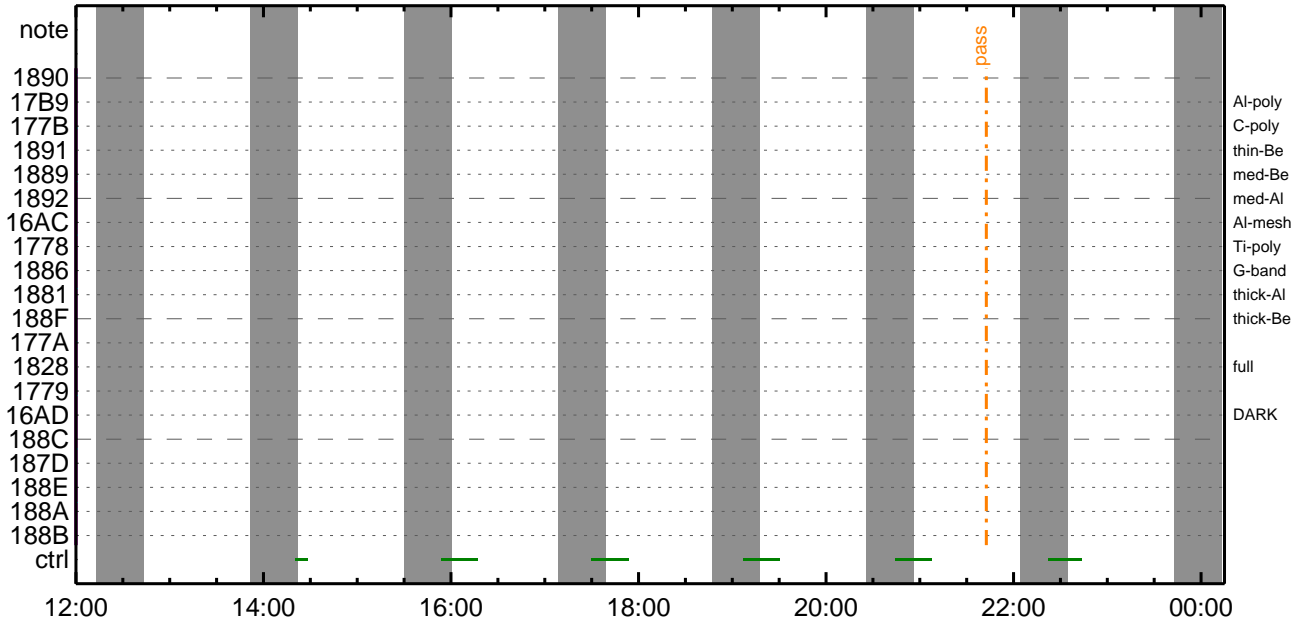
CMDI #0001 2011/06/28



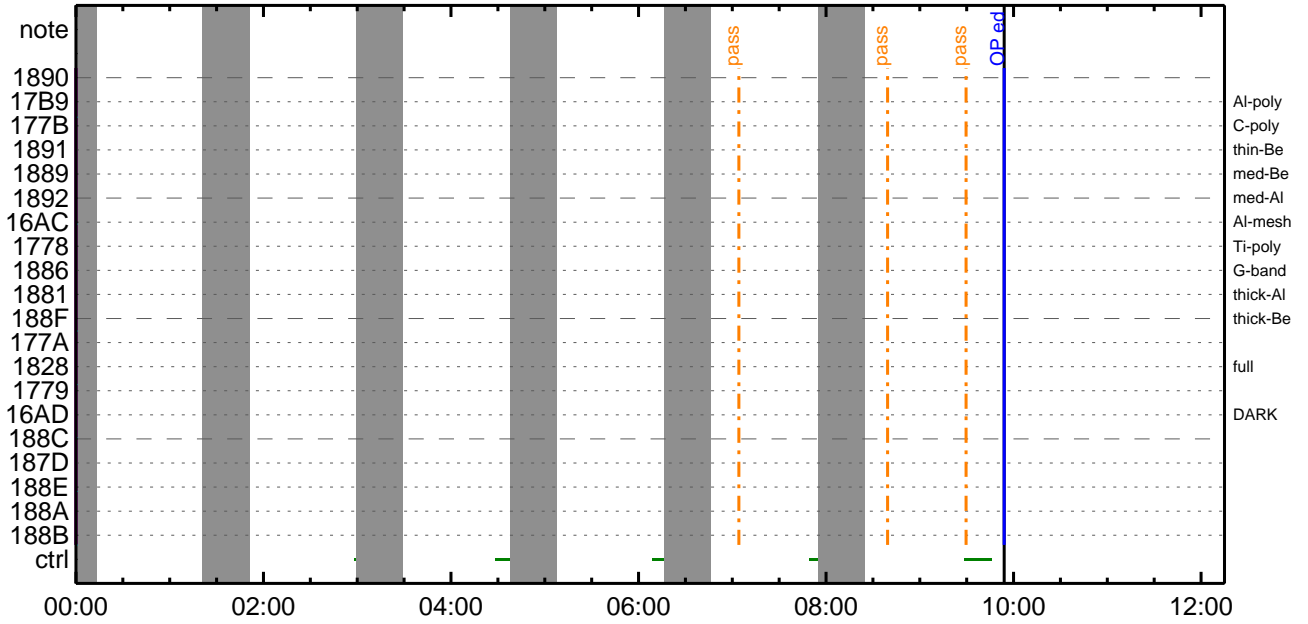
CMDI #0001 2011/06/29



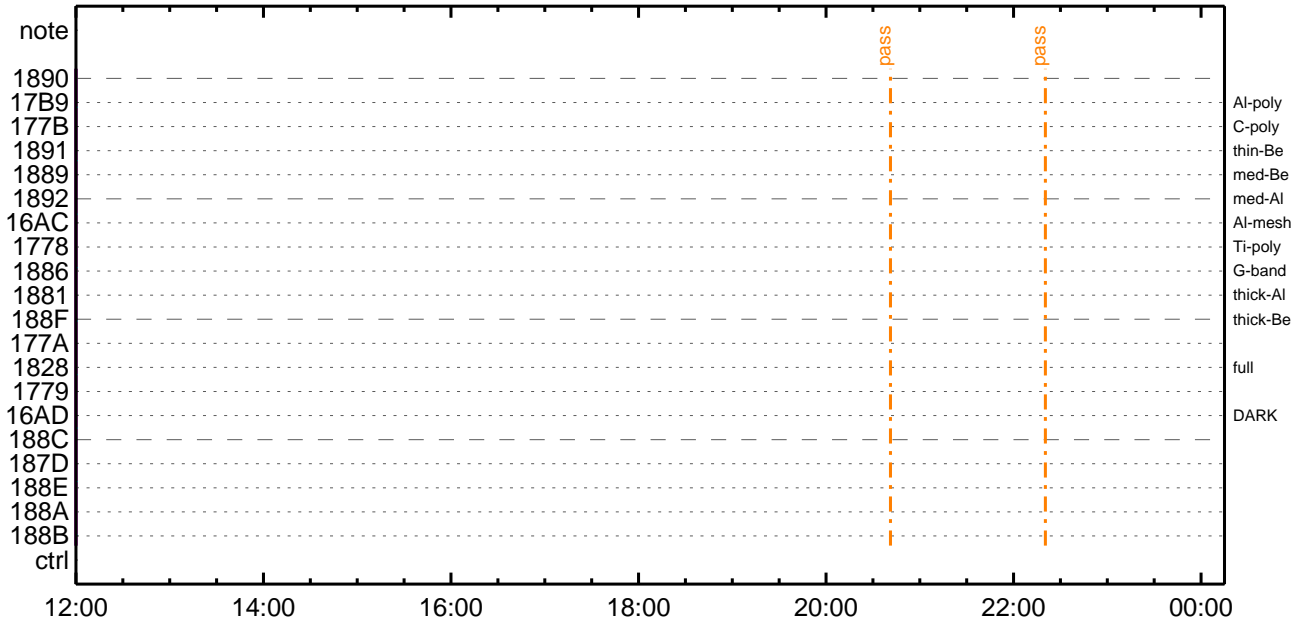
CMDI #0001 2011/06/29



CMDI #0001 2011/06/30



CMDI #0001 2011/06/30




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-157:OP
0104 ( )
0105 S. OG og-157:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYôYx½ê½çòðÁÔæòÇ¼ª°"òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°iYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YôYÉ²òðÁDî¿(UT)
0183 +. TI 2011-06-25 10:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-06-25 10:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-06-25 10:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-06-25 10:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.           ¢¢[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ºîÄè%îíñºîîŷÄŷ§ŷÄŷ¹àîü
0200 C.           ¢¢[HK1_TI_CMD_ENA/DIS]             EQ      ENA
0201 C.           ¢¢[HK1_TI_CMD_NUM]                EQ      4
0202 C.           ¢¢[HK1_NEXT_EXEC_PIM]             EQ      DHU
0203 C.           ¢¢[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.           ¢¢[HK1_DMP_TOP_ADRS_1]           EQ      07
0213 C.           ¢¢[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214 C.           ¢¢[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.           ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.           ¢¢[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.           ¢¢[HK1_PKT_FORM_NO]              EQ      7
0220 C.           ¢¢[HK1_PKT_GEN_TIME]             EQ      0.25 s
0221 C.           ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.           ¢¢[HK1_DMP_CHK_FLG]             EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»º³îÇ§
0226 C.           ¢¢[HK1_DMP_CHK_FLG]             EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîî¼È¹ç•è²îOKº³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èºðîãº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.           ¢¢[HK1_PKT_FORM_NO]              EQ      2
0234 C.           ¢¢[HK1_PKT_GEN_TIME]             EQ      0.5S
0235 C.           ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-06-25 10:04:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-06-25 10:04:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2011-06-25 10:04:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.           [ ] [HK1_TI_CMD_NUM]             EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-06-25 10:04:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.           [ ] [HK1_TI_CMD_NUM]             EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ŷºëÄº¹ºëDCBC•x²è *****
0276 C. (¼ªºîŷÖŷÄŷÈŷŷŷÄŷÇŷèºÈ¼ºª¼ª»Ûº¹ºè)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñºë´ºº¹ºëDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 47s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-06-25 10:04:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÄîâî»ö¼ÝðËÄÐð¹èDCBC•x²è *****
0153 C. (¼ä°îÝÖÝÄÝËÝÞÝËÝÄÝ¼Ýèè¾¼ð¼Ä»Üð¹è)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝÐÝ¹•ï Daily±¿ÎÑè'Øð¹èDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÝÁÝ§ÝÄÝ¹¼Ä»Ü;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-159 2011-06-25 13:36:56 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YFYYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;ÈççäÄ•µ°Æ»Í×ÄÇçîYçYÄY×Yí;¼YÉ;ÈÈè%µ•ííÈ;ÈÈÈ¼°ÇÖç•çç¼í¹ççí;çÄ®, ùç¹çÈçÈççÄ+ç®ç•çÈççç³çÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2011-06-25 10:04:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 08 08)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 60 20 18)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c a0 80 18 20)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d 80 90 06 08)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0f 80 80 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 10 80 80 08 08)
0094 +. DC 07-F0 MDP_XRT_FLD_ENA
0095 BC (d8)
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0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 +. DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 0c)
0106 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 08)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 +. DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 +. TI 2011-06-25 10:04:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 C.
0122 . C. ***** MDP 'úÃîñî»ö¼ÝñÊÂðñ¹ñèDCBC•x²è *****
0123 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÀÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ¥Ðÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿ§ÿÄÿÿ-¼Ã»Û;ã
0134 C.
0135 . C. ***** LOS *****
0136 C.

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*** OP Sequence for XRT ***

2011/06/25	10:14:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	10:14:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/25	10:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/06/25	10:15:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/25	10:15:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/25	10:15:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/25	10:17:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2011/06/25	10:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/25	10:35:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 2e 35 b9 cb		
2011/06/25	18:29:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	18:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/06/25	18:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 ac 00 00 00		
2011/06/25	18:30:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/25	18:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/25	18:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/25	18:32:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2011/06/25	18:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/25	20:29:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	20:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/06/25	20:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 54 00		
2011/06/25	20:30:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/25	20:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/25	20:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/25	20:32:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2011/06/25	20:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/25	23:29:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	23:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/25	23:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/06/25	23:30:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/06/25	23:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/06/25	23:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/25	23:32:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2011/06/25	23:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/25	23:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	23:40:00.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 bf 75 2e 4d		
2011/06/25	23:42:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/06/25	23:42:46.0	XRT_FLD_ENA_426_OG [0x1aa]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/06/25	23:42:48.0	XRT_FLRCTRL_ENA_435_OG [0x1b3]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/06/25	23:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/06/25	23:42:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/06/25	23:42:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/06/25	23:42:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2011/06/25	23:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/06/25	23:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/06/25	23:50:00.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/06/25	23:52:32.0	XRT_FOCUS_POSITION_409_OG [0x199]					

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2011/06/25	23:52:52.0	XRT_FLD_ENA_426_OG [0x1aa]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2011/06/25	23:52:54.0	XRT_FLRCTRL_ENA_435_OG [0x1b3]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/06/25	23:52:56.0	XRT_AEC_RESET_405_OG [0x195]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/06/25	23:52:58.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/06/25	23:53:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/25	23:53:02.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2011/06/25	23:53:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/06/25	23:53:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	00:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	00:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	00:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/06/26	00:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/06/26	01:01:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/26	01:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	02:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	02:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	02:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/06/26	02:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/06/26	02:40:00.0	XRT_Custom_418_OG [0x1a2]							
2011/06/26	02:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	03:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	03:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	03:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/06/26	03:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/06/26	04:18:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/26	04:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	05:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	05:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	05:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/06/26	05:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/06/26	05:56:30.0	XRT_Custom_418_OG [0x1a2]							
2011/06/26	05:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	06:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	06:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	06:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/06/26	07:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/06/26	07:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	01	00	00	00
2011/06/26	07:35:00.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/06/26	07:37:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/06/26	07:37:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/06/26	07:37:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/06/26	07:37:56.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/06/26	07:37:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/06/26	07:38:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/06/26	07:38:02.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/06/26	07:38:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/06/26	07:38:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/06/26	08:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2011/06/26	08:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	08:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	08:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	09:13:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	09:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	10:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	10:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	10:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	10:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	10:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/26	11:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/06/26	11:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/06/26	11:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/06/26	11:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/26	11:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2011/06/26	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	11:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	11:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 ec e6 b2 36	
2011/06/26	11:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/06/26	11:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/26	11:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/26	11:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/26	11:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/26	11:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	11:22:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/06/26	11:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/26	11:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	12:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	12:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	12:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	12:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	12:30:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	12:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	13:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	13:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	13:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	13:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	14:13:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	14:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	15:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	15:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	15:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	15:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	16:04:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	16:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	16:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	16:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	16:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					

2011/06/26	16:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	17:41:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	17:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	18:34:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	18:34:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	18:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	18:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	19:18:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	19:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	20:12:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	20:12:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	20:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	20:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	20:55:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/26	20:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	21:51:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	21:51:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	21:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	21:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/26	22:31:00.5	XRT_Custom_418_OG [0x1a2]					
2011/06/26	22:32:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/26	23:29:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/26	23:29:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/26	23:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/26	23:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/27	00:00:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/27	00:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/27	00:39:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/27	00:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/27	00:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2011/06/27	00:40:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/06/27	00:40:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/06/27	00:40:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/27	00:42:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2011/06/27	00:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/27	00:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/27	01:00:00.0	AOCS_Ore-point_Start_8_OG [0x09e]					
			AOCU_NM	5	02-76	00 cc 73 3d 3f	
2011/06/27	01:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/27	01:02:46.0	XRT_FLD_DIS_423_OG [0x1a7]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/06/27	01:02:48.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/06/27	01:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/27	01:02:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/27	01:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/27	01:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2011/06/27	01:02:58.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/27	01:08:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/27	01:08:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/27	01:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/27	01:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					

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2011/06/27	01:38:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	01:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/06/27	02:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	02:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	02:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	02:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/27	03:17:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	03:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/06/27	04:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	04:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	04:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	04:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/27	04:55:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	04:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/06/27	05:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	05:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	05:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	05:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/27	06:34:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	06:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/06/27	06:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	07:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	07:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	00 ec e6 b2 36				
2011/06/27	07:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/06/27	07:02:48.5	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/06/27	07:02:50.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/06/27	07:02:52.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/06/27	07:02:54.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/27	07:02:56.5	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	07:02:58.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/06/27	07:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/06/27	07:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	07:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	07:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	07:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/27	08:12:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	08:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/06/27	09:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/06/27	09:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	09:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/06/27	09:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/06/27	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/06/27	09:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/06/27	10:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/06/27	10:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/06/27	10:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/06/27	10:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/06/27	10:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/06/27	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				

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2011/06/27	10:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	10:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	10:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	00 ec e6 b2 36
2011/06/27	10:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/06/27	10:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/06/27	10:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/06/27	10:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/06/27	10:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/06/27	10:22:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	10:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/06/27	10:23:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/06/27	10:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	10:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	10:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	11:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	11:29:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	11:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	12:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	12:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	12:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	12:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	13:07:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	13:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	14:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	14:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	14:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	14:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	14:53:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	14:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	15:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	15:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	15:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	15:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	16:41:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	16:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	17:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	17:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	17:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	17:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	18:17:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	18:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	19:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	19:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/27	19:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/27	19:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/27	19:54:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/27	19:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/06/27	20:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/27	20:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da

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2011/06/27	20:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/27	20:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/27	21:31:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/27	21:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/27	22:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/27	22:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/27	22:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/27	22:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/27	23:06:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/27	23:07:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/27	23:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/27	23:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/06/27	23:45:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/06/27	23:45:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/06/27	23:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/06/27	23:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/27	23:47:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2011/06/27	23:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/28	00:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/28	00:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/28	00:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/28	00:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/28	00:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/28	00:40:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 ec e6 b2 36	
2011/06/28	00:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/06/28	00:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/06/28	00:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/06/28	00:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/06/28	00:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/06/28	00:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/28	00:42:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/06/28	00:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/06/28	00:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/28	01:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/28	01:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/28	01:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/28	01:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/28	02:15:30.0	XRT_Custom_418_OG [0x1a2]					
2011/06/28	02:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/28	03:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/28	03:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/28	03:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/28	03:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/06/28	03:54:00.0	XRT_Custom_418_OG [0x1a2]					
2011/06/28	03:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/06/28	04:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/06/28	04:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/06/28	04:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/06/28	04:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					

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2011/06/28	05:32:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/28	05:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/06/28	06:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/28	06:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/28	06:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/28	06:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/28	07:11:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/28	07:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/06/28	08:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/28	08:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/28	08:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/28	08:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/28	08:49:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/28	08:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/06/28	09:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/06/28	09:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/06/28	09:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/06/28	09:53:44.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/06/28	09:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/06/28	10:28:00.0	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00