

# XRT Timeline to be uploaded on 2011/07/07

Period: 2011/07/07 10:56:00 - 2011/07/12 11:15:00

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Normal mode

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## XOB #1891: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1

Term	Pointing (x, y)	Comment
07/07 11:09:00 - 07/07 11:15:54	Fixed ( 0.0, 0.0)	# OP start + 10min + Synoptic + HOP186
07/08 10:08:00 - 07/08 10:14:54	Fixed ( 0.0, 0.0)	Synoptic + HOP186
<b>PROG= 17 1-time(s)</b>		
<b>Subr= 1 1-time(s) 12.0sec</b>		
<b>Seqn= 7 1-time(s) 4.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 5 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 8 1-time(s) 4.0sec</b>		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 4 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 72 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
07/07 11:19:00 - 07/07 13:53:00	Track ( 728.9, 207.4) <sup>Ⓜ 07/07 11:16:00</sup>	* AR11243 Track
07/07 14:30:30 - 07/07 15:31:30	Fixed ( 890.0, 213.0)	* HOP188 : Joint Obs. with ROSA-IBIS
07/07 21:33:00 - 07/07 23:19:54	Track ( 779.2, 211.5) <sup>Ⓜ 07/07 21:30:00</sup>	* AR11243 Track
07/08 00:18:00 - 07/08 07:50:30	Track ( 791.5, 212.7) <sup>Ⓜ 07/08 00:15:00</sup>	* AR11243 Track
07/08 10:18:00 - 07/08 13:59:54	Track ( 831.3, 217.2) <sup>Ⓜ 07/08 10:15:00</sup>	* AR11243 Track
07/08 14:03:00 - 07/08 17:47:30	Track ( 881.1, 210.9) <sup>Ⓜ 07/08 14:00:00</sup>	* HOP188 Joint Obs. with ROSA-IBIS AR 11244
07/08 18:33:00 - 07/08 23:59:54	Track ( 857.7, 221.0) <sup>Ⓜ 07/08 18:15:00</sup>	* AR 11243 Track
07/09 00:13:00 - 07/09 08:27:30	Track ( 873.9, 223.9) <sup>Ⓜ 07/09 00:10:00</sup>	* AR 11243 Track

### PROG= 04 Inf.-time(s)

<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn=100 4-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 6 18-time(s) 2.0sec</b>		
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 1 2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 2 2.0sec
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad

Term	Pointing (x, y)	Comment
07/07 18:03:00 - 07/07 20:27:00	Track ( 731.8, 186.2) <sup>Ⓜ 07/07 18:00:00</sup>	* HOP193 Joint Obs. with SUMMER AR 11243

### PROG= 01 Inf.-time(s)

<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 13 55-time(s) 22.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)**

Term	Pointing (x, y)	Comment
07/07 23:23:00 - 07/07 23:44:00	Fixed ( 0.0, 0.0)	synoptic, shifted + SOT CT diag
07/09 00:03:00 - 07/09 00:09:54	Fixed ( 0.0, 0.0)	Synoptic - shifted

**PROG= 19 1-time(s)**

Subr= 1 1-time(s) 12.0sec

Seqn= 7 1-time(s) 4.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
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Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
07/07 11:19:00 - 07/07 13:53:00	Track ( 728.9, 207.4) <sup>Ⓢ 07/07 11:16:00</sup>	* AR11243 Track
07/07 14:30:30 - 07/07 15:31:30	Fixed ( 890.0, 213.0)	* HOP188 : Joint Obs. with ROSA-IBIS
07/07 18:03:00 - 07/07 20:27:00	Track ( 731.8, 186.2) <sup>Ⓢ 07/07 18:00:00</sup>	* HOP193 Joint Obs. with SUMMER AR 11243
07/07 21:33:00 - 07/07 23:19:54	Track ( 779.2, 211.5) <sup>Ⓢ 07/07 21:30:00</sup>	* AR11243 Track
07/08 00:18:00 - 07/08 07:50:30	Track ( 791.5, 212.7) <sup>Ⓢ 07/08 00:15:00</sup>	* AR11243 Track
07/08 10:18:00 - 07/08 13:59:54	Track ( 831.3, 217.2) <sup>Ⓢ 07/08 10:15:00</sup>	* AR11243 Track
07/08 14:03:00 - 07/08 17:47:30	Track ( 881.1, 210.9) <sup>Ⓢ 07/08 14:00:00</sup>	* HOP188 Joint Obs. with ROSA-IBIS AR 11244
07/08 18:33:00 - 07/08 22:14:54	Track ( 857.7, 221.0) <sup>Ⓢ 07/08 18:15:00</sup>	* AR 11243 Track
07/08 23:21:00 - 07/08 23:59:54	Track ( 857.7, 221.0) <sup>Ⓢ 07/08 18:15:00</sup>	* AR 11243 Track
07/09 00:13:00 - 07/09 08:27:30	Track ( 873.9, 223.9) <sup>Ⓢ 07/09 00:10:00</sup>	* AR 11243 Track

**PROG= 08 1-time(s)**

Subr= 1 30-time(s) 20.0sec

Seqn= 87 1-time(s) 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 60 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3 30-time(s) 60.0sec

Seqn= 87 1-time(s) 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 88 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3 30-time(s) 60.0sec

Seqn= 87 1-time(s) 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 88 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3    30-time(s)    60.0sec													
└─ Seqn= 87    1-time(s)    2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 88    1-time(s)    2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4    24-time(s)    600.0sec													
└─ Seqn= 89    1-time(s)    2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

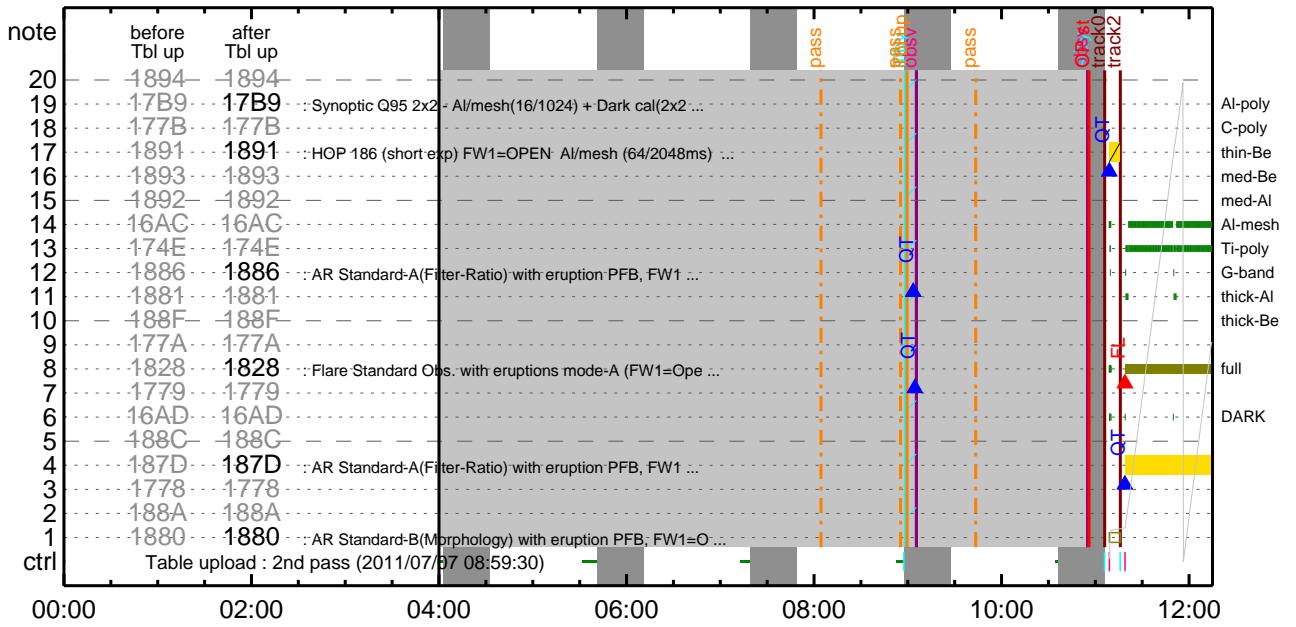
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### Flare Detection

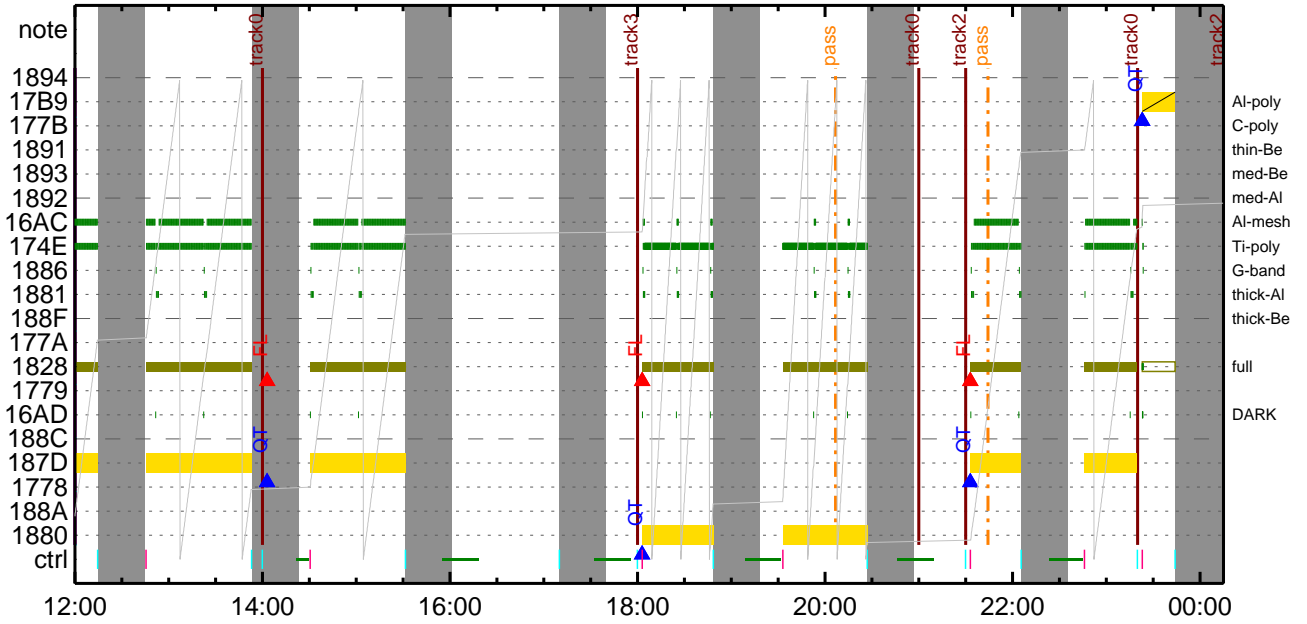
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FLD Patrol												
Term		Pointing (x, y)						Comment				
07/07 11:18:46 - 07/07 23:20:16	Track ( 728.9, 207.4)	⑧ 07/07 11:16:00						* AR11243 Track				
07/08 00:17:46 - 07/08 10:05:16	Track ( 791.5, 212.7)	⑧ 07/08 00:15:00						* AR11243 Track				
07/08 10:17:46 - 07/09 00:00:16	Track ( 831.3, 217.2)	⑧ 07/08 10:15:00						* AR11243 Track				
07/09 00:12:46 - 07/12 11:15:00	Track ( 873.9, 223.9)	⑧ 07/09 00:10:00						* AR 11243 Track				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

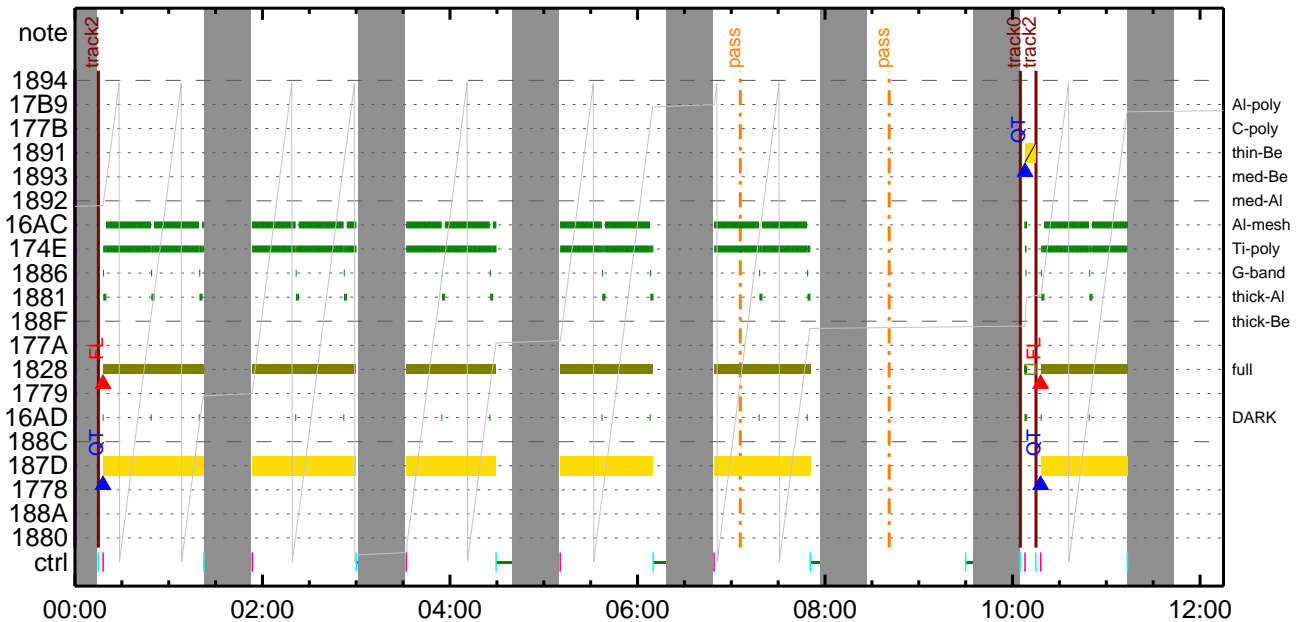
### CMDI #0019 2011/07/07



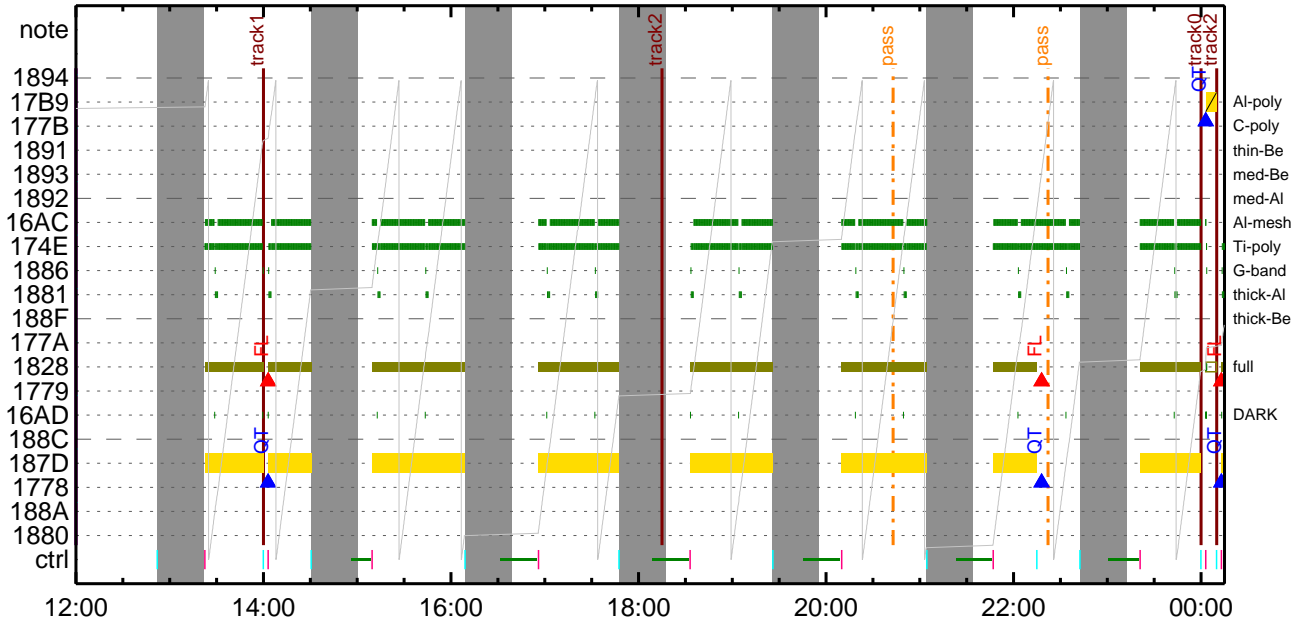
### CMDI #0019 2011/07/07



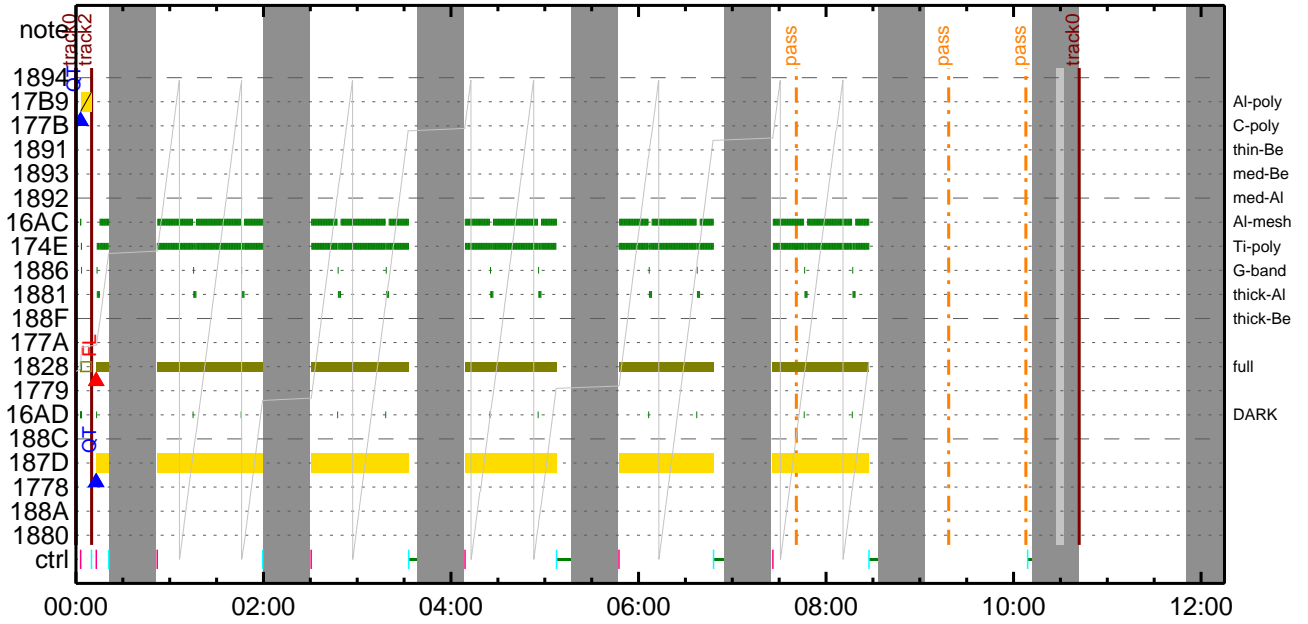
### CMDI #0019 2011/07/08



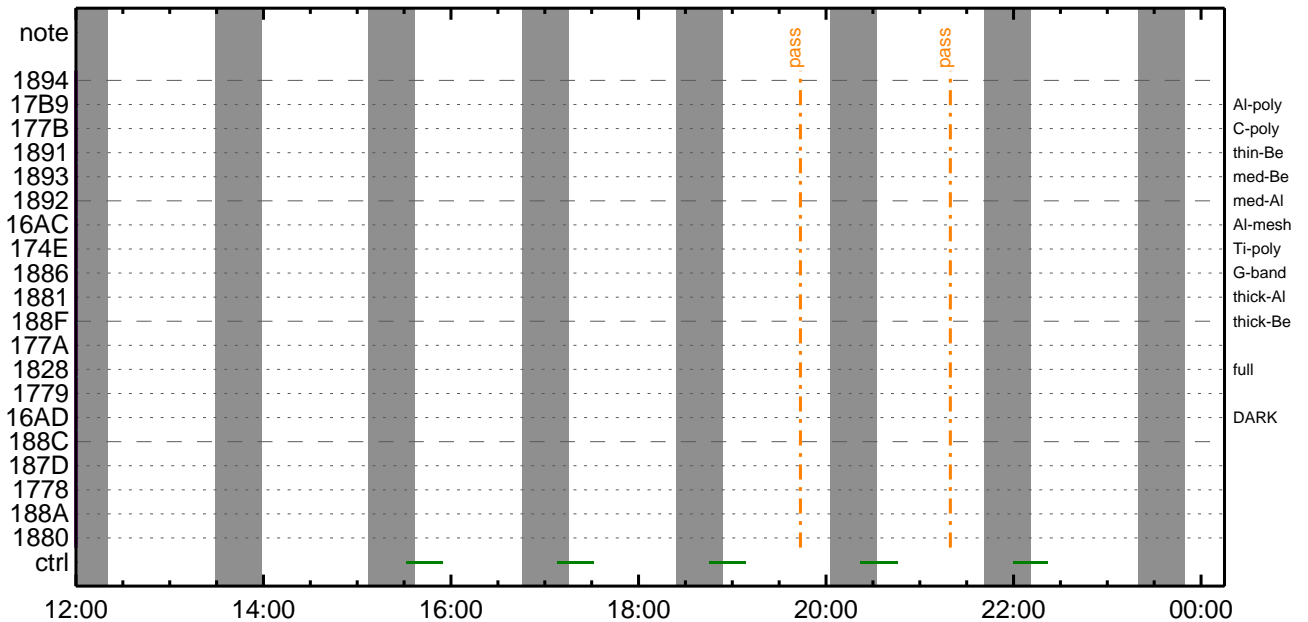
CMDI #0019 2011/07/08



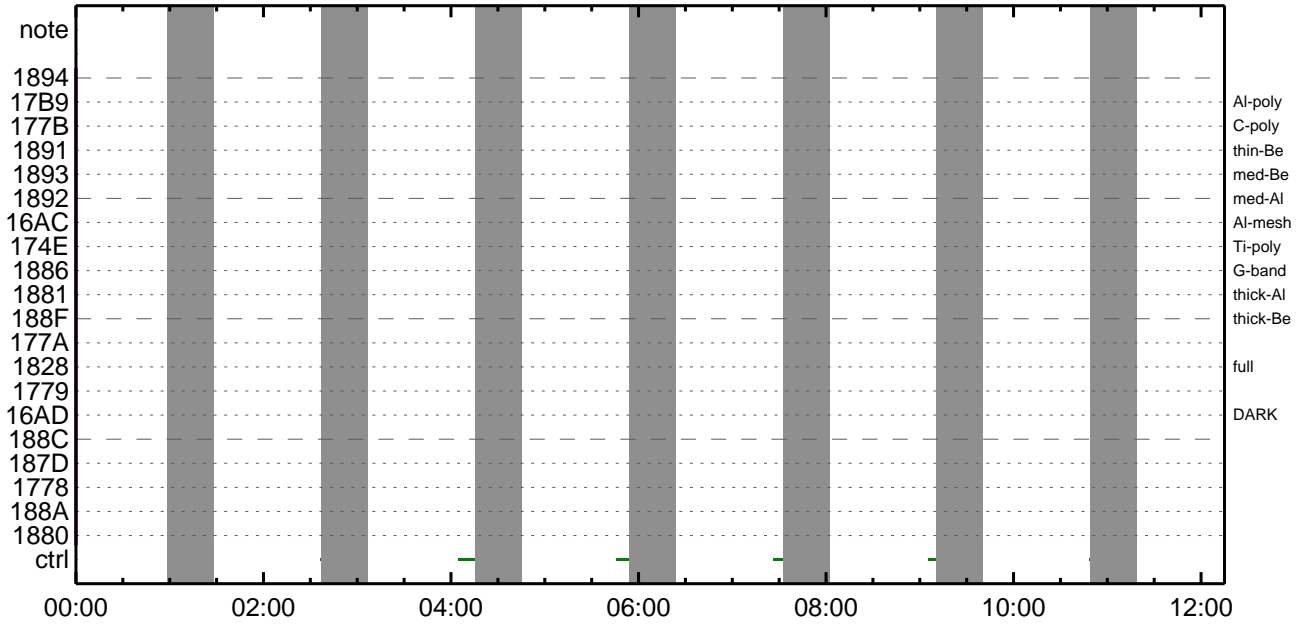
CMDI #0019 2011/07/09



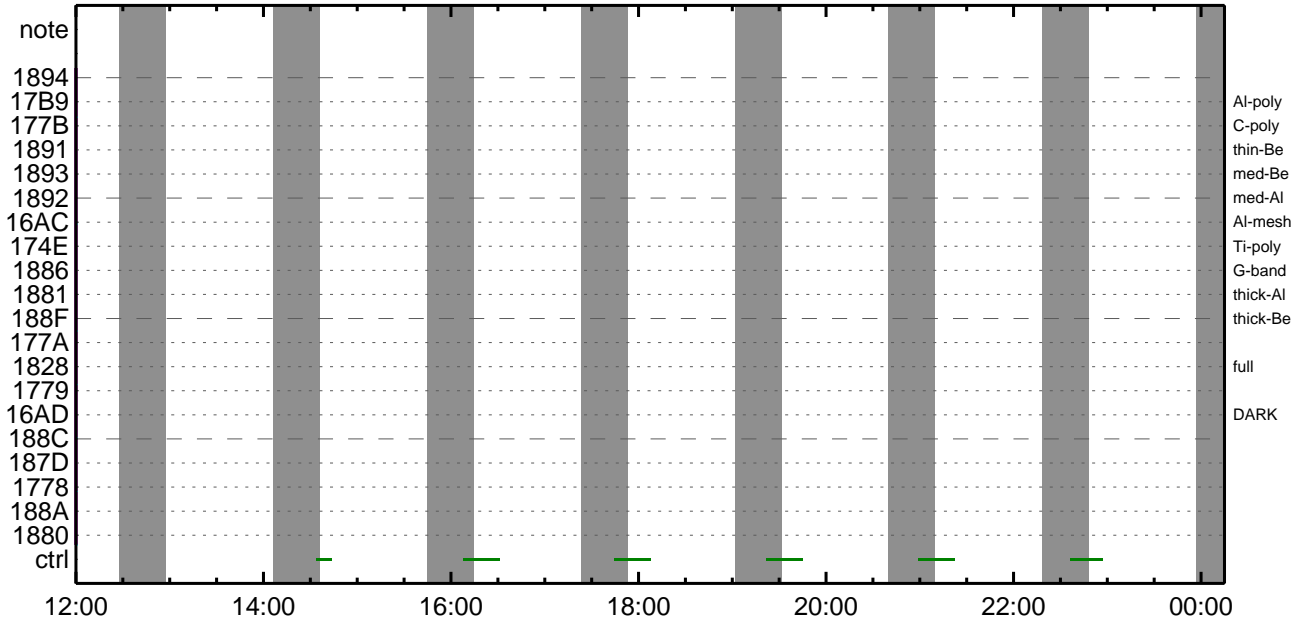
CMDI #0019 2011/07/09



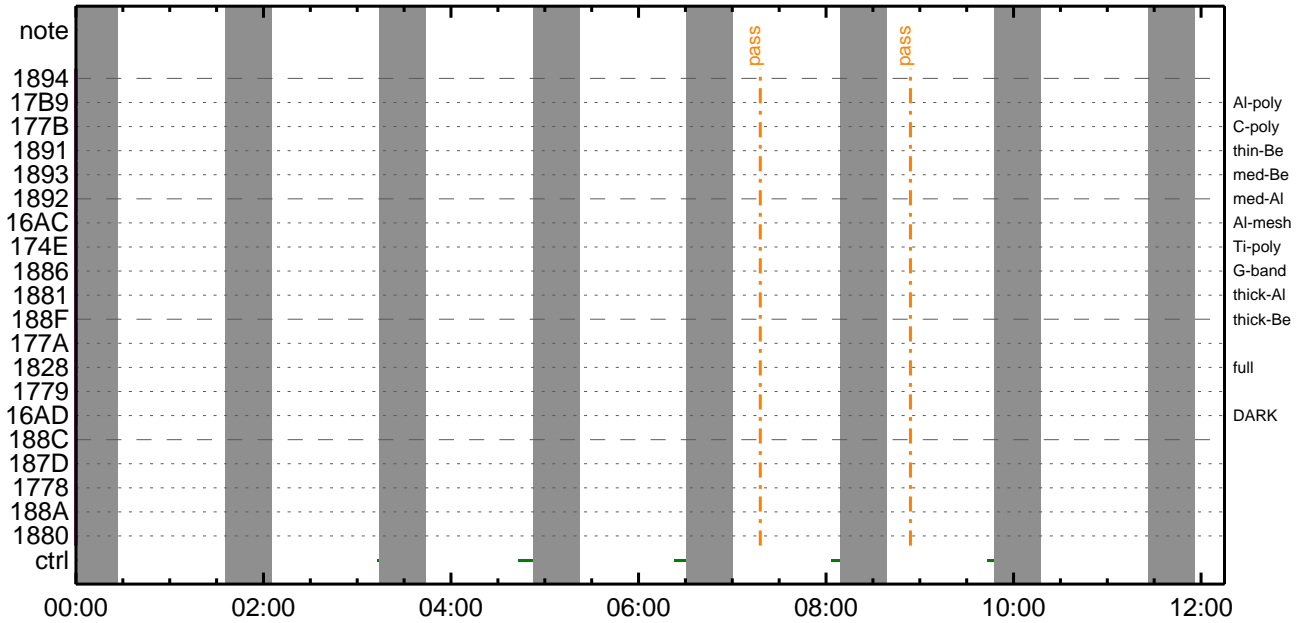
### CMDI #0019 2011/07/10



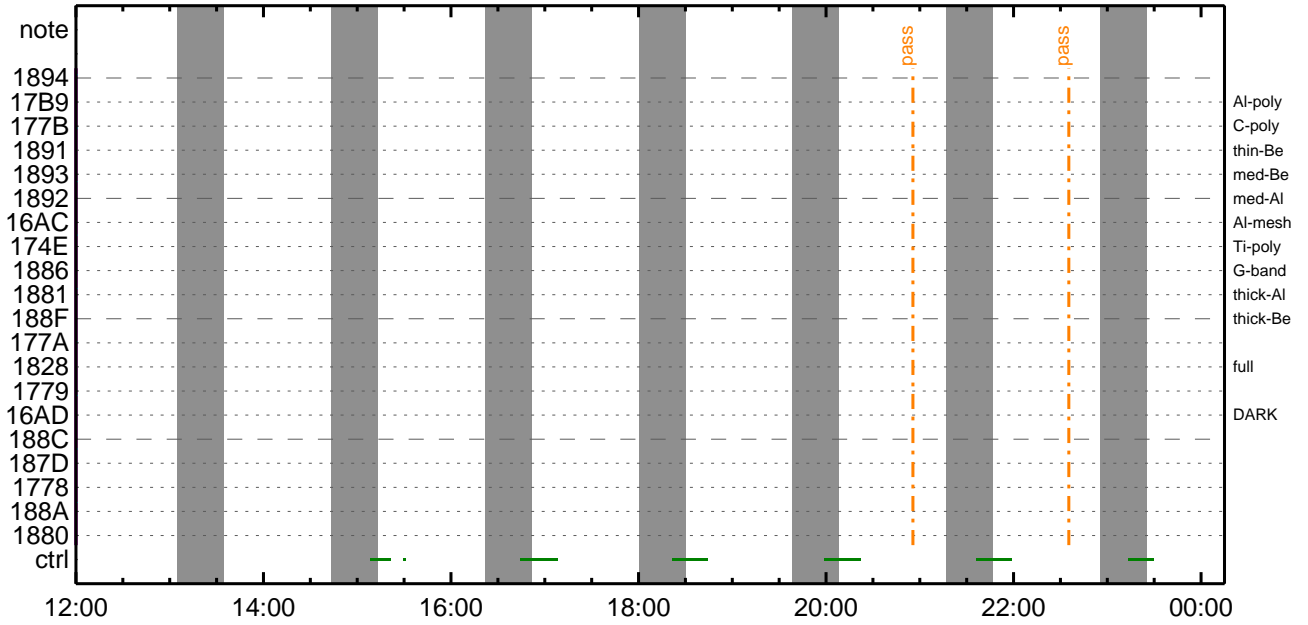
### CMDI #0019 2011/07/10



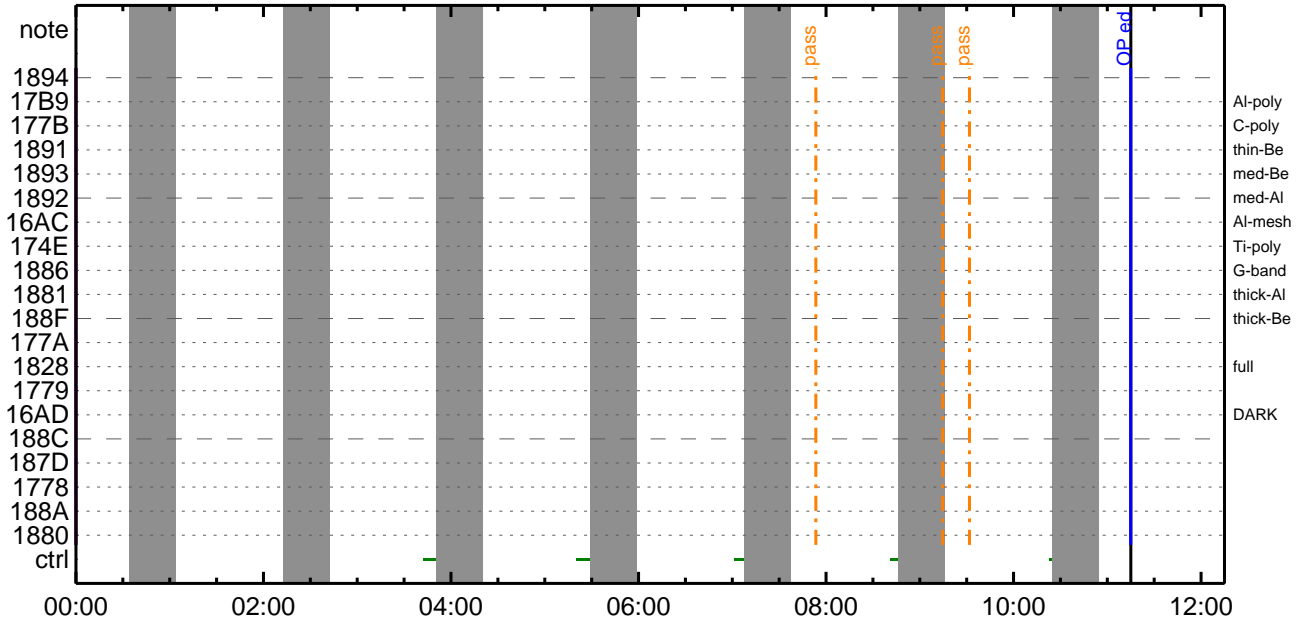
### CMDI #0019 2011/07/11



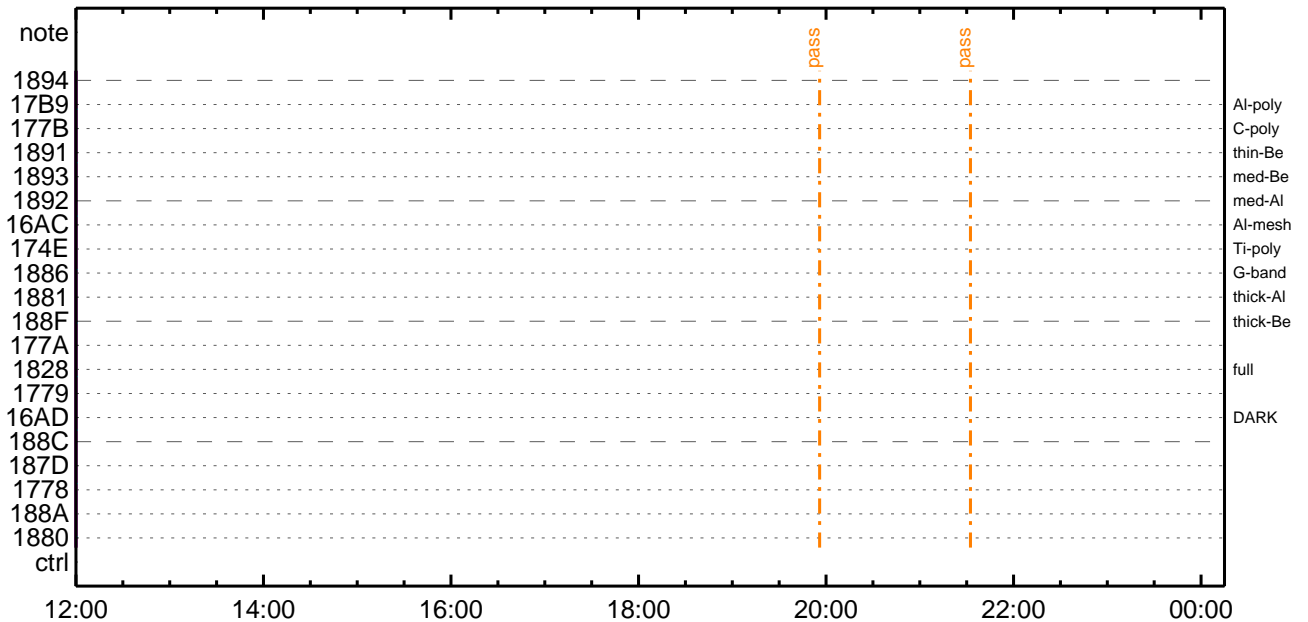
CMDI #0019 2011/07/11



CMDI #0019 2011/07/12



CMDI #0019 2011/07/12







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-181:OP
0104 ( )
0105 S. OG og-181:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. AYOXx½ª î»ð³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼Ê¹ç•è² İOKãð³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. AYOXx½ª î»ð³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼Ê¹ç•è² İOKãð³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. AYOXx½ª î»ð³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼Ê¹ç•è² İOKãð³ ÎÇ§
0165 C.
0166 C. ***** òÊ²¼ãî¼Ã´¶Á°òÊÊ¬ã°Á÷¿@ (¼ãµ-AYOXx½ª ê¼çãðÁÓÆãç¼ªã° ðÊ¼i¹ççãâ) *****
0167 C. DHUãâ;4YE;Ê¼Y½, Y;¼YE;Êãðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã¬Á÷¿@NGãî¼i¹ç;ç°Ê²¼ãî¼TI-CMDÁ÷¿@ãî¼Á¹Ôã° ðÊãã³ ðÊ;f
0180 C. ãðã¿;çSETãEDUMPAİÆ±°iYÑY¹ãÇ¹Ôã|ã³ãÊ;f
0181 C.
0182 C. TIY³YpYóYEòðÁDî¿(UT)
0183 +. TI 2011-07-07 10:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-07-07 10:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-07-07 10:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2011-07-07 10:55:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      $Q[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C.      °È²¼αİÄē%İİñαİŷÄŷ§ŷÄŷ-¹âİŰ
0200 C.      $Q[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.      $Q[HK1_TI_CMD_NUM]                EQ      4
0202 C.      $Q[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.      $Q[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TİİŰ°èŷÄŷÖŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      $Q[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.      $Q[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.      $Q[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.      $Q[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.      $Q[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      $Q[HK1_PKT_FORM_NO]              EQ      7
0220 C.      $Q[HK1_PKT_GEN_TIME]             EQ      0.25 s
0221 C.      $Q[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.      $Q[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.      $Q[HK1_DMP_CHK_FLG]             EQ      EXEC
0224 C.
0225 C.      ŷÄŷÖŷ×½ªİ»αδ³İÇ§
0226 C.      $Q[HK1_DMP_CHK_FLG]             EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLαİŰÈ¹Ç•è²İOKαδ³İÇ§
0229 C.
0230 C.      DHUŷâ;¼ŷÈ;È¼ŷ½.ŷİ;¼ŷÈ;Èαδİãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      $Q[HK1_PKT_FORM_NO]              EQ      2
0234 C.      $Q[HK1_PKT_GEN_TIME]             EQ      0.5S
0235 C.      $Q[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.      $Q[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2011-07-07 10:55:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2011-07-07 10:55:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-07-07 10:55:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]              EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2011-07-07 10:55:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]              EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP `úÄİαİ»ö¼ŷαÈÄα¹αēDCBC•x²è *****
0276 C.      (¾á°İŷÖŷÄŷÈŷŷÈŷäŷÇŷèÈ¼αα¼Ä»Űα¹è)
0277 C.      S. DC-BC dcbc-402:DCBC
0278 C.      (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αēDCBC•x²è *****
0282 C.      S. DC-BC dcbc-153:DCBC
0283 C.      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOŷŷÄŷ§ŷÄŷ-¼Ä»Ű;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-182 2011-07-07 11:59:18 183 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈøÈ¼°ÇÔø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. FPP DC MODE OFF
0047 C. *****
0048 C.
0049 . C. < FPP Decontam Mode Disable >
0050 +. DC 07-F2 FPP_PWR_ON_ENA
0051 BC (12 02)
0052 +. DC 07-F2 FPP_DE_CONTAM_MODE
0053 BC (24 03 00)
0054 . C. -----
0055 C. FPP_DE_CONTAM_MODE = OFF [ ]
0056 C. -----
0057 C.
0058 . C. < Set duties for CCD Op heaters >
0059 +. DC 07-F2 FPP_FG_OPHTER_DUTY
0060 BC (54 03 00)
0061 +. DC 07-F2 FPP_SP_OPHTER_DUTY
0062 BC (4c 03 19)
0063 . C. -----
0064 C. FPP_FG_CCDHTR_DC = 0 [ ]
0065 C. FPP_SP_CCDHTR_DC = 25 [ ]
0066 C. -----
0067 C.
0068 . C. < Enable SP Heater >
0069 +. DC 07-F2 FPP_PWR_ON_ENA
0070 BC (12 02)
0071 +. DC 07-F2 FPP_SP_OPHTER_ENA
0072 BC (48 02)
0073 . C. -----
0074 C. FPP_SPHTER_STAT > 01 [ ]
0075 C. -----
0076 C.
0077 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0078 +. DC 07-FC EIS_MODE_MANU
0079 BC (21 02)
0080 . C. Verify EIS in MANUAL mode
0081 . C. Estimated OBSTBL upload time is 10s
0082 C. *****
0083 C. EIS START OBSTBL LOAD
0084 C. *****
0085 . S. RAM ram-820:EIS_OBSTBL
0086 ( )
0087 +. DC 07-FC EIS_DUMP_OBSTBL
0088 BC (07 07 07 00 00 70 00)
0089 C.
0090 C. Execute, after the success of OBSTBL upload.
0091 C. Set EIS TI-commands
0092 +. TI 2011-07-07 10:55:50.0
0093 DC 07-FC EIS_MODE_CHG_ENA
0094 BC (20)
0095 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
```

```

0096 C. *****
0097 C. EIS END OBSTBL LOAD
0098 C. *****
0099 C.
0100 C. ***** XRT START *****
0101 C.
0102 +. DC 07-F0 MDP_XRT_CTRL_MANU
0103 BC (c1)
0104 +. DC 07-F0 MDP_XRT_MODE_STBY
0105 BC (c3)
0106 . C. ----- Success Verify ? OK / NG____
0107 C.
0108 C. XRT Obs. Table Upload
0109 . S. RAM ram-291:MDP_OBS_X
0110 ( )
0111 C.
0112 +. DC 07-F0 MDP_DUMP_XRTTBL
0113 BC (84 07 00 00 00 3a d4)
0114 . C. ----- Comparison Check ? OK / ERR ____
0115 C.
0116 C.
0117 +. DC 07-F0 MDP_XRT_ROI_SET
0118 BC (cd 01 b1 b1 04 04)
0119 +. DC 07-F0 MDP_XRT_ROI_SET
0120 BC (cd 02 b1 b1 08 08)
0121 +. DC 07-F0 MDP_XRT_ROI_SET
0122 BC (cd 03 b1 b1 08 08)
0123 +. DC 07-F0 MDP_XRT_ROI_SET
0124 BC (cd 04 b1 b1 06 06)
0125 +. DC 07-F0 MDP_XRT_ROI_SET
0126 BC (cd 05 85 83 08 08)
0127 +. DC 07-F0 MDP_XRT_ROI_SET
0128 BC (cd 06 85 83 06 06)
0129 +. DC 07-F0 MDP_XRT_ROI_SET
0130 BC (cd 07 85 83 08 08)
0131 +. DC 07-F0 MDP_XRT_ROI_SET
0132 BC (cd 08 80 80 20 20)
0133 +. DC 07-F0 MDP_XRT_ROI_SET
0134 BC (cd 09 80 80 20 08)
0135 +. DC 07-F0 MDP_XRT_ROI_SET
0136 BC (cd 0a 80 80 08 20)
0137 +. DC 07-F0 MDP_XRT_ROI_SET
0138 BC (cd 0f 80 80 06 06)
0139 +. DC 07-F0 MDP_XRT_ROI_SET
0140 BC (cd 10 80 80 08 08)
0141 +. DC 07-F0 MDP_XRT_FLD_ENA
0142 BC (d8)
0143 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0144 BC (c8)
0145 +. DC 07-F0 MDP_XRT_AEC_RESET
0146 BC (d0)
0147 +. DC 07-F0 MDP_XRT_ARS_DIS
0148 BC (d5)
0149 +. DC 07-F0 MDP_XRT_FLD_RESET
0150 BC (da)
0151 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0152 BC (c4 0c)
0153 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0154 BC (c4 08)
0155 . C. ----- Success Verify ? OK / NG ____
0156 C.
0157 C.
0158 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0159 C.
0160 +. DC 07-F0 MDP_XRT_MODE_OBSV
0161 BC (c2)
0162 +. TI 2011-07-07 10:55:02.0
0163 DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 . C. ----- Success Verify ? OK / NG ____
0166 C.
0167 C. ***** XRT END *****
0168 C.
0169 . C. ***** MDP `ûÃîoî»ô¼YòÈÀð¹òèDCBC•x²è *****
0170 C. (¼á°îYÓYÁYÈYÞYÈYáYçYèòE¼¼¼¼»Û¹òè)
0171 . S. DC-BC dcbc-402:DCBC
0172 (MDP_known_event)
0173 C.
0174 C.
0175 . C. ***** YDÿ!•Ï Daily±;îÑòÈ`Ø¹òèDCBC•x²è *****
0176 . S. DC-BC dcbc-153:DCBC
0177 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0178 C.
0179 C.
0180 . C. ;ãLOSŸÁŸSŸÃŸ-¼Å»Û;ã
0181 C.
0182 . C. ***** LOS *****
0183 C.

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Jul 07, 11 11:59

## XRT\_OGLIST\_0019.chk

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\*\*\* OP Sequence for XRT \*\*\*

2011/07/07	11:05:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	11:05:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/07/07	11:06:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2011/07/07	11:06:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/07/07	11:06:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/07/07	11:06:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/07/07	11:08:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2011/07/07	11:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/07	11:15:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	11:16:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2011/07/07	11:18:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/07/07	11:18:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/07/07	11:18:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/07/07	11:18:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/07/07	11:18:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/07/07	11:18:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/07	11:18:56.0	XRT_QT_PROG_SET_403_OG [0x193]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2011/07/07	11:18:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/07/07	11:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/07	12:14:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	12:14:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/07	12:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/07	12:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/07	12:44:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/07	12:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/07	13:53:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	13:53:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/07	13:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/07	13:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/07	13:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	14:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 ed 0f b0 e6		
2011/07/07	14:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/07/07	14:02:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/07/07	14:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/07/07	14:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/07/07	14:02:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/07/07	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/07	14:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2011/07/07	14:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/07/07	14:29:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/07	14:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/07	15:31:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	15:31:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/07	15:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/07	15:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/07	17:10:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/07	17:10:02.0	XRT_FLD_RESET_412_OG [0x19c]					

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2011/07/07	17:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/07	17:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/07	17:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2011/07/07	18:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/07	18:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/07	18:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/07	18:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/07	18:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/07	18:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/07	18:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/07	18:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/07	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/07	18:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	18:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/07	18:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/07	18:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/07	19:32:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/07	19:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/07	20:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	20:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/07	20:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/07	20:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/07	21:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 c5 69 38 e5
2011/07/07	21:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	21:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00
2011/07/07	21:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/07	21:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/07	21:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/07	21:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/07	21:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/07	21:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/07	21:32:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/07/07	21:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/07	21:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/07	22:05:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	22:05:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/07	22:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/07	22:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/07	22:45:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/07	22:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/07	23:19:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/07	23:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/07	23:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2011/07/07	23:20:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/07	23:20:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/07	23:20:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5



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2011/07/07	23:22:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2011/07/07	23:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/07	23:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/07	23:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/07	23:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/07	23:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	00:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	00:15:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/07/08	00:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/07/08	00:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/07/08	00:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/07/08	00:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/07/08	00:17:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/08	00:17:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	00:17:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/07/08	00:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/07/08	00:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/08	01:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	01:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	01:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	01:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	01:52:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/08	01:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/08	03:00:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	03:00:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	03:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	03:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	03:31:00.0	XRT_Custom_418_OG [0x1a2]					
2011/07/08	03:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/08	04:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	04:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	04:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	04:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	05:09:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/08	05:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/08	06:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	06:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	06:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	06:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	06:48:00.0	XRT_Custom_418_OG [0x1a2]					
2011/07/08	06:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/08	07:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	07:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	07:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	07:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/08	09:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/08	09:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/08	09:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/08	09:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					

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2011/07/08	10:04:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	10:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/07/08	10:05:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00	00
2011/07/08	10:05:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/08	10:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/08	10:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/08	10:07:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2011/07/08	10:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	10:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	10:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00	00
2011/07/08	10:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/08	10:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/08	10:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/08	10:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/08	10:17:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/08	10:17:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	10:17:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/07/08	10:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/08	10:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	11:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	11:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	11:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	11:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	12:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	12:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	12:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	12:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	13:21:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/08	13:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	13:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	14:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	01	00	00	00	00
2011/07/08	14:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/08	14:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/08	14:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/08	14:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/08	14:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/08	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	14:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/07/08	14:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/08	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	14:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	14:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	14:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	14:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	15:08:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/08	15:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	16:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2011/07/08	16:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	16:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	16:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	16:55:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/08	16:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	17:47:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	17:47:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	17:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	17:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	18:15:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/07/08	18:32:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/08	18:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	19:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	19:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	19:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	19:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	20:09:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/08	20:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	21:04:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	21:04:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	21:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	21:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	21:46:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/08	21:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	22:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	22:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/08	22:17:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/08	22:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/08	22:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/08	22:17:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/08	22:17:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	22:17:56.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/07/08	22:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/08	22:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	22:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/08	22:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/08	22:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/08	23:20:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/08	23:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/08	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/08	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/09	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/09	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/09	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/09	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/09	00:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2011/07/09	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	00:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				

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2011/07/09	00:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/09	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/09	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/09	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/09	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/09	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	00:12:56.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/07/09	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/09	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	00:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	00:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	00:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	00:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	00:51:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	00:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	01:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	01:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	01:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	02:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	02:29:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	02:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	03:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	03:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	03:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	03:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	04:08:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	04:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	05:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	05:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	05:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	05:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	05:46:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	05:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	06:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	06:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	06:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	06:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	07:25:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	07:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/09	08:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	08:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	08:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	08:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	10:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/09	10:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/09	10:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/09	10:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/09	10:42:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	00	00