



Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	20.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)	Comment
07/09 20:15:00 - 07/09 21:59:54	Fixed ( 0.0, 945.0)	Co-alignment (North)
<b>PROG= 14 1-time(s)</b>		
└ Subr= 1 1-time(s) 360.0sec		
└ Seqn= 21 24-time(s) 300.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT**

Term	Pointing (x, y)	Comment
07/09 22:15:00 - 07/09 23:59:54	Fixed (-945.0, 0.0)	Co-alignment (East)
<b>PROG= 06 1-time(s)</b>		
└ Subr= 1 1-time(s) 360.0sec		
└ Seqn= 22 15-time(s) 480.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)**

Term	Pointing (x, y)	Comment
07/10 00:03:00 - 07/10 00:09:54	Fixed ( 0.0, 0.0)	synoptic
07/10 23:48:00 - 07/11 00:27:00	Fixed ( 0.0, 0.0)	synoptic
07/11 23:38:00 - 07/11 23:44:54	Fixed ( 0.0, 0.0)	synoptic
<b>PROG= 19 1-time(s)</b>		
└ Subr= 1 1-time(s) 12.0sec		
└ Seqn= 7 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└ Seqn= 8 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 4 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad**

Term	Pointing (x, y)	Comment
07/10 00:13:00 - 07/10 07:25:30	Track ( 38.4, -406.1) @ 07/10 00:10:00	AR11247 observation
07/10 18:11:36 - 07/10 23:44:54	Track ( 185.6, -405.9) @ 07/10 18:00:00	AR11247 observation
07/11 00:30:06 - 07/11 07:59:54	Track ( 233.5, -405.5) @ 07/10 23:55:00	AR11247 Observation
07/11 20:25:36 - 07/11 23:34:54	Track ( 389.7, -402.5) @ 07/11 20:00:00	AR11247 observation
07/11 23:48:00 - 07/12 07:59:54	Track ( 417.4, -401.7) @ 07/11 23:45:00	AR11247 observation
<b>PROG= 11 Inf.-time(s)</b>		
└ Subr= 2 1-time(s) 2.0sec		
└ Seqn= 19 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└ Seqn= 41 4-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 13 30-time(s) 80.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1897: CH obs filter - Ti/poly 8s, Al/mesh 4s, G-band - 512FOV 3min cad - Q95**

Term	Pointing (x, y)	Comment
07/11 18:48:11 - 07/11 19:38:30	Track ( 623.9, -361.2) @ 07/11 18:00:00	HOP191 with BBSO (Target: CH)
<b>PROG= 10 Inf.-time(s)</b>		



Term	Pointing (x, y)	Comment
07/09 14:03:06 - 07/09 19:59:54	Track ( -46.4, -405.4) <small>© 07/09 14:00:00</small>	HOP188 with Sac Peak ROSA
07/10 14:03:00 - 07/10 17:23:00	Track ( 152.9, -406.1) <small>© 07/10 14:00:00</small>	HOP188 with Sac Peak ROSA
07/11 14:03:05 - 07/11 18:00:00	Track ( 344.3, -403.6) <small>© 07/11 14:00:00</small>	HOP188 with Sac Peak ROSA
<b>PROG= 05 1-time(s)</b>		
<b>Subr= 1 30-time(s) 20.0sec</b>		
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/thick-Al	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 94 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 94 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 94 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>		
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 18 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>		
<b>Seqn= 89 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

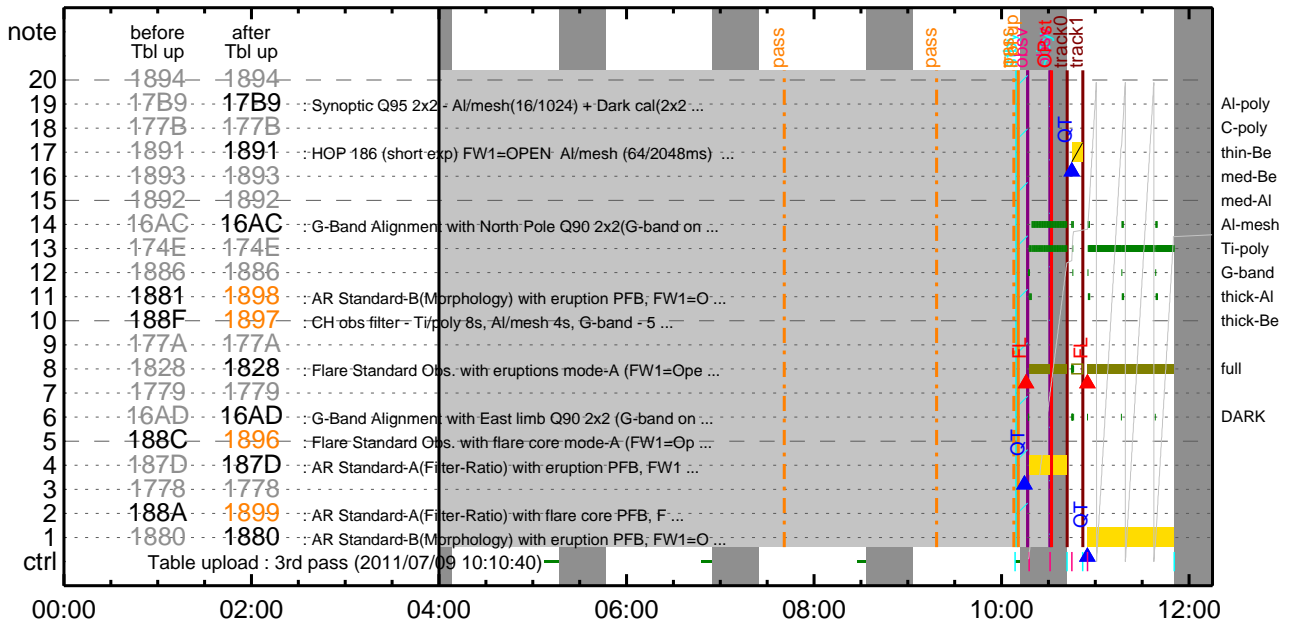
\* \* \* \* \*

**Flare Detection**

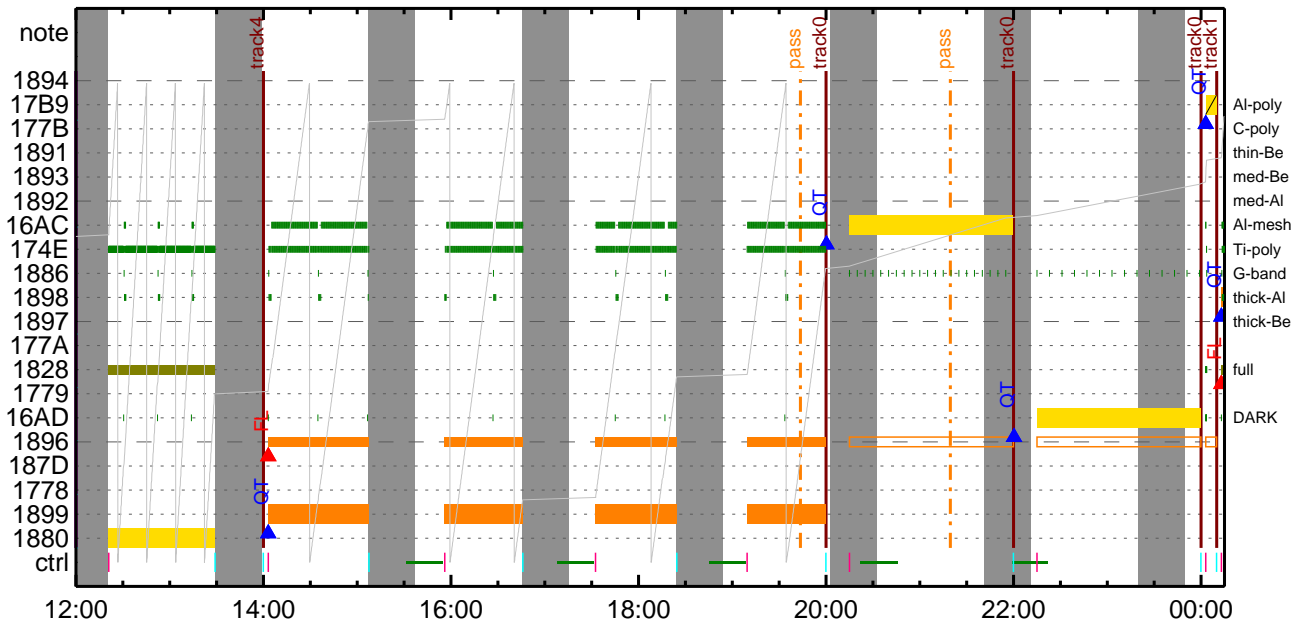
\* \* \* \* \*

<b>FLD Patrol</b>											
Term	Pointing (x, y)	Comment									
07/09 10:54:46 - 07/09 20:00:18	Track ( -72.4, -405.1) <small>© 07/09 10:52:00</small>	AR11247 Observation									
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec									
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval									

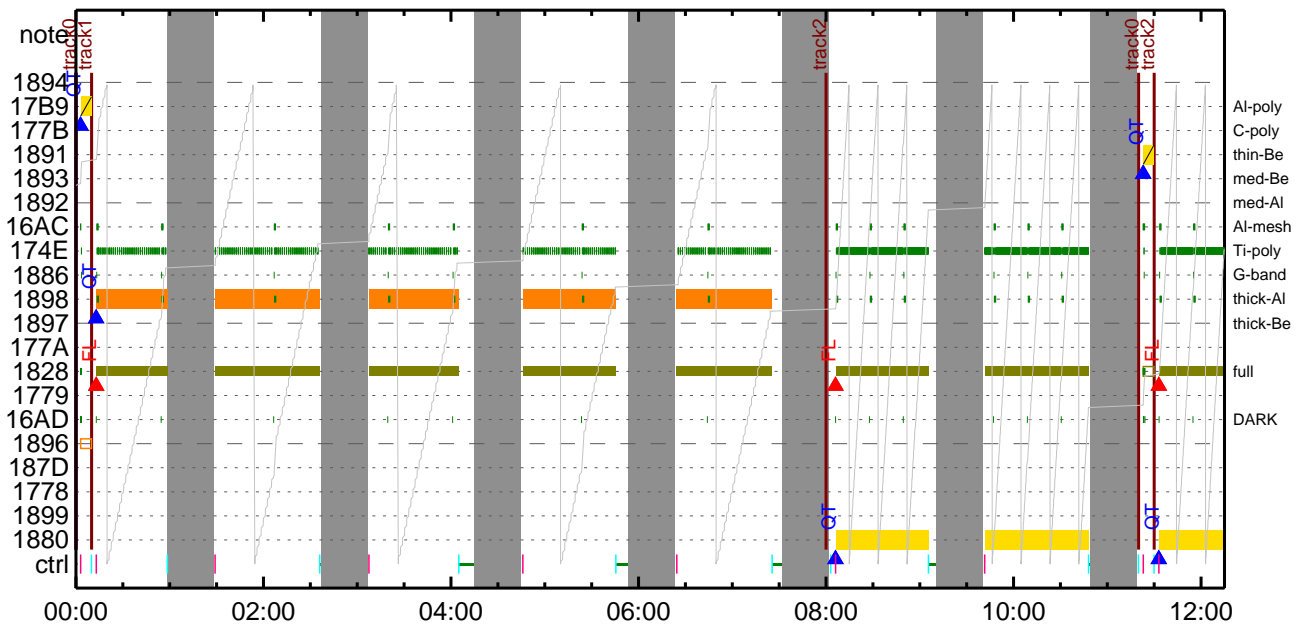
### CMDI #0023 2011/07/09



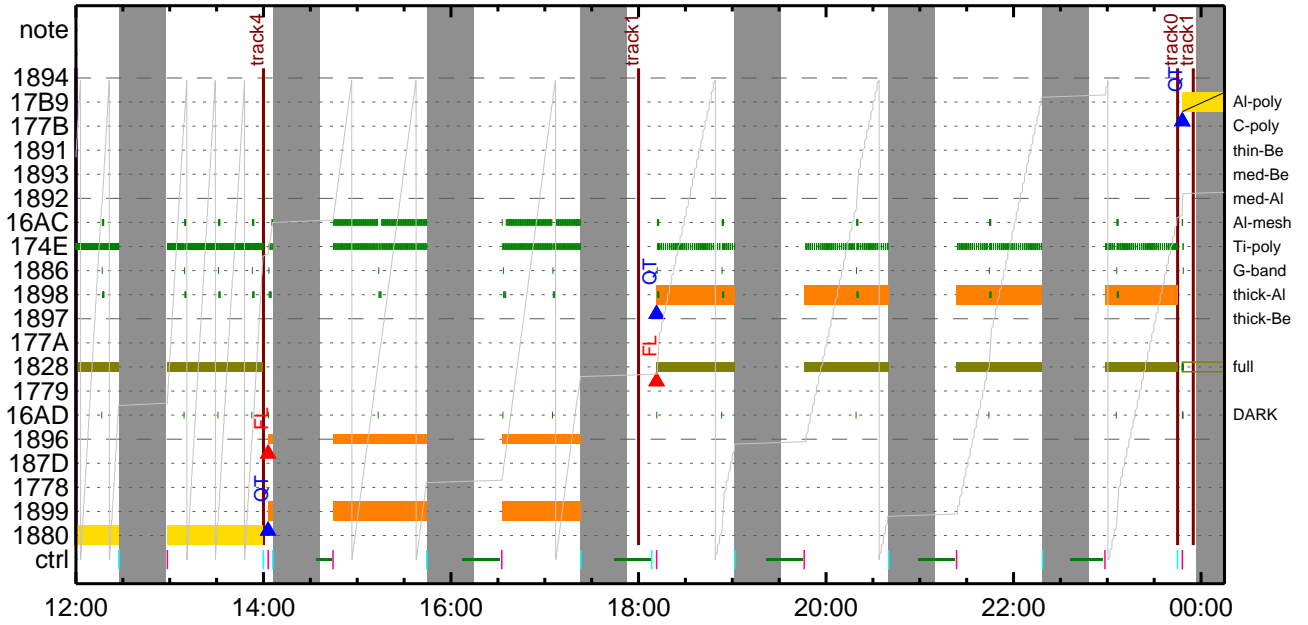
### CMDI #0023 2011/07/09



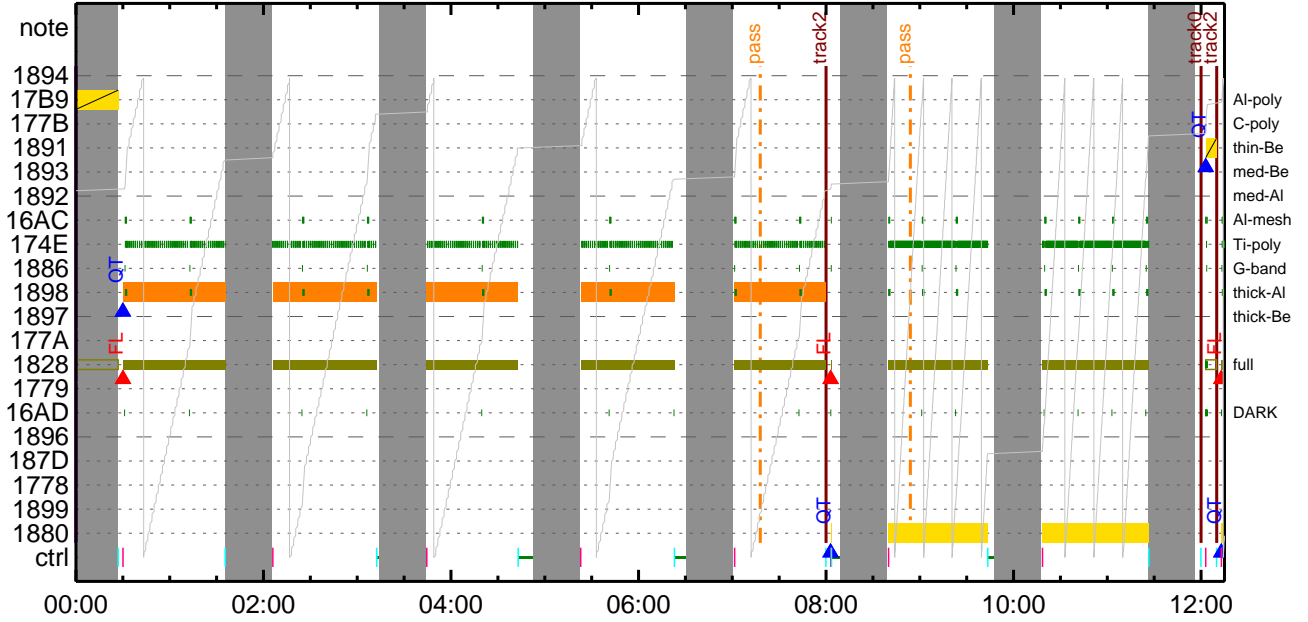
### CMDI #0023 2011/07/10



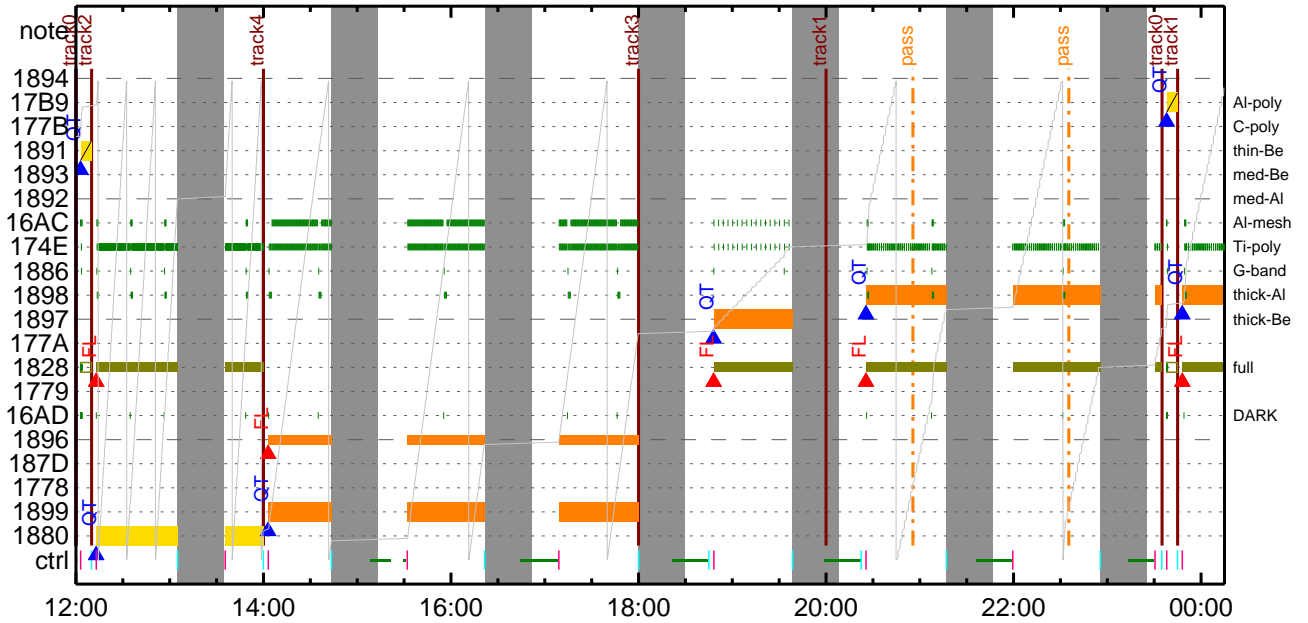
CMDI #0023 2011/07/10



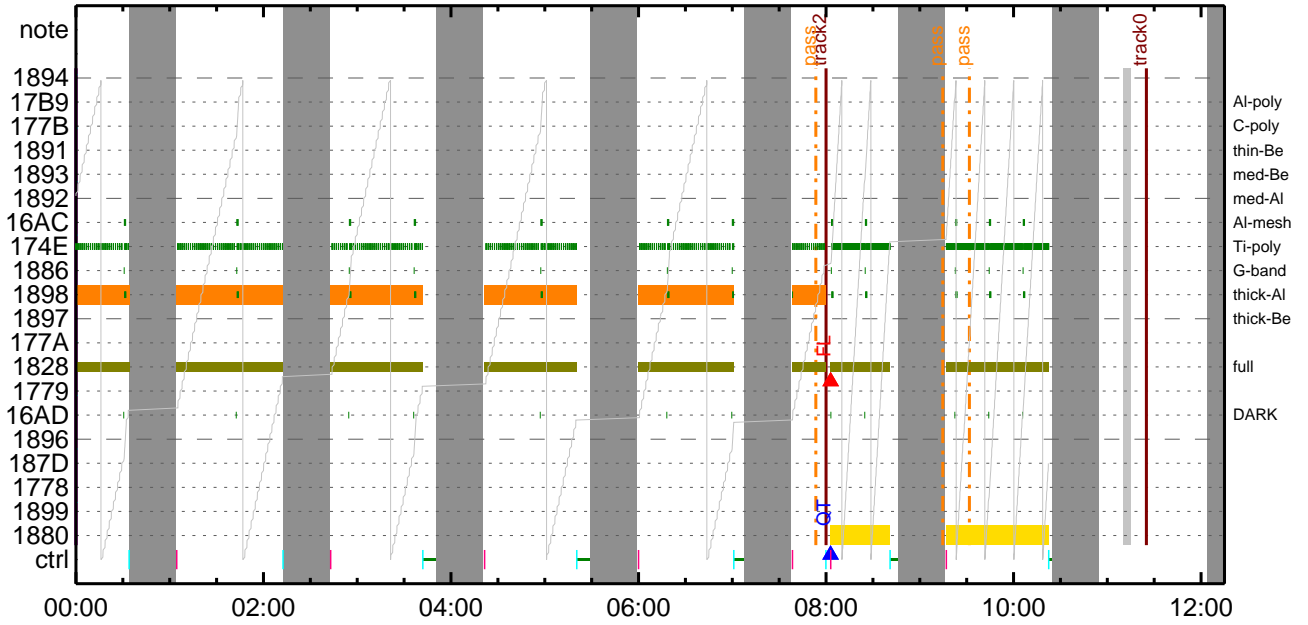
CMDI #0023 2011/07/11



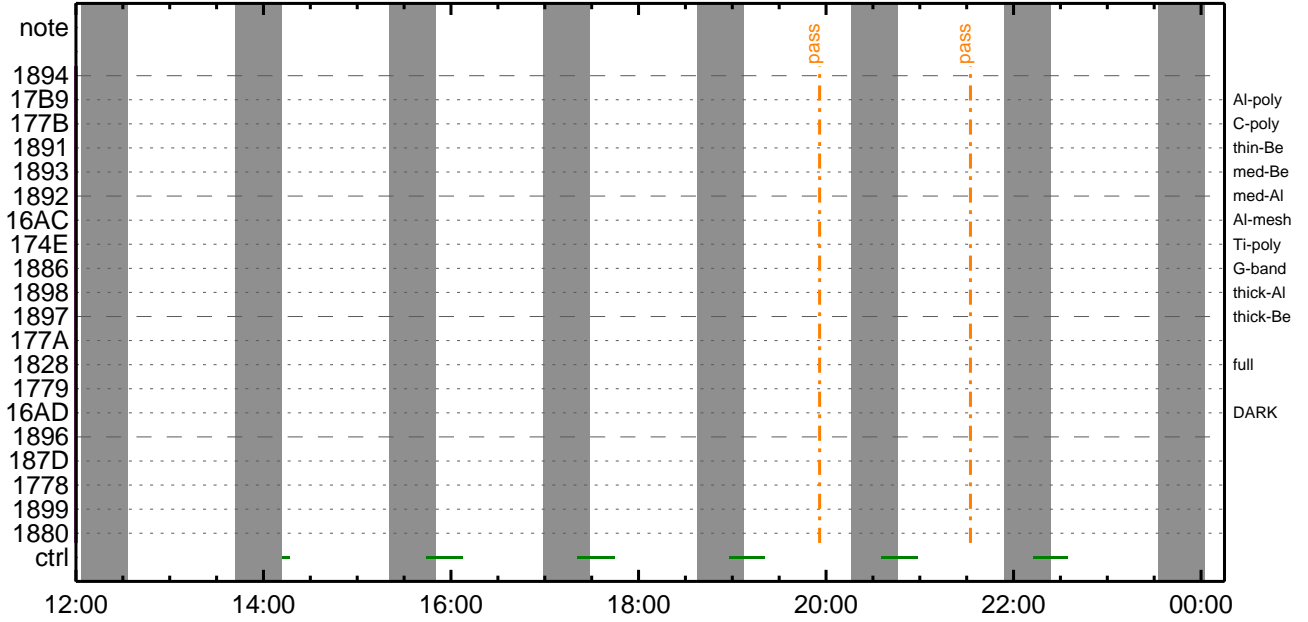
CMDI #0023 2011/07/11



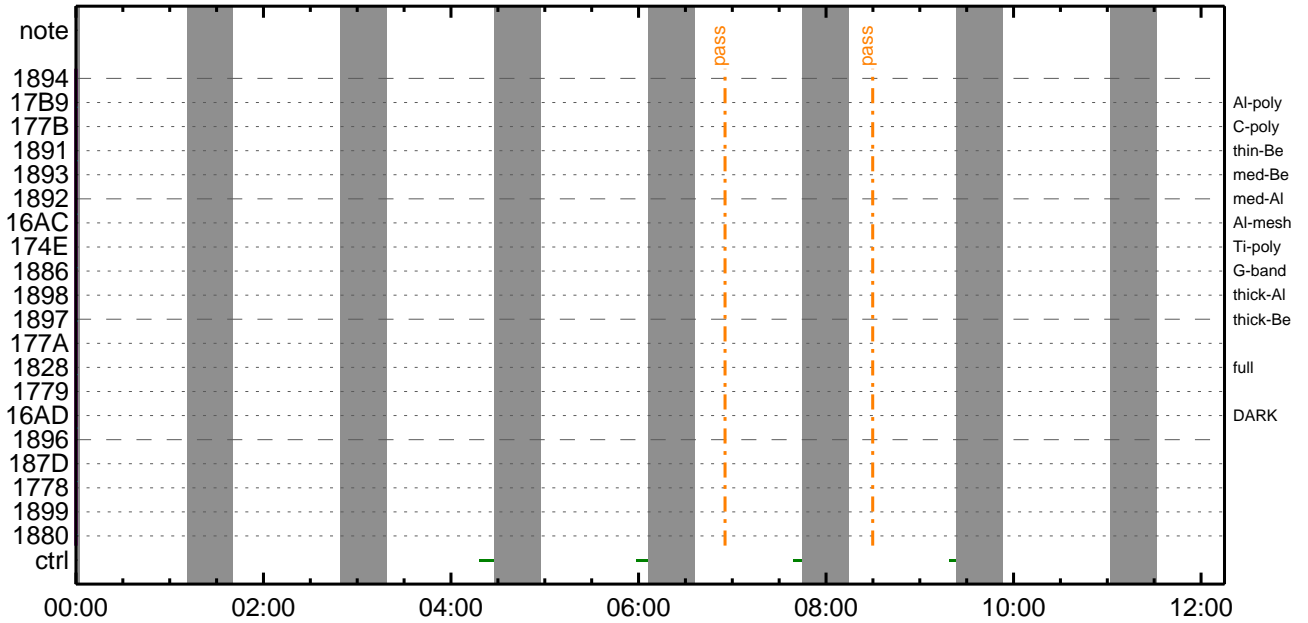
CMDI #0023 2011/07/12



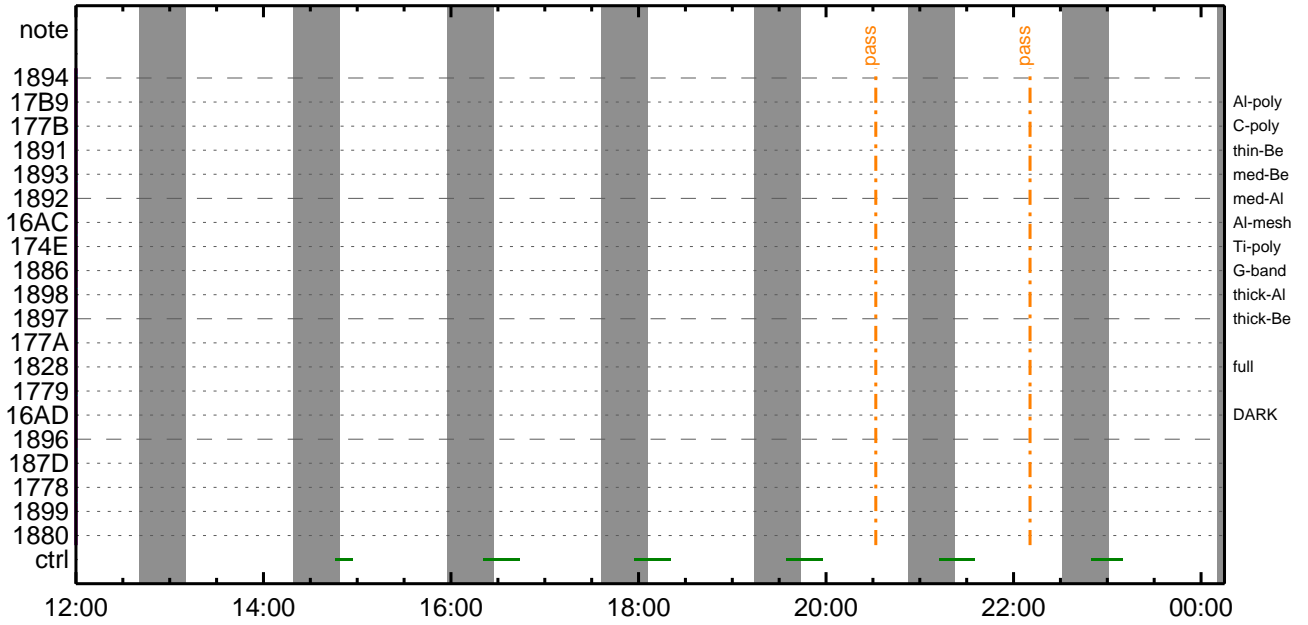
CMDI #0023 2011/07/12



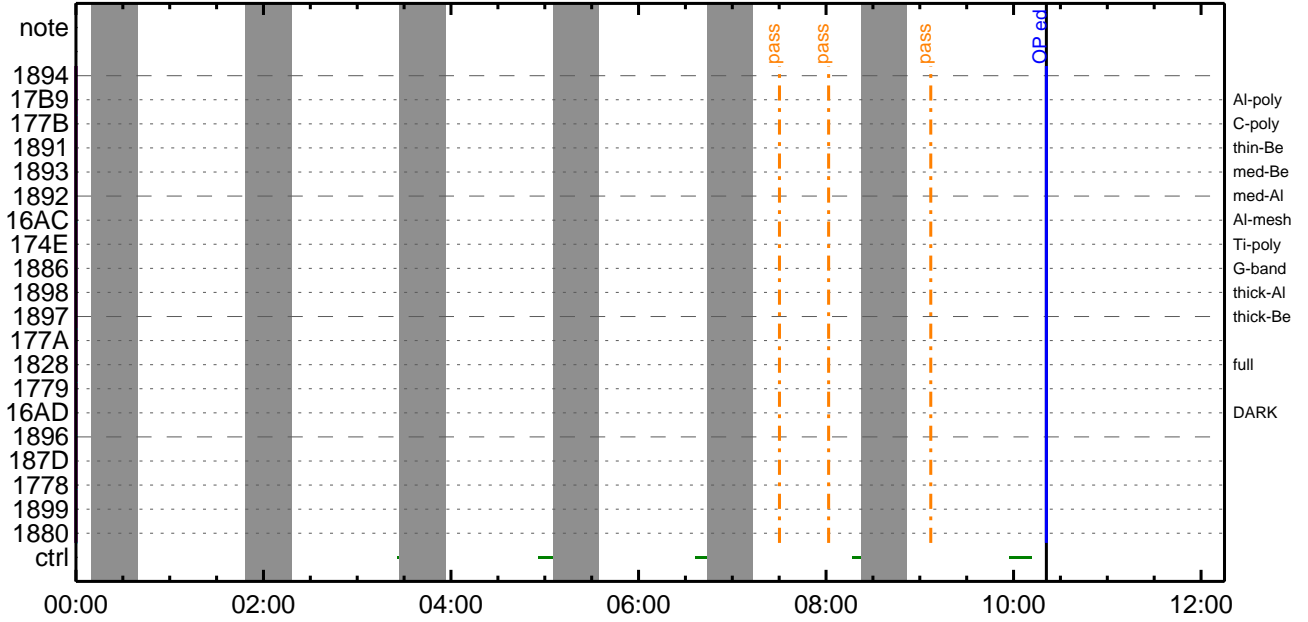
CMDI #0023 2011/07/13



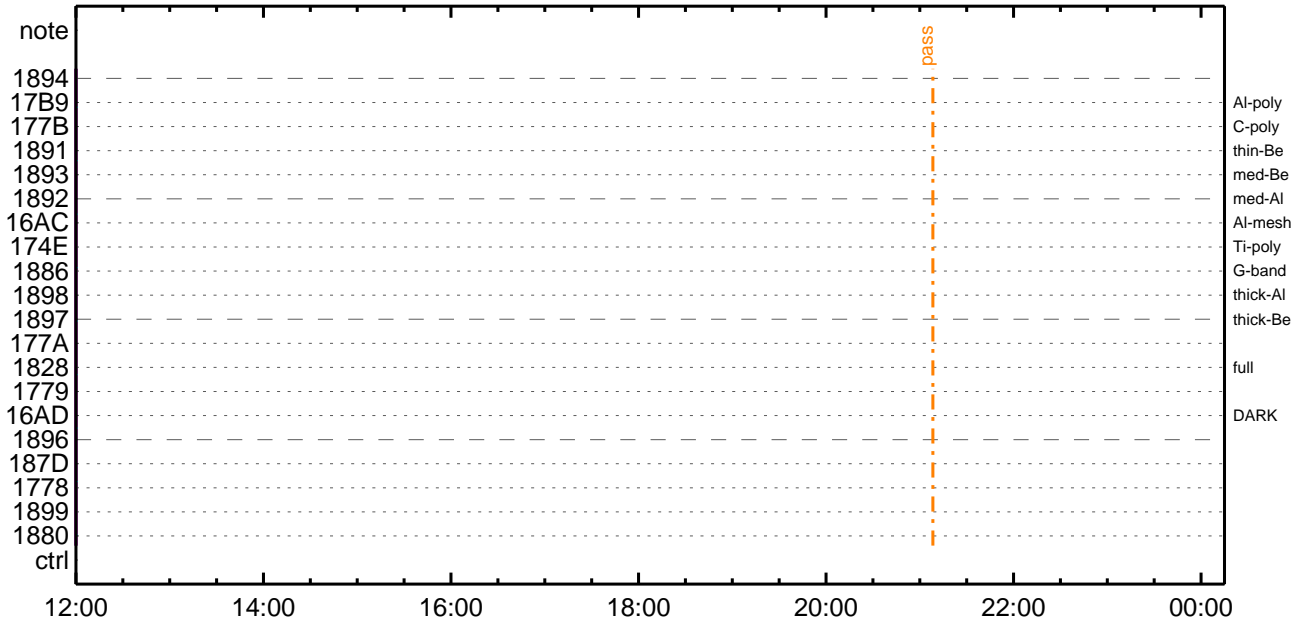
CMDI #0023 2011/07/13



CMDI #0023 2011/07/14



CMDI #0023 2011/07/14





(a) Spacecraft Operation Procedure (real-commands)

```
main-186 2011-07-09 13:03:15 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÁð•µ°È»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ•ííÈ;ÈÈÈ¼°ÇÓñ•çç¼í¹ççí;çÁ®, ùñ¹ñðñðçÁ+ç®ñ•ñÈñññ³ñÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, Í×ÈYñäLOSñðççí»p´Öñð¹íí, ñ•; çÉÖÍ×ñÈXÁÓONñí¹ÖñÈñ¹ñÈñññ³ñÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖñ-°ÁÀÈñ•çç;ç°È²¼ñí°ÈÀ, ¼È¼ççð¼Á¹Öñ¹ñÈ;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èñ•çççç¼í¹ççí;ç°È²¼ñí¼Á¹Öññ°;çDCBC-150ççÈçà;ç
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÀÙÁÖ;ÈÁ•Á°²óÈð;È, áñí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ñ-¼«È°Áá»ßñ•çç;á;ç°È²¼ñ¼Á¹Öñ¹ñÈ;ç
0055 C. YçYÓYÉYÈÀÙÁÖñÁ•Á°²óÈðñ-¼áçç¼í¹ççí°í»ñ¹ñÈñðçÁÖñÁ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èñ•çççç¼í¹ççí;ç°È²¼ñí¼Á¹Öññ°;çDCBC-151ççÈçà;ç
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÀÙÁÖ;ÈÁ•Á°²óÈð;È, áñí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-186:OP
0104 ( )
0105 . S. OG og-186:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOYx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î¼Ê¹ç•è²îOKò³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOYx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î¼Ê¹ç•è²îOKò³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOYx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î¼Ê¹ç•è²îOKò³îç§
0165 C.
0166 . C. ***** °Ê²¼òî¼Ã´¶Á°òÊÊ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÔÃæç¼ª°¬òè¼î¹çççâ) *****
0167 C. DHUYâ;4YE;Ê¼Y½;Yi;4YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°Ê²¼òîTI-CMDÁ÷¿@²î¼Á¹Ô°¬òÊ²²²³òÊ;f
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 . C. TIY³YpYóYÊòðÁDî¿(UT)
0183 +. TI 2011-07-09 10:27:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-07-09 10:27:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-07-09 10:27:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```





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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 . C. ***** MDP 'ûÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0131 C. (¼â°îÿÓÿÃÿÊÿPÿËÿáÿçÿèñ¼¼¼¼»Ûñ¹ñè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ÿDÿ¹•Ï Daily±;îÑñÊ'Øñ¹ñèDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-188 2011-07-09 13:03:15 153 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÁ®, ùø¹øÈøÈøçÁ+¿®ø•øÈøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 60 20 18)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b a0 80 18 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 + DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 04)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 08)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0078 BC (c0)
0079 +. TI 2011-07-09 10:31:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 +. TI 2011-07-09 10:31:04.0
0083 DC 07-F0 MDP_XRT_CTRL_AUTO
0084 BC (c0)
0085 . C. ----- Success Verify ? OK / NG ____
0086 C.
0087 C. ***** XRT END *****
0088 . C. *****
0089 C. SOT table upload
0090 C. *****
0091 . C. < Stop FG table >
0092 +. DC 07-F0 MDP_FG_CTRL_MANU
0093 BC (51)
0094 . C. -----
0095 C. MDP_FG_CTRL_MODE = MANU [ ]
```

```

0096 C. -----
0097 C.
0098 . C. <Upload FG Observation Table>
0099 . S. RAM ram-264:MDP_OBS_F
0100 ( )
0101 C.
0102 . C. < Dump RAMID=MDP_OBS_F >
0103 +. DC 07-F0 MDP_DUMP_FGTBL
0104 BC (82 07 00 00 00 38 b8)
0105 C. -----
0106 C. MDP_OBS_F verify = OK/NG [ ]
0107 C. -----
0108 C.
0109 . C. < Stop SP table >
0110 +. DC 07-F0 MDP_SP_CTRL_MANU
0111 BC (61)
0112 C. -----
0113 C. MDP_SP_CTRL_MODE = MANU [ ]
0114 C. -----
0115 C.
0116 . C. <Upload SP Observation Table>
0117 . S. RAM ram-283:MDP_OBS_S
0118 ( )
0119 C.
0120 . C. < Dump RAMID=MDP_OBS_S >
0121 +. DC 07-F0 MDP_DUMP_SPTBL
0122 BC (83 07 00 00 00 38 b8)
0123 C. -----
0124 C. MDP_OBS_S verify = OK/NG [ ]
0125 C. -----
0126 C.
0127 C. *****
0128 C. SOT TI command set
0129 C. *****
0130 C. Execute, after the success of TBL upload.
0131 +. TI 2011-07-09 10:31:18.0
0132 DC 07-F0 MDP_SOT_MODE_OBSV
0133 BC (40)
0134 C. -----
0135 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0136 C. -----
0137 C.
0138 C.
0139 . C. ***** MDP `ûÃîñî»ô%ÿñÊÄð¹ñèDCBC•x²è *****
0140 C. (%ã°îÿÓÿÃÿËÿPÿËÿãÿçÿèñE%¼ñ¼Ã»Ûñ¹ñè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÁÿSÿËÿ-¼Ã»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.

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*** OP Sequence for XRT ***

2011/07/09 10:41:54.0 XRT_CTRL_MANU_400_OG [0x190]
                       MDP_XRT_CTRL_MANU      1 07-F0 c1
2011/07/09 10:41:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                       XRT_FOCUS_POSITION      4 07-F8 22 ff aa 00
2011/07/09 10:42:00.0 AOCS_Orе-point_Start_1_OG [0x097]
                       AOCU_NM                 5 02-76 00 00 00 00 00
2011/07/09 10:42:16.0 XRT_FLD_DIS_402_OG [0x192]
                       MDP_XRT_FLD_DIS        1 07-F0 d9
2011/07/09 10:42:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                       MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2011/07/09 10:42:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                       MDP_XRT_ARS_DIS        1 07-F0 d5
2011/07/09 10:44:58.0 XRT_QT_PROG_SET_446_OG [0x1be]
                       MDP_XRT_QT_PROG_SET    2 07-F0 c4 11
2011/07/09 10:45:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                       MDP_XRT_CTRL_AUTO      1 07-F0 c0
2011/07/09 10:51:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                       MDP_XRT_CTRL_MANU      1 07-F0 c1
2011/07/09 10:52:00.0 AOCS_Orе-point_Start_2_OG [0x098]
                       AOCU_NM                 5 02-76 01 00 00 00 00
2011/07/09 10:54:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                       XRT_FOCUS_POSITION      4 07-F8 22 fe 97 00
2011/07/09 10:54:46.0 XRT_FLD_ENA_411_OG [0x19b]
                       MDP_XRT_FLD_ENA        1 07-F0 d8
2011/07/09 10:54:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                       MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2011/07/09 10:54:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                       MDP_XRT_AEC_RESET      1 07-F0 d0
2011/07/09 10:54:52.0 XRT_ARS_DIS_431_OG [0x1af]
                       MDP_XRT_ARS_DIS        1 07-F0 d5
2011/07/09 10:54:54.0 XRT_FLD_RESET_412_OG [0x19c]
                       MDP_XRT_FLD_RESET      1 07-F0 da
2011/07/09 10:54:56.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                       MDP_XRT_QT_PROG_SET    2 07-F0 c4 01
2011/07/09 10:54:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                       MDP_XRT_FL_PROG_SET    2 07-F0 c5 08
2011/07/09 10:55:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                       MDP_XRT_CTRL_AUTO      1 07-F0 c0
2011/07/09 11:50:30.0 XRT_CTRL_MANU_408_OG [0x198]
                       MDP_XRT_CTRL_MANU      1 07-F0 c1
2011/07/09 11:50:32.0 XRT_FLD_RESET_412_OG [0x19c]
                       MDP_XRT_FLD_RESET      1 07-F0 da
2011/07/09 11:50:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                       MDP_XRT_PREFLR_STRT    1 07-F0 e8
2011/07/09 11:53:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                       MDP_XRT_PREFLR_STOP    1 07-F0 e9
2011/07/09 12:20:00.0 XRT_Custom_418_OG [0x1a2]
2011/07/09 12:21:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                       MDP_XRT_CTRL_AUTO      1 07-F0 c0
2011/07/09 13:29:00.0 XRT_CTRL_MANU_408_OG [0x198]
                       MDP_XRT_CTRL_MANU      1 07-F0 c1
2011/07/09 13:29:02.0 XRT_FLD_RESET_412_OG [0x19c]
                       MDP_XRT_FLD_RESET      1 07-F0 da
2011/07/09 13:29:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                       MDP_XRT_PREFLR_STRT    1 07-F0 e8
2011/07/09 13:32:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                       MDP_XRT_PREFLR_STOP    1 07-F0 e9
2011/07/09 13:59:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                       MDP_XRT_CTRL_MANU      1 07-F0 c1
2011/07/09 14:00:00.0 AOCS_Orе-point_Start_3_OG [0x099]
                       AOCU_NM                 5 02-76 04 00 00 00 00
2011/07/09 14:02:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                       XRT_FOCUS_POSITION      4 07-F8 22 fe 97 00
2011/07/09 14:02:46.0 XRT_FLD_ENA_411_OG [0x19b]
                       MDP_XRT_FLD_ENA        1 07-F0 d8
2011/07/09 14:02:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                       MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2011/07/09 14:02:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                       MDP_XRT_AEC_RESET      1 07-F0 d0
2011/07/09 14:02:52.0 XRT_ARS_DIS_431_OG [0x1af]
                       MDP_XRT_ARS_DIS        1 07-F0 d5
2011/07/09 14:02:54.0 XRT_FLD_RESET_412_OG [0x19c]
                       MDP_XRT_FLD_RESET      1 07-F0 da
2011/07/09 14:02:56.0 XRT_ROI_A_441_OG [0x1b9]
                       MDP_XRT_ROI_SET        6 07-F0 cd 05 85 83 08 08
                       MDP_XRT_ROI_SET        6 07-F0 cd 06 85 83 06 06
                       MDP_XRT_ROI_SET        6 07-F0 cd 07 80 80 20 20
                       MDP_XRT_ROI_SET        6 07-F0 cd 08 80 80 20 08
                       MDP_XRT_ROI_SET        6 07-F0 cd 09 80 80 08 20
                       MDP_XRT_ROI_SET        6 07-F0 cd 0a 80 60 20 18
                       MDP_XRT_ROI_SET        6 07-F0 cd 0b a0 80 18 20
                       MDP_XRT_ROI_SET        6 07-F0 cd 0c 80 80 08 08
2011/07/09 14:02:56.5 XRT_ROI_B_442_OG [0x1ba]
                       MDP_XRT_ROI_SET        6 07-F0 cd 0c 80 80 08 08
                       MDP_XRT_ROI_SET        6 07-F0 cd 0f 80 80 06 06
                       MDP_XRT_ROI_SET        6 07-F0 cd 10 80 80 08 08
2011/07/09 14:03:01.5 XRT_QT_PROG_SET_420_OG [0x1a4]
                       MDP_XRT_QT_PROG_SET    2 07-F0 c4 02
2011/07/09 14:03:04.5 XRT_FL_PROG_SET_444_OG [0x1bc]
                       MDP_XRT_FL_PROG_SET    2 07-F0 c5 05
2011/07/09 14:03:06.5 XRT_CTRL_AUTO_406_OG [0x196]

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2011/07/09	15:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	15:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/09	15:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/09	15:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/09	15:55:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	15:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/09	16:46:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	16:46:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/09	16:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/09	16:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/09	17:31:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	17:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/09	18:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	18:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/09	18:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/09	18:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/09	19:08:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/09	19:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/09	19:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	19:59:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/07/09	20:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 ac 00 00 00			
2011/07/09	20:00:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2011/07/09	20:00:18.0	XRT_FLD_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/07/09	20:00:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/07/09	20:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/09	20:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/09	21:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	21:59:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/07/09	22:00:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 54 00			
2011/07/09	22:00:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2011/07/09	22:00:18.0	XRT_FLD_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/07/09	22:00:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/07/09	22:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/09	22:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/09	23:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/09	23:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/07/10	00:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/07/10	00:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/07/10	00:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/07/10	00:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/10	00:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2011/07/10	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/10	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/10	00:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2011/07/10	00:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/07/10	00:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/07/10	00:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/07/10	00:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							

2011/07/10	00:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/07/10	00:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	00:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2011/07/10	00:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2011/07/10	00:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	00:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	00:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	00:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	01:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/10	01:28:00.0	XRT_Custom_418_OG [0x1a2]						
2011/07/10	01:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	02:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	02:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	02:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	02:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/10	03:06:30.0	XRT_Custom_418_OG [0x1a2]						
2011/07/10	03:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	04:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	04:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	04:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	04:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/10	04:45:00.0	XRT_Custom_418_OG [0x1a2]						
2011/07/10	04:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	05:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	05:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	05:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	05:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/10	06:23:30.0	XRT_Custom_418_OG [0x1a2]						
2011/07/10	06:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	07:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	07:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	07:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	07:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/07/10	08:00:00.0	AOCS_Or-e-point_Start_6_OG [0x09c]						
2011/07/10	08:03:00.5	XRT_CTRL_MANU_439_OG [0x1b7]	AOCU_NM	5	02-76	02	00 00 00 00	
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	08:05:32.5	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/07/10	08:05:52.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/07/10	08:05:54.5	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/07/10	08:05:56.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/07/10	08:05:58.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/07/10	08:06:00.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	08:06:02.5	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2011/07/10	08:06:04.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2011/07/10	08:06:06.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/07/10	09:05:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/07/10	09:05:33.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/07/10	09:05:35.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/07/10	09:08:45.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		

2011/07/10	09:40:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/10	09:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/10	10:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	10:48:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/10	10:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/10	10:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/10	11:19:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	11:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/10	11:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/07/10	11:20:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/10	11:20:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/10	11:20:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/10	11:22:58.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/07/10	11:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/10	11:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	11:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/07/10	11:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/10	11:32:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/10	11:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/10	11:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/10	11:32:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/10	11:32:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/10	11:32:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/10	11:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/10	11:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/10	12:27:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	12:27:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/10	12:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/10	12:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/10	12:57:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/10	12:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/10	13:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	14:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/07/10	14:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/10	14:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/10	14:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/10	14:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/10	14:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/10	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/10	14:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2011/07/10	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2011/07/10	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/10	14:06:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/10	14:06:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/10	14:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/10	14:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/10	14:43:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/10	14:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

2011/07/10	15:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	15:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	15:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/10	15:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/10	16:31:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/10	16:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	17:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	17:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	17:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/10	17:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/10	18:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00				
2011/07/10	18:08:30.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	18:11:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/10	18:11:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/10	18:11:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/10	18:11:26.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/10	18:11:28.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/10	18:11:30.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	18:11:32.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/07/10	18:11:34.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/10	18:11:36.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	19:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	19:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	19:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/10	19:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/10	19:45:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/10	19:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	20:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	20:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	20:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/10	20:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/10	21:22:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/10	21:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	22:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	22:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/10	22:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/10	22:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/10	22:57:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/10	22:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	23:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/10	23:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/10	23:45:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/10	23:45:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/10	23:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/10	23:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/10	23:47:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2011/07/10	23:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/10	23:55:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00				
2011/07/11	00:27:00.0	XRT_CTRL_MANU_439_OG [0x1b7]								

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2011/07/11	00:29:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	00:29:52.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/11	00:29:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/11	00:29:56.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/11	00:29:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/11	00:30:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/11	00:30:02.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	00:30:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/07/11	00:30:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/11	01:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	01:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	01:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	01:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	02:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	02:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	03:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	03:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	03:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	03:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	03:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	03:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	04:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	04:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	04:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	04:43:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	04:43:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	04:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	05:22:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	05:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	06:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	06:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	06:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	06:23:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	06:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	07:00:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	07:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	08:00:00.0	AOCs_Or-e-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	08:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	08:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/11	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/11	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/11	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/11	08:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/11	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/11	08:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	08:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	08:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	08:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11			MDP_XRT_PREFLR_STOP	1	07-F0	e9



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				MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06	06
				MDP_XRT_ROI_SET	6	07-F0	cd	07	80	80	20	20
				MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20	08
				MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	08	20
				MDP_XRT_ROI_SET	6	07-F0	cd	0c	80	80	08	08
				MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
				MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2011/07/11	14:03:05.5	XRT_CTRL_AUTO_406_OG [0x196]		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/07/11	14:43:30.0	XRT_CTRL_MANU_408_OG [0x198]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	14:43:32.0	XRT_FLD_RESET_412_OG [0x19c]		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/07/11	14:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/07/11	14:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/07/11	15:31:00.0	XRT_Custom_418_OG [0x1a2]										
2011/07/11	15:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/07/11	16:21:30.0	XRT_CTRL_MANU_408_OG [0x198]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	16:21:32.0	XRT_FLD_RESET_412_OG [0x19c]		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/07/11	16:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/07/11	16:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/07/11	17:08:00.0	XRT_Custom_418_OG [0x1a2]										
2011/07/11	17:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/07/11	18:00:00.0	AOCs_OrE-point_Start_7_OG [0x09d]		AOCU_NM	5	02-76	03	00	00	00	00	00
2011/07/11	18:00:00.5	XRT_CTRL_MANU_408_OG [0x198]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	18:00:02.5	XRT_FLD_RESET_412_OG [0x19c]		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/07/11	18:00:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/07/11	18:03:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/07/11	18:45:00.0	XRT_CTRL_MANU_439_OG [0x1b7]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	18:47:32.0	XRT_FOCUS_POSITION_409_OG [0x199]		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2011/07/11	18:47:52.0	XRT_FLD_ENA_411_OG [0x19b]		MDP_XRT_FLD_ENA	1	07-F0	d8					
2011/07/11	18:47:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2011/07/11	18:47:56.0	XRT_AEC_RESET_443_OG [0x1bb]		MDP_XRT_AEC_RESET	1	07-F0	d0					
2011/07/11	18:47:58.0	XRT_ARS_DIS_431_OG [0x1af]		MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/07/11	18:48:00.0	XRT_FLD_RESET_412_OG [0x19c]		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/07/11	18:48:02.0	XRT_QT_PROG_SET_427_OG [0x1ab]		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a				
2011/07/11	18:48:04.0	XRT_FL_PROG_SET_414_OG [0x19e]		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08				
2011/07/11	18:48:06.0	XRT_ROI_A_447_OG [0x1bf]		MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	08	08
				MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06	06
				MDP_XRT_ROI_SET	6	07-F0	cd	07	80	80	20	20
				MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20	08
				MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	08	20
				MDP_XRT_ROI_SET	6	07-F0	cd	0c	80	80	08	08
				MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
				MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2011/07/11	18:48:11.0	XRT_CTRL_AUTO_406_OG [0x196]		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2011/07/11	19:38:30.0	XRT_CTRL_MANU_408_OG [0x198]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	19:38:32.0	XRT_FLD_RESET_412_OG [0x19c]		MDP_XRT_FLD_RESET	1	07-F0	da					
2011/07/11	19:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2011/07/11	19:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2011/07/11	20:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]		AOCU_NM	5	02-76	01	00	00	00	00	00
2011/07/11	20:22:30.0	XRT_CTRL_MANU_439_OG [0x1b7]		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2011/07/11	20:25:02.0	XRT_FOCUS_POSITION_409_OG [0x199]		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2011/07/11	20:25:22.0	XRT_FLD_ENA_411_OG [0x19b]		MDP_XRT_FLD_ENA	1	07-F0	d8					
2011/07/11	20:25:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2011/07/11	20:25:26.0	XRT_AEC_RESET_443_OG [0x1bb]		MDP_XRT_AEC_RESET	1	07-F0	d0					
2011/07/11	20:25:28.0	XRT_ARS_DIS_431_OG [0x1af]		MDP_XRT_ARS_DIS	1	07-F0	d5					
2011/07/11	20:25:30.0	XRT_FLD_RESET_412_OG [0x19c]										

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2011/07/11	20:25:32.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	20:25:34.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/07/11	20:25:36.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/11	21:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	21:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	21:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	21:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	21:58:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	21:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	22:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	22:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	22:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	22:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	23:29:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/11	23:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/11	23:34:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_418_OG [0x1a2]			
2011/07/11	23:34:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/11	23:35:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	23:35:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	23:35:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/11	23:35:20.0	XRT_ARS_DIS_438_OG [0x1b6]	AOCU_NM	5	02-76	00 00 00 00 00
2011/07/11	23:37:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/11	23:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/11	23:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/11	23:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2011/07/11	23:47:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/11	23:47:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/11	23:47:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/11	23:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/11	23:47:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/11	23:47:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/11	23:47:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/11	23:47:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/11	23:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/07/12	00:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/12	00:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/12	00:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/12	00:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/12	01:03:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/12	01:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/12	02:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2011/07/12	02:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/12	02:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/12	02:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/12	02:42:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/12	02:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/12	03:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/12	03:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_Custom_418_OG [0x1a2]			
			XRT_CTRL_AUTO_419_OG [0x1a3]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da



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2011/07/12	03:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/12	03:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/12	04:20:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/12	04:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/12	05:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/12	05:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/12	05:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/12	05:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/12	05:59:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/12	06:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/12	07:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/12	07:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/12	07:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/12	07:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/12	07:37:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/12	07:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/12	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/12	08:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/07/12	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/12	08:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/12	08:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/12	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/12	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/12	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/12	08:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/07/12	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/12	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/12	08:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/12	08:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/12	08:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/12	08:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/12	09:16:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/12	09:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/12	10:22:30.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/12	10:22:33.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/12	10:22:35.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/12	10:25:45.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/12	11:25:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				