XRT Timeline to be uploaded on 2011/07/12

Period: 2011/07/12 11:15:00 - 2011/07/16 09:57:00

* * * * * **Normal mode** * * * * *

XOB #1891: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 5

Term	Pointing (x, y)	Comment
07/12 11:28:00 - 07/12 11:34:54	Fixed (0.0, 0.0)	# OP start + 10min; Synoptics HOP186
07/13 11:35:00 - 07/13 11:41:54	Fixed (0.0, 0.0)	synoptic, HOP186

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 17 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 72 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	2.00s	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad

Term	Pointing (x, y)	Comment
07/12 11:38:00 - 07/12 13:42:00	Track (501.3, -398.6) @ 07/12 11:35:00	HOP192
07/13 08:33:00 - 07/13 11:02:00	Track (633.5, -391.4) @ 07/13 08:30:00	HOP192
07/13 11:45:00 - 07/13 12:40:30	Track (651.6, -390.0) @ 07/13 11:42:00	HOP192 (cont.)
07/14 08:55:06 - 07/14 09:57:00	Track (754.1, -380.4) @ 07/14 08:30:00	HOP192

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 01 Inf.-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 55-time(s) 22.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec

XOB #1899: AR Standard-A(Filter-Ratio) with flare core PFB, FW1=Open, 384x384 at 1064 1048, 100s cad-3

Term	Pointing (x, y)	Comment
07/12 14:21:06 - 07/12 17:59:54	Track (517.7, -397.9) @ 07/12 14:00:00	HOP188
07/13 13:14:06 - 07/13 17:36:00	Track (659.7, -389.4) @ 07/13 13:10:00	HOP188

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 02 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn=100 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 3 18-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	1	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	20.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	20.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #188A: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 40s cadence

Term	Pointing (x, y)	Comment
07/12 18:03:00 - 07/12 19:59:54	Track (235.5, 91.5) @ 07/12 18:00:00	HOP191
07/13 18:25:06 - 07/13 19:14:30	Track (-4.0, -41.0) @ 07/13 18:00:00	HOP191. tracking at disk center for chrom obs.

PROG= 13 Inf.-time(s)
Subr= 1 1-time(s) 180.0sec
Seqn= 84 60-time(s) 40.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #187D: AR Standard-A(Filter-Ratio) with eruption PFB, FW1=Open, 384x384 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
07/12 20:03:00 - 07/12 23:33:00	Track (557.2, -396.0) @ 07/12 20:00:00	Track AR11247
07/13 01:03:00 - 07/13 08:29:54	Track (588.7, -394.2) @ 07/13 01:00:00	# Track AR11247
07/13 20:03:00 - 07/13 23:59:54	Track (695.8, -386.4) @ 07/13 20:00:00	Track AR11247
07/14 00:43:06 - 07/14 08:16:30	Track (716.4, -384.5) @ 07/14 00:10:00	Track AR11247

PROG= 04 Inf.-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 19 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn=100 4-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 6 18-time(s) 2.0sec

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	1	20.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	2	20.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	20.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
07/13 00:06:00 - 07/13 00:13:00	Fixed (0.0, 0.0)	synoptic, shifted manually. Extended for SOT
07/14 00:03:00 - 07/14 00:10:00	Fixed (0.0, 0.0)	synoptic, shifted manually

PROG= 19 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 7 1-time(s) 4.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
07/13 00:16:06 - 07/13 00:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually. Extended for SOT

PROG= 15 Inf.-time(s)
Subr= 1 1-time(s) 120.0sec
Seqn= 33 1-time(s) 4.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
07/12 11:38:00 - 07/12 13:42:00	Track (501.3, -398.6) @ 07/12 11:35:00	HOP192

07/12 18:03:00 - 07/12 19:59:54	Track (235.5, 91.5) @ 07/12 18:00:00	HOP191
07/12 20:03:00 - 07/12 23:33:00	Track (557.2, -396.0) @ 07/12 20:00:00	Track AR11247
07/13 00:16:06 - 07/13 00:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually. Extended for SOT
07/13 01:03:00 - 07/13 08:29:54	Track (588.7, -394.2) @ 07/13 01:00:00	# Track AR11247
07/13 08:33:00 - 07/13 11:02:00	Track (633.5, -391.4) @ 07/13 08:30:00	HOP192
07/13 11:45:00 - 07/13 12:40:30	Track (651.6, -390.0) @ 07/13 11:42:00	HOP192 (cont.)
07/13 18:25:06 - 07/13 19:14:30	Track (-4.0, -41.0) @ 07/13 18:00:00	HOP191. tracking at disk center for chrom obs.
07/13 20:03:00 - 07/13 23:59:54	Track (695.8, -386.4) @ 07/13 20:00:00	Track AR11247
07/14 00:43:06 - 07/14 08:16:30	Track (716.4, -384.5) @ 07/14 00:10:00	Track AR11247
07/14 08:55:06 - 07/14 09:57:00	Track (754.1, -380.4) @ 07/14 08:30:00	HOP192

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 08 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1896: Flare Standard Obs. with flare core mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
07/12 14:21:06 - 07/12 17:59:54	Track (517.7, -397.9) @ 07/12 14:00:00	HOP188
07/13 13:14:06 - 07/13 17:36:00	Track (659.7, -389.4) @ 07/13 13:10:00	HOP188

PROG= 05 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 18 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 94 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3 30-time(s) 60.0sec													
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 94 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 94 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 18 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

Active Region Search

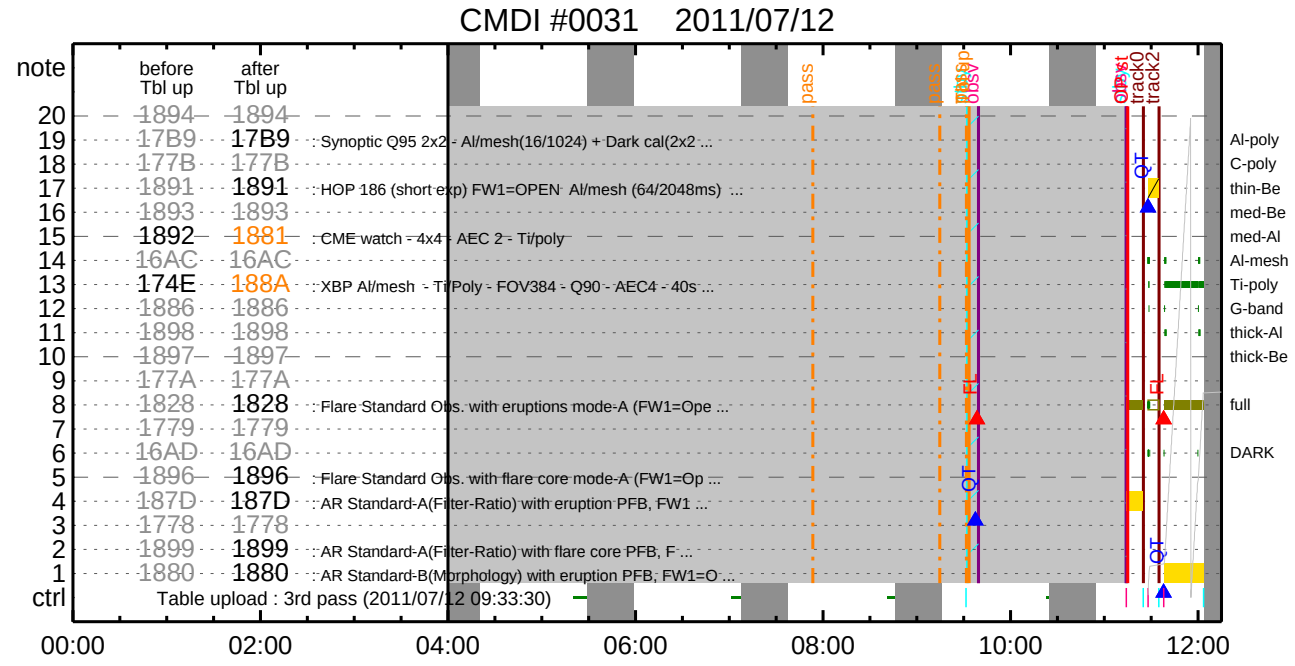
NOT USED

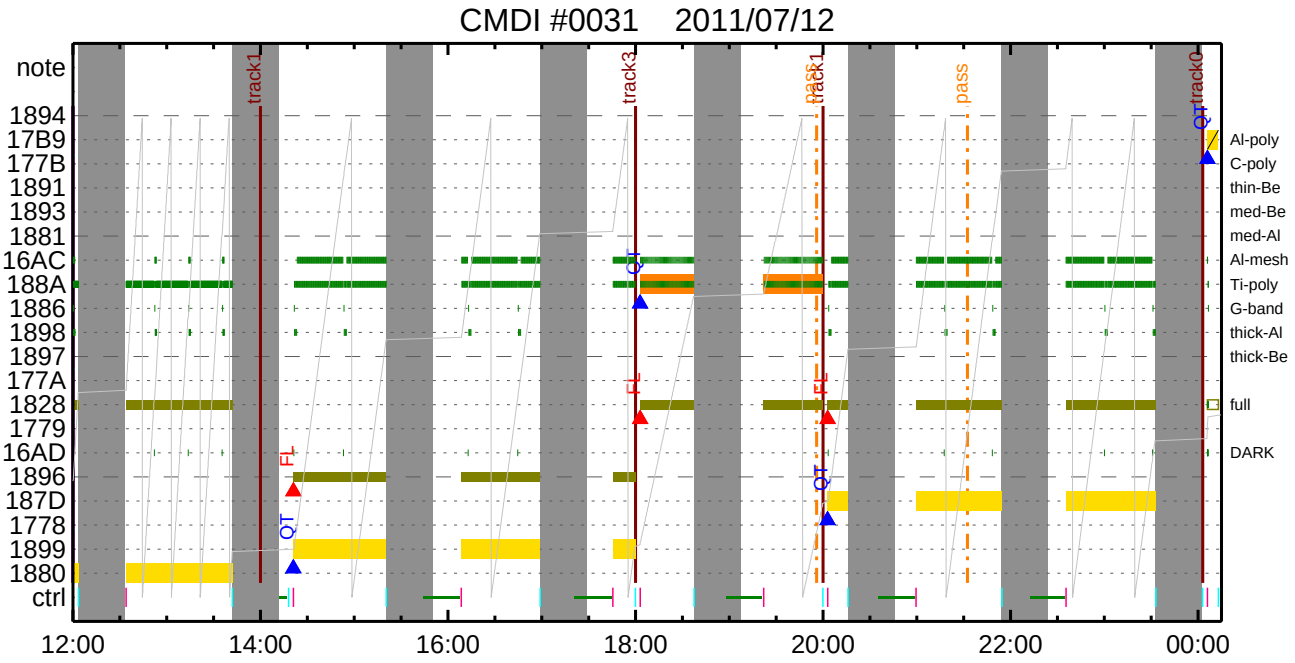
Flare Detection

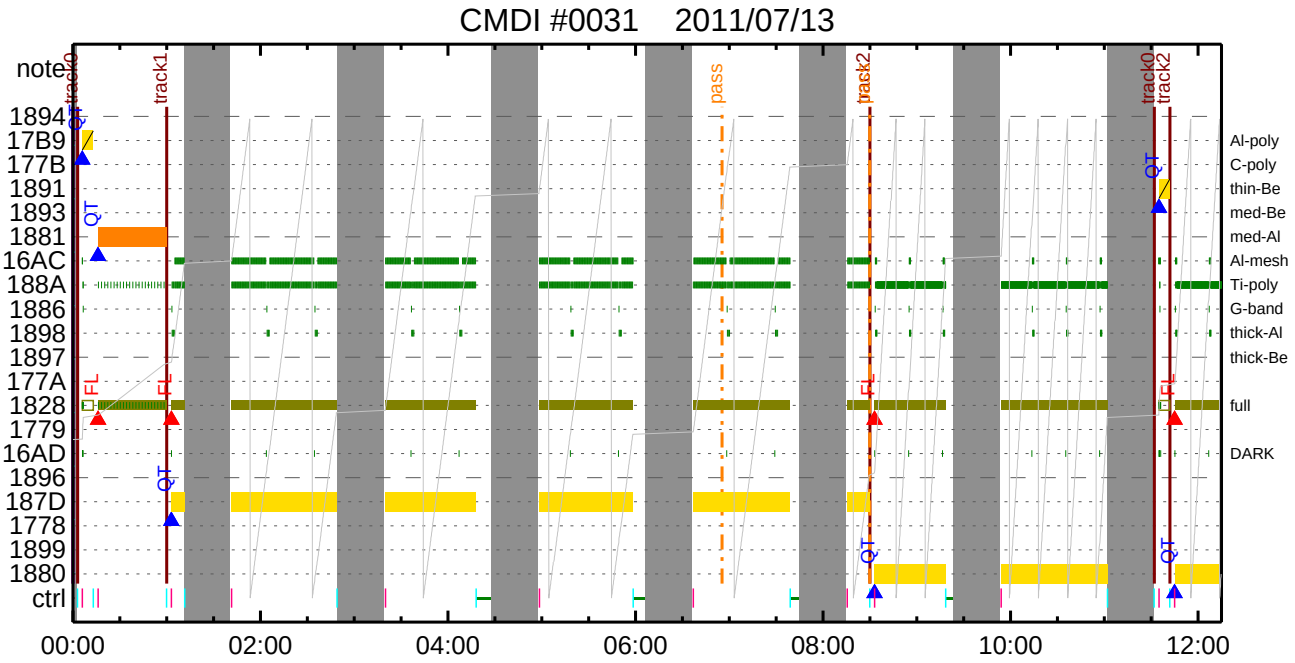
FLD Patrol

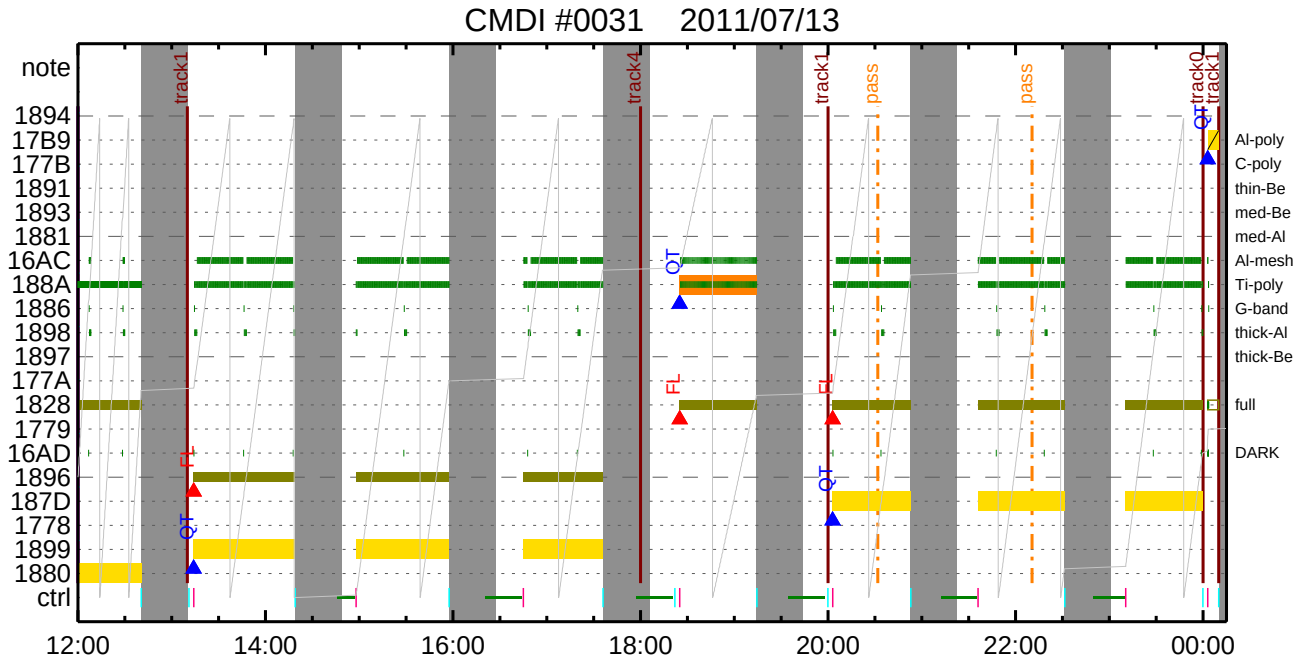
Term	Pointing (x, y)	Comment
07/12 11:37:46 - 07/13 00:03:16	Track (501.3, -398.6) @ 07/12 11:35:00	HOP192
07/13 00:15:52 - 07/13 11:32:16	Fixed (0.0, 0.0)	synoptic, shifted manually. Extended for SOT
07/13 11:44:46 - 07/14 00:00:16	Track (651.6, -390.0) @ 07/13 11:42:00	HOP192 (cont.)
07/14 00:42:52 - 07/16 09:57:00	Track (716.4, -384.5) @ 07/14 00:10:00	Track AR11247

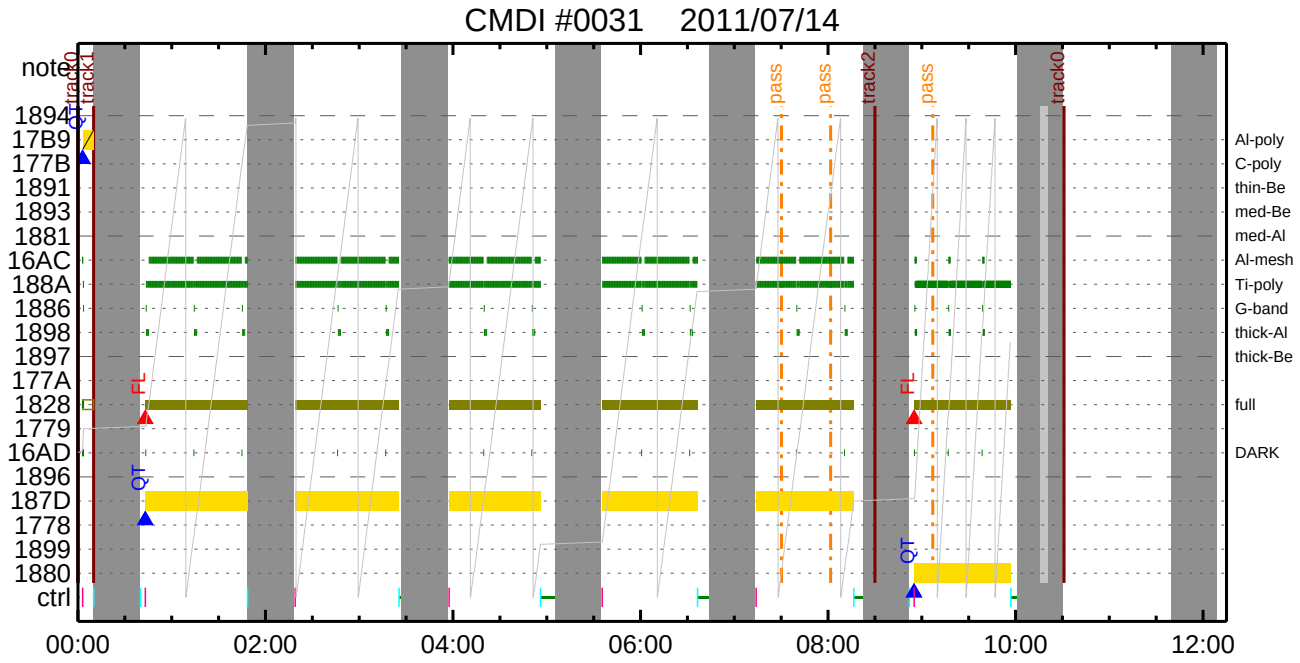
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

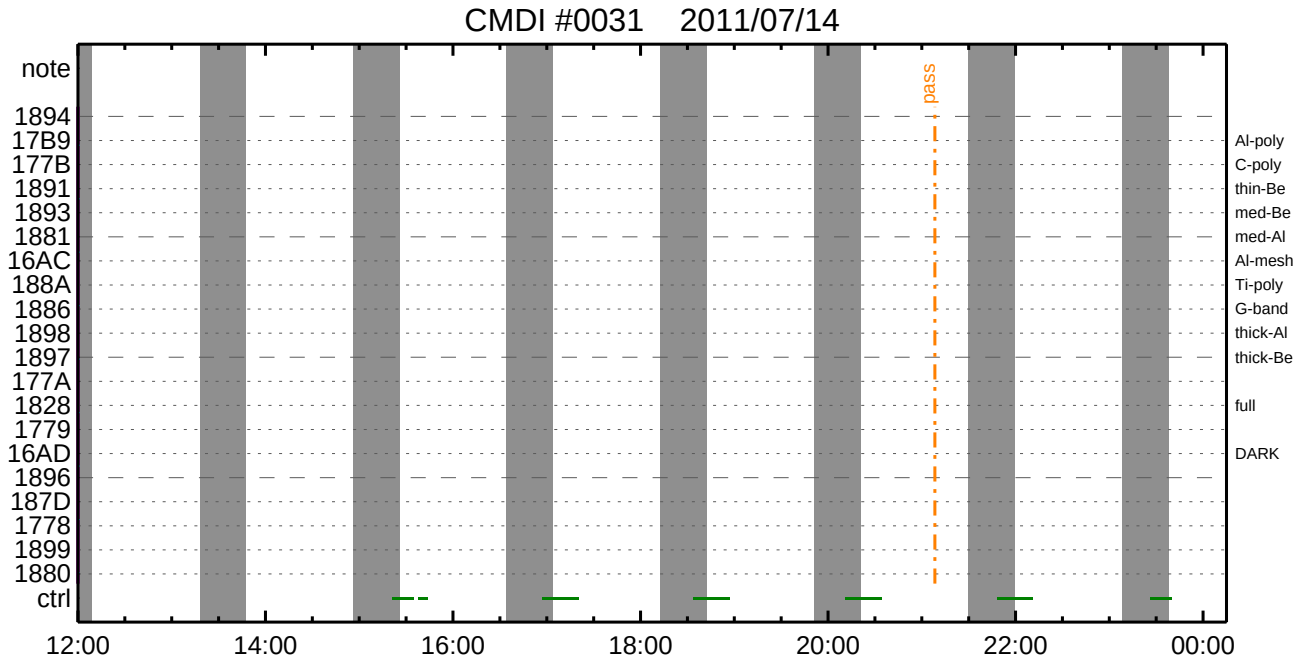

CMDI #0031 2011/07/12
note
before Tbl up
after Tbl up
: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
: HOP 186 (short exp) FW1=OPEN Al/mesh (64/2048ms) ...
: CME watch - 4x4 - AEC 2 - Ti/poly
: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 40s ...
: Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
: Flare Standard Obs. with flare core mode-A (FW1=Op ...
: AR Standard-A(Filter-Ratio) with eruption PFB, FW1 ...
: AR Standard-A(Filter-Ratio) with flare core PFB, F ...
: AR Standard-B(Morphology) with eruption PFB, FW1=O ...
Table upload : 3rd pass (2011/07/12 09:33:30)
pass
track0
track2
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

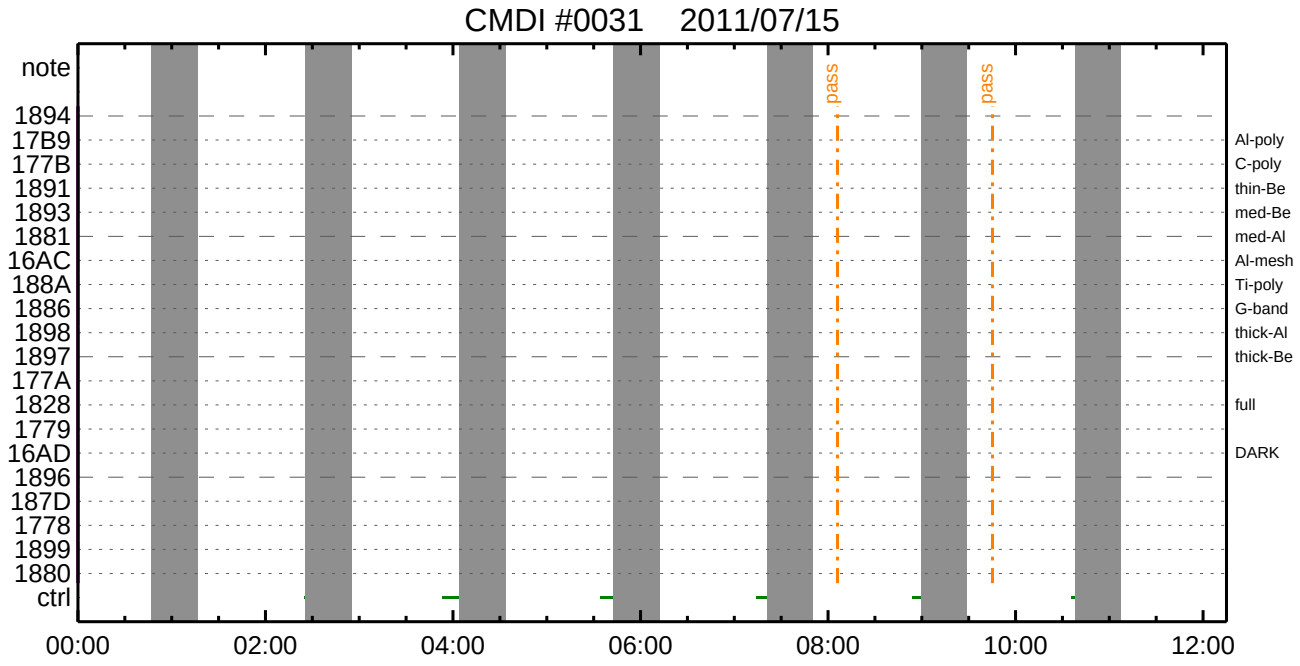

CMDI #0031 2011/07/12
note
track1
track3
pass
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

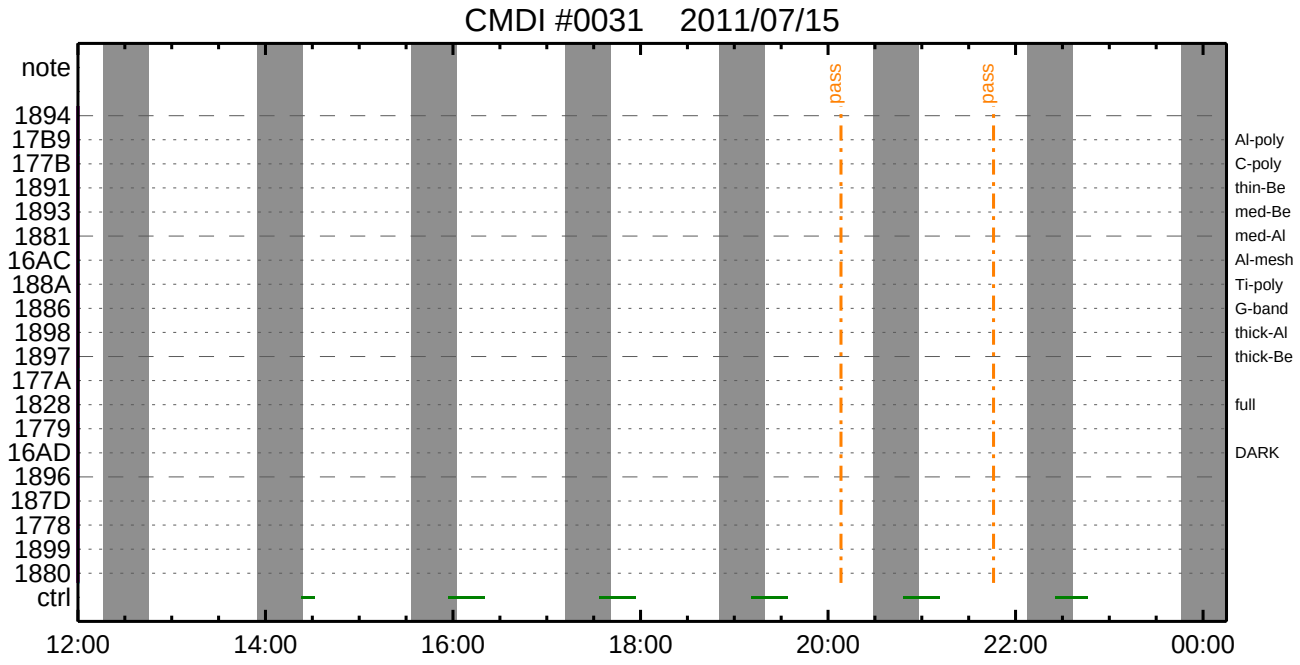

CMDI #0031 2011/07/13
note
track0
track1
pass
track2
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

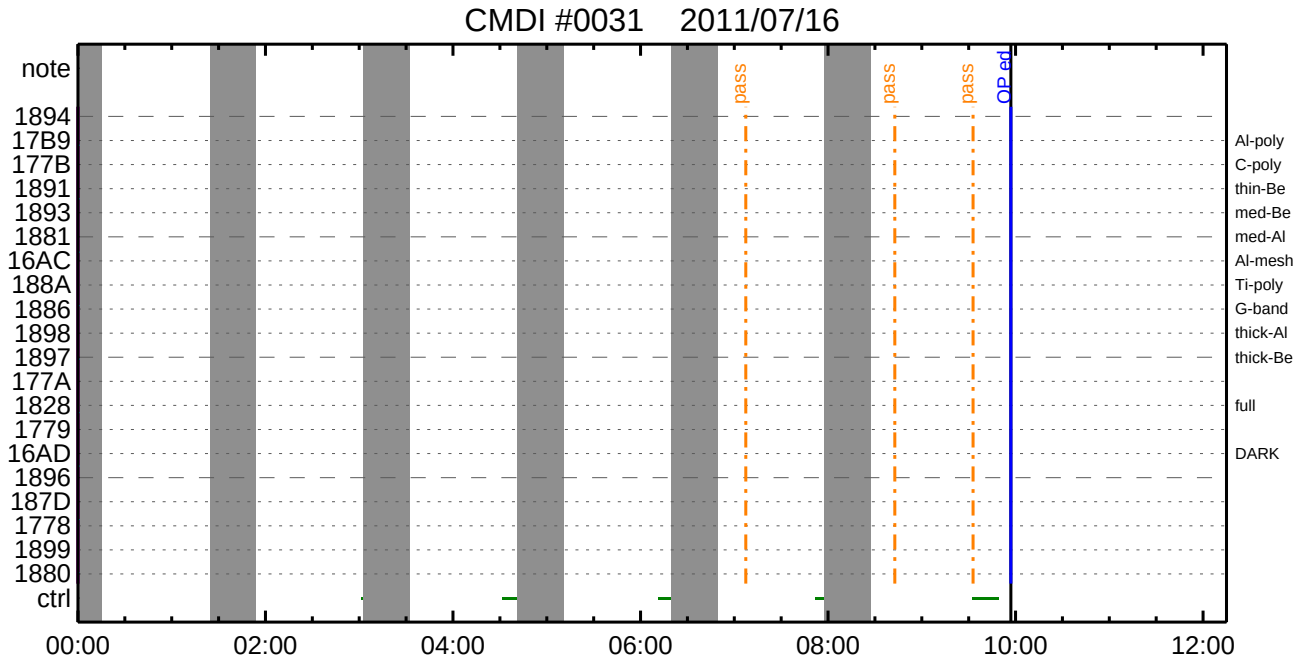

CMDI #0031 2011/07/13
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
track1
track4
track1
pass
pass
track0
track1
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0031 2011/07/14
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
track0
track1
QT
FL
pass
pass
track2
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0031 2011/07/14
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0031 2011/07/15
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0031 2011/07/15
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0031 2011/07/16
note
1894
17B9
177B
1891
1893
1881
16AC
188A
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1897
177A
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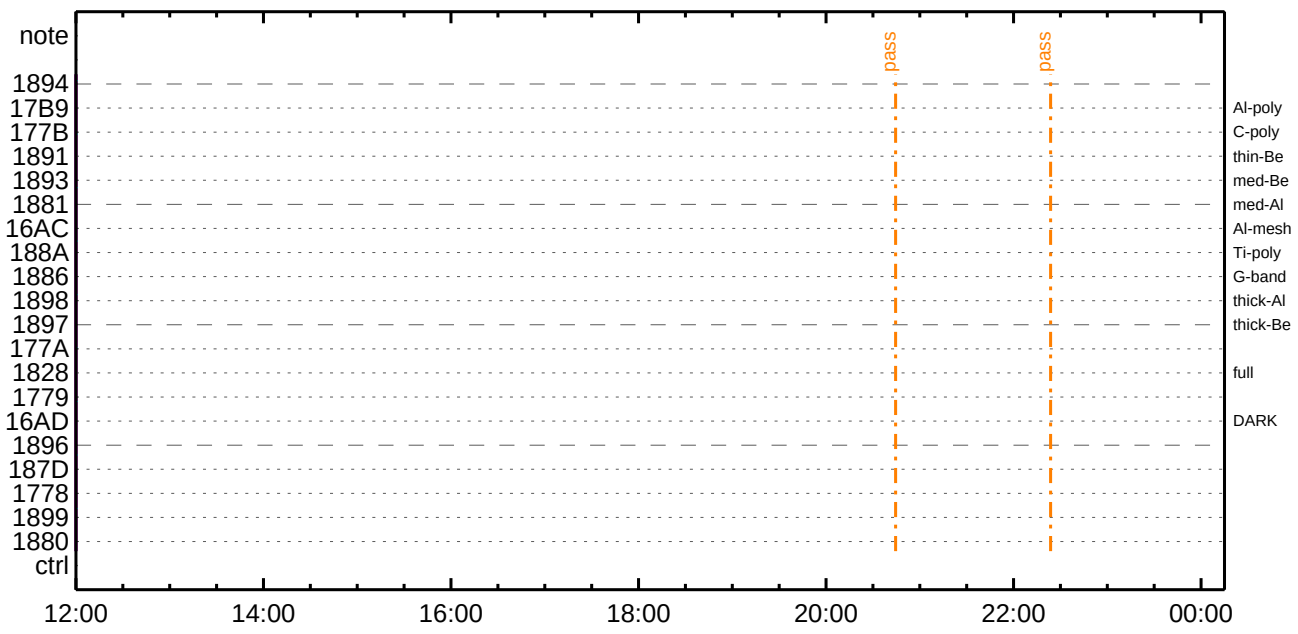
1779
16AD
1896
187D
1778
1899
1880
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0031 2011/07/16
note
1894
17B9
177B
1891
1893
1881
16AC
188A
1886
1898
1897
177A
1828
1779
16AD
1896
187D
1778
1899
1880
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-191 2011-07-12 13:20:27 289 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019    +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021    +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023    +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029    . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031    . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036    . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037    +. DC 01-29 DHU_S/X_VC4_OFF
0038    +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040    +  DC 06-B3 DR_REP_START
0041    +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047    +. DC 06-B3 DR_REP_START
0048    +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054    . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057    . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062    . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063    +. DC 01-29 DHU_S/X_VC4_OFF
0064    +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066    +  DC 06-B3 DR_REP_START
0067    +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073    +. DC 06-B3 DR_REP_START
0074    +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079    . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083    . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084    +. DC 06-B4 DR_REP_STOP
0085    +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090    . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091    +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093    +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-191:OP
0104     ()
0105   . S. OG    og-191:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2011-07-12 11:10:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2011-07-12 11:10:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2011-07-12 11:10:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2011-07-12 11:14:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2011-07-12 11:14:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2011-07-12 11:14:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2011-07-12 11:14:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2011-07-12 11:14:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-192 2011-07-12 13:20:27 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC =   51993.0 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 41s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2011-07-12 11:14:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-193 2011-07-12 13:20:27 257 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F8 XRT_OPERATE
0102     BC       (03 02)
0103  +. DC 07-F0 MDP_XRT_CTRL_MANU
0104     BC       (c1)
0105  +  DC 07-F0 MDP_XRT_MODE_STBY
0106     BC       (c3)
0107   . C. ----------- Success Verify ?              OK / NG____
0108     C.
0109     C. XRT Obs. Table Upload
0110   . S. RAM   ram-291:MDP_OBS_X
0111     ()
0112     C.
0113  +. DC 07-F0 MDP_DUMP_XRTTBL
0114     BC       (84 07 00 00 00 3a d4)
0115   . C. ----------- Comparison Check ?                        OK / ERR ____
0116     C.
0117     C.
0118  +. DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 01 b1 b1 04 04)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 02 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 03 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 04 b1 b1 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 05 85 83 08 08)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 06 85 83 06 06)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 07 80 80 20 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 08 80 80 20 08)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 09 80 80 08 20)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0a 80 80 06 06)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0f 80 80 06 06)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 10 80 80 08 08)
0142  +  DC 07-F0 MDP_XRT_FLD_ENA
0143     BC       (d8)
0144  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145     BC       (c8)
0146  +  DC 07-F0 MDP_XRT_ARS_DIS
0147     BC       (d5)
0148  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0149     BC       (c4 04)
0150  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0151     BC       (c5 08)
0152   . C. ---------- Success Verify ?            OK / NG ____
0153     C.
0154     C.
0155   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0156     C.
0157  +. DC 07-F0 MDP_XRT_MODE_OBSV
0158     BC       (c2)
0159  +. TI 2011-07-12 11:14:02.0
0160     DC 07-F0 MDP_XRT_MODE_OBSV
0161     BC       (c2)
0162  +. TI 2011-07-12 11:14:04.0
0163     DC 07-F0 MDP_XRT_CTRL_AUTO
0164     BC       (c0)
0165   . C. ---------- Success Verify ?            OK / NG ____
0166     C.
0167     C. ************ XRT END ***********
0168   . C. ********************************
0169     C.  SOT table upload
0170     C. ********************************
0171   . C. < Stop FG table >
0172  +. DC 07-F0 MDP_FG_CTRL_MANU
0173     BC       (51)
0174   . C. ------------------------------------
0175     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0176     C. ------------------------------------
0177     C.
0178   . C. <Upload FG Observation Table>
0179   . S. RAM   ram-265:MDP_OBS_F
0180     ()
0181     C.
0182   . C. < Dump RAMID=MDP_OBS_F >
0183  +. DC 07-F0 MDP_DUMP_FGTBL
0184     BC       (82 07 00 00 00 38 b8)
0185     C. ------------------------------------
0186     C.  MDP_OBS_F verify = OK/NG        [ ]
0187     C. ------------------------------------
0188     C.
0189   . C. < Stop SP table >
0190  +. DC 07-F0 MDP_SP_CTRL_MANU
0191     BC       (61)
0192     C. ------------------------------------
0193     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
```

```
0194     C. ------------------------------------
0195     C.
0196   . C. <Upload SP Observation Table>
0197   . S. RAM   ram-284:MDP_OBS_S
0198     ()
0199     C.
0200   . C. < Dump RAMID=MDP_OBS_S >
0201  +. DC 07-F0 MDP_DUMP_SPTBL
0202     BC       (83 07 00 00 00 38 b8)
0203     C. ------------------------------------
0204     C.  MDP_OBS_S verify = OK/NG        [ ]
0205     C. ------------------------------------
0206     C.
0207   . C. < Upload DPL table >
0208     C.
0209     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0210     C.
0211   . S. RAM   ram-271:MDP_DPL
0212     ()
0213     C.
0214   . C. < Dump RAMID=MDP_DPL >
0215  +. DC 07-F0 MDP_DUMP_FGTBL
0216     BC       (82 07 00 38 b8 00 40)
0217     C. ------------------------------------
0218     C.  MDP_DPL verify          = OK    [ ]
0219     C. ------------------------------------
0220     C.
0221     C. STS_CHK¤òON¤Ë¤¹¤ë
0222     C.
0223   . C. < Update MDP DSC PAR1 >
0224  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0225     BC       (4c)
0226     C.  MDP_CMD_CODE          = F04C0700[ ]
0227     C.  MDP_CMD_CNT        (count-up 1) [ ]
0228     C. ------------------------------------
0229     C.
0230   . C.
0231     C. ******************************
0232     C.  SOT TI command set
0233     C. ******************************
0234     C. Execute, after the success of TBL upload.
0235  +. TI 2011-07-12 11:14:18.0
0236     DC 07-F0 MDP_SOT_MODE_OBSV
0237     BC       (40)
0238   . C. ------------------------------------
0239     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0240     C. ------------------------------------
0241     C.
0242     C.
0243   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0244     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0245   . S. DC-BC dcbc-402:DCBC
0246     (MDP_known_event)
0247     C.
0248     C.
0249   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0250   . S. DC-BC dcbc-153:DCBC
0251     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0252     C.
0253     C.
0254   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0255     C.
0256   . C. ********** LOS **********
0257     C.
```

```
*** OP Sequence for XRT ***

2011/07/12  11:24:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  11:24:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/07/12  11:25:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/07/12  11:25:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/07/12  11:25:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/07/12  11:25:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/12  11:27:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/07/12  11:28:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  11:34:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  11:35:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/07/12  11:37:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/12  11:37:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/12  11:37:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/12  11:37:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/12  11:37:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/12  11:37:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  11:37:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2011/07/12  11:37:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/12  11:38:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  12:03:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  12:03:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  12:03:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  12:06:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  12:33:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  12:34:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  13:42:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  13:42:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  13:42:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  13:45:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  14:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2011/07/12  14:18:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  14:20:32.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/12  14:20:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/12  14:20:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/12  14:20:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/12  14:20:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/12  14:21:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  14:21:02.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2011/07/12  14:21:04.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 05
2011/07/12  14:21:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  15:20:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  15:20:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  15:20:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  15:23:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  16:07:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  16:08:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  16:59:00.0  XRT_CTRL_MANU_408_OG [0x198]
```

```
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  16:59:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  16:59:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  17:02:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  17:44:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  17:45:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  17:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  18:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2011/07/12  18:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/12  18:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/12  18:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/12  18:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/12  18:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/12  18:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  18:02:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2011/07/12  18:02:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/12  18:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  18:37:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  18:37:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  18:37:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  18:40:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  19:21:01.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  19:22:01.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  19:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  20:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2011/07/12  20:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/12  20:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/12  20:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/12  20:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/12  20:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/12  20:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  20:02:56.0  XRT_QT_PROG_SET_403_OG [0x193]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/07/12  20:02:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/12  20:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  20:16:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  20:16:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  20:16:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  20:19:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  20:58:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  20:59:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  21:54:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  21:54:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  21:54:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/12  21:57:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/12  22:34:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/12  22:35:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/12  23:33:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/12  23:33:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/12  23:33:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
```

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2011/07/12  23:36:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  00:02:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  00:02:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/07/13  00:03:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/07/13  00:03:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/07/13  00:03:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/07/13  00:03:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  00:05:58.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2011/07/13  00:06:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  00:13:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  00:15:32.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  00:15:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  00:15:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  00:15:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  00:15:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  00:16:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  00:16:02.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2011/07/13  00:16:04.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  00:16:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  00:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  01:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2011/07/13  01:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  01:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  01:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  01:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  01:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  01:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  01:02:56.0  XRT_QT_PROG_SET_403_OG [0x193]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/07/13  01:02:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  01:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  01:11:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  01:11:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  01:11:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  01:14:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  01:40:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  01:41:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  02:49:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  02:49:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  02:49:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  02:52:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  03:19:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  03:20:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  04:18:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  04:18:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  04:18:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  04:21:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  04:57:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  04:58:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

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2011/07/13  05:58:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  05:58:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  05:58:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  06:01:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  06:36:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  06:37:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  07:39:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  07:39:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  07:39:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  07:42:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  08:14:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  08:15:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  08:29:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  08:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/07/13  08:32:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  08:32:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  08:32:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  08:32:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  08:32:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  08:32:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  08:32:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2011/07/13  08:32:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  08:33:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  09:18:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  09:18:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  09:18:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  09:21:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  09:53:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  09:54:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  11:02:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  11:02:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  11:02:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  11:05:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  11:31:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  11:31:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/07/13  11:32:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2011/07/13  11:32:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2011/07/13  11:32:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/07/13  11:32:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  11:34:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2011/07/13  11:35:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  11:41:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  11:42:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/07/13  11:44:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  11:44:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  11:44:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  11:44:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  11:44:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
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2011/07/13  11:44:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  11:44:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2011/07/13  11:44:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  11:45:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  12:40:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  12:40:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  12:40:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  12:43:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  13:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2011/07/13  13:11:00.5  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  13:13:32.5  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  13:13:52.5  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  13:13:54.5  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  13:13:56.5  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  13:13:58.5  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  13:14:00.5  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  13:14:02.5  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2011/07/13  13:14:04.5  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 05
2011/07/13  13:14:06.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  14:19:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  14:19:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  14:19:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  14:22:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  14:57:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  14:58:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  15:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  15:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  15:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  16:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  16:44:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  16:45:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  17:36:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  17:36:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  17:36:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  17:39:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  18:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 04 00 00 00 00
2011/07/13  18:22:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  18:24:32.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  18:24:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  18:24:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  18:24:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  18:24:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  18:25:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  18:25:02.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2011/07/13  18:25:04.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  18:25:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  19:14:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  19:14:32.0  XRT_FLD_RESET_412_OG [0x19c]
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                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  19:14:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  19:17:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  19:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  20:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2011/07/13  20:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/13  20:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/13  20:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/13  20:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/13  20:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/13  20:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  20:02:56.0  XRT_QT_PROG_SET_403_OG [0x193]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/07/13  20:02:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/13  20:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  20:53:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  20:53:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  20:53:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  20:56:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  21:35:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  21:36:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  22:31:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  22:31:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/13  22:31:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/13  22:34:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/13  23:09:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/13  23:10:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/13  23:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/13  23:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2011/07/14  00:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2011/07/14  00:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2011/07/14  00:00:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2011/07/14  00:00:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/14  00:02:58.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2011/07/14  00:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  00:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2011/07/14  00:10:00.5  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  00:10:02.5  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  00:10:04.5  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  00:13:14.5  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  00:40:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  00:42:32.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/14  00:42:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/14  00:42:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/14  00:42:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/14  00:42:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/14  00:43:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  00:43:02.0  XRT_QT_PROG_SET_403_OG [0x193]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2011/07/14  00:43:04.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
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2011/07/14  00:43:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  01:48:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  01:48:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  01:48:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  01:51:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  02:18:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/14  02:19:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  03:25:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  03:25:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  03:25:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  03:28:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  03:56:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/14  03:57:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  04:56:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  04:56:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  04:56:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  04:59:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  05:34:30.0  XRT_Custom_418_OG [0x1a2]
2011/07/14  05:35:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  06:36:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  06:36:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  06:36:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  06:39:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  07:13:00.0  XRT_Custom_418_OG [0x1a2]
2011/07/14  07:14:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  08:16:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  08:16:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  08:16:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  08:19:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  08:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 02 00 00 00 00
2011/07/14  08:52:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  08:54:32.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2011/07/14  08:54:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2011/07/14  08:54:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2011/07/14  08:54:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2011/07/14  08:54:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2011/07/14  08:55:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  08:55:02.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2011/07/14  08:55:04.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 08
2011/07/14  08:55:06.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2011/07/14  09:57:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2011/07/14  09:57:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2011/07/14  09:57:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2011/07/14  10:00:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2011/07/14  10:31:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
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