

# XRT Timeline to be uploaded on 2011/07/23

Period: 2011/07/23 10:59:00 - 2011/07/28 11:18:00

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Normal mode

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XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad													
Term	Pointing (x, y)		Comment										
07/23 11:12:00 - 07/23 11:59:54	Track ( -172.7, 278.5) @ 07/23 11:09:00		# OP start + 10min, HOP 166, cont., AR 11259.										
07/24 07:33:00 - 07/24 11:59:54	Track ( -4.7, 275.9) @ 07/24 07:30:00		* HOP 166										
07/25 06:03:00 - 07/25 16:49:30	Track ( 181.0, 276.3) @ 07/25 06:00:00		* AR 11259.										
07/25 21:03:00 - 07/26 01:59:54	Track ( 300.8, 278.6) @ 07/25 21:00:00		# AR 11259.										
07/26 02:03:00 - 07/26 03:54:54	Track ( 832.7, -424.1) @ 07/26 02:00:00		* HOP 173, EPO, AR 11254.										
07/26 04:47:30 - 07/26 10:15:54	Track ( 355.3, 280.3) @ 07/26 04:05:00		* HOP 173 cont. (officially until 06 UT), AR 11259.										
<b>PROG= 01 Inf.-time(s)</b>													
Subr= 2 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec													
Seqn= 13 55-time(s) 22.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term	Pointing (x, y)		Comment										
07/23 14:34:00 - 07/23 17:59:54	Track ( -643.1, -15.7) @ 07/23 14:00:00		* HOP 197, filament formation.										
07/23 18:13:00 - 07/23 19:59:54	Track ( -614.6, -18.2) @ 07/23 18:10:00		# Cont.										
07/23 20:03:00 - 07/24 01:59:54	Fixed ( -905.0, 194.0)		* EIS temp scans of AR at E limb.										
07/24 14:03:00 - 07/24 17:37:24	Track ( -466.0, -28.4) @ 07/24 14:00:00		* HOP 197.										
07/24 17:50:30 - 07/24 19:29:30	Track ( -435.4, -30.1) @ 07/24 17:47:30		# Cont.										
07/24 20:13:30 - 07/25 01:59:54	Fixed ( -905.0, 194.0)		* EIS temp scans of AR at E limb.										
<b>PROG= 11 Inf.-time(s)</b>													
Subr= 2 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec													
Seqn= 13 30-time(s) 80.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)													
Term	Pointing (x, y)		Comment										
07/23 18:03:00 - 07/23 18:09:54	Fixed ( 0.0, 0.0)		synoptic										
07/24 04:03:00 - 07/24 04:09:54	Fixed ( 0.0, 0.0)		synoptic, shifted manually.										
07/24 17:40:30 - 07/24 17:47:24	Fixed ( 0.0, 0.0)		synoptic, shifted -22.5 min										
07/25 04:13:00 - 07/25 04:19:54	Fixed ( 0.0, 0.0)		synoptic, shifted manually.										
07/25 18:06:30 - 07/25 20:59:54	Track ( -10.1, 0.1) @ 07/25 17:00:00		* HOP 196, with drive-by synoptic at 18:06:30 UT.										
07/26 03:58:00 - 07/26 04:04:54	Fixed ( 0.0, 0.0)		synoptic, shifted manually.										
<b>PROG= 18 1-time(s)</b>													
Subr= 1 1-time(s) 12.0sec													
Seqn= 20 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 31 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 77 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**XOB #17F6: AR Standard-A(Filter-Ratio) for FW1=Open, 384x384 at 1064 1048, 2min-cad**

Term	Pointing (x, y)	Comment
07/24 02:03:00 - 07/24 03:59:54	Track ( 673.8, -455.1) <sup>Ⓜ 07/24 02:00:00</sup>	* HOP 173, joint with high schools, AR 11254.
07/24 04:13:00 - 07/24 07:29:54	Track ( -32.4, 276.1) <sup>Ⓜ 07/24 04:10:00</sup>	# HOP 173, cont., AR 11259.
07/25 02:32:00 - 07/25 03:35:00	Track ( 772.8, -440.7) <sup>Ⓜ 07/25 02:00:00</sup>	* HOP 173, AR 11254.
07/25 04:23:00 - 07/25 05:59:54	Track ( 167.4, 276.2) <sup>Ⓜ 07/25 04:20:00</sup>	# HOP 173, cont., AR 11259.

**PROG= 20 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 19 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn=100 4-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 29 20-time(s) 120.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
07/23 11:12:00 - 07/23 11:59:54	Track ( -172.7, 278.5) <sup>Ⓜ 07/23 11:09:00</sup>	# OP start + 10min, HOP 166, cont., AR 11259.
07/23 14:34:00 - 07/23 17:59:54	Track ( -643.1, -15.7) <sup>Ⓜ 07/23 14:00:00</sup>	* HOP 197, filament formation.
07/23 18:13:00 - 07/23 19:59:54	Track ( -614.6, -18.2) <sup>Ⓜ 07/23 18:10:00</sup>	# Cont.
07/23 20:03:00 - 07/24 01:59:54	Fixed ( -905.0, 194.0)	* EIS temp scans of AR at E limb.
07/24 02:03:00 - 07/24 03:59:54	Track ( 673.8, -455.1) <sup>Ⓜ 07/24 02:00:00</sup>	* HOP 173, joint with high schools, AR 11254.
07/24 04:13:00 - 07/24 07:29:54	Track ( -32.4, 276.1) <sup>Ⓜ 07/24 04:10:00</sup>	# HOP 173, cont., AR 11259.
07/24 07:33:00 - 07/24 11:59:54	Track ( -4.7, 275.9) <sup>Ⓜ 07/24 07:30:00</sup>	* HOP 166
07/24 14:03:00 - 07/24 17:37:24	Track ( -466.0, -28.4) <sup>Ⓜ 07/24 14:00:00</sup>	* HOP 197.
07/24 17:50:30 - 07/24 19:29:30	Track ( -435.4, -30.1) <sup>Ⓜ 07/24 17:47:30</sup>	# Cont.
07/24 20:13:30 - 07/25 01:59:54	Fixed ( -905.0, 194.0)	* EIS temp scans of AR at E limb.
07/25 02:32:00 - 07/25 03:35:00	Track ( 772.8, -440.7) <sup>Ⓜ 07/25 02:00:00</sup>	* HOP 173, AR 11254.
07/25 04:23:00 - 07/25 05:59:54	Track ( 167.4, 276.2) <sup>Ⓜ 07/25 04:20:00</sup>	# HOP 173, cont., AR 11259.
07/25 06:03:00 - 07/25 16:49:30	Track ( 181.0, 276.3) <sup>Ⓜ 07/25 06:00:00</sup>	* AR 11259.
07/25 21:03:00 - 07/26 01:59:54	Track ( 300.8, 278.6) <sup>Ⓜ 07/25 21:00:00</sup>	# AR 11259.
07/26 02:03:00 - 07/26 03:54:54	Track ( 832.7, -424.1) <sup>Ⓜ 07/26 02:00:00</sup>	* HOP 173, EPO, AR 11254.
07/26 04:47:30 - 07/26 10:15:54	Track ( 355.3, 280.3) <sup>Ⓜ 07/26 04:05:00</sup>	* HOP 173 cont. (officially until 06 UT), AR 11259.

**PROG= 08 1-time(s)**

- Subr= 1 30-time(s) 20.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 60 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
- Subr= 3 30-time(s) 60.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 88 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
- Subr= 3 30-time(s) 60.0sec
  - Seqn= 87 1-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
  - Seqn= 88 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3    30-time(s)    60.0sec</b>												
<b>Seqn= 87    1-time(s)    2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4    24-time(s)    600.0sec</b>												
<b>Seqn= 89    1-time(s)    2.0sec</b>												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

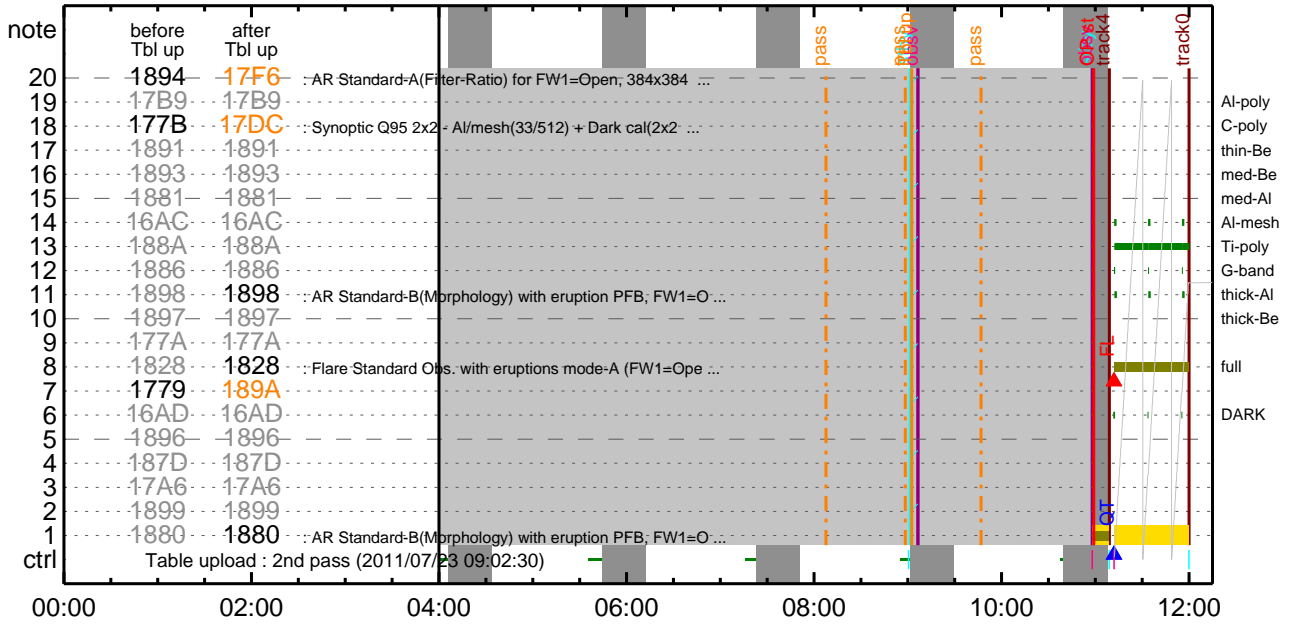
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### Flare Detection

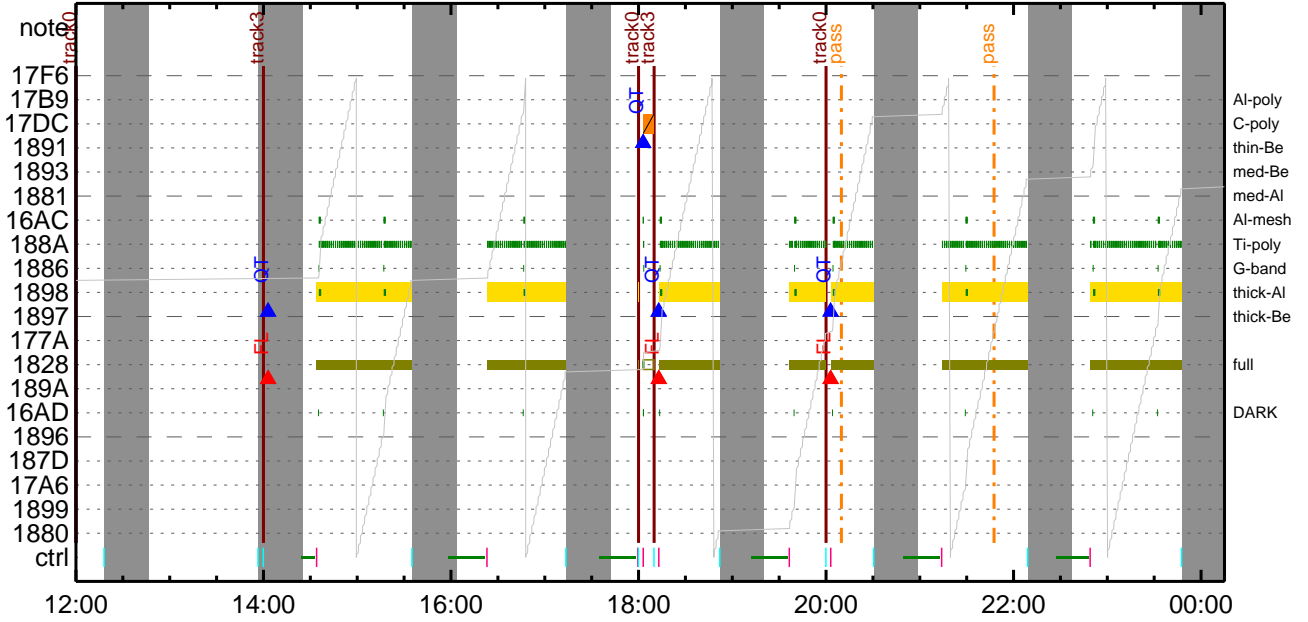
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<b>FLD Patrol</b>												
Term	Pointing (x, y)				Comment							
07/23 18:12:46 - 07/24 04:00:16	Track (	-614.6,	-18.2)	@ 07/23 18:10:00	# Cont.							
07/24 04:12:46 - 07/24 17:37:46	Track (	-32.4,	276.1)	@ 07/24 04:10:00	# HOP 173, cont., AR 11259.							
07/24 17:50:16 - 07/25 04:10:16	Track (	-435.4,	-30.1)	@ 07/24 17:47:30	# Cont.							
07/25 04:22:46 - 07/25 18:03:46	Track (	167.4,	276.2)	@ 07/25 04:20:00	# HOP 173, cont., AR 11259.							
07/25 21:02:46 - 07/26 03:55:16	Track (	300.8,	278.6)	@ 07/25 21:00:00	# AR 11259.							
07/26 04:07:46 - 07/28 11:18:00	Track (	355.3,	280.3)	@ 07/26 04:05:00	* HOP 173 cont. (officially until 06 UT), AR 11259.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

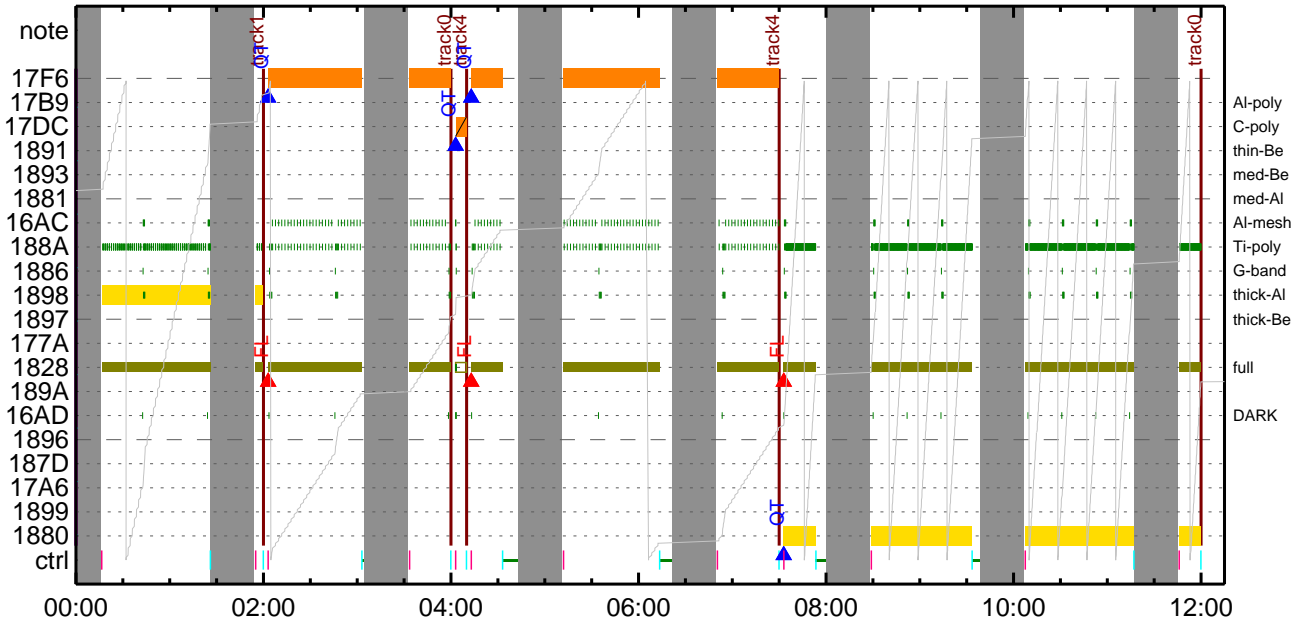
### CMDI #0054 2011/07/23



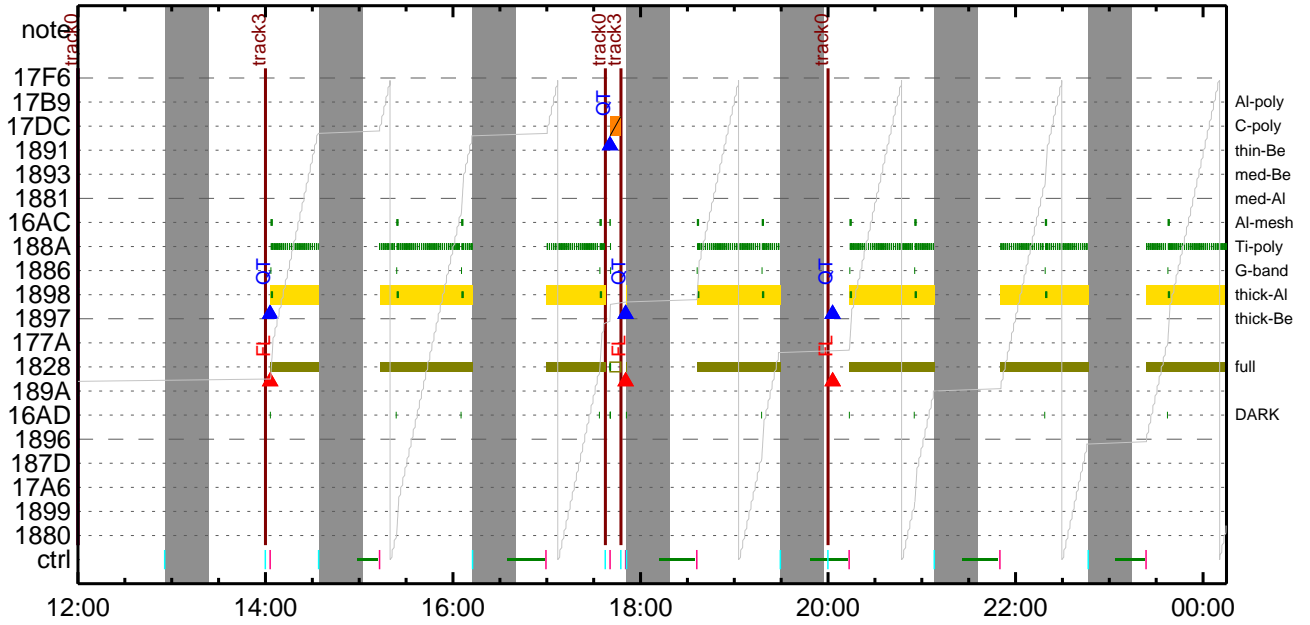
### CMDI #0054 2011/07/23



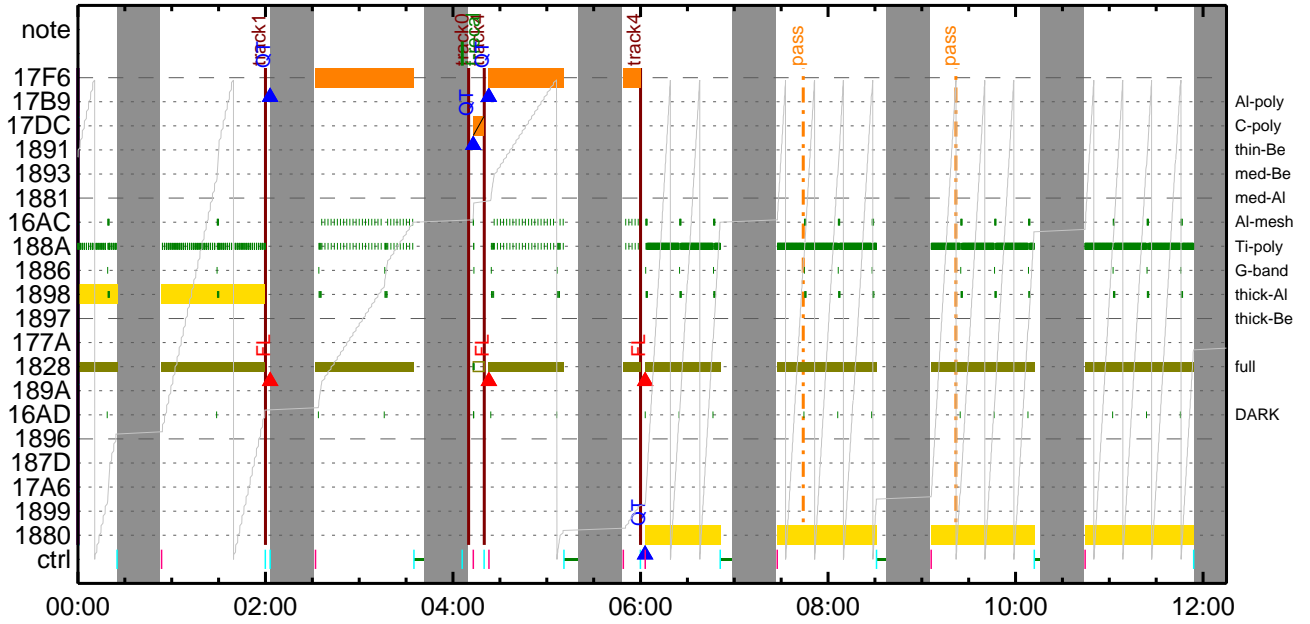
### CMDI #0054 2011/07/24



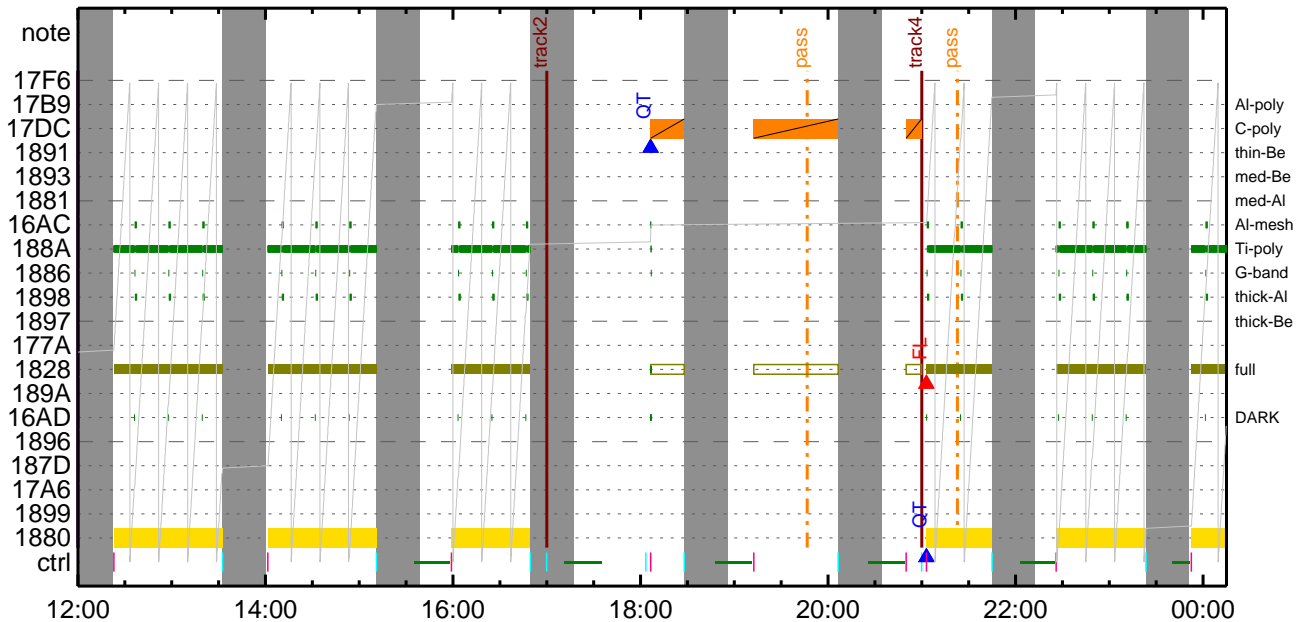
CMDI #0054 2011/07/24



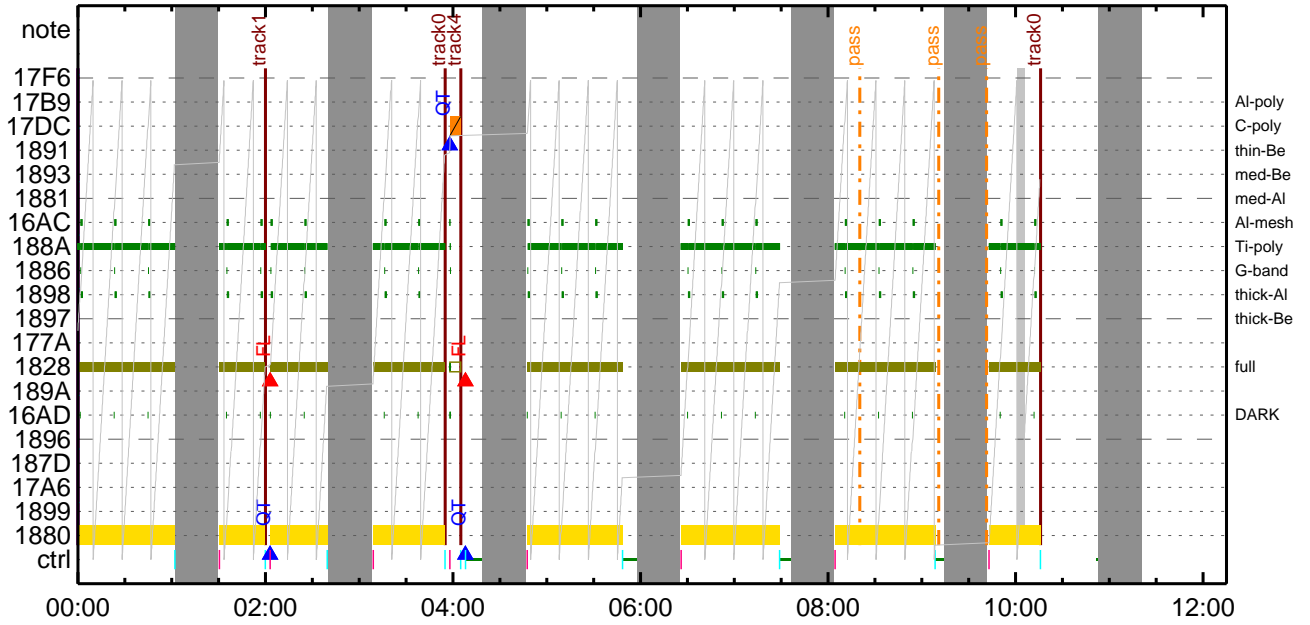
CMDI #0054 2011/07/25



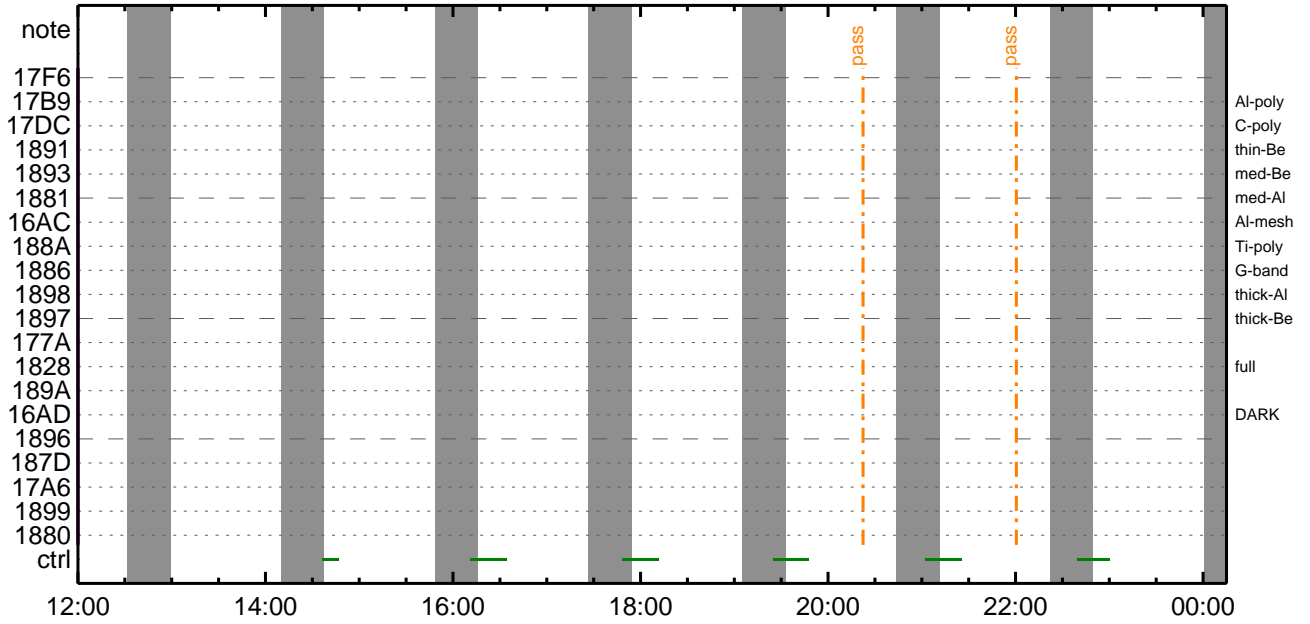
CMDI #0054 2011/07/25



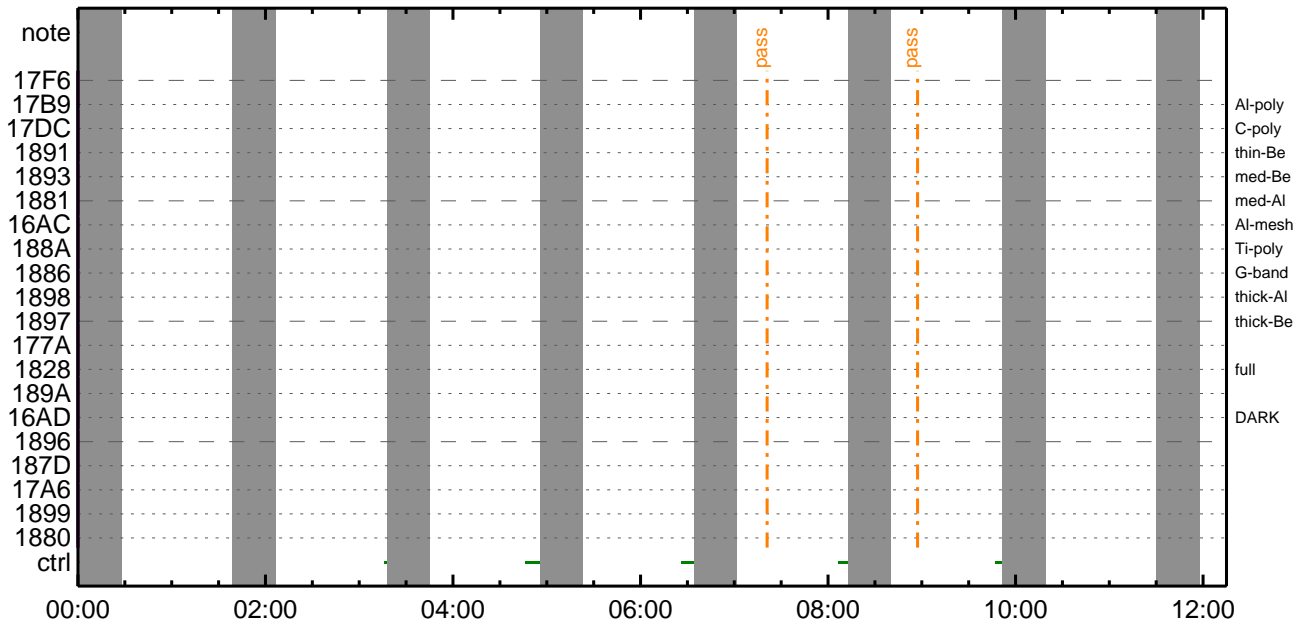
CMDI #0054 2011/07/26



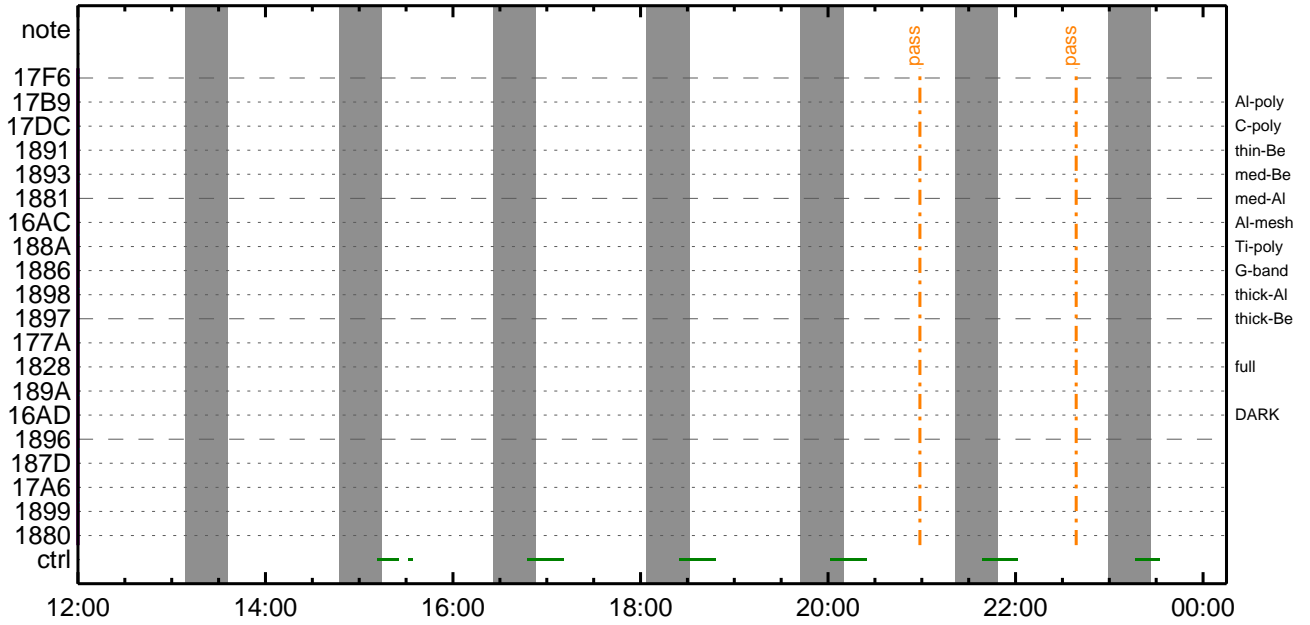
CMDI #0054 2011/07/26



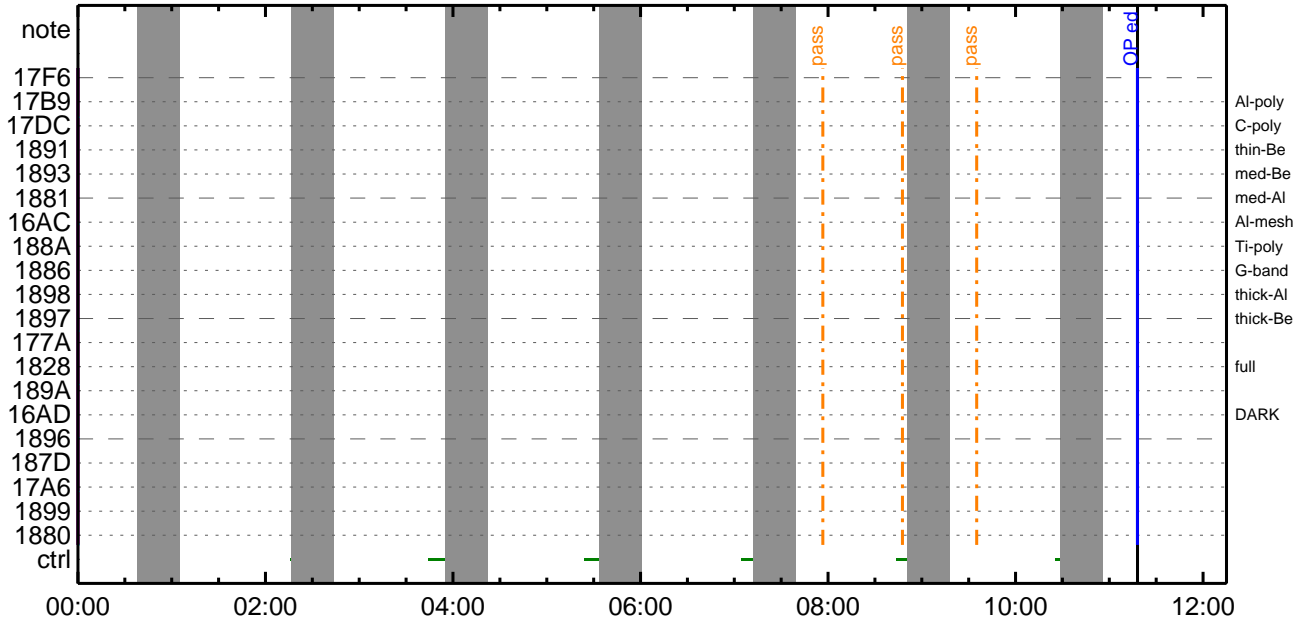
CMDI #0054 2011/07/27



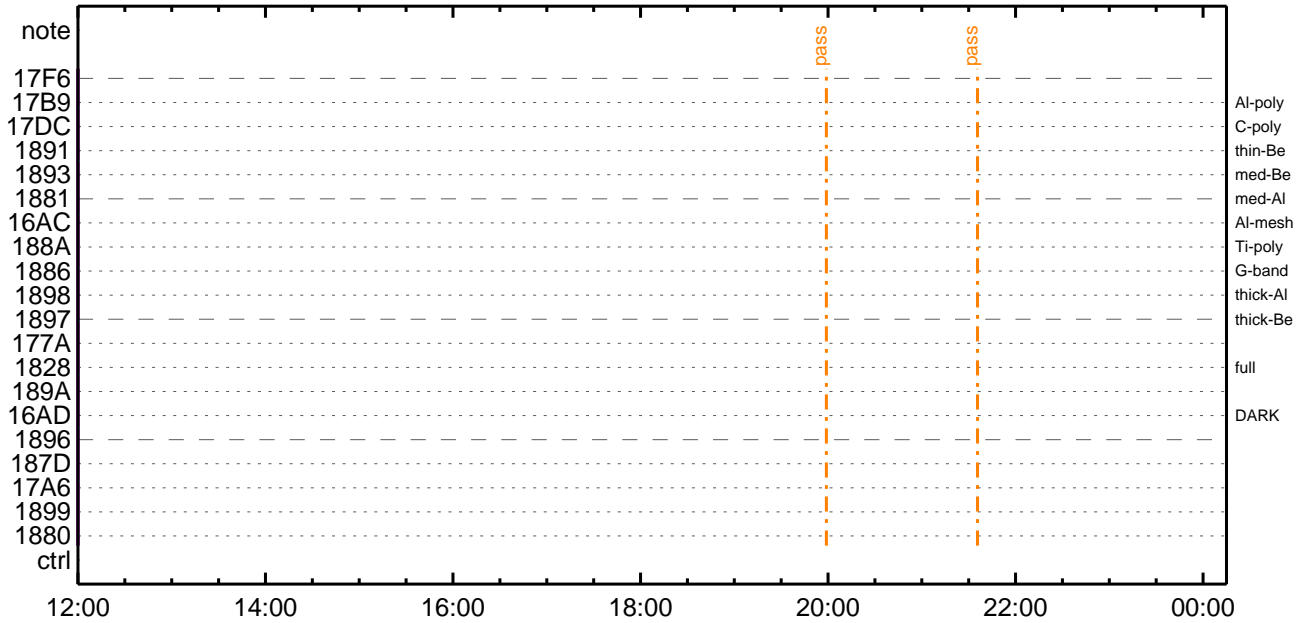
CMDI #0054 2011/07/27



CMDI #0054 2011/07/28



CMDI #0054 2011/07/28







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-214:OP
0104 ( )
0105 S. OG og-214:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ë¹ç•è²îOK²³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ë¹ç•è²îOK²³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ë¹ç•è²îOK²³îç§
0165 C.
0166 C. ***** °Ë²¼²î¼Ë¹ç•è²îOK²³îç§ *****
0167 C. DHUYâ;4YE;Ë¼Y½;Yi;4YE;Ëòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼Ë¹ç•è²îOK²³îç§
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYË²òðÁDîç(UT)
0183 +. TI 2011-07-23 10:54:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-07-23 10:54:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-07-23 10:54:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-07-23 10:58:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼ñîÄè%îíññîîŷÄŷ§ŷÄŷ¹àîŷ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]       EQ      7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ñ³îÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñîŷÈ¹çªè²îOKñ³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èññîñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]       EQ      2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-07-23 10:58:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-07-23 10:58:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-07-23 10:58:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-07-23 10:58:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃîñî»ö¼ŷñèÄñ¹ñèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼ññ¼ª»ŷñ¹ñè)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîññè'Øñ¹ñèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼ª»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-215 2011-07-23 13:31:40 140 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÝOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È²¿²Á²·µ°È»Í×ÁÇ²ÍYçYÁY×Yí;¼YÉ;ÈÈ²µ·fíÉ;È²È¼°ÇÖ²·²¿¼l¹ç²Í;çÁ®, ù²¹²è²²²çÁ+¿®²·²È²²²²²²²È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 C. ***** XRT START *****
0047 C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 BC (c1)
0050 + DC 07-F0 MDP_XRT_MODE_STBY
0051 BC (c3)
0052 . C. ----- Success Verify ? OK / NG ____
0053 C.
0054 C. XRT Obs. Table Upload
0055 . S. RAM ram-291:MDP_OBS_X
0056 ( )
0057 C.
0058 +. DC 07-F0 MDP_DUMP_XRTTBL
0059 BC (84 07 00 00 00 3a d4)
0060 . C. ----- Comparison Check ? OK / ERR ____
0061 C.
0062 C.
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 01 b1 b1 04 04)
0065 + DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 02 b1 b1 08 08)
0067 + DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 03 b1 b1 08 08)
0069 + DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 04 b1 b1 06 06)
0071 + DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 05 85 83 08 08)
0073 + DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 06 85 83 06 06)
0075 + DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 07 80 80 20 20)
0077 + DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 0f 80 80 06 06)
0079 + DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 10 80 80 08 08)
0081 + DC 07-F0 MDP_XRT_FLD_ENA
0082 BC (d8)
0083 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0084 BC (c8)
0085 + DC 07-F0 MDP_XRT_ARS_DIS
0086 BC (d5)
0087 . C. ----- Success Verify ? OK / NG ____
0088 C.
0089 C.
0090 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0091 C.
0092 +. DC 07-F0 MDP_XRT_MODE_OBSV
0093 BC (c2)
0094 +. TI 2011-07-23 10:58:02.0
0095 DC 07-F0 MDP_XRT_MODE_OBSV

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0096 BC (c2)
0097 +. TI 2011-07-23 10:58:04.0
0098 DC 07-F0 MDP_XRT_CTRL_AUTO
0099 BC (c0)
0100 . C. ----- Success Verify ? OK / NG ____
0101 C.
0102 C. ***** XRT END *****
0103 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0104 +. DC 07-FC EIS_MODE_MANU
0105 BC (21 02)
0106 . C. Verify EIS in MANUAL mode
0107 . C. Estimated OBSTBL upload time is 3s
0108 C. *****
0109 C. EIS START OBSTBL LOAD
0110 C. *****
0111 . S. RAM ram-821:EIS_OBSTBL
0112 ( )
0113 +. DC 07-FC EIS_DUMP_OBSTBL
0114 BC (07 07 07 00 00 70 00)
0115 C.
0116 C. Execute, after the success of OBSTBL upload.
0117 C. Set EIS TI-commands
0118 +. TI 2011-07-23 10:58:50.0
0119 DC 07-FC EIS_MODE_CHG_ENA
0120 BC (20)
0121 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0122 C. *****
0123 C. EIS END OBSTBL LOAD
0124 C. *****
0125 C.
0126 . C. ***** MDP `úÃîñî»ö%ÝðĒĀð¹ñĒDCBC•x²è *****
0127 C. (%Á°îŸÓŸĀŸĒŸĲŸĒŸĀŸĲŸĒĒĒ%¼ñ¼Ā»Ūñ¹ñĒ)
0128 . S. DC-BC dcbc-402:DCBC
0129 (MDP_known_event)
0130 C.
0131 C.
0132 . C. ***** ŸDŸ¹•Ī Daily±ĵİññĒ`Øñ¹ñĒDCBC•x²è *****
0133 . S. DC-BC dcbc-153:DCBC
0134 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135 C.
0136 C.
0137 . C. ;ãLOSŸĀŸŸĀŸ~¼Ā»Ū;ã
0138 C.
0139 . C. ***** LOS *****
0140 C.

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(a) Spacecraft Operation Procedure (real-commands)

main-216 2011-07-23 13:31:40 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. \*\*\*\*\* AOS \*\*\*\*\*
0003 C.
0004 . C. ;ãAOSYÃYŸYÃY~¼Ä»Û;ã
0005 C.
0006 C. YÃYŸ;¼Y³YŸYŸYÉÄ+ç®
0007 +. DC 00-00 NULL\_DUMMY\_CMD
0008 C.
0009 . C. \*\*\*\*\* AOCs : Reload orbital element (send every contact) \*\*\*\*\*
0010 C. Äí;ËççãÄã•μ°Ë»Í×ÁÇçÍYçYÃY×YÍ;¼YÉ;ËÈÈ¼μ•ÍÉ;ËãÈ¼°ÇÖã•çç¼í¹ççÍ;çÄ®, ùç¹ãÈãããçÄ+ç®ã•ãÈãããçÈ;ç
0011 +. DC 02-8E AOCU\_ORB\_UPD
0012 C.
0013 C.
0014 . C. \*\*\*\*\*
0015 C. XÃ+ççμ;ON
0016 C. \*\*\*\*\*
0017 C. ç¨ °ÄÄ, Í×ËYãÄLOSpãçãÍ»p´Öãð¹Íí, ç. ; çÈÖÍ×ãÈXÄÖONãÍ¹ÖãÈãíãÈãããçÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA\_XPA\_ON/HI
0020 M. WAIT\_SEC 1
0021 + DC 03-84 TCIA\_XMOD\_ON
0022 M. WAIT\_SEC 1
0023 + DC 03-95 TCIA\_XMOD\_QPSK
0024 C. çç[HK1\_XPA\_ON/OFF] EQ ON
0025 C. çç[HK1\_XPA\_PWR\_HI/LO] EQ HI
0026 C. çç[HK1\_XMOD\_ON/OFF] EQ ON
0027 C. çç[HK1\_XMOD\_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYŸYÉYÍYÃY~¼ÖÄÖã~°ÄÄãã•çç;ç. ç°È²¼ççÍ°ÄÄ, ¼È¼ççç¼Ä¹Öçç;ç
0030 C.
0031 . C. \*\*\*\*\*
0032 C. DR PT1 Äí¼í°ÄÄ,
0033 C. \*\*\*\*\*
0034 C. ç¨ RESTART;ËPT1;Ëã•çççç¼í¹ççÍ;ç°È²¼ççÍ¼Ä¹Öçç»°;çDCBC-150çççÈãã;ç
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»Í;ã
0037 +. DC 01-29 DHU\_S/X\_VC4\_OFF
0038 + DC 06-C8 DR\_PT1\_REP\_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR\_REP\_START
0041 + DC 01-32 DHU\_X\_VC4\_ON
0042 C. çç[HK1\_REP\_PT\_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1\_REP\_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYŸYËYËÄÄÄÖ;ËÄ•Ä°²ôÈð;Ë, áãÍ°ÄÄ, °Ë³«;ã
0047 +. DC 06-B3 DR\_REP\_START
0048 + DC 01-32 DHU\_X\_VC4\_ON
0049 C. çç[HK1\_REP\_PT\_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1\_REP\_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, ç~¼«Ë°ÄÄ»Ÿã•çç;ã;ç°È²¼ççç¼Ä¹Öçç;ç
0055 C. YçYŸYËYËÄÄÄÖãÄ•Ä°²ôÈðã~¼ãã¼í¹ççÍ'°Í»ã¹ãÈããçÄÖãÄ;ç
0056 C.
0057 . C. \*\*\*\*\*
0058 C. DR PT2 Äí¼í°ÄÄ,
0059 C. \*\*\*\*\*
0060 C. ç¨ RESTART;ËPT2;Ëã•çççç¼í¹ççÍ;ç°È²¼ççÍ¼Ä¹Öçç»°;çDCBC-151çççÈãã;ç
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»Í;ã
0063 +. DC 01-29 DHU\_S/X\_VC4\_OFF
0064 + DC 06-C8 DR\_PT2\_REP\_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR\_REP\_START
0067 + DC 01-32 DHU\_X\_VC4\_ON
0068 C. çç[HK1\_REP\_PT\_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1\_REP\_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYŸYËYËÄÄÄÖ;ËÄ•Ä°²ôÈð;Ë, áãÍ°ÄÄ, °Ë³«;ã
0073 +. DC 06-B3 DR\_REP\_START
0074 + DC 01-32 DHU\_X\_VC4\_ON
0075 C. çç[HK1\_REP\_PT\_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1\_REP\_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. \*\*\*\*\*
0080 C. DR°ÄÄ, Äã»Ÿ;çXÃ+ççμ;OFF
0081 C. \*\*\*\*\*
0082 C.
0083 . C. ;ãDR°ÄÄ, Äã»Ÿ;ã
0084 +. DC 06-B4 DR\_REP\_STOP
0085 + DC 01-29 DHU\_S/X\_VC4\_OFF
0086 C. çç[HK1\_REP\_STA/STP] EQ STOP
0087 C. çç[HK1\_S\_VC4\_ON/OFF] EQ OFF
0088 C. çç[HK1\_X\_VC4\_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÃ+ççμ;OFF;ã
0091 +. DC 03-85 TCIA\_XMOD\_OFF
0092 M. WAIT\_SEC 1
0093 + DC 03-B5 TCIA\_XPA\_OFF
0094 C. çç[HK1\_XMOD\_ON/OFF] EQ OFF
0095 C. çç[HK1\_XPA\_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-269:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2011-07-23 10:58:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 . C. ***** MDP 'ũÃîñî»ö¼ýñÊÐñ¹ñèDCBC•x²è *****
0132 C. (¼ã°îÿÔÿÃÿÊÿPÿËÿäÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ÿDÿ¹•ï Daily±¿îññÈ´Øñ¹ñèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSÿÃÿ§ÿÿÃÿÿ¼Ã»Û;ä
0143 C.
0144 . C. ***** LOS *****
0145 C.
```

\*\*\* OP Sequence for XRT \*\*\*

```

2011/07/23 11:08:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 11:09:00.5 AOCS_ORe-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 04 00 00 00 00
2011/07/23 11:11:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/07/23 11:11:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/07/23 11:11:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/07/23 11:11:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/07/23 11:11:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/07/23 11:11:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 11:11:56.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2011/07/23 11:11:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 08
2011/07/23 11:12:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/07/23 11:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 12:00:00.0 AOCS_ORe-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/07/23 12:18:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 12:18:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 12:18:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/07/23 12:21:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/07/23 13:56:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 13:56:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 13:56:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/07/23 13:59:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/07/23 13:59:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 14:00:00.0 AOCS_ORe-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 03 00 00 00 00
2011/07/23 14:02:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/07/23 14:02:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/07/23 14:02:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/07/23 14:02:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/07/23 14:02:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/07/23 14:02:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 14:02:56.0 XRT_QT_PROG_SET_404_OG [0x194]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2011/07/23 14:02:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 08
2011/07/23 14:33:00.0 XRT_Custom_418_OG [0x1a2]
2011/07/23 14:34:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/07/23 15:35:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 15:35:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 15:35:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/07/23 15:38:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/07/23 16:22:00.0 XRT_Custom_418_OG [0x1a2]
2011/07/23 16:23:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/07/23 17:13:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 17:13:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/07/23 17:13:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/07/23 17:16:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/07/23 17:58:30.0 XRT_Custom_418_OG [0x1a2]
2011/07/23 17:59:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/07/23 17:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/07/23 17:59:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00

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2011/07/23	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2011/07/23	18:00:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/23	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/23	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/23	18:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2011/07/23	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	18:10:00.5	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	03	00	00	00	00
2011/07/23	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/23	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/23	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/23	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/23	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/23	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/23	18:12:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/07/23	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/23	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	18:52:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	18:52:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/23	18:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/23	18:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/23	19:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/23	19:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	19:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	20:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00	ee	c1	50	72
2011/07/23	20:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/23	20:02:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/23	20:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/23	20:02:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/23	20:02:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/23	20:02:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/23	20:02:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2011/07/23	20:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/23	20:03:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	20:30:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	20:30:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/23	20:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/23	20:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/23	21:13:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/23	21:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	22:09:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	22:09:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/23	22:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/23	22:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/23	22:48:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/23	22:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/23	23:47:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/23	23:47:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				



2011/07/23	23:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/23	23:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	00:15:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	00:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	01:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	01:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	01:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	01:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	01:54:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	01:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	02:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/07/24	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/24	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/24	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/24	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/24	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	02:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/07/24	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/24	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	03:03:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	03:03:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	03:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	03:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	03:32:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	03:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	03:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	03:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/24	04:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/24	04:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/24	04:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/24	04:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	04:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/07/24	04:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	04:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/24	04:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/24	04:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/24	04:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/24	04:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/24	04:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	04:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	04:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/07/24	04:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/24	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	04:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	04:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/07/24	04:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	04:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	05:11:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	05:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	06:13:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	06:13:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	06:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	06:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	06:49:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	06:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	07:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	07:30:00.0	AOCs_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/24	07:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/24	07:32:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/24	07:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/24	07:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/24	07:32:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	07:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	07:32:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/07/24	07:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/24	07:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	07:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	07:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	07:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	07:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	08:28:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	08:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	09:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	09:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	09:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	09:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	10:06:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	10:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	11:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	11:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	11:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	11:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	11:45:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	11:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	11:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	12:00:00.0	AOCs_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/24	12:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	12:55:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	12:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	12:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	13:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	14:00:00.0	AOCs_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/07/24	14:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/24	14:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				

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2011/07/24	14:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/24	14:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/24	14:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	14:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/07/24	14:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/24	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	14:34:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	14:34:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	14:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	14:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	15:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	15:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	16:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	16:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	16:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	16:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	16:58:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/24	16:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	17:37:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	17:37:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/24	17:37:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/24	17:37:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/24	17:37:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/24	17:37:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	17:40:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/07/24	17:40:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	17:47:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	17:47:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/07/24	17:49:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/24	17:50:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/24	17:50:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/24	17:50:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/24	17:50:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/24	17:50:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	17:50:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/07/24	17:50:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/24	17:50:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	17:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	17:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	17:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	17:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/24	18:35:00.5	XRT_Custom_418_OG [0x1a2]							
2011/07/24	18:36:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/24	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/24	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/24	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/24	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2011/07/24	19:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/24	20:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 ee c1 50 72	
2011/07/24	20:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/07/24	20:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/07/24	20:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/07/24	20:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/07/24	20:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/24	20:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/24	20:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/07/24	20:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/07/24	20:12:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/24	20:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/24	21:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/24	21:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/24	21:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/24	21:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/24	21:49:00.0	XRT_Custom_418_OG [0x1a2]					
2011/07/24	21:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/24	22:46:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/24	22:46:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/24	22:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/24	22:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/24	23:22:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/24	23:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/25	00:25:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/25	00:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/25	00:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/25	00:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/25	00:52:30.0	XRT_Custom_418_OG [0x1a2]					
2011/07/25	00:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/25	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/25	02:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/07/25	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/07/25	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/07/25	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/07/25	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/07/25	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/25	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/25	02:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2011/07/25	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/07/25	02:03:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/25	02:03:03.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/25	02:03:06.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/25	02:06:16.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/25	02:31:00.0	XRT_Custom_418_OG [0x1a2]					
2011/07/25	02:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/25	03:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/25	03:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/25	03:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2011/07/25	03:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	04:05:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	04:05:56.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/07/25	04:09:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/25	04:10:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/25	04:10:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/25	04:10:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/25	04:10:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/25	04:12:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/07/25	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	04:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	04:20:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/25	04:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/25	04:22:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/25	04:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/25	04:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/25	04:22:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/25	04:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	04:22:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/07/25	04:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/25	04:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	05:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	05:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	05:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	05:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	05:48:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/25	05:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	05:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	06:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/25	06:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/07/25	06:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/25	06:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/25	06:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/25	06:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/25	06:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	06:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/07/25	06:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/07/25	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	06:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	06:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	06:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	06:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	07:26:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/25	07:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	08:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	08:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	08:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	08:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2011/07/25	09:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	09:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	10:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	10:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	10:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	10:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	10:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	10:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	11:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	11:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	11:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	11:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	11:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	12:22:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	12:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	13:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	13:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	13:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	13:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	13:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	13:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	14:00:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/25	14:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	15:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	15:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	15:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	15:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	15:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	15:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	15:58:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/25	15:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	16:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	16:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	16:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	16:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	16:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	16:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	16:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	16:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	17:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	18:03:24.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	02 00 00 00 00				
2011/07/25	18:03:24.0	XRT_CTRL_MANU_400_OG [0x190]								
2011/07/25	18:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	18:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/07/25	18:03:46.0	XRT_FLD_DIS_402_OG [0x192]								
2011/07/25	18:03:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/07/25	18:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
2011/07/25	18:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/07/25	18:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]								
2011/07/25	18:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/25	18:06:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]								
2011/07/25	18:06:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/07/25	18:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
2011/07/25	18:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	18:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	18:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	18:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	18:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	18:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/25	19:11:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/25	19:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2011/07/25	20:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/25	20:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/25	20:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/25	20:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	20:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	20:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/25	20:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2011/07/25	20:49:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/25	20:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/25	20:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/25	21:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/07/25	21:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/25	21:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/25	21:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/25	21:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/25	21:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/25	21:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/25	21:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/25	21:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/25	21:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/25	21:45:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/25	21:45:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/25	21:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/25	21:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/25	22:25:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/25	22:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/25	23:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/25	23:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/25	23:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/25	23:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/25	23:51:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/25	23:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	01:02:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	01:02:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	01:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	01:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	01:29:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/26	01:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	02:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/07/26	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/26	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/26	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/26	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/26	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/26	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	02:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/26	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/26	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	02:39:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	02:39:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	02:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	02:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	03:08:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/26	03:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	03:54:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2011/07/26	03:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/07/26	03:55:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2011/07/26	03:55:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/07/26	03:55:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/07/26	03:55:20.5	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/26	03:57:58.5	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2011/07/26	03:58:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/26	04:04:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	04:05:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00
2011/07/26	04:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/07/26	04:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/07/26	04:07:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/07/26	04:07:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/07/26	04:07:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/26	04:07:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/26	04:07:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2011/07/26	04:07:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/07/26	04:08:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	04:08:03.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/26	04:08:05.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/26	04:11:15.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/26	04:46:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/26	04:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/26	05:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	05:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/26	05:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/26	05:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/26	06:25:00.5	XRT_Custom_418_OG [0x1a2]							
2011/07/26	06:26:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/26	07:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	07:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/26	07:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/26	07:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/26	08:03:30.0	XRT_Custom_418_OG [0x1a2]							
2011/07/26	08:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/26	09:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	09:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/26	09:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/26	09:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/26	09:42:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/26	09:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/26	10:15:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/26	10:16:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00